

Simulation Results steps

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1 First Step

Table 1: Benchmark used

Benchmark	# qubits	# gates	two-qubit gates (%)
4gt11 ₈₂	5	27	67

Table 2: Step 1 results

Mapper	Init. place	t1	t2	meas. err.	P. success	f	V_Q
minextendrc	No	3000	3000	0.03	0.96	0.9404637	1582
minextendrc	Yes	3000	3000	0.03	0.98	0.9675513	1038
minextend	No	3000	3000	0.03	0.98	0.9623432	1264
minextend	Yes	3000	3000	0.03	0.98	0.9683911	834
No	No	3000	3000	0.03	0.99	0.98879	390

Table 3: Other mapper statistics

Mapper	Init. place	# qubits (after mapping)	depth	# SWAPS
minextendrc	No		7	237
minextendrc	Yes		6	174
minextend	No		8	228
minextend	Yes		6	165
No	No		5	84