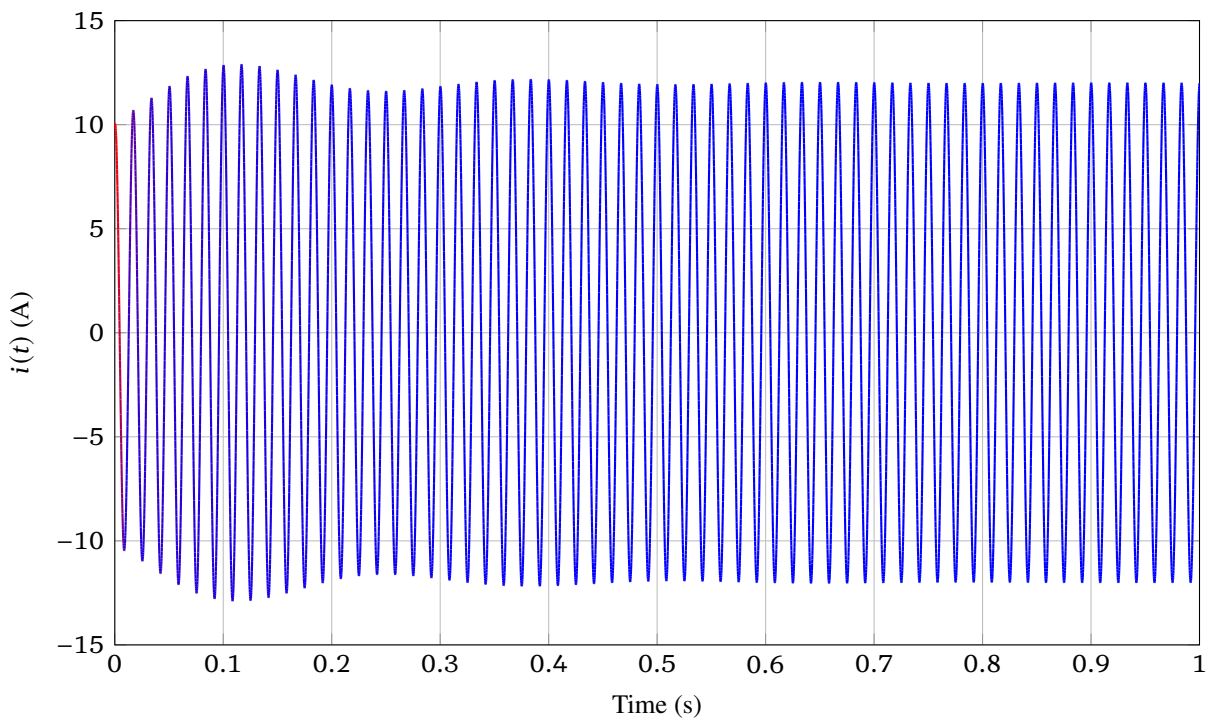


Bus current time domain signal for scenario 1 (current setpoint disturbance)



Bus current time domain signal for scenario 2 (infinite bus voltage disturbance)

