TEC-1G

Power Delivery

Memory Management Unit

Memory Protection

General Input/Output

System Latch

I/O Decoders

System Input

64k Memory

CPU & Clock

Matrix Keyboard & Joystick

HexPad Encoder

The TEC Deck

LCD 20 Characters x 4 Lines

7 Segment Display Unit

Disco LEDs

Modelled on the TEC-1 rev.D with DAT add-on
Originally designed by John Hardy, Ken Stone & Jim Robertson
published in Talking Electronics Magazine, 1983 – 1985
Thanks for assistance from: Craig Hart, Brian Chiha, Ian McLean, James Elphick

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File: TEC-1G.kicad_sch

Title: TEC-1G (Board revision:)

Size: A2	Date: 2023-09-30	Rev: 1.11
KiCad E.D.A.	kicad (6.0.10)	Id: 1/1