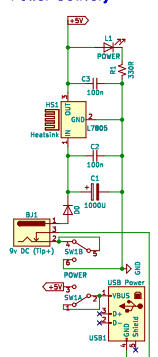
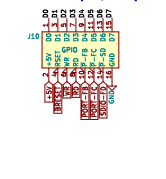


TEC-1G

Power Delivery

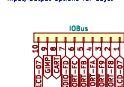


General Input/Output

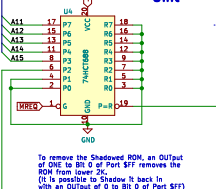


The TEC Deck

The new way to expand your TEC- With appropriate long-legged head expansion boards can be stacked each other, just like the original, but now you have access to ALL as well as port and memory select. No more ugly fly leads or cables. Memory Expansion of 512k with input/output options for dual



Memory Management Unit



Memory Protection



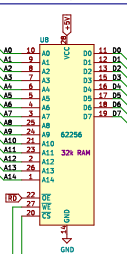
System Latch



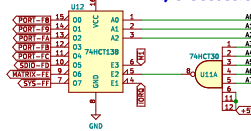
LCD
20 Characters
x 4 Lines

64k Memory

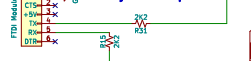
The lower 32K is all RAM in a single chip. The upper 16K of the memory map is reserved for the system ROM, although it is made up of a 32K EEPROM to allow one of two monitors to be selected by the user with a switch.



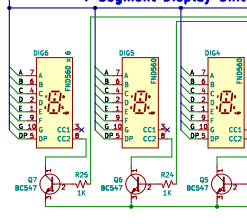
I/O Decoders



System Input



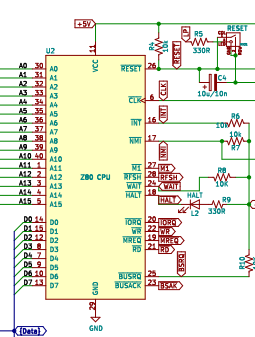
Segment Display Unit



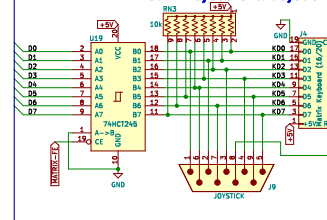
Cisco



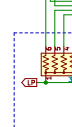
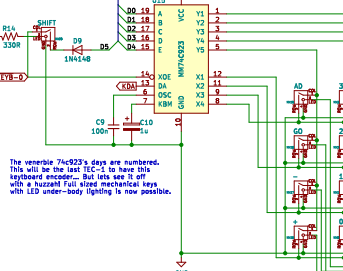
CPU & Clock



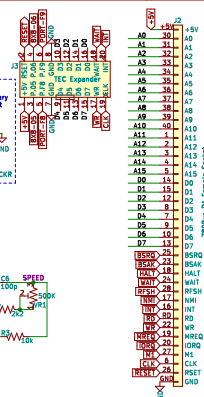
Matrix Keyboard & Joystick



HexPad Encoder



Expansion Connectors



Modelled on the TEC-1 rev.D with DAT add-on
Originally designed by John Hardy, Ken Stone & Jim Robertson
published in Talking Electronics Magazine, 1983 - 1985
Thanks for assistance from: Craig Hart, Brian Chiha, Ian McLean
© Mark Jelic, 2023

Sheet: /
File: TEC-1G.kicad_sch
Title: TEC-1G
Size: A2 Date: 2023-08-31
KiCad E.D.A. kicad (6.0.10)