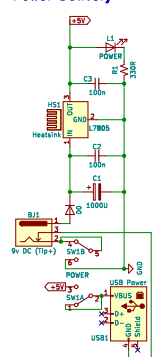
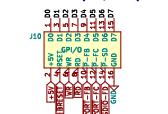


TEC-1G

Power Delivery

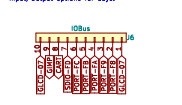


General Input/Output



The TEC Deck

The new way to expand your TEC-11 With appropriate long-legged headers, expansion boards can be stacked on top of each other, just like the original TEC-1 but now you have access to ALL the 280 pins as well as port and memory select lines. No more ugly fly leads or cables. Memory Expansion of 512K with ease. Input/Output options for daisy

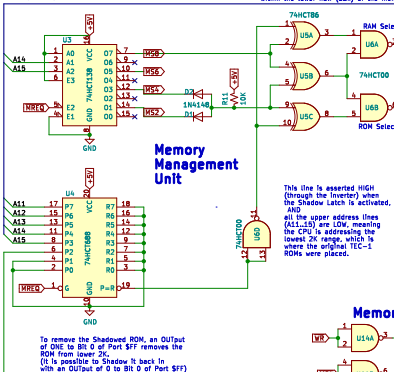


The ROM is selected (asserted LOW)

Any address in the lower 2K is requested (with Shadow ON)

OTHERWISE
The RAM is selected if the address falls within the lower half (32k) of the memory map.

Memory Management Unit



Memory Protection

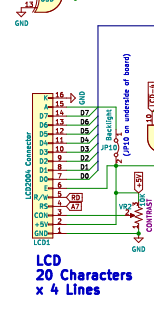


System Latch

This critical latch controls a lot of important functions, and will also be the centre of huge new expansions to come.



LCD
20 Characters
x 4 Lines

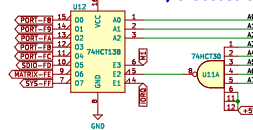


64k Memory

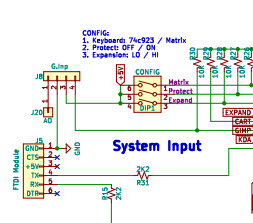
The lower 32K is all RAM in a single chip.

The upper 16K of the memory map is reserved for the system ROM, although it is made up of up to a 64K EPROM to allow selection of multiple monitors, using a pair of switches.

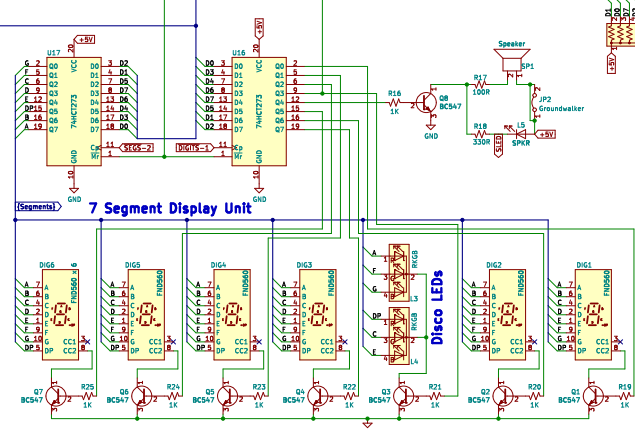
I/O Decoders



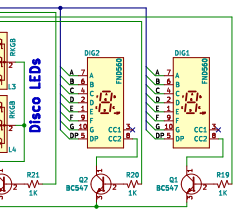
System Input



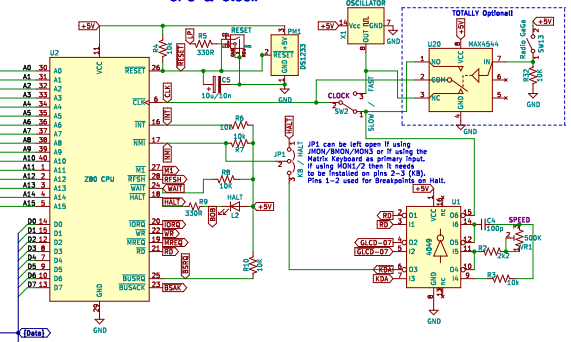
Segment Display Unit



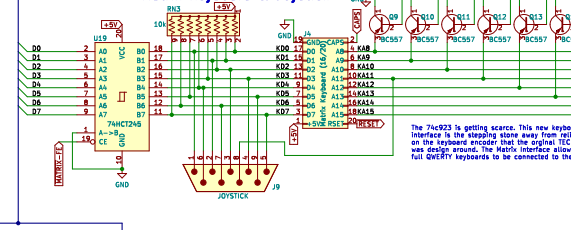
Cisco



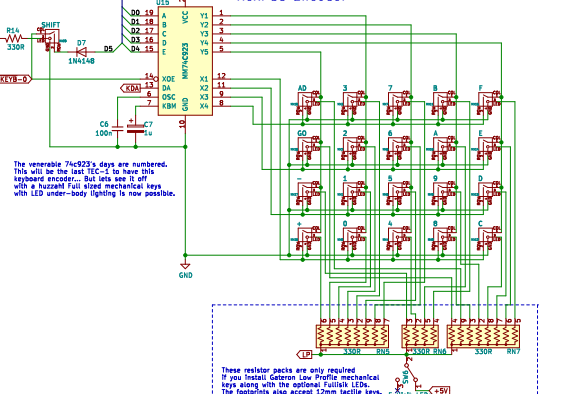
CPU & Clock



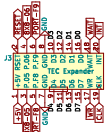
Matrix Keyboard & Joystick



HexPad Encoder



Expansion Connectors



Modelled on the TEC-1 rev.D with DAT add-on
Originally designed by John Hardy, Ken Stone & Jim Robertson
published in Talking Electronics Magazine, 1983 – 1985
Thanks for assistance from: Craig Hart, Brian Chalk, Ian McLean, James Elphick
© Mark Jelic, 2023

Sheet: /
File: TEC-1G.kicad_sch

| Title: TEC-1G (Board revision:) | | Rev: |
|----------------------------------|------------------|--------|
| Size: A2 | Date: 2023-09-30 | |
| KiCad E.D.A. kicad (6.0.10) | | Id: 1/ |