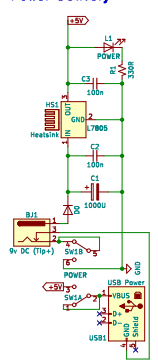
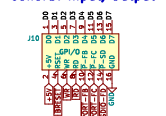


TEC-1G

Power Delivery

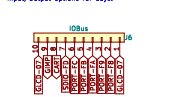


General Input/Output

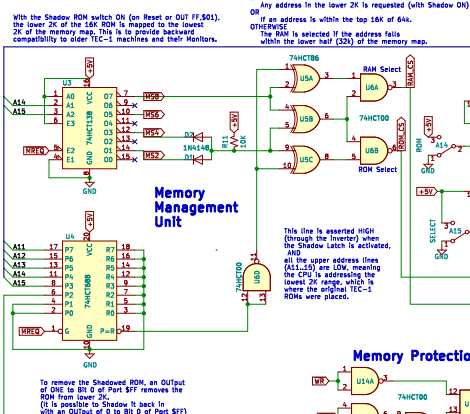


The TEC Deck

The new way to expand your TEC-11 With appropriate long-legged headers, expansion boards can be stacked on top of each other, just like the original TEC-1, but now you have access to ALL the Z80 pins as well as port and memory select lines. No more ugly fly leads or cables. Memory Expansion of 512K with ease. Input/Output options for daisy



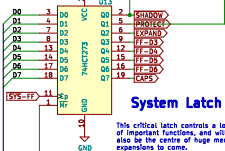
Memory Management Unit



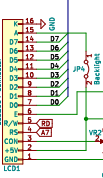
Memory Protection



System Latch

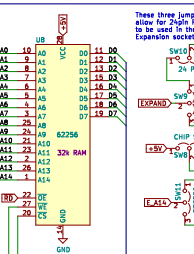


LCD
20 Characters
x 4 Lines

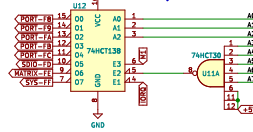


64k Memory

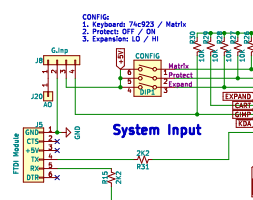
The lower 32K is all RAM in a single chip. The upper 16K of the memory map is reserved for the system ROM, although it is made up of a 32K EEPROM to allow one of two monitors to be selected by the user with a switch.



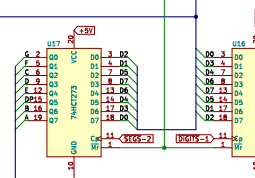
I/O Decoders



System Input



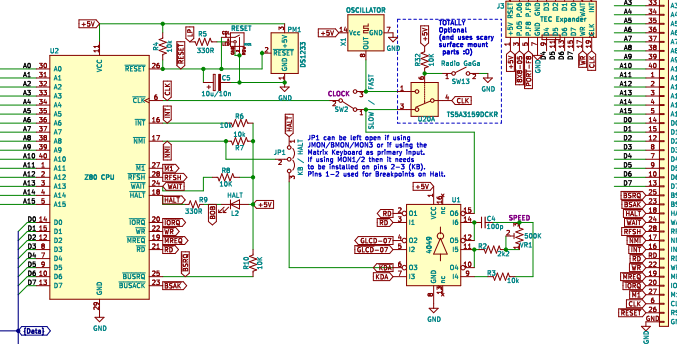
Segment Display Unit



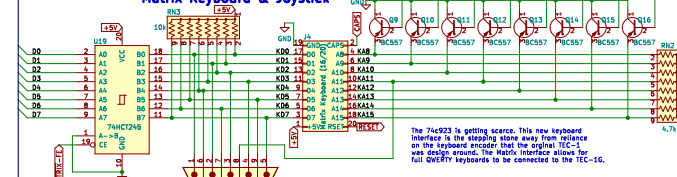
tsc



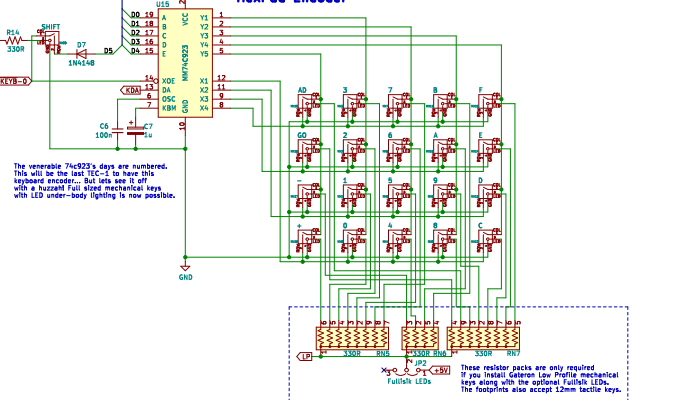
CPU & Clock



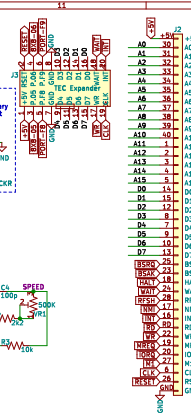
Matrix Keyboard & Joystick



HexPad Encoder



Expansion Connectors



Modelled on the TEC-1 rev.D with DAT add-on
Originally designed by John Hardy, Ken Stone & Jim Robertson
published in Talking Electronics Magazine, 1983 - 1985
Thanks for assistance from: Craig Hart, Brian Chiha, Ian McLean
© Mark Jelic, 2023

Sheet: /
File: TEC-1G.kicad_sch
Title: TEC-1G
Size: A2 Date: 2023-09-19
KiCad E.D.A. kicad (6.0.10)