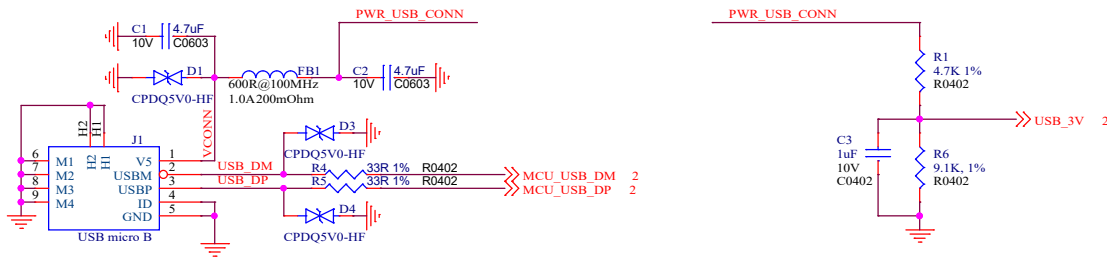
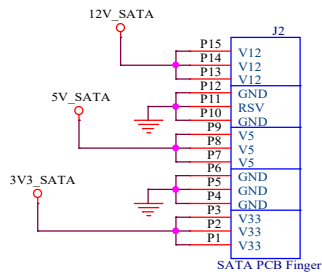
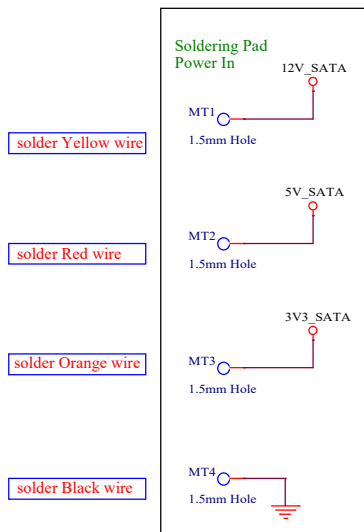


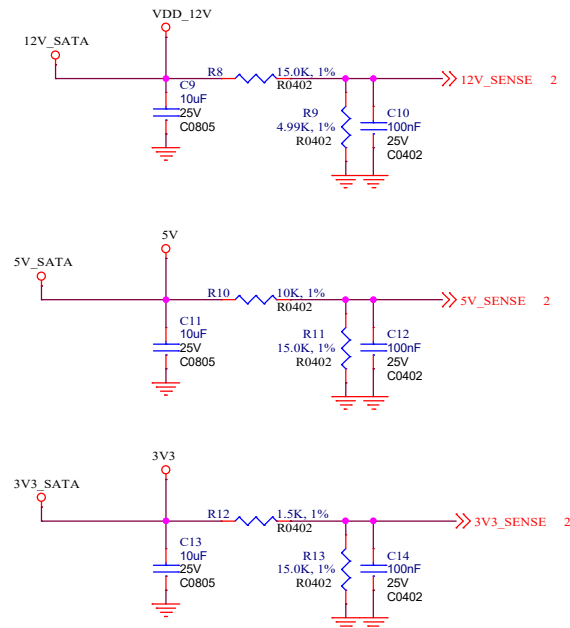
USB Port



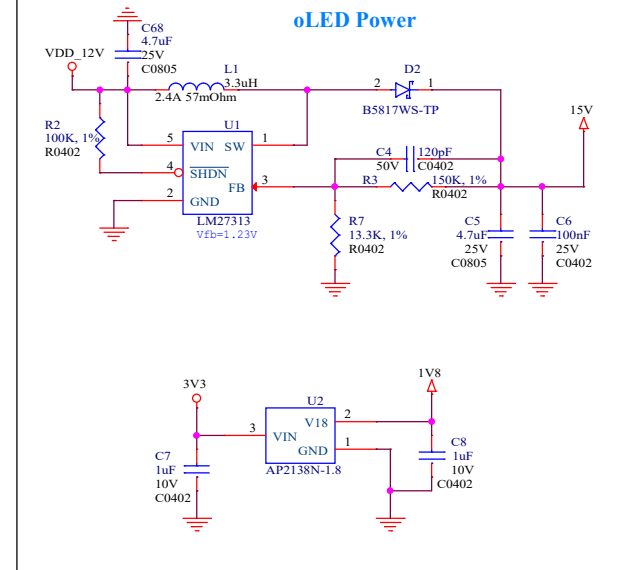
SATA Power



Power Voltage Sense



oLED Power



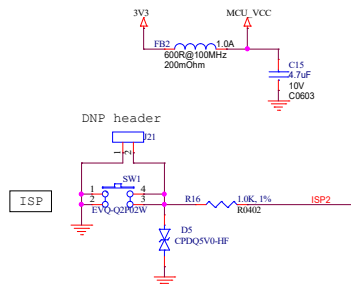
F1 FIDUCIAL_CMP_15
F2 FIDUCIAL_CMP_15
F3 FIDUCIAL_CMP_15
F4 FIDUCIAL_CMP_15
F5 FIDUCIAL_CMP_15
F6 FIDUCIAL_CMP_15

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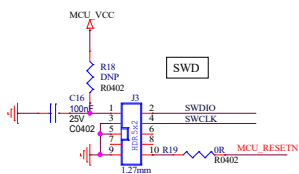
Title		
Cheddar		
Size	Document Number	Rev
B	USB Port & Power source	1.0
Date: Monday, August 06, 2018 Sheet 1 of 5		



LPC546xx boot modes:

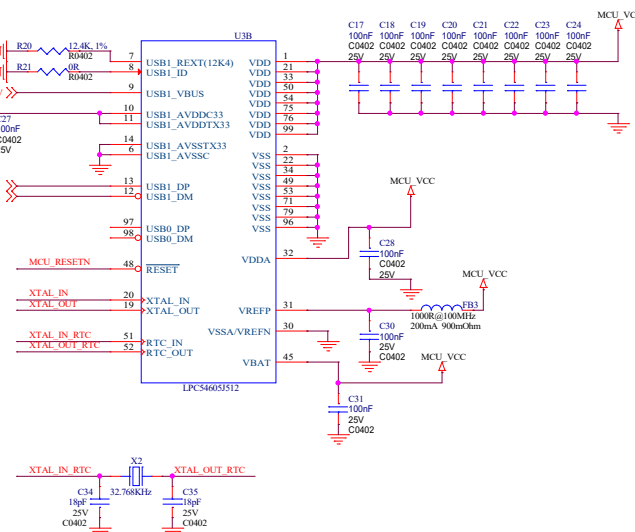
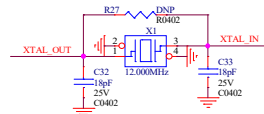
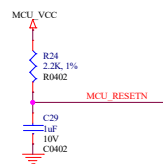
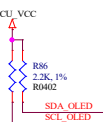
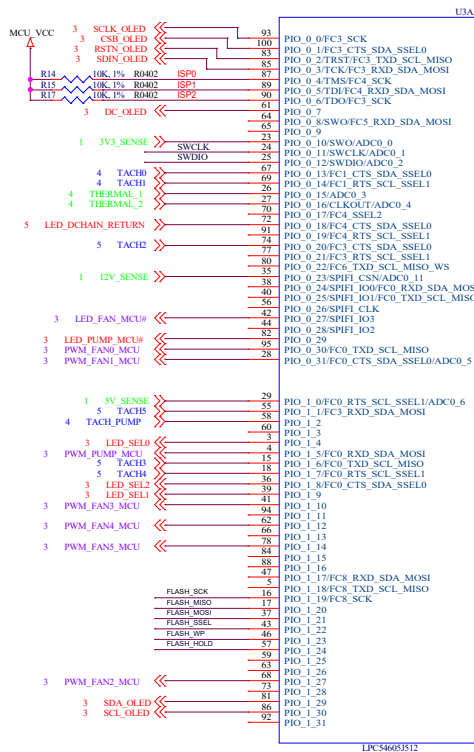
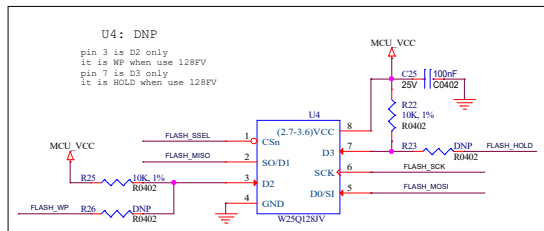
Mode / Boot source	ISP2 (P0-6)	ISP1 (P0-5)	ISP0 (P0-4)
Internal flash boot	High	High	High
USB1 (High speed)	Low	High	High
CAN	High	Low	Low
USB0 (Full speed)	High	Low	High
USART1/2C/SPI	High	High	Low

Debug Connector/Interface



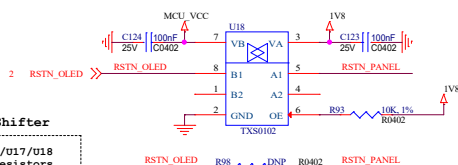
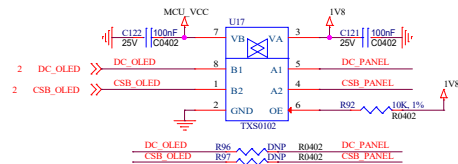
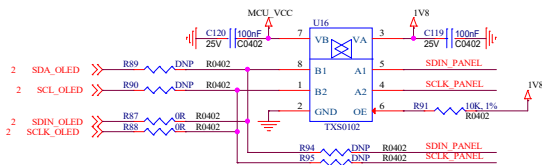
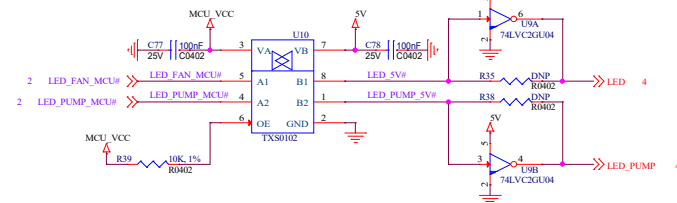
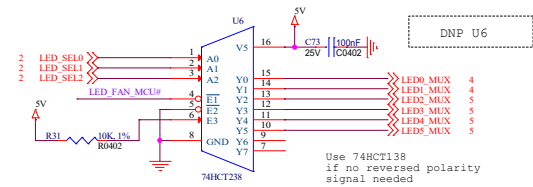
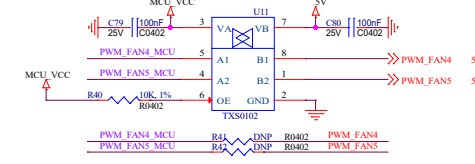
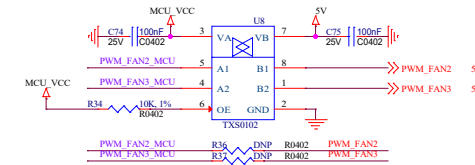
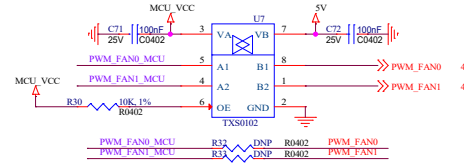
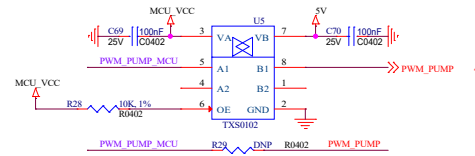
U4: DNP

pin 3 is D2 only
it is WP when use 128FV
pin 7 is D3 only
it is HOLD when use 128FV



Level Shifter

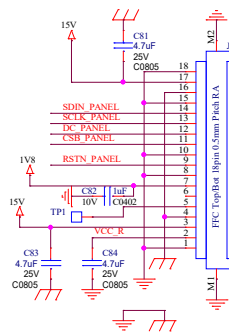
install U5/U7/U8/U11
or jumper resistors,
but not both



Level Shifter

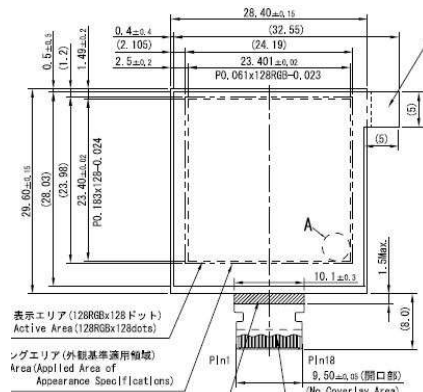
install U16/U17/U18
or jumper resistors,
but not both

oLED interface 18 pin FFC 0.5mm pitch



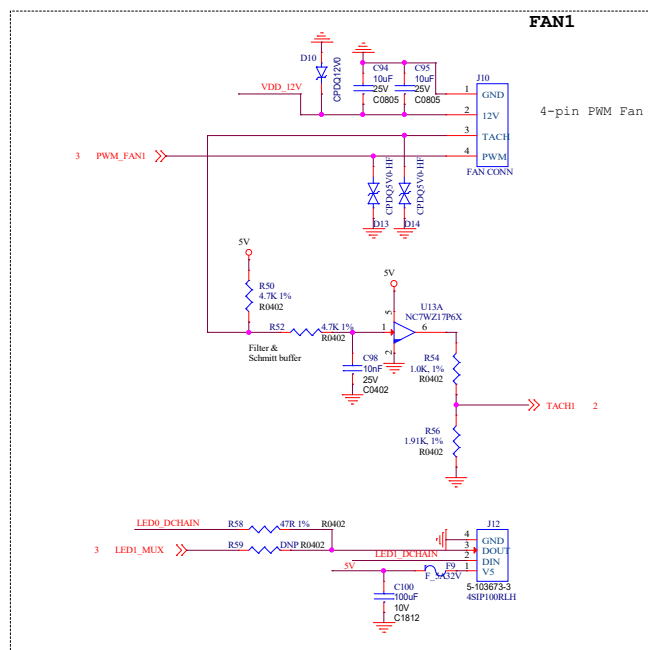
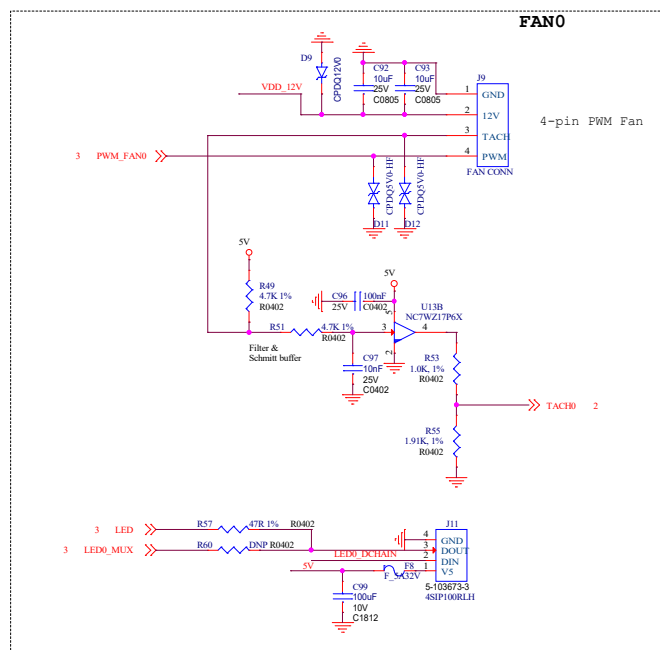
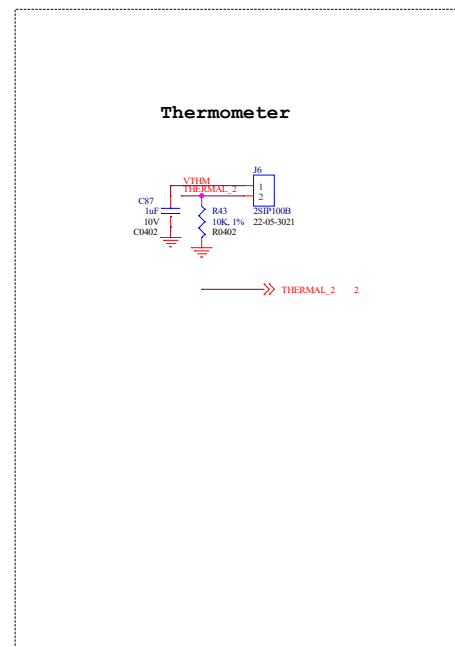
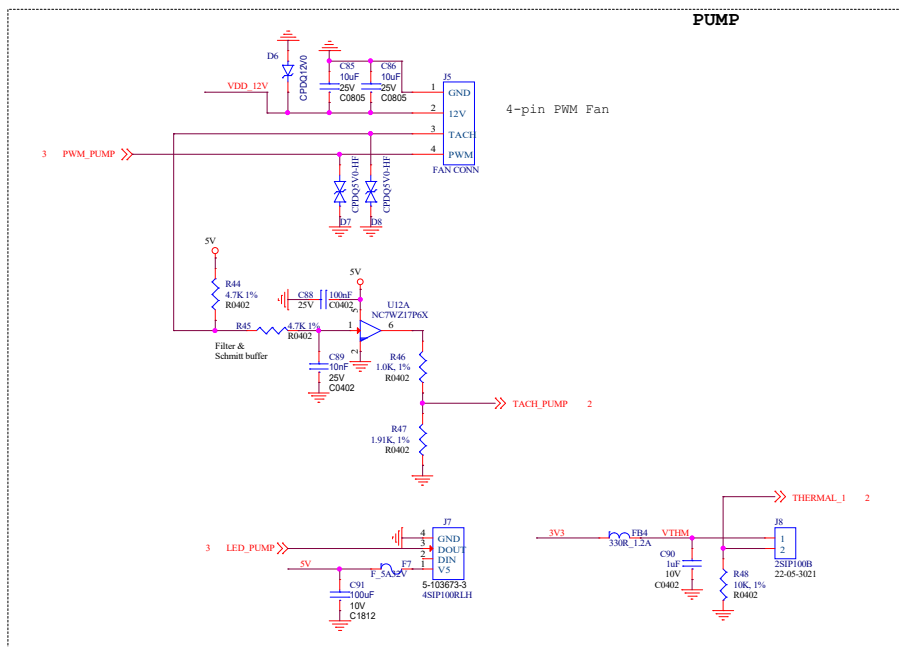
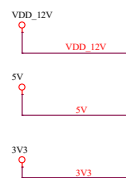
Pin No.	名称	機能	Type
1	GND	グラウンド(ロジック用)	P
2	VCC_O	OLED 駆動電圧	P
3	AGND	グラウンド(OLED駆動電源用)	P
4	GND	グラウンド(ロジック用)	P
5	SDIN	シリアルデータ	I
6	SCLK	シリアルクロック	I
7	DC	データコマンド選択	I
8	CSB	チップセレクト	I
9	GND	グラウンド(ロジック用)	P
10	RSTN	リセット	I
11	GND	グラウンド(ロジック用)	P
12	VDD	ロジック電圧	P
13	NC	NC	-
14	VDDOL	内部レギュレータ電圧	P
15	AGND	グラウンド(OLED駆動電源用)	P
16	VCC_O	OLED 駆動電圧	P
17	VCC_R	COM High Voltage	P
18	GND	グラウンド(ロジック用)	P

Placement



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File	Cheddar	
Size	Document Number	Rev
C	Level Shifter & oLED	1.0
Date	Thursday, August 16, 2018	Sheet 3 of 5



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File	Cheddar		
Size	Document Number	2xFan/Pump/Thermometer	Rev 1.0
Date	Friday, August 03, 2018	Sheet	4 of 5

