



Registers

A	50
BC	32 00
DE	00 00
HL	00 00
PSW	00 00
PC	42 0C
SP	FF FF
Int-Reg	00

Flag

S	0
Z	0
AC	1
P	1
C	0

Load me at

```
1 LDA 8500
2 MOV B, A
3 LDA 8501
4 ADD B
5 STA 8502
6 RST 1
```

Decimal - Hex Conversion

Decimal

Hex

0

0

To Hex

To Dec

I/O Ports

0

-

+

00

Update Port Value

Memory

0

-

+

00

Update Memory

Data Stack KeyPad Memory I/O Ports

Start 8500

OK

Address (Hex)	Address	Data
2134	8500	50
2135	8501	30
2136	8502	80
2137	8503	0
2138	8504	0
2139	8505	0
213A	8506	0
213B	8507	0
213C	8508	0
213D	8509	0
213E	8510	0
213F	8511	0
2140	8512	0
2141	8513	0

Line No Assembler Message

0 Program assembled successfully





Registers

A	03
BC	02 03
DE	00 00
HL	00 00
PSW	00 00
PC	42 1A
SP	FF FF
Int-Reg	00

Flag

S	1
Z	0
AC	0
P	0
C	1

Load me at

```
1 LDA 8501
2 MOV B, A
3 LDA 8500
4 MVI C, 00
5 LOOP: CMP B
6 JC LOOP1
7 SUB B
8 INR C
9 JMP LOOP
10 LOOP1: STA 8502
11 MOV A, C
12 STA 8503
13 RST 1
```

Decimal - Hex Conversion

Decimal

Hex

0

0

To Hex

To Dec

I/O Ports

0

-

+

00

Update Port Value

Memory

0

-

+

00

Update Memory

Data Stack KeyPad Memory I/O Ports

Start 8500

OK

Address (Hex) Address Data

2134	8500	6
2135	8501	2
2136	8502	0
2137	8503	3
2138	8504	0
2139	8505	0
213A	8506	0
213B	8507	0
213C	8508	0
213D	8509	0
213E	8510	0
213F	8511	0
2140	8512	0
2141	8513	0

Line No Assembler Message

0 Program assembled successfully

Simulator: Idle





Registers

A	14
BC	14 00
DE	00 00
HL	00 00
PSW	00 00
PC	42 0C
SP	FF FF
Int-Reg	00

Flag

S	0
Z	0
AC	0
P	1
C	0

Load me at

```
1 LDA 8000
2 MOV B, A
3 LDA 8001
4 SUB B
5 STA 8002
6 RST 1
```

Decimal - Hex Conversion

Decimal

Hex

0

0

To Hex

To Dec

I/O Ports

0

-

+

00

Update Port Value

Memory

0

-

+

00

Update Memory

Data Stack KeyPad Memory I/O Ports

Start 8000

OK

Address (Hex)	Address	Data
1F40	8000	20
1F41	8001	40
1F42	8002	20
1F43	8003	0
1F44	8004	0
1F45	8005	0
1F46	8006	0
1F47	8007	0
1F48	8008	0
1F49	8009	0
1F4A	8010	0
1F4B	8011	0
1F4C	8012	0
1F4D	8013	0

Line No Assembler Message

0 Program assembled successfully





Registers

A	20
BC	04 00
DE	00 00
HL	00 00
PSW	00 00
PC	42 1A
SP	FF FF
Int-Reg	00

Flag

S	0
Z	1
AC	0
P	1
C	0

Load me at

```
1 LDA 8500
2 MOV B, A
3 LDA 8501
4 MOV C, A
5 CPI 00
6 JZ LOOP
7 XRA A
8 LOOP1: ADD B
9 DCR C
10 JZ LOOP
11 JMP LOOP1
12 LOOP: STA 8502
13 RST 1
```

Decimal - Hex Conversion

Decimal

Hex

0

0

To Hex

To Dec

I/O Ports

0

-

+

00

Update Port Value

Memory

0

-

+

00

Update Memory

Data Stack KeyPad Memory I/O Ports

Start 8500

OK

Address (Hex)	Address	Data
2134	8500	4
2135	8501	8
2136	8502	32
2137	8503	0
2138	8504	0
2139	8505	0
213A	8506	0
213B	8507	0
213C	8508	0
213D	8509	0
213E	8510	0
213F	8511	0
2140	8512	0
2141	8513	0

Line No Assembler Message

0 Program assembled successfully

Simulator: Idle