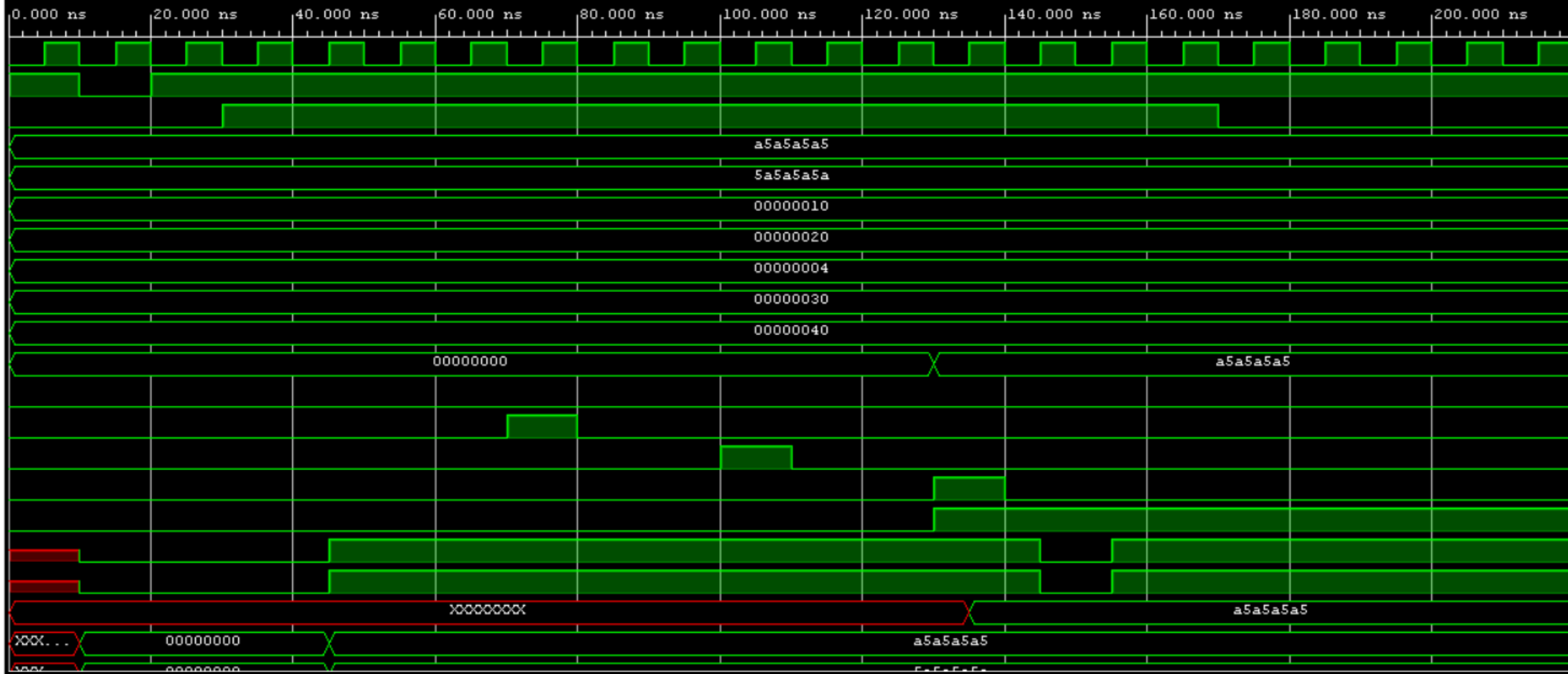


Name	Value
clk	1
rst	1
start	0
> vector_a[31:0]	a5a5a5a5
> vector_b[31:0]	5a5a5a5a
> vector_a_addr[31:0]	00000010
> vector_b_addr[31:0]	00000020
> vector_len[31:0]	00000004
> output_addr[31:0]	00000030
> read_data_addr[31:0]	00000040
> read_data[31:0]	a5a5a5a5
status	0
processing_done	0
store_done	0
read_done	0
rvalid	1
wdvalid	1
awvalid	1
> rdata[31:0]	a5a5a5a5
> wdata_a[31:0]	a5a5a5a5
> wdata_b[31:0]	5a5a5a5a



status 0
processing_done 0
store_done 0
read_done 0
rvalid 1
wvalid 1
awvalid 1
> rdata[31:0] a5a5a5a5
> wdata_a[31:0] a5a5a5a5
> wdata_b[31:0] 5a5a5a5a
> waddr_a[31:0] 00000010
> waddr_b[31:0] 00000020
> waddr_output[31:0] 00000030
> vector_len_o[31:0] 00000004
start_fetch 0
start_compute 1
start_write 0
start_read 0

