

LAB #2

1- (6 pts) Fetch-Decode-Execute

MEMORY				
Address		Value		Operation
0000	1100	0110	1011	Load the value at address 1000 0000 and store it in the register
0000	1101	1111	0010	Do a logical NOT on the register
0000	1102	0010	0001	Store the value in the register to memory at address 1111 0010
...		...		
1000	0000	1111	0000	
...		...		
1111	0010	0101	1111	

Program Counter = 0000 1100 → Register = ?

Program Counter = 0000 1101 → Register = ?

Program Counter = 0000 1102 → 1111 0010 = ?

(8086 Address Selector Scheme)

2- (2 pts) Consider the byte at address 23CDF within a 64K segment defined by selector value 20BA. What is its offset?

(Compiler)

3- (2 pts) What is Compiler and Interpreter? Give examples for them.