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¡Abstract here;

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Introduction

- 1.1 Background and Recent Research
- 1.1.1 jany sub section here;
- 1.1.2 Literature Survey
- 1.2 Layout of Paper

 ${\it iSub-subsection\ title\ \it i}$ some text[1], some more text

 ${}_{i}$ Sub-subsection title; even more text¹, and even more.

1.3 Motivation

¹;footnote here;

Technical Background

- 2.1 Isogenies
- 2.1.1 ¡Sub-section title;
- 2.1.2 ¡Sub-section title;

some text[2], some more text

- 2.1.3 ¡Sub-section title;
- 2.1.4 ¡Sub-section title;

Refer figure 4.1.

- 2.2 SIDH
- 2.2.1 Zero-Knowledge Proof of Identity
- 2.3 Fiat-Shamir Heuristic
- 2.3.1 Unruh's Construction
- 2.4 Isogeny Based Signatures

Algorithm 1 KeyGen(λ)

- 1: Pick a random point S of order $\ell_A^{e_A}$
- 2: Compute the isogeny $\phi: E \to E/\langle S \rangle$
- 3: $pk \leftarrow (E/\langle S \rangle, \phi(P_B), \phi(Q_B))$
- 4: $sk \leftarrow S$
- 5: **return** (pk,sk)

Algorithm 2 Sign(sk, m)

```
1: for i = 1..2\lambda do
            Pick a random point R of order \ell_R^{e_B}
 2:
            Compute the isogeny \psi: E \to E/\langle R \rangle
 3:
            Compute either \phi': E/\langle R \rangle \to E/\langle R, S \rangle or \psi': E/\langle S \rangle \to E/\langle R, S \rangle
 4:
            (E_1, E_2) \leftarrow (E/\langle R \rangle, E/\langle R, S \rangle)
 5:
            com_i \leftarrow (E_1, E_2)
 6:
 7:
            ch_{i,0} \leftarrow_R \{0,1\}
            (\mathtt{resp}_{i,0},\mathtt{resp}_{i,1}) \leftarrow ((R,\phi(R)),\psi(S))
 8:
            if ch_{i,0} = 1 then
 9:
                  swap(resp_{i,0}, resp_{i,1})
10:
            h_{i,j} \leftarrow G(\mathtt{resp}_{i,j})
11:
12: J_1 \parallel ... \parallel J_{2\lambda} \leftarrow H(pk, m, (com_i)_i, (ch_{i,j})_{i,j}, (h_{i,j})_{i,j})
13: return \sigma \leftarrow ((\mathsf{com}_i)_i, (\mathsf{ch}_{i,j})_{i,j}, (h_{i,j})_{i,j}, (\mathsf{resp}_{i,J_i})_i)
```

Algorithm 3 Verify(pk, m, σ)

```
1: J_1 \parallel ... \parallel J_{2\lambda} \leftarrow H(pk, m, (com_i)_i, (ch_{i,j})_{i,j}, (h_{i,j})_{i,j})
 2: for i = 0..2\lambda do
          \operatorname{check} h_{i,J_i} = G(\operatorname{resp}_{i,J_i})
 3:
          if ch_{i,J_i} = 0 then
 4:
               Parse (R, \phi(R)) \leftarrow \mathtt{resp}_{i,J_i}
 5:
               check (R, \phi(R)) have order \ell_R^{e_B}
 6:
               check R generates the kernel of the isogeny E \to E_1
 7:
               check \phi(R) generates the kernel of the isogeny E/\langle S \rangle \to E_2
 8:
 9:
          else
               Parse \psi(S) \leftarrow \mathsf{resp}_{i.J_i}
10:
               check \psi(S) has order \ell_A^{e_A}
11:
               check \psi(S) generates the kernel of the isogeny E_1 \to E_2
12:
13: if all checks succeed then
          return 1
14:
```

If we transcribe the above to the language of the Microsoft SIDH API, we have in essense the following:

Algorithm 4 KeyGen(λ)

```
1: (pk, sk) \leftarrow KeyGeneration_B()
2: \mathbf{return} (pk, sk)
```

```
Algorithm 5 Sign(sk, m)
```

```
1: for i = 1..2\lambda do

2: (E, R, \psi) \leftarrow \text{KeyGeneration\_A}()

3: E_1 \leftarrow E/\langle R \rangle

4: (E_2, E/\langle R, S \rangle) \leftarrow \text{SecretAgreement\_B}()

5: (E_1, E_2) \leftarrow (E/\langle R \rangle, E/\langle R, S \rangle)

6: \text{com}[i] \leftarrow (E_1, E_2)

7: \text{ch}[i] \leftarrow_R \{0, 1\}

8: (\text{resp}[i]_0, \text{resp}[i]_1) \leftarrow ((R, \phi(R)), \psi(S))

9: J_1 \parallel ... \parallel J_{2\lambda} \leftarrow H(pk, m, (\text{com}_i)_i, (\text{ch}_i)_i, (h_{i,j})_{i,j})

10: \text{return } \sigma \leftarrow ((\text{com}_i)_i, (\text{ch}_{i,j})_{i,j}, ((\text{resp})[J_i])
```

Batching Operations for Isogenies

3.1 Batching Procedure in Detail

One of our main contributions is the embedding of a low-level \mathbb{F}_{p^2} procedure into Microsofts pre-existing SIDH library. The procedure in question reduces arbitrarily many unrelated/potentially parallel \mathbb{F}_{p^2} inversions to a sequence of \mathbb{F}_p multiplications & additions, as well as one \mathbb{F}_p inversion.

More specifically, the procedure takes us from $n \mathbb{F}_{p^2}$ inversions to:

- $2n \mathbb{F}_p$ squarings
- $n \mathbb{F}_p$ additions
- 1 \mathbb{F}_p inversion
- $3(n-1) \mathbb{F}_p$ multiplications
- $2n \mathbb{F}_p$ multiplications

The procedure is as follows:

3.1.1 Projective Space

Because the work of Yoo et al. was built on top of the original Microsoft SIDH library, all underlying field operations (and isogeny arithmetic) are performed in projective space. Doing field arithmetic in projective space allows us to avoid many inversion operations. The downside of this (for our work) is that the number opportunities for implementing the batched inversion algorithm becomes greatly limited.

3.1.2 Remaining Opportunities

There are two functions called in the isogeny signature system that perform a \mathbb{F}_{p^2} inversion: j_inv and inv_4_way. These functions are called once in SecretAgreement and KeyGeneration operations respectively. SecretAgreement and KeyGeneration are in turn called from each signing and verification thread.

Algorithm 6 Batched Partial-Inversion

```
1: procedure PARTIAL_BATCHED_INV(\mathbb{F}_{p^2}[\ ] VEC, \mathbb{F}_{p^2}[\ ] DEST, INT N)
         initialize \mathbb{F}_p den[n]
 2:
         for i = 0..(n-1) do
 3:
              den[i] \leftarrow a[i][0]^2 + a[i][1]^2
 4:
         a[0] \leftarrow den[0]
 5:
         for i = 1..(n-1) do
 6:
              a[i] \leftarrow a[i-1]*den[i]
 7:
         a_{inv} \leftarrow inv(a[n-1])
 8:
         for i = n-1..1 do
 9:
              a[i] \leftarrow a_{inv} * dest[i-1]
10:
              a_{inv} \leftarrow a_{inv} * den[i]
11:
         dest[0] \leftarrow a_{inv}
12:
         for i = 0..(n-1) do
13:
              dest[i][0] \leftarrow a[i] * vec[i][0]
14:
              vec[i][1] \leftarrow -1 * vec[i][1]
15:
              dest[i][1] \leftarrow a[i] * vec[i][1]
16:
```

This means that in the signing procedure there are 2 opportunities for implementing batched partial-inversion with a batch size of 248 elements. In the verify procedure, however, there are 3 opportunities for implementing batched inversion with a batch size of roughly 124 elements.

3.2 Implementation

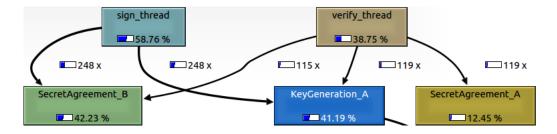


Figure 3.1: ¡Caption here;

3.3 Results

Two different machines were used for benchmarking. System A denotes a single-core, 1.70 GHz Intel Celeron CPU. System B denotes a quad-core, 3.1 GHz AMD A8-7600.

The two figures below provide benchmarks for KeyGen, Sign, and Verify procedures with both batched partial inversion implemented (in the previously mentioned locations) and not implemented. All benchmarks are averages computed from 100 randomized sample runs. All results are measured in clock cycles.

| Procedure | System A Without Batching | System A With Batching |
|--|--|--|
| KeyGen Signature Sign Signature Verify | 68,881,331 15,744,477,032 11,183,112,648 | 68,881,331 15,565,738,003 10,800,158,871 |
| | 11,200,21 2 ,010 | 10,000,100,011 |
| Procedure | System B Without Batching | System B With Batching |
| KeyGen Signature Sign Signature Verify | 84,499,270 10,227,466,210 7,268,804,442 | 84,499,270 10,134,441,024 7,106,663,106 |

System A: With inversion batching turned on we notice a 1.1% performance increase for key signing and a 3.5% performance increase for key verification.

System B: With inversion batching turned on we a observe a 0.9% performance increase for key signing and a 2.3% performance increase for key verification.

3.3.1 Analysis

It should first be noted that, because our benchmarks are measured in terms of clock cycles, the difference between our two system clock speeds should be essentially ineffective.

In the following table, "Batched Inversion" signifies running the batched partial-inversion procedure on 248 \mathbb{F}_{p^2} elements. The procedure uses the binary GCD \mathbb{F}_p inversion function which, unlike regular \mathbb{F}_{p^2} montgomery inversion, is not constant time.

| Procedure | Performance |
|---|-------------|
| Batched Inversion | 1721718 |
| \mathbb{F}_{p^2} Montgomery Inversion | 874178 |

Do performance increases observed make sense?

Compressed Signatures

- 4.1 Compression of Public Keys
- 4.1.1 ¡Sub-section title;
- 4.1.2 ¡Sub-section title;

some text[2], some more text

- 4.1.3 ¡Sub-section title¿
- 4.1.4 ¡Sub-section title;

Refer figure 4.1.



Figure 4.1: ¡Caption here;

- 4.1.5 ¡Sub-section title¿
- 4.2 Implementation
- 4.3 Results

Discussion & Conclusion

- 5.1 Results & Comparisons
- 5.2 Additional Opportunities for Batching
- 5.3 Future Work

¡Conclusion here;

Acknowledgments

 ${\it j} Acknowledgements\ here {\it i}$

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¡Month and Year here; National Institute of Technology Calicut

References

- [1] iName of the reference here;, ${\tt curlhere}$
- [2] iName of the reference here;, ``infty