



# VITrendz FFCS Planner

Screenshot Timetable (enable background graphics)

FILTER BY SLOT / FACULTY



ADD COURSE

Theory Hours	08:00 AM 08:50 AM	09:00 AM 09:50 AM	10:00 AM 10:50 AM	11:00 AM 11:50 AM	12:00 PM 12:50 PM	----- -----	L U N C H	02:00 PM 02:50 PM	03:00 PM 03:50 PM	04:00 PM 04:50 PM	05:00 PM 05:50 PM	06:00 PM 06:50 PM	07:00 PM 07:50 PM
Lab Hours	08:00 AM 08:45 AM	08:46 AM 09:30 AM	10:00 AM 10:45 AM	10:46 AM 11:30 AM	11:31 AM 12:15 PM	12:16 PM 01:00 PM		02:00 PM 02:45 PM	02:46 PM 03:30 PM	04:00 PM 04:45 PM	04:46 PM 05:30 PM	05:31 PM 06:15 PM	06:16 PM 07:00 PM
MON	A1/L1	F1/L2	D1/L3	TB1/L4	TG1/L5	L6		A2/L31	F2/L32	D2/L33	TB2/L34	TG2/L35	V3/L36
TUE	B1/L7	G1/L8	E1/L9	TC1/L10	TAA1/L11	L12		B2/L37	G2/L38	E2/L39	TC2/L40	TAA2/L41	V4/L42
WED	C1/L13	A1/L14	F1/L15	V1/L16	V2/L17	L18		C2/L43	A2/L44	F2/L45	TD2/L46	TBB2/L47	V5/L48
THU	D1/L19	B1/L20	G1/L21	TE1/L22	TCC1/L23	L24		D2/L49	B2/L50	G2/L51	TE2/L52	TCC2/L53	V6/L54
FRI	E1/L25	C1/L26	TA1/L27	TF1/L28	TD1/L29	L30	E2/L55	C2/L56	TA2/L57	TF2/L58	TDD2/L59	V7/L60	

CUSTOMIZED COURSE ADDITION

AUTO SLOT PICKER

FACULTY REVIEWS

Slot	Code	Course	Faculty	Venue	
D1+TD1	ISWE301L	COA	sunilKumarG		
G1	ISTST	Qualitytative skills practice	FACE		
TCC1	ICSE	Java:Programming	Ushapreethi		
L45+L46+L49+L50	ICSE	Java:Programming-Lab	Ushapreethi		
A1+TA1+TAA1	IMAT201L	Complecs variables	Srinivasa Murthy		
B1+TB1	ISWE102P	DSA	Yusuf Akhthar		
L37+L38	ISWE102P	DSA-Lab	Yusuf Akhthar		
L53+L54	IENG102P	Technical Report	Rukmini		
E1+TE1	ISWE304L	Software Architecture	AshaN		
C1+TC1	ISWE204L	Operating Systems	Arul Tressa Mathew		
L39+L40	ISWE204L	Operating Systems-lab	Arul Tressa Mathew		