

Common Signal Names & Naming Conventions

Signals beginning with capital letters denote an I/O pin

Signals beginning with lower case letters are internal to a cell

VDD – Supply Voltage	VDD
$\overline{\text{RST}}$ – Reset (Active Low)	$\overline{\text{RST}}$
RST – Reset (Active High)	RST
GND – Ground or VSS equivalent	GND

L – Left (DI Encoded Input Channel)

Le – Left enable

R – Right (DI Encoded Output Channel)

Re – Right enable

S – Split (DI Encoded Input Control Channel)

SLe – Split & Left Enable

M – Merge (DI Encoded Input Control Channel)

Me – Merge Enable

A, Ae; B, Be; C, Ce; D, De; etc:

Input or output channels (cell dependent) w/ corresponding enable signals

en – internal enable

lv – left valid


rv – right valid

sv – split valid

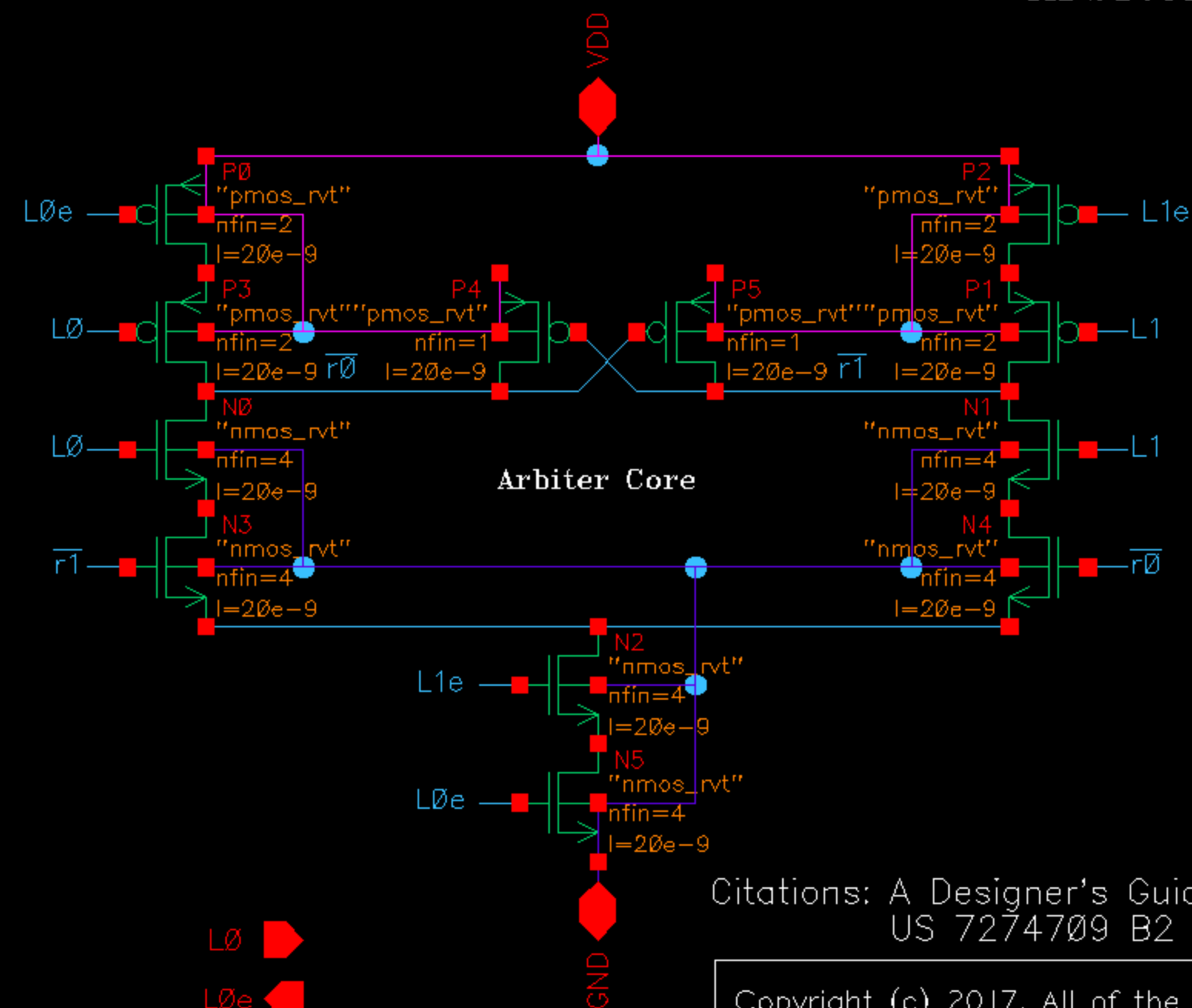
slv – split & left valid

$\overline{\text{slv}}$ – not split & left valid (inverted slv)

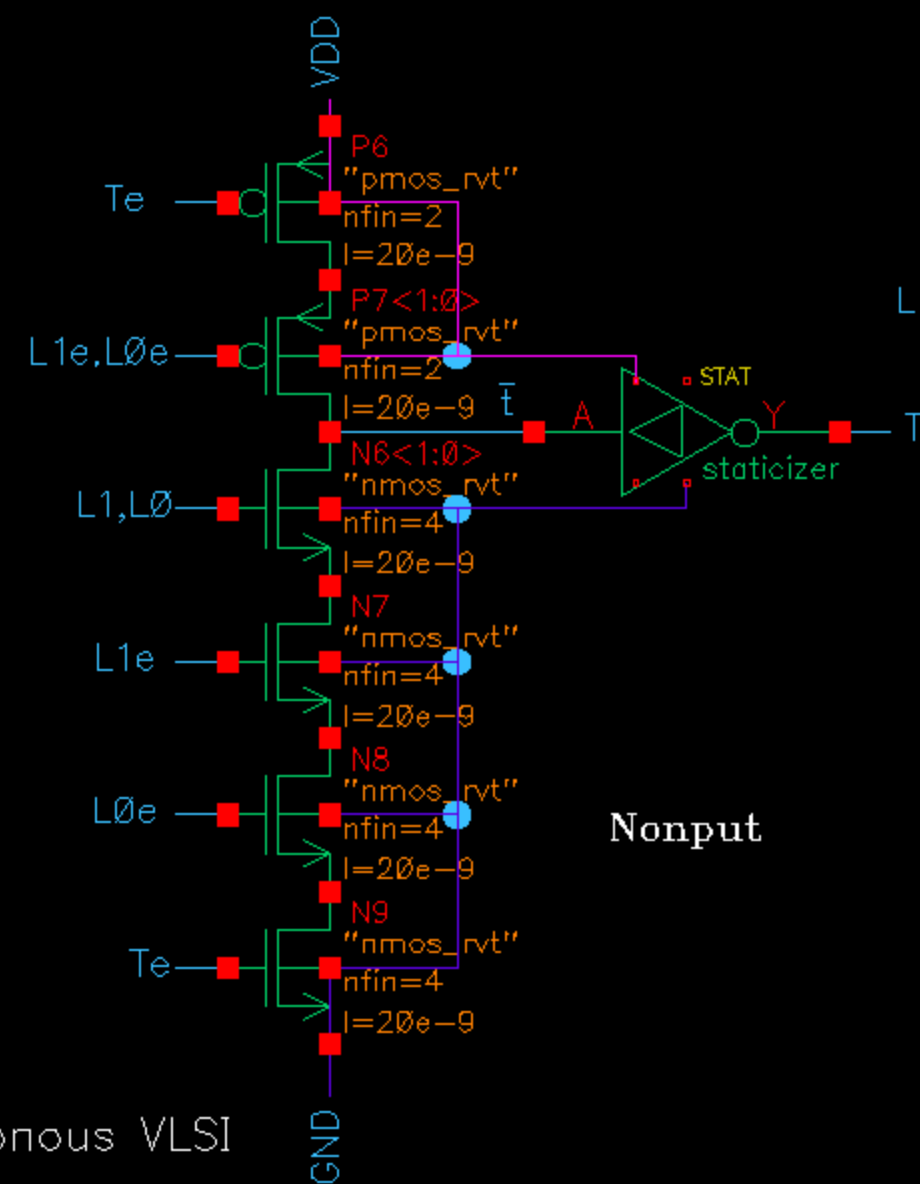
etc.

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Authors: R St. Denis, C. Nitta, R. Amirtharajah, M. Farrens V. Akella	
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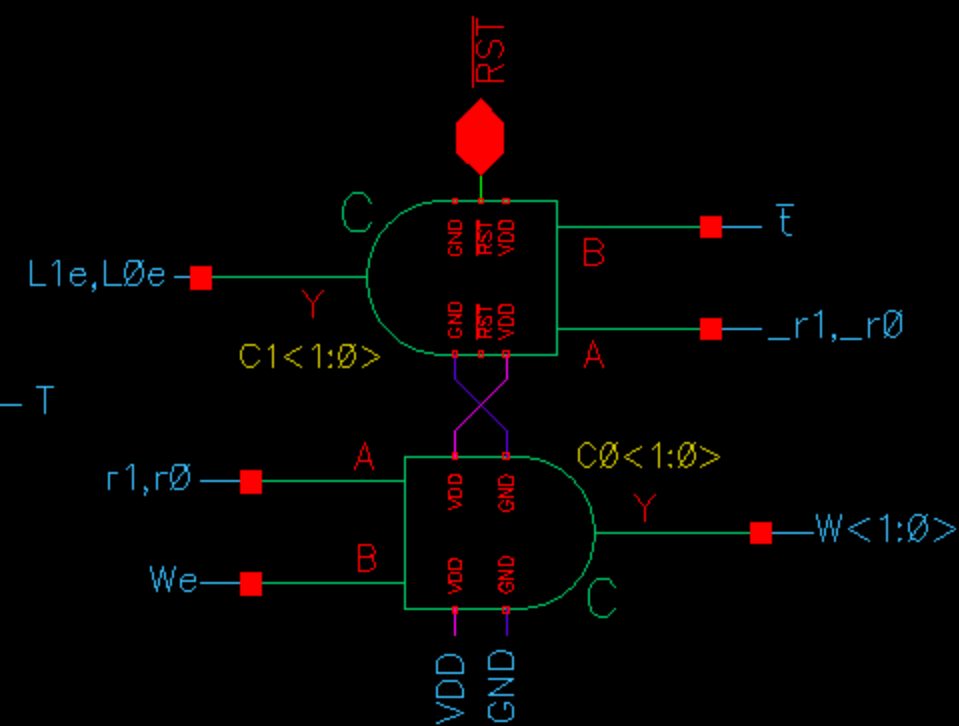
Arbiter_QDI_Nonput



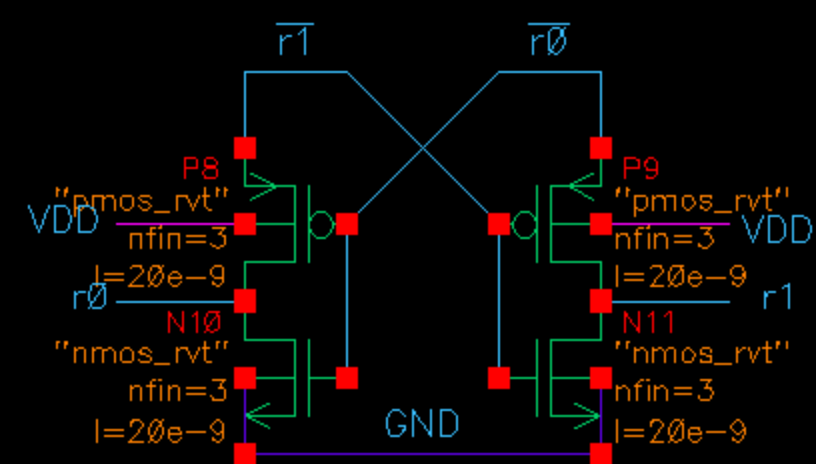
- L0
- L0e
- L1
- L1e
- W<1:0>
- We
- T
- Te



Nonput



Left Enables & Winner Output Rails



Metastability Filter

Citations: A Designer's Guide to Asynchronous VLSI
US 7274709 B2

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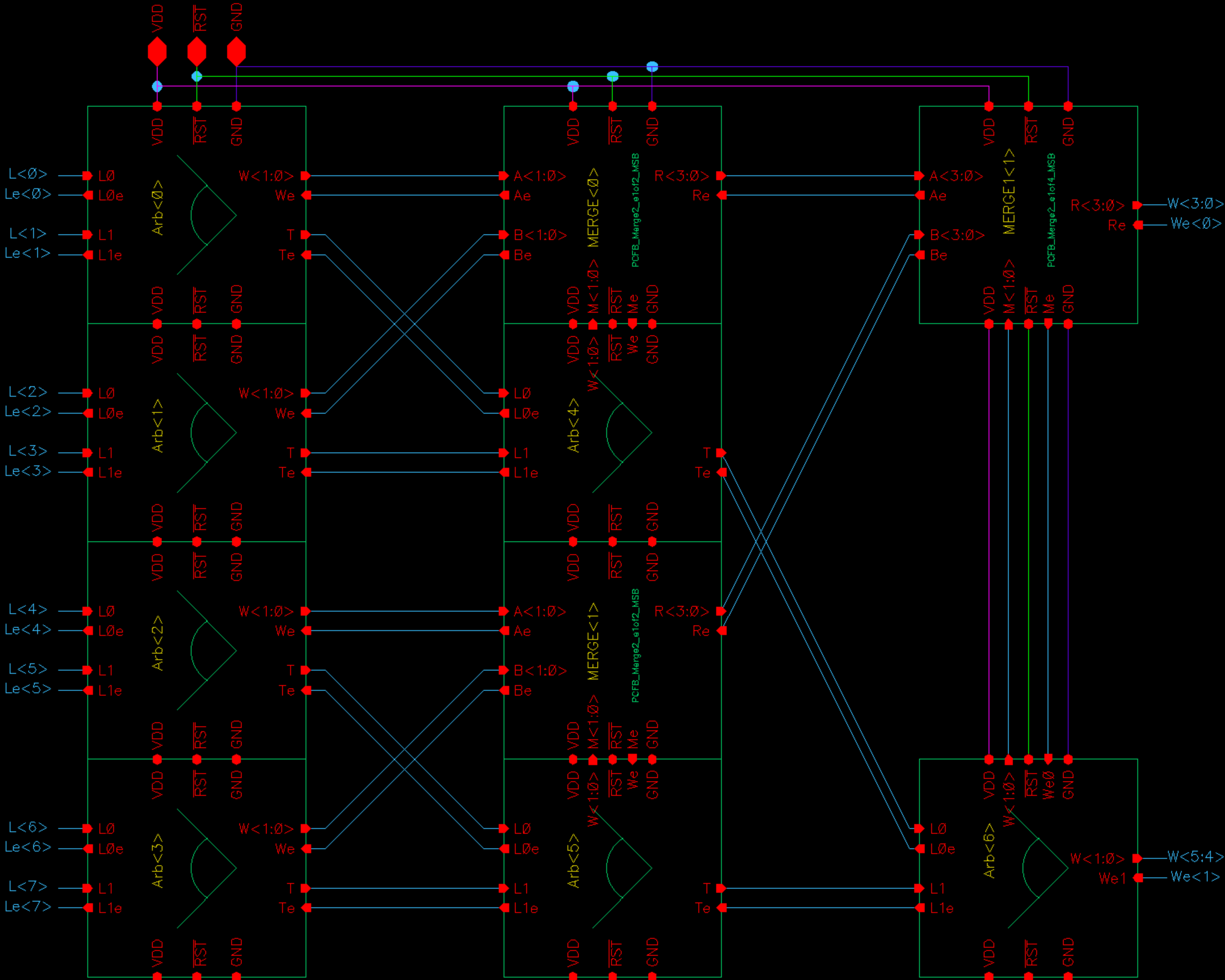
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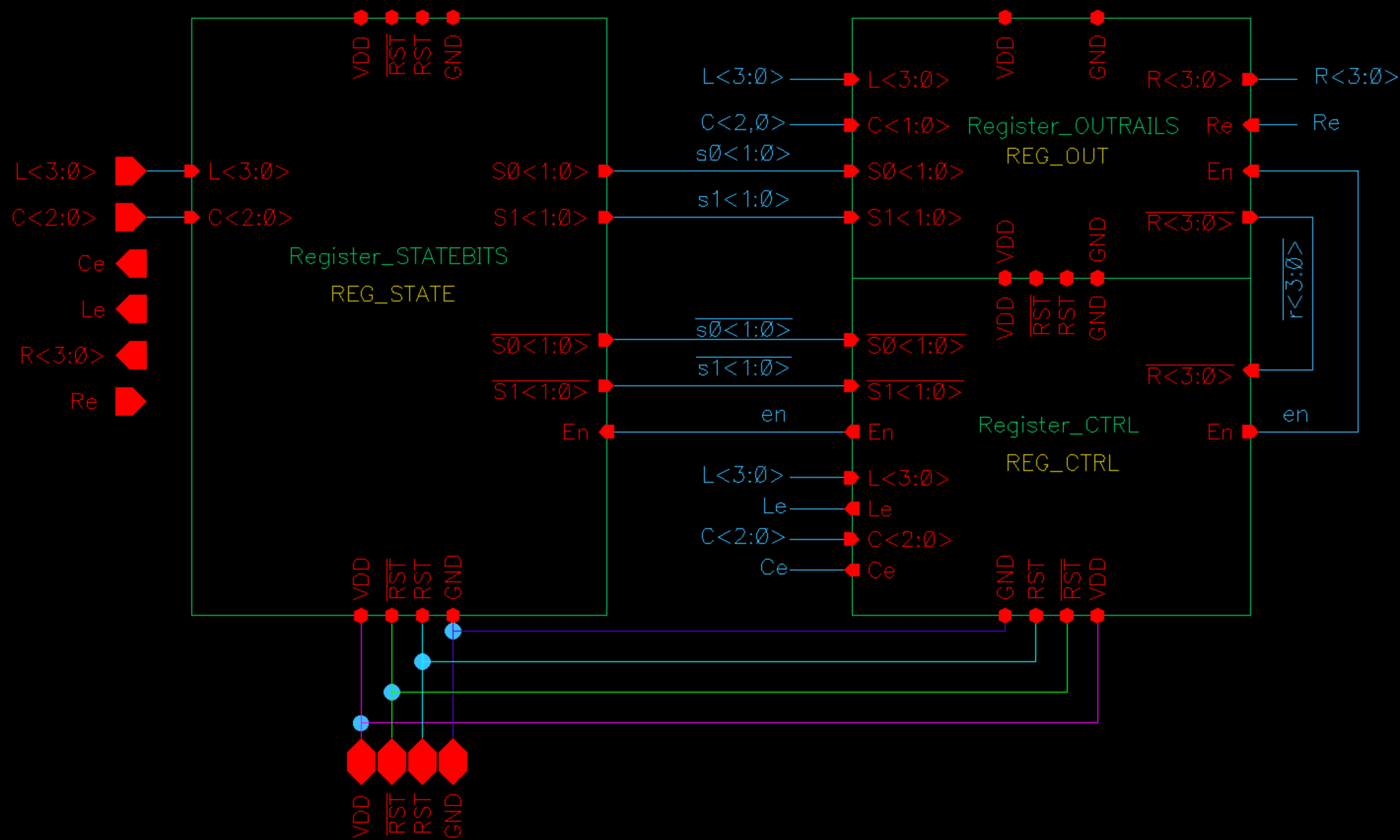


L<7:0>
Le<7:0>
W<5:0>
We<1:0>



Register_e1of4

C=0, internally stored data sent to output
C=1, data on L consumed and stored internally
C=2, data on L consumed, stored internally, and sent to output



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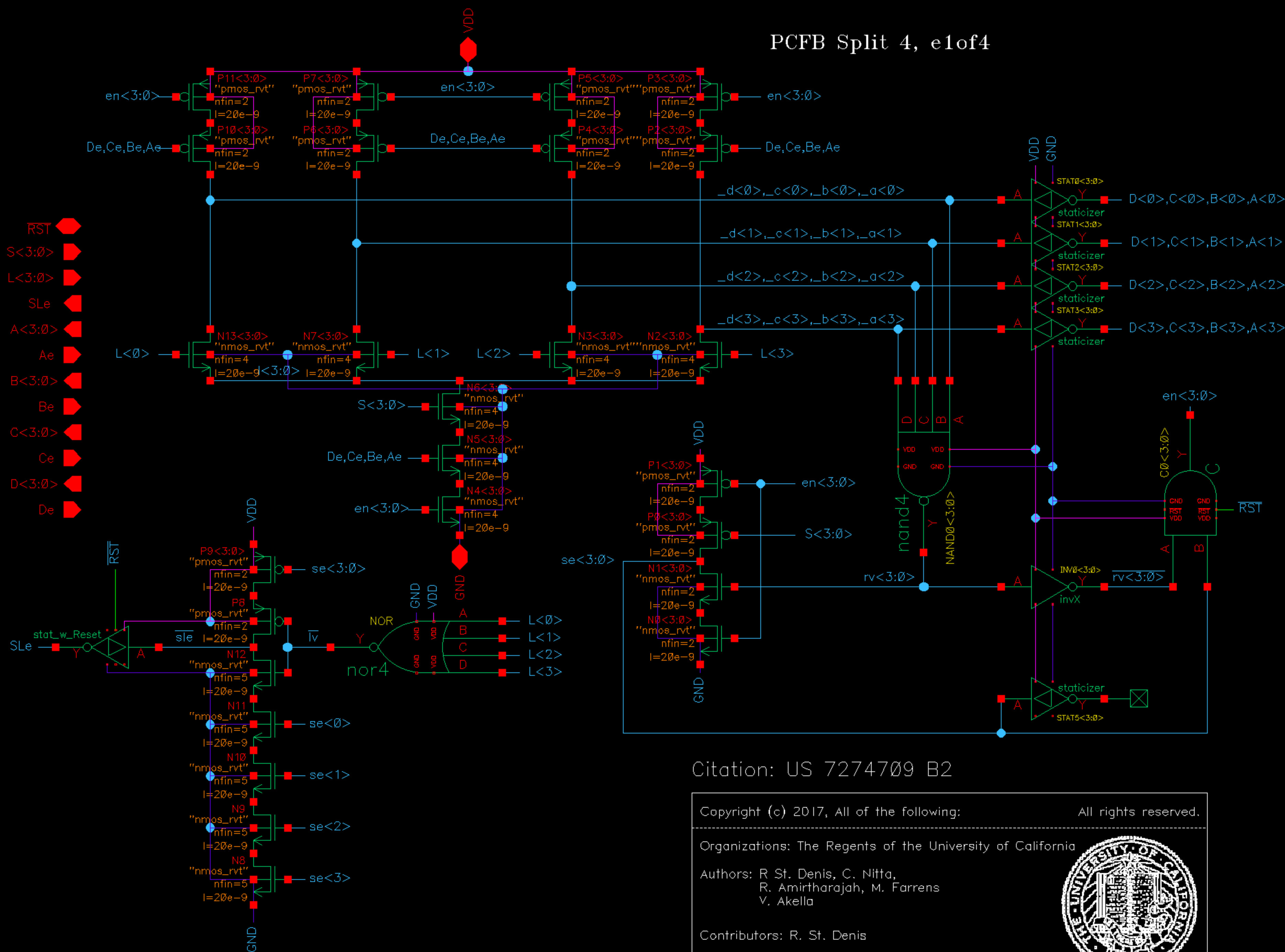
Contributors: R. St. Denis

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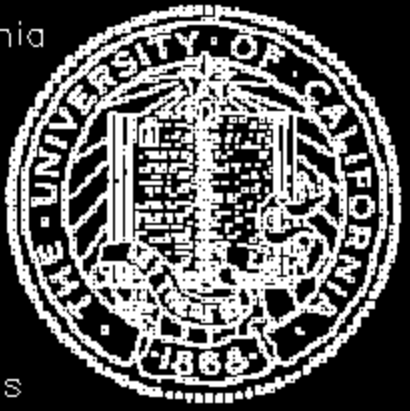


PCFB Split 4, e1of4



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The seal of the University of California is a circular emblem. It features a central shield with a book, a sun, and a miner. The shield is surrounded by the words "THE UNIVERSITY OF CALIFORNIA" and the year "1868" at the bottom.

