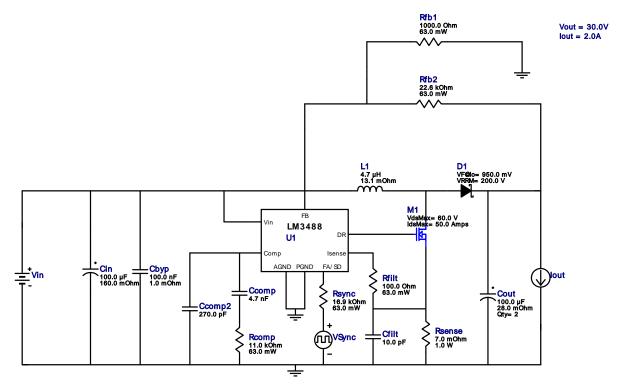


WEBENCH® Design Report

VinMin = 10.0V VinMax = 14.0V Vout = 30.0V lout = 2.0A Device = LM3488MMX/NOPB Topology = Boost Created = 2020-02-01 09:10:06.972 BOM Cost = \$4.64 BOM Count = 17 Total Pd = 4.6W

Design: 13 LM3488MMX/NOPB LM3488MMX/NOPB 10V-14V to 30.00V @ 2A

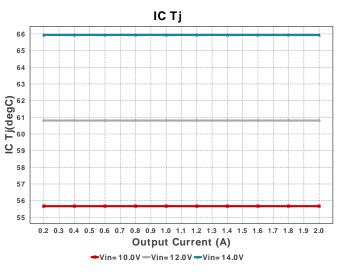


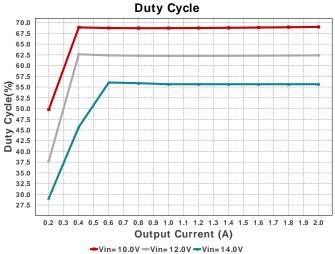
1. With the low turn of voltage of the LM34x8 your power supply may current limit before you reach your working input voltage. If this happens, or to preempt this from happening, you can include a low pass RC filter from input voltage to Vin on the IC. Make sure the rise time on the RC network is slower than your supply's rise time. If you are not using the syncronization feature of the part use the LM3478.

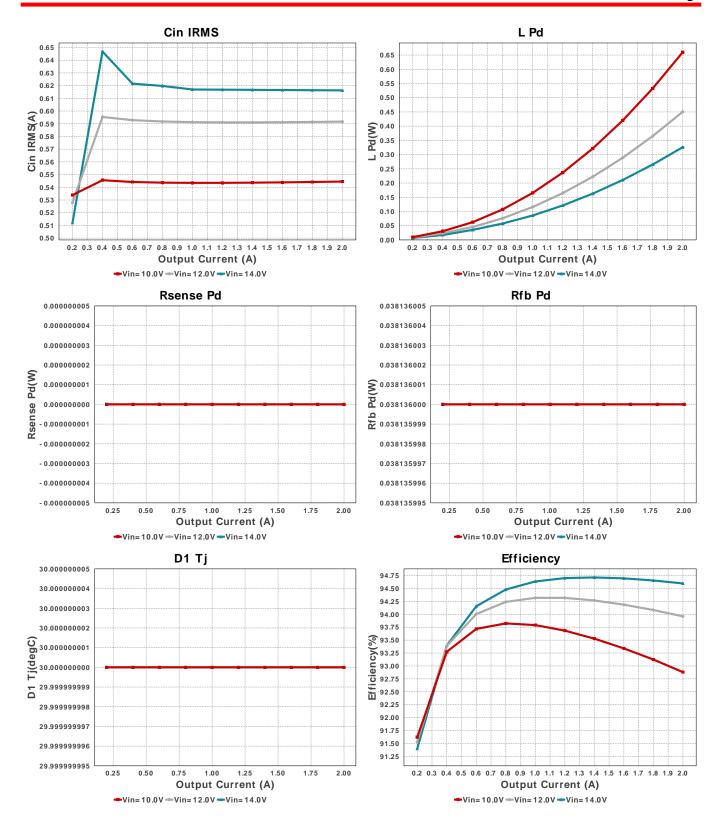
Electrical BOM

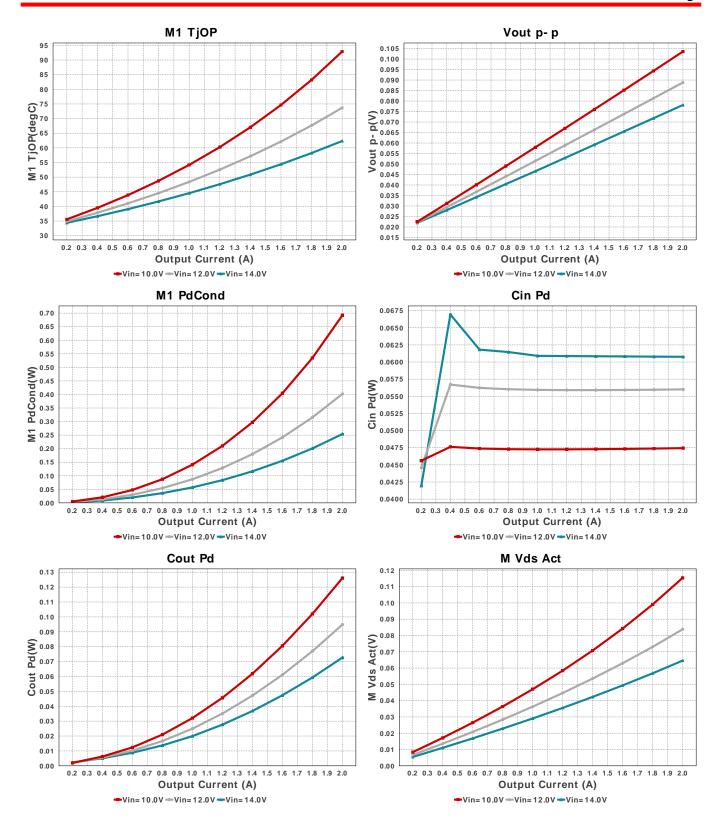
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Cbyp	MuRata	GRM155R71C104KA88D Series= X7R	Cap= 100.0 nF ESR= 1.0 mOhm VDC= 16.0 V IRMS= 0.0 A	1	\$0.01	0402 3 mm ²
Ccomp	TDK	CGA4C2C0G1H472J060AA Series= C0G/NP0	Cap= 4.7 nF VDC= 50.0 V IRMS= 0.0 A	1	\$0.04	0805 7 mm ²
Ccomp2	Taiyo Yuden	UMK105CG271JV-F Series= C0G/NP0	Cap= 270.0 pF VDC= 50.0 V IRMS= 0.0 A	1	\$0.01	0402 3 mm ²
Cfilt	Samsung Electro- Mechanics	CL21C100JBANNNC Series= C0G/NP0	Cap= 10.0 pF VDC= 50.0 V IRMS= 0.0 A	1	\$0.01	0805 7 mm ²
Cin	Panasonic	EEE-FK1V101P Series= FK	Cap= 100.0 uF ESR= 160.0 mOhm VDC= 35.0 V IRMS= 600.0 mA	1	\$0.21	SM RADIAL F 124 mm ²

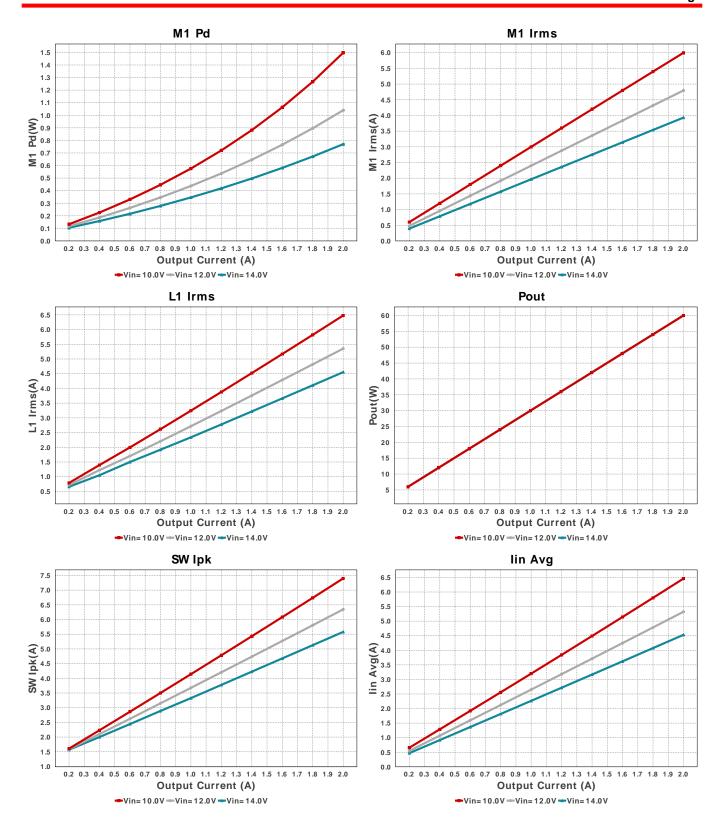
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Cout	Panasonic	EEHZA1H101P Series= ZA	Cap= 100.0 uF ESR= 28.0 mOhm VDC= 50.0 V IRMS= 2.0 A	2	\$1.00	SM_RADIAL_10BMM 160 mm²
D1	SMC Diode Solutions	SBRD10200TR	VF@Io= 950.0 mV VRRM= 200.0 V	1	\$0.12	DPAK 102 mm ²
L1	Coilcraft	XAL6060-472MEB	L= 4.7 μH 13.1 mOhm	1	\$0.82	XAL6060 72 mm ²
M1	ON Semiconductor	NTMFS5C673NLT1G	VdsMax= 60.0 V IdsMax= 50.0 Amps	1	\$0.34	FP- NTMFS5C673NLT1G_DFN5- MFG 0 mm ²
Rcomp	Vishay-Dale	CRCW040211K0FKED Series= CRCWe3	Res= 11.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
Rfb1	Vishay-Dale	CRCW04021K00FKED Series= CRCWe3	Res= 1000.0 Ohm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
Rfb2	Vishay-Dale	CRCW040222K6FKED Series= CRCWe3	Res= 22.6 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
Rfilt	Vishay-Dale	CRCW0402100RFKED Series= CRCWe3	Res= 100.0 Ohm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
Rsense	Susumu Co Ltd	PRL1632-R007-F-T1 Series= PRL1632	Res= 7.0 mOhm Power= 1.0 W Tolerance= 1.0%	1	\$0.20	0612 11 mm ²
Rsync	Vishay-Dale	CRCW040216K9FKED Series= CRCWe3	Res= 16.9 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
U1	Texas Instruments	LM3488MMX/NOPB	Switcher	1	\$0.83	MUA08A 24 mm ²

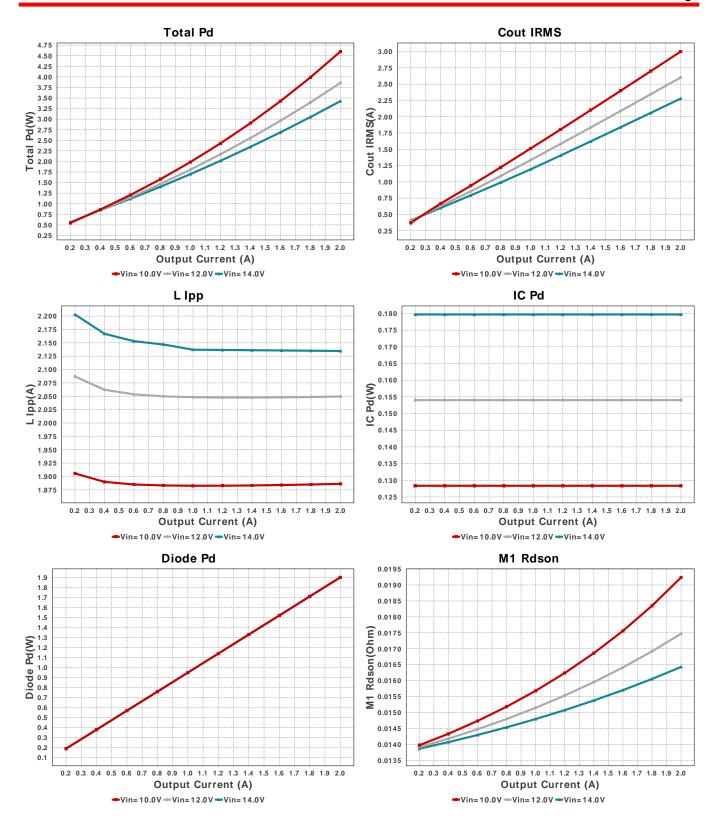


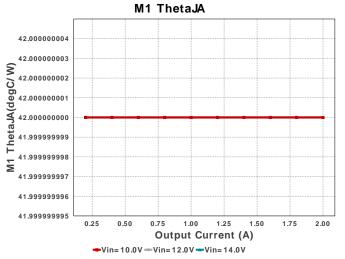


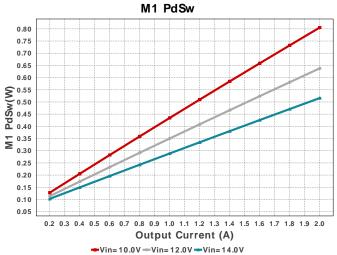


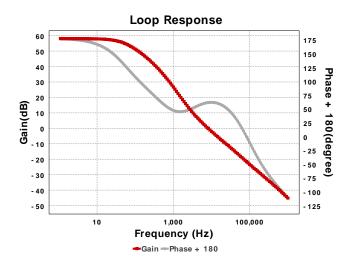












Operating Values

aming variable			
Name	Value	Category	Description
Cin IRMS	544.514 mA	Capacitor	Input capacitor RMS ripple current
Cin Pd	46.511 mW	Capacitor	Input capacitor power dissipation
Cout IRMS	3.0 A	Capacitor	Output capacitor RMS ripple current
Cout Pd	122.13 mW	Capacitor	Output capacitor power dissipation
D1 Tj	30.0 degC	Diode	D1 junction temperature
Diode Pd	1.9 W	Diode	Diode power dissipation
IC Pd	128.35 mW	IC	IC power dissipation
IC Tj	55.67 degC	IC	IC junction temperature
IC Tolerance	15.3 mV	IC	IC Feedback Tolerance
ICThetaJA	200.0 degC/W	IC	IC junction-to-ambient thermal resistance
lin Avg	6.46 A	IC	Average input current
L lpp	1.886 A	Inductor	Peak-to-peak inductor ripple current
L Pd	631.63 mW	Inductor	Inductor power dissipation
L1 Irms	6.478 A	Inductor	Inductor ripple current
M Vds Act	115.385 mV	Mosfet	M Vds
M1 Irms	5.998 A	Mosfet	M1 MOSFET Irms
M1 Pd	1.497 W	Mosfet	M1 MOSFET total power dissipation
M1 PdCond	692.09 mW	Mosfet	M1 MOSFET conduction losses
M1 PdSw	805.31 mW	Mosfet	M1 MOSFET switching losses
M1 Rdson	19.237 mOhm	Mosfet	Drain-Source On-resistance
M1 ThetaJA	42.0 degC/W	Mosfet	MOSFET junction-to-ambient thermal resistance
M1 TjOP	92.891 degC	Mosfet	M1 MOSFET junction temperature
Cin Pd	47.439 mW	Power	Input capacitor power dissipation
Cout Pd	126.04 mW	Power	Output capacitor power dissipation
Diode Pd	1.9 W	Power	Diode power dissipation
IC Pd	128.35 mW	Power	IC power dissipation
L Pd	659.77 mW	Power	Inductor power dissipation
M1 Pd	1.497 W	Power	M1 MOSFET total power dissipation
M1 PdCond	692.09 mW	Power	M1 MOSFET conduction losses
M1 PdSw	805.31 mW	Power	M1 MOSFET switching losses
Rfb Pd	38.136 mW	Power	Rfb Power Dissipation
Rsense Pd	0.0 W	Power	LED Current Rsns Power Dissipation
	Cin IRMS Cin Pd Cout IRMS Cout Pd D1 Tj Diode Pd IC Pd IC Tj IC Tolerance ICThetaJA Iin Avg L Ipp L Pd L1 Irms M Vds Act M1 Irms M1 Pd M1 PdCond M1 PdSw M1 Rdson M1 ThetaJA M1 TjOP Cin Pd Cout Pd Diode Pd IC Pd L Pd M1 Pd M1 PdCond M1 Pd	Cin IRMS 544.514 mA Cin Pd 46.511 mW Cout IRMS 3.0 A Cout Pd 122.13 mW D1 Tj 30.0 degC Diode Pd 1.9 W IC Pd 128.35 mW IC Tj 55.67 degC IC Tolerance 15.3 mV ICThetaJA 200.0 degC/W lin Avg 6.46 A L Ipp 1.886 A L Pd 631.63 mW L1 Irms 6.478 A M Vds Act 115.385 mV M1 Irms 5.998 A M1 Pd 1.497 W M1 PdSw 805.31 mW M1 PdSw 805.31 mW M1 TjOP 92.891 degC Cin Pd 47.439 mW Cout Pd 126.04 mW Diode Pd 1.9 W IC Pd 128.35 mW L Pd 659.77 mW M1 Pd 1.497 W M1 Pd 1.497 W M1 Pd 1.497 W M1 Pd 1.497 W M1	Cin IRMS 544.514 mA Capacitor Cin Pd 46.511 mW Capacitor Cout IRMS 3.0 A Capacitor Cout Pd 122.13 mW Capacitor D1 Tj 30.0 degC Diode Diode Pd 1.9 W Diode IC Pd 128.35 mW IC IC Tj 55.67 degC IC IC Tolerance 15.3 mV IC IC ThetaJA 200.0 degC/W IC II Avg 6.46 A IC L Ipp 1.886 A Inductor L Pd 631.63 mW Inductor L Pd 631.63 mW Inductor L Irms 6.478 A Inductor M Vds Act 115.385 mV Mosfet M1 Irms 5.998 A Mosfet M1 Pd 1.497 W Mosfet M1 Pd MPd 1.497 W Mosfet M1 PdSw 805.31 mW Mosfet M1 TjOP 92.891 degC Mosfet M1 TjOP 92.891 degC

#	Name	Value	Category	Description
33.	Total Pd	4.599 W	Power	Total Power Dissipation
34.	Rfb Pd	38.136 mW	Resistor	Rfb Power Dissipation
35.	Rsense Pd	268.21 mW	Resistor	LED Current Rsns Power Dissipation
36.	BOM Count	17	System	Total Design BOM count
00.	20000		Information	1 otal 2 ooig.1 2 o iii o o a.ii
37.	Cross Freq	6.009 kHz	System	Bode plot crossover frequency
	,		Information	
38.	Duty Cycle	69.019 %	System	Duty cycle
			Information	
39.	Efficiency	92.881 %	System	Steady state efficiency
			Information	
40.	FootPrint	686.0 mm ²	System	Total Foot Print Area of BOM components
			Information	
41.	Frequency	775.0 kHz	System	Switching frequency
			Information	
42.	Gain Marg	-18.835 dB	System	Bode Plot Gain Margin
			Information	
43.	lout	2.0 A	System	lout operating point
4.4	Laur Francosia	E4 000 JD	Information	Cain at Alla
44.	Low Freq Gain	54.926 dB	System	Gain at 1Hz
45.	Mode	CCM	Information	Conduction Mode
45.	Mode	CCIVI	System Information	Conduction Mode
46.	Phase Marg	57.663 deg	System	Bode Plot Phase Margin
40.	i nase marg	37.003 deg	Information	Bode Flot Friase Margin
47.	Pout	60.0 W	System	Total output power
	1 out	00.0 11	Information	Total output porror
48.	SW lpk	7.399 A	System	Peak switch current
	- '		Information	
49.	Total BOM	\$4.64	System	Total BOM Cost
			Information	
50.	Vin	10.0 V	System	Vin operating point
			Information	
51.	Vout	30.0 V	System	Operational Output Voltage
			Information	
52.	Vout Actual	29.736 V	System	Vout Actual calculated based on selected voltage divider resistors
			Information	
53.	Vout Tolerance	3.172 %	System	Vout Tolerance based on IC Tolerance (no load) and voltage divider
			Information	resistors if applicable
54.	Vout p-p	103.581 mV	System	Peak-to-peak output ripple voltage
			Information	

Design Inputs

Name	Value	Description	
lout	2.0	Maximum Output Current	
VinMax	14.0	Maximum input voltage	
VinMin	10.0	Minimum input voltage	
Vout	30.0	Output Voltage	
base_pn	LM3488	Base Product Number	
source	DC	Input Source Type	
Та	30.0	Ambient temperature	
UserFsw	775.0 k	Customer Selected Frequency	

WEBENCH® Assembly

Component Testing

Some published data on components in datasheets such as Capacitor ESR and Inductor DC resistance is based on conservative values that will guarantee that the components always exceed the specification. For design purposes it is usually better to work with typical values. Since this data is not always available it is a good practice to measure the Capacitance and ESR values of Cin and Cout, and the inductance and DC resistance of L1 before assembly of the board. Any large discrepancies in values should be electrically simulated in WEBENCH to check for instabilities and thermally simulated in WebTHERM to make sure critical temperatures are not exceeded.

Soldering Component to Board

If board assembly is done in house it is best to tack down one terminal of a component on the board then solder the other terminal. For surface mount parts with large tabs, such as the DPAK, the tab on the back of the package should be pre-tinned with solder, then tacked into place by one of the pins. To solder the tab town to the board place the iron down on the board while resting against the tab, heating both surfaces simultaneously. Apply light pressure to the top of the plastic case until the solder flows around the part and the part is flush with the PCB. If the solder is not flowing around the board you may need a higher wattage iron (generally 25W to 30W is enough).

Initial Startup of Circuit

It is best to initially power up the board by setting the input supply voltage to the lowest operating input voltage 10.0V and set the input supply's current limit to zero. With the input supply off connect up the input supply to Vin and GND. Connect a digital volt meter and a load if needed to set the minimum lout of the design from Vout and GND. Turn on the input supply and slowly turn up the current limit on the input supply. If the voltage starts to rise on the input supply continue increasing the input supply current limit while watching the output voltage. If the current increases on the input supply, but the voltage remains near zero, then there may be a short or a component misplaced on the board. Power down the board and visually inspect for solder bridges and recheck the diode and capacitor polarities. Once the power supply circuit is operational then more extensive testing may include full load testing, transient load and line tests to compare with simulation results.

Load Testing

The setup is the same as the initial startup, except that an additional digital voltmeter is connected between Vin and GND, a load is connected between Vout and GND and a current meter is connected in series between Vout and the load. The load must be able to handle at least rated output power + 50% (7.5 watts for this design). Ideally the load is supplied in the form of a variable load test unit. It can also be done in the form of suitably large power resistors. When using an oscilloscope to measure waveforms on the prototype board, the ground leads of the oscilloscope probes should be as short as possible and the area of the loop formed by the ground lead should be kept to a minimum. This will help reduce ground lead inductance and eliminate EMI noise that is not actually present in the circuit.



Design Assistance

- 1. Master key: CEA4B19E92263706[v1]
- 2. LM3488 Product Folder: http://www.ti.com/product/LM3488: contains the data sheet and other resources.

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