



Bottom Up parser

Root

$$S \rightarrow aABe.$$

$$A \rightarrow Abc/b$$

$$B \rightarrow d$$

\Rightarrow Abbcde (leaf)

$$\Rightarrow aAbcde [A \rightarrow b]$$

$$\Rightarrow aAde [A \rightarrow Abc]$$

$$\Rightarrow aABe [B \rightarrow d]$$

$$\Rightarrow S // (\text{root})$$

\rightarrow Recursive parser, Reduction
 \leftarrow RMD in reverse.

~~(x) 2m~~

handle

Bottomup

LR(k)

operator precedence
(operator par.)

LR(0)

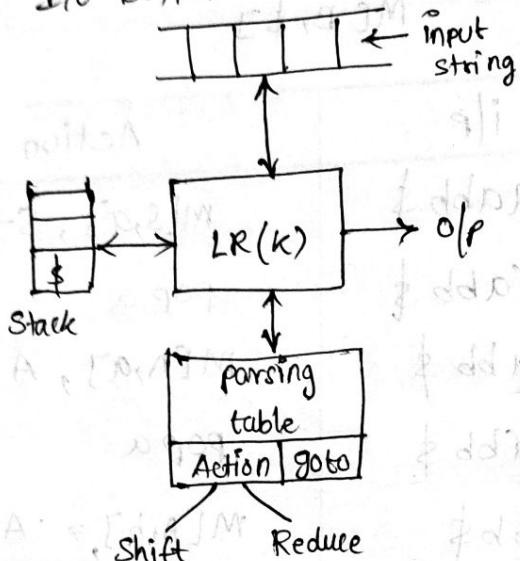
SLR CLR LALR LR(k)

L - Left to Right scanning

R - Reduction / derivation in
reverse order.

k - No of lookahead symbols
to parse the grammar

I/O Buffer



Stack

parsing
table
+ Action | goto

Shift Reduce

~~(x) 1)~~ a

\rightarrow LR(0)

$K=0$

$$S \rightarrow AA$$

$$A \rightarrow aA/b$$

S1) $I_0: S \rightarrow S$ \leftarrow Augmented grammar

$$S \rightarrow AA$$

A $\rightarrow \cdot aA$ Canonical items (\cdot should be added)

$$A \rightarrow \cdot b$$

goto (I_0, S)

$I_1: S \rightarrow S.$

goto (I_0, A)

$I_2: S \rightarrow A \cdot A$

$$A \rightarrow \cdot aA$$

$$A \rightarrow \cdot b$$

/ if non terminal after \cdot

write all production of the next symbol)

goto (I_0, a)

$I_3 : A \rightarrow a, A$
 $A \rightarrow \cdot aA$
 $A \rightarrow \cdot b$

goto (I_0, b)

$I_4 : A \rightarrow b.$

goto (I_2, A)

$I_5 : S \rightarrow AA.$

goto (I_2, a)

$I_3 : A \rightarrow a \cdot A$

$A \rightarrow \cdot aA$

$A \rightarrow \cdot b$

goto (I_2, b)

$I_4 : A \rightarrow b.$

goto (I_3, A)

$I_5 : A \rightarrow AA.$

goto (I_3, a)

$I_3 : A \rightarrow a \cdot A$

$A \rightarrow \cdot aA$

$A \rightarrow \cdot b$

goto (I_3, b)

$I_4 : A \rightarrow b.$

AUG

EX:

SLR(1) or LR(1)

$E \rightarrow E + T | T$

$T \rightarrow T * F | F$

$F \rightarrow (E) | id.$

$I_0 : \begin{cases} E' \rightarrow \cdot E \\ E \rightarrow \cdot E + T \end{cases}$

$E \rightarrow \cdot T$

$T \rightarrow \cdot T * F$

$T \rightarrow \cdot F$

$F \rightarrow \cdot (E)$

$F \rightarrow \cdot id$

	Terminals (Action)			NT (Actions)	
	a	b	\$	s	A
I_0	S_3	S_4		1	2
I_1			Accepted		
I_2	S_3	S_4			5
I_3	S_3	S_4			6
I_4	γ_3	γ_3	γ_3		
I_5	γ_1	γ_1	γ_1		
I_6	γ_2	γ_2	γ_2		

$\xrightarrow{S \rightarrow S}$

follow(S) = { \$ }

follow(A) = { a, b, \$ }

$\xrightarrow{A \rightarrow AA}$

$\xrightarrow{A \rightarrow b}$

$I_1 : S \xrightarrow{S \rightarrow S} \Rightarrow \gamma_0$

$I_5 : S \xrightarrow{S \rightarrow AA} \Rightarrow \gamma_1$

$I_6 : A \xrightarrow{A \rightarrow AA} \Rightarrow \gamma_2$

$I_4 : A \xrightarrow{A \rightarrow b} \Rightarrow \gamma_3$

AUG

EX:

SLR(1) or LR(1)

① $E' \rightarrow E$

$I_0 : E \rightarrow E + T$

$E \rightarrow T$

$T \rightarrow T * F$

$T \rightarrow F$

$F \rightarrow (E)$

$F \rightarrow id$

②

$I_0 : \begin{cases} E' \rightarrow \cdot E \\ E \rightarrow \cdot E + T \end{cases}$

$E \rightarrow \cdot T$

$T \rightarrow \cdot T * F$

$T \rightarrow \cdot F$

$F \rightarrow \cdot (E)$

$F \rightarrow \cdot id$

	$\text{goto } (I_0, E)$	$\text{goto } (I_4, T)$	$\text{goto } (I_7, id)$
I ₁ : $E \rightarrow E.$	$E \rightarrow E \cdot + T$	$T \rightarrow T \cdot * F$	$I_5: F \rightarrow id.$
	$E \rightarrow E$	$\text{goto } (I_4, F)$	$\text{goto } (I_8,)$
	$\text{goto } (I_0, T)$	$I_3: T \rightarrow F \cdot$	$I_{11}: F \rightarrow (E).$
I ₂ : $E \rightarrow T.$	$T \rightarrow T \cdot * F$	$\text{goto } (I_4, ()$	$\text{goto } (I_8, +)$
	$\text{goto } (I_0, F)$	$I_4: F \rightarrow (\cdot E)$	$I_6: E \rightarrow E \cdot + T$
I ₃ : $T \rightarrow F \cdot$		$E \rightarrow \cdot E + T$	$T \rightarrow \cdot T \cdot * F$
	$\text{goto } (I_0, ()$	$E \rightarrow \cdot T$	$T \rightarrow F$
I ₄ : $F \rightarrow (\cdot E)$	$T \rightarrow \cdot T \cdot * F$	$F \rightarrow \cdot (E)$	$F \rightarrow id$
$E \rightarrow \cdot E + T$	$T \rightarrow \cdot F$	$\text{goto } (I_9, *)$	
$E \rightarrow \cdot T$	$F \rightarrow \cdot (E)$	$I_7: T \rightarrow T \cdot * F$	
$T \rightarrow \cdot T \cdot * F$	$F \rightarrow \cdot id$	$F \rightarrow \cdot (E)$	
$T \rightarrow \cdot F$	$\text{goto } (I_4, id)$	$F \rightarrow \cdot id$	
$F \rightarrow \cdot (E)$	$I_5: F \rightarrow id.$		
$F \rightarrow \cdot id$	$\text{goto } (I_6, T)$		
$\text{goto } (I_0, id)$	$I_9: E \rightarrow E + T \cdot$		
I ₅ : $F \rightarrow id.$	$T \rightarrow T \cdot * F$		
$\text{goto } (I_1, +)$	$\text{goto } (I_6, F)$		
I ₆ : $E \rightarrow E \cdot + T$	$I_3: T \rightarrow F \cdot$		
$T \rightarrow \cdot T \cdot * F$	$\text{goto } (I_6, ()$		
$F \rightarrow \cdot (E)$	$I_4: F \rightarrow (\cdot E)$		
$F \rightarrow \cdot id$	$E \rightarrow \cdot E + T$		
$\text{goto } (I_2, *)$	$E \rightarrow \cdot T$		
I ₇ : $T \rightarrow T \cdot * F$	$T \rightarrow \cdot T \cdot * F$		
$F \rightarrow \cdot (E)$	$T \rightarrow \cdot F$		
$F \rightarrow \cdot id$	$F \rightarrow \cdot (E)$		
$\text{goto } (I_4, E)$	$F \rightarrow \cdot id$		
I ₈ : $F \rightarrow (E \cdot)$	$\text{goto } (I_6, id)$		
$E \rightarrow E \cdot + T$	$I_5: F \rightarrow id.$		
	$\text{goto } (I_7, F)$		
	$I_{10}: T \rightarrow T \cdot * F \cdot$		
	$\text{goto } (I_7, ()$		
	$I_4: F \rightarrow (\cdot E)$		
	$E \rightarrow \cdot E + T$		
	$E \rightarrow \cdot T$		
	$T \rightarrow \cdot T \cdot * F$		
	$T \rightarrow \cdot F$		
	$F \rightarrow \cdot id$		

	Terminals (Action)					NT (goto)			
	+	*	()	id	shift	E	T.	F
I ₀					S ₄	S ₅	1	2	3
I ₁		S ₆					Accepted		
I ₂			S ₇		R ₂	R ₂			
I ₃		R ₄			R ₄	R ₄			
I ₄				S ₄ :	S ₅		8	2	3
I ₅		R ₆	R ₆		R ₆	R ₆			
I ₆				S ₄	S ₅	S ₅	9	3	
I ₇				S ₄	S ₅				10
I ₈		S ₆			S ₁₁				
I ₉		R ₁	S ₇		R ₁	R ₁			
I ₁₀		R ₃	R ₃		R ₃	R ₃			
I ₁₁		R ₅	R ₅		R ₅	R ₅			

Stack	i/p string	Action
\$ O	id * id + id \$	M[0,id], Shifts
\$ O I D S	* id + id \$	M[5,*], R ₆ F → id, RHS 1 2 symbol from stack apply O on F M[0,F] = 3
\$ O		↓ symbol then pop 2 symbol
\$ O F 3	* id + id \$	M[3,*], R = H T → F
\$ O T 2	* id + id \$	pop 2 symbol from stack T → T ₂ M[2,*] S ₇ + → T ₂
\$ O T ₂ * 7	id + id \$	M[7,id] S ₅
\$ O T ₂ * 7 I D 5	+ id \$	M[5,+] R ₆ → F → id pop 2 symbol
\$ O T ₂ * 7 F 1 0	+ id \$	F = > F ₁₀ M[10,+], R ₃ → T → T * F
\$ O T ₂ * 7 F 1 0 + id \$		There is 3 symbol. so apply pop + → T ₂
\$ O T ₂ * 7 F 1 0 + id \$		

$\$0t_2 + id\$$ MC[2,4], 32 → E → T
E → E1

OEI + id# 12 MCI, + 3, SL

\$6E1+6 idb M[6,id], 85

\$OE1+6ids \$ M[S, f], \gamma_6 \rightarrow F^{id} \\ F = F_3

$$\begin{matrix} \$OE1 + 6F3 \\ \longleftarrow \end{matrix} \quad \$ \quad M[3,5], r_4 \rightarrow T \rightarrow R \\ \quad \quad \quad T \rightarrow Tq$$

$$\begin{array}{c} \text{#} \\ \text{O} \in I + bTq \\ \downarrow \end{array} \quad \# \quad \begin{array}{l} M[9,13], \gamma_1 \rightarrow E \rightarrow E+T \\ E \rightarrow E' \end{array}$$

\$ OEI \$ M[1, \$] now accepted.

String cannot
be empty
do process
until it is
accepted

2

~~SLR~~

$$S \rightarrow L = R / R$$

$$L \rightarrow * R / id$$

$$R \rightarrow L$$

i)

$$S' \rightarrow S$$

$$S \rightarrow L = R / R$$

$$L \rightarrow * R / id$$

$$R \rightarrow L$$

ii)

$$I_0: S' \rightarrow S$$

$$S \rightarrow L = R$$

$$S \rightarrow R$$

$$L \rightarrow . * R$$

$$L \rightarrow . id$$

$$R \rightarrow L$$

goto (I_0, S)

$$I_1: S' \rightarrow S.$$

goto (I_0, L)

$$I_2: S' \rightarrow L. = R$$

$$R \rightarrow L. /$$

goto (I_0, R)

$$I_3: S \rightarrow L. = R$$

$$S \rightarrow R. /$$

$$\cancel{L \rightarrow * R}$$

$$\cancel{R \rightarrow L}$$

goto ($I_0, *$)

$$I_4: L \rightarrow * . R$$

$$R \rightarrow . L$$

goto (I_0, id)

$$I_5: S \rightarrow L. = R$$

goto (I_0, id)

$$I_5: L \rightarrow id. ,$$

goto ($I_2, =$)

$$I_6: S \rightarrow L = . R \quad L \rightarrow . * R$$

$$R \rightarrow . L \quad L \rightarrow . id$$

goto (I_4, R)

$$I_7: S \rightarrow L = . R \quad L \rightarrow . id$$

$$R \rightarrow . L$$

goto (I_3, R)

$$I_8: L \rightarrow * . R \quad L \rightarrow . id$$

goto ($I_4, *$)

$$I_9: S \rightarrow L = . R \quad L \rightarrow . L$$

$$R \rightarrow . L$$

goto (I_6, R)

$$I_{10}: L \rightarrow * . R \quad L \rightarrow . id$$

$$R \rightarrow . L$$

goto (I_6, id)

$$I_{11}: L \rightarrow * R \quad L \rightarrow . id$$

$$R \rightarrow . L$$

goto ($I_6, *$)

0	$S \rightarrow S.$	I_1	γ^0
1	$S \rightarrow L = R.$	I_9	γ^1
2	$S \rightarrow R.$	I_3	γ^2
3	$L \rightarrow * R.$	I_8	γ^3
4	$L \rightarrow id.$	I_5	γ^4
5	$R \rightarrow L.$	(I_8) and I_2	γ^5

	=	*	id	\$	S	L	R
I ₀		S ₄	S ₅		1	2	3
I ₁	S ₆ \r ₅				Accepted		
I ₂				r ₅			
I ₃				r ₅			
I ₄		S ₄	S ₅		8	7	
I ₅	r ₄						
I ₆		S ₄	S ₅		8	9	
I ₇	r ₃			r ₃			
I ₈	r ₅			r ₅			
I ₉				r ₉			

$$\text{follow}(S) = \lambda \ $ \ \gamma$$

$$\text{follow}(L) = \lambda = , \$ \ \gamma$$

$$\text{follow}(R) = \lambda = , \$ \ \gamma$$

(S, r₆) →
Shift
reduce
conflict

(S, r₆) ad₆

Q ① Construct Canonical LR parser (CLR) for the given string grammar and parse the input string

$S \rightarrow cc$
 $c \rightarrow cC/d$
dd - input string

LR(0)
↳ SLR(1) = follow
↳ CLR \rightarrow LR(0) + lookahead = LR(1)

S1: $S \rightarrow S$
 $S \rightarrow cc$
 $c \rightarrow cC$
 $c \rightarrow d$

S2: canonical collection of LR(1) items

I₀: $S' \rightarrow .S, \$$ lookahead
 $S \rightarrow .cc, \$$
 $c \rightarrow .cc, c/d$
 $c \rightarrow .d, c/d$.

Goto (I₀, \$)

I₁: $S' \rightarrow S., \$$

Goto (I₀, C)

I₂: $S \rightarrow C.c, \$$

$C \rightarrow .cc, \$$

$C \rightarrow .d, \$$

Goto (I₀, C)

I₃: $C \rightarrow C.c, c/d$

$C \rightarrow .cc, c/d$

$C \rightarrow .d, c/d$

Goto (I₀, d)

I₄: $c \rightarrow d.c, c/d$

goto (I₂, C)

I₅ : S → CC, \$

goto (I₂, C)

I₆ : C → C·C, \$

C → ·CC, \$

C → ·d, \$

goto (I₂, d)

I₇ : C → d·, \$

goto (I₃, C)

I₈ : C → CC·, C/d

goto (I₃, C)

I₉ : C → C·C, C/d

C → ·d, C/d

goto (I₃, d)

I₄ : C → d·, C/d

goto (I₆, C)

I₉ : C → CC·, \$

goto (I₆, C)

I₆ : C → C·C, \$

C → ·CC, \$

C → ·d, \$

goto (I₆, d)

I₇ : C → d·, \$

	C	d	\$	S	C
I ₀	S ₃	S ₄		1	2
I ₁					Accepted
I ₂	S ₆	S ₇			5
I ₃	S ₃	S ₄			8
I ₄	R ₃	R ₃			
I ₅				R ₁	
I ₆	S ₆	S ₇			9
I ₇				R ₃	
I ₈	R ₂	R ₂			
I ₉				R ₂	

I₁ : S' → S·, \$

I₅ : S → CC·, \$

I₇ : C → d·, \$

I₄ : C → d·, C/d

I₈ : C → CC·, C/d

I₉ : C → CC·, \$

S' → ·S : o

S → CC· - 1 - R₁

C → CC - 2 - R₂

C → d - 3 - R₃

Stack	I/p string	Action
\$0	dd\$	M[0,d], S ₄
\$0d4	d\$	M[4,d], R ₃ , C → d
\$0C2	d\$	M[2,d], S ₇
\$0C2d7	\$	M[7,\$], R ₃ , C → d
\$0C2C5	\$	M[5,\$], R ₁ , S → CC
\$0S1.	\$	Accepted

LALR (Similar to CLR) after previous steps, sum

Look Ahead LR

$$LR(1) = LR(0) + LA$$

$$I_1, s' \rightarrow s_0, \$ \quad c/d$$

$$I_2, \$ \quad c/d$$

$$I_3 \sqcup I_6, \$ \quad c/d \quad I_{36} \rightarrow \text{combine states}$$

$$I_4 \sqcup I_7, \$ \quad c/d \quad I_{47} \rightarrow \text{combine states}$$

I_5

$$I_8 \sqcup I_9, \$ \quad c/d \quad I_{89}$$

	C	d	\$	S	C
I_0	s_{36}	s_{47}		1	2
I_1				Accepted	
I_2	s_{36}	s_{47}			500
I_{36}	s_{36}	s_{47}			89
I_{47}	r_3	r_3	r_3		000
I_5			r_1		
I_{89}	r_2	r_2	r_2		000

$$I_1 : s' \rightarrow s, \$$$

$$I_5 : s \rightarrow cc, \$$$

$$\begin{cases} I_7 : c \rightarrow d, \$ \\ I_4 : c \rightarrow d., cld \end{cases} \quad \begin{cases} \$ \\ cld \end{cases}$$

$$\begin{cases} I_8 : cc., cld \\ I_9 : cc., \$ \end{cases} \quad \begin{cases} \$ | cld \\ \$ \end{cases}$$

$I_{36} \rightarrow$

(cld | \$)

$$\begin{aligned} I_3 : & c \rightarrow c.c, cld \\ & c \rightarrow .cc, cld \\ & c \rightarrow .d, cld \end{aligned}$$

$$\begin{aligned} I_6 : & c \rightarrow c.c, \$ \\ & c \rightarrow .cc, \$ \\ & c \rightarrow .d, \$ \end{aligned}$$

$$0 \quad s' \rightarrow .s$$

$$1 \quad s \rightarrow .cc \quad r_1$$

$$2 \quad c \rightarrow cc \quad r_2$$

$$3 \quad c \rightarrow d \quad r_3$$

$M[1, 2]$
is always
alpha

Stack	i/p string	Action
\$0	dd \$	$M[0, d] S_{17}$
\$0d47	d \$	$M[0, d] S_{47}$
\$0C2	d \$	$M[0, d] S_{47}$
\$0C2d47	\$	$\cancel{M[0, d]} \rightarrow d$
\$0C2d5	\$	$M[0, d], r_3, r_2, c \rightarrow d, c \rightarrow c.s$
\$0S1	\$	$M[0, d], r_1, s \rightarrow cc, s \rightarrow \$$
		Accepted

① Construct CLR and LALR for grammar

$S \rightarrow L = R | R$
 $L \rightarrow * R | id$
 $R \rightarrow L$
 if string: $id = id$

$S' \rightarrow S$
 $S \rightarrow L = R$
 $S \rightarrow R$
 $L \rightarrow * R$
 $L \rightarrow id$
 $R \rightarrow L$

goto (I_0, S)
 $I_1: S' \rightarrow S . , \$$
 goto (I_0, L)
 $I_2: S \rightarrow L . = R , \$$
 $R \rightarrow L . , \$$
 goto (I_0, R)

$I_3: S \rightarrow R . , \$$
 goto ($I_0, *$)
 $I_4: L \rightarrow * . R , = / \$$
 $R \rightarrow . L , = / \$$
 goto (I_0, L)
 $L \rightarrow . * R , = / \$$
 $L \rightarrow . id , = / \$$
 goto (I_0, id)

$I_5: L \rightarrow id . , = / \$$
 goto ($I_2, =$)

$I_6: S \rightarrow L = . R , \$$
 $R \rightarrow . L , \$$
 $L \rightarrow . * R , \$$
 $L \rightarrow . id , \$$
 goto (I_4, R)
 $I_7: L \rightarrow * R . , = / \$$

$I_0: S' \rightarrow . S , \$$
 $S \rightarrow . L = R , \$$
 $S \rightarrow . R , \$$
 $L \rightarrow . * R , = / \$$
 $L \rightarrow . id , = / \$$
 $R \rightarrow . L , \$$

goto (I_4, L)
 $I_8: R \rightarrow L . , = / \$$
 goto ($I_4, *$)
 $I_4: L \rightarrow * . R , = / \$$
 $R \rightarrow . L , = / \$$
 $L \rightarrow . * R , = / \$$
 $L \rightarrow . id , = / \$$
 goto (I_4, id)
 $I_5: L \rightarrow id . , = / \$$
 goto (I_6, R)

$I_9: S \rightarrow L = R . , \$$
 goto (I_6, L)
 $I_{10}: R \rightarrow L . , \$$
 goto ($I_6, *$)
 $I_{11}: L \rightarrow * . R , \$$
 $R \rightarrow . L , \$$
 $L \rightarrow . * R , \$$
 $L \rightarrow . id , \$$
 goto (I_6, id)

$I_{12}: L \rightarrow id . , \$$
 goto (I_{11}, R)
 $I_{13}: L \rightarrow * R . , \$$
 goto (I_{11}, L)
 $I_{10}: R \rightarrow L . , \$$

goto ($I_{11}, *$)
 $I_{11}: L \rightarrow * . R , \$$
 $R \rightarrow . L . , \$$
 $L \rightarrow . * R , \$$
 $L \rightarrow id , \$$
 goto (I_{11}, id)
 $I_{12}: L \rightarrow id . , \$$

	=	*	id	\$	S	L	R
0			s4	s5	1	2	3
1					Accepted		
2		s6		r5			
3				r2			
4		s4.	s5		8	7	
5		r4		r4			
6		s6	s12		10	9	
7		r3		r3			
8		r5		r5			
9				r1	(2,0)		
10				r5			
11			s12		10	13	
12				r4			
13				r3			

\$ 0	id = id \$	S5 0 0 0 0
\$ 0 id 5	= id \$	r4, L → id
\$ 0 L 2	= id \$	S6
\$ 0 L 2 = 6	id \$	S12
\$ 0 L 2 = 6 id 12	\$	r4, L → id
\$ 0 L 2 = 6 R 9	\$	r1, S → L = R
\$ 0 S 1	\$	Accepted

I ₁	S → S	I ₂ : R → L = \$
0	S → L = R r1	I ₃ : S → R, \$
1	S → R r2	I ₅ : L → id, \$
2	L → *R r3	I ₇ : L → *R, = \$
3	L → id r4	I ₈ : R → L, = \$
4	R → L r5	I ₉ : S → L = R, \$
5		I ₁₀ : R → L, \$
I ₁₁		I ₁₂ : L → id, \$
I ₁₃		I ₁₄ : L → *R, = \$

Conflicts

- precedence level
 ↗ can be solved by
 ↗ 1) GLD parser
 ↗ 2) Yacc parser.
- * 1) Shift - Reduce Conflict (S_1, R_2)
 - * 2) Reduce - Reduce conflict (r_3, r_5)

Error handling

- * Good compiler - helps in identifying and locating errors.
- * Errors may be :
 - Lexical : misspelling of identifiers , keywords
 - Syntactic : expression with incorrect format.

Syntax Error handling

- * Error detection and recovery - Syntax analysis phase .
- * Accuracy of parsing methods
- * Nature of errors .

Goals of the error handler in a parser

- * Must report the presence of errors
- * Recovery quickly
- * Fast
- * LL and LR methods detect error as early as possible -
visible prefix property .
- * Common errors

- 1) punctuation errors (, and ;)
- 2) operator and operand errors
- 3) keyword errors

- * Recovery should not lead to Spurious (meaningless) errors .
 - Skipping declarations .

Error Recovery Strategies

- panic mode
- * Discards input symbols until tokens in the synchronizing set are encountered

- * Skips input
- * Synchronizing set must be chosen carefully
- * Simple .

phrase level

- * Local correction , replaces prefix
- * should not lead to infinite loops

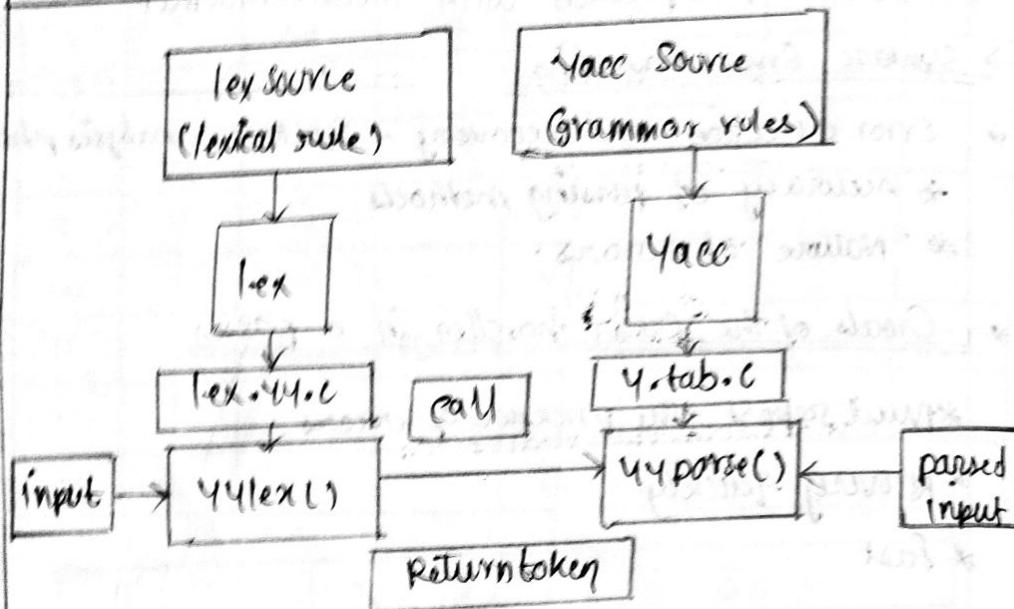
3) Error productions

- * Augment grammar
- * can generate error diagnostics

4) Global correction

- * minimal sequence of changes needed to transform x_0
- * costly.

5) YACC - Yet Another Compiler Compiler



% {

#include <ctype.h>

% y

% %

line : expr '\n' { printf("%d\n", \$1); } ;

;

expr : expr '+' term { \$\$ = \$1 + \$3; } ;

term

;

term : term '*' factor { \$\$ = \$1 * \$3; } ;

factor

factor : '(' Expr ')' { \$\$ = \$2; } ;

DIGIT

% %

yylead()

c = getchar();

if (isdigit(c))

yyval = c - '48';

return DIGIT;

return c;

E2

T=2

F=3

$E \rightarrow E + T \mid T$

$T \rightarrow T * F \mid F$

$F \rightarrow (E) \mid \text{digit}$

Bottom up

LR(0) SLR CLR LALR

operator precedence

* operator precedence parser.

operator grammar:-

mathematical Exp

No two NT should
be adjacent - RHS

No symbol
at RHS

$$\begin{array}{l} E \rightarrow E + E \mid E * E \\ E \rightarrow EA E \mid id \\ A \rightarrow + / * \\ E \rightarrow E \oplus E \\ E \rightarrow E \otimes E \end{array}$$

→ steps for operator precedence parser

i) Check the grammar is operator grammar or not

ii) Construct operator precedence relation table

based on terminals

iii) Parse the input string

iv) Construct parse tree.

Eg:

$E \rightarrow E + E \mid E * E \mid id, id + id * id \$$

stack

	+	*	id	\$	r/p
+	>	<	<	>	
*	>	>	<	>	
id	>	>	-	>	
\$	<	<	<	accepted	

id, a, b → high

\$ → low

→ + > * > +

↓ stack

high priority

if string + has low priority

Stack	Relation	ip/string	Action
\$	<	id + id * id \$	push ip, shift id
\$ id	>	+ id * id \$	pop, Reduce E → id
\$ E	<	+ id * id \$	shift +
\$ E +	<	id * id \$	shift id
\$ E + id	>	* id \$	Reduce E → id
\$ E + E	✗	* id \$	shift *
\$ E + E *	<	id \$	shift id
\$ E + E * id	>	\$	Reduce E → id
\$ E + E * E	>	\$	Reduce E → E * E
\$ E + E	>	\$	Reduce E → E * E
\$ E		\$	Accepted

with parenthesis

Stack	.	+	*	()	id	\$
+	>	<	<	>	<	>	
*	>	>	<	>	<	>	
(<	<	<	=	<	-	
)	>	>	-	>	-	>	
id	>	>	-	>	-	-	
\$	<	<	<	-	-	-	Accepted

SYNTAX DIRECTED TRANSLATION AND

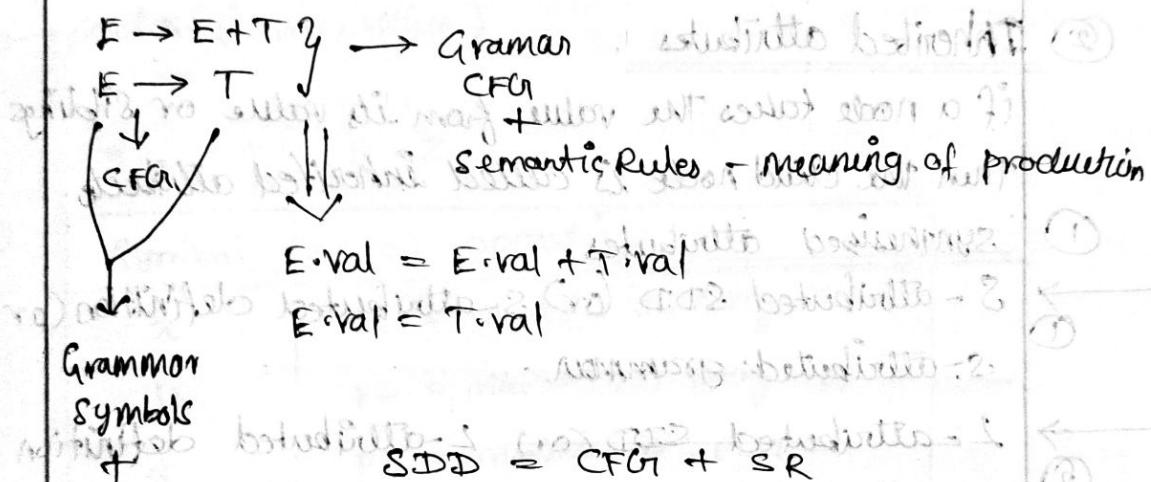
INTERMEDIATE CODE GENERATION.

ohlo24

Syntax directed definitions - Construction of syntax

tree - Bottom-up - Evaluation of S-Attribute definitions -
Design of predictive translator - Type systems -
specification of a simple type checker - Equivalence of
type expressions - Type conversions. Intermediate languages:
Syntax Tree, Three Address code, types and declarations,
translation of Expressions, type checking, backpatching.

→ Syntax directed (grammar) definition (SDD).



Attributes
(reference
no string)

If x is a grammar symbol, a be any
string $x.a$ is SDD

→ types of attributes

1) Synthesized attribute

$$S \rightarrow ABC$$

$$S \cdot \text{syn} \simeq A \cdot \text{syn}$$

$$S \cdot \text{syn} \simeq B \cdot \text{syn} \quad S \cdot \text{syn} \simeq C \cdot \text{syn}$$

2) Inherited attributes

$$S \rightarrow ABC$$

$$B \cdot i = S \cdot i$$

$$B \cdot i \simeq A \cdot i$$

$$B \cdot i \simeq C \cdot i$$

S^t
SDD

SDD is a context free grammar with semantic rules. The attributes are associated with grammar rules and semantic rules are associated with productions. Every symbol contains an attribute and every production contains a semantic rule. Semantic rule provides meaning for the production. Attributes may be numbers, strings, data types and references.

* Types of attributes.

(1)

Synthesized attribute.

If a node takes value from its children, the parent node is called the synthesized attribute.

(2) Inherited attributes.

If a node takes the value from its value or siblings. Then the child node is called inherited attribute.

(1)

Synthesized attributes.

S-attributed SDD (or) S-attributed definition (or)

S-attributed grammar

→ (1)

2-attributed SDD (or) 2-attributed definition (or)

(2)

2-attributed grammar.

Exm

S-attributed SDD

SDD that uses only synthesized attribute is called S-attributed.

Eg:- $S \rightarrow ABC$

$$S \cdot syn = A \cdot syn$$

$$S \cdot syn = B \cdot syn$$

$$S \cdot syn = C \cdot syn$$

2-attributed SDD

A-SDD that uses both synthesized and inherited attributes but each inherited attribute is restricted to inherit from parent or left sibling only is called 2-attributed SDD.

$$A \rightarrow xyz$$

$$y \cdot i = A \cdot i$$

$$y \cdot i = x \cdot i$$

$$y \cdot i = z \cdot i$$

Semantic actions are placed at any place in RHS.

* Semantic actions are always placed at right end

of the production and it is called postfix SDD.

attributes are evaluated with bottom-up-parsing.

attributes are inherited by traversing parse tree depth first left to right order.

Construction of Syntax trees

operations for given symbol / Expressions

SDD (production + Semantic Rules)

syntax tree construction

operator → mknode (operator (op), left, right)
 identifier → mkleaf (id, entry-id)
 number → mkleaf (num, value)

operators → mknode
 identifier → mkleaf
 number → mkleaf

Eg: $x * y + 5 - z$

①

Symbol	operations
x	$p_1 = \text{mkleaf}(\text{id}, \text{entry-}x)$
y	$p_2 = \text{mkleaf}(\text{id}, \text{entry-}y)$
$*$	$p_3 = \text{mknode}(*, p_1, p_2)$
5	$p_4 = \text{mkleaf}(\text{num}, 5)$
$+$	$p_5 = \text{mknode}(+, p_3, p_4)$
z	$p_6 = \text{mkleaf}(\text{id}, \text{entry-}z)$
$-$	$p_7 = \text{mknode}(-, p_5, p_6)$

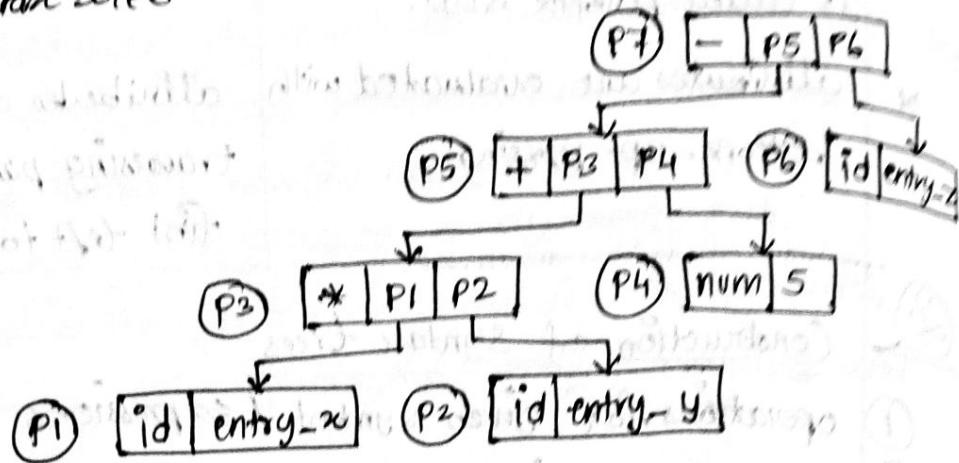
②

SDD (production + Semantic rules) E → attribute → any attribute
 → grammar symbol
 → operator

production	Semantic rules
$E = E_1 \oplus T$	$E \cdot \text{node} = E_1 \cdot \text{node} - T \cdot \text{node}$ (or) $\oplus \text{mknode}(-, E_1 \cdot \text{node}, T \cdot \text{node})$
$E = E_1 \oplus T$	$E \cdot \text{node} = E_1 \cdot \text{node} + T \cdot \text{node}$ (or) $\oplus \text{mknode}(+, E_1 \cdot \text{node}, T \cdot \text{node})$
$E = E_1 * T$	$E \cdot \text{node} = E_1 \cdot \text{node} * T \cdot \text{node}$ (or) $* \text{mknode}(*, E_1 \cdot \text{node}, T \cdot \text{node})$
$E = T$	$E \cdot \text{node} = T \cdot \text{node}$
$T = id$	$T \cdot \text{node} = \text{mkleaf}(\text{id}, \text{entry-}id)$
$T = num$	$T \cdot \text{node} = \text{mkleaf}(\text{num}, \text{num.value})$

(3)

Syntax tree



(actual symbols to substitute in tree are
particularized with red color)

2)

Construct the Syntax for $a - 4 + c$

(1)

Symbol	operations.
a	$p_1 = \text{mkleaf}(\text{id}, \text{entry-}a)$
4	$p_2 = \text{mkleaf}(\text{num}, 4)$
-	$p_3 = \text{mknoden}(-, p_1, p_2)$
c	$p_4 = \text{mkleaf}(\text{id}, \text{entry-}c)$
+	$p_5 = \text{mknoden}(+, p_3, p_4)$

(2)

SDD

production rule

$$E = E_1 + T \quad E \cdot \text{node} = E_1 \cdot \text{node} + T \cdot \text{node} = \text{mknoden}(+, E_1 \cdot \text{node}, T \cdot \text{node})$$

$$E = E_1 - T \quad E \cdot \text{node} = E_1 \cdot \text{node} - T \cdot \text{node} = \text{mknoden}(-, E_1 \cdot \text{node}, T \cdot \text{node})$$

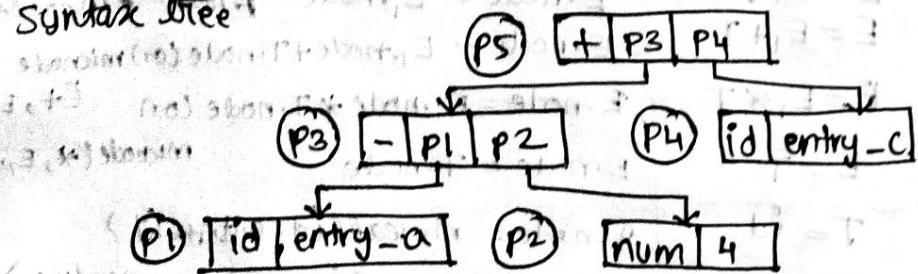
$$E = T \quad E \cdot \text{node} = T \cdot \text{node}$$

$$T = \text{id} \quad T \cdot \text{node} = \text{mkleaf}(\text{id}, \text{entry-id})$$

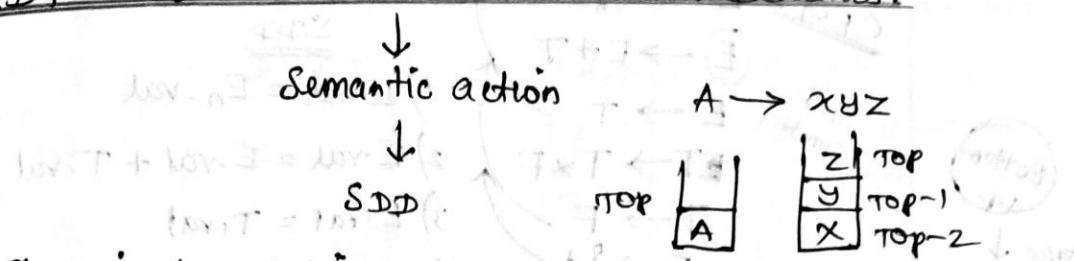
$$T = \text{num} \quad T \cdot \text{node} = \text{mkleaf}(\text{num}, \text{num-value})$$

(3)

Syntax tree



SDT - Syntax Directed Translation Schemes.



Stack implementation

Evaluated Bottom up
 ↗ ↘
 ↗ top down

x	y	z
TOP-2	TOP-1	TOP

 \Rightarrow

A	
TOP	

SDT

* It is the complementary notation of SDD - Syntax direction definition can be implemented using SDT - syntax directed translation schemes.

* SDT can be defined by the combination of CCA (with program segments embedded in production bodies and it is called semantic actions)

* Semantic actions can appear anywhere in the production body

* Semantic actions are represented by ~~open { } y~~ $\xrightarrow{\text{SDT}}$

$E.\text{node} \xrightarrow{\text{SDD}} E_1.\text{node} + T.\text{node} \quad \left\{ E.\text{val} = E_1.\text{val} + T.\text{val} \right. \right.$

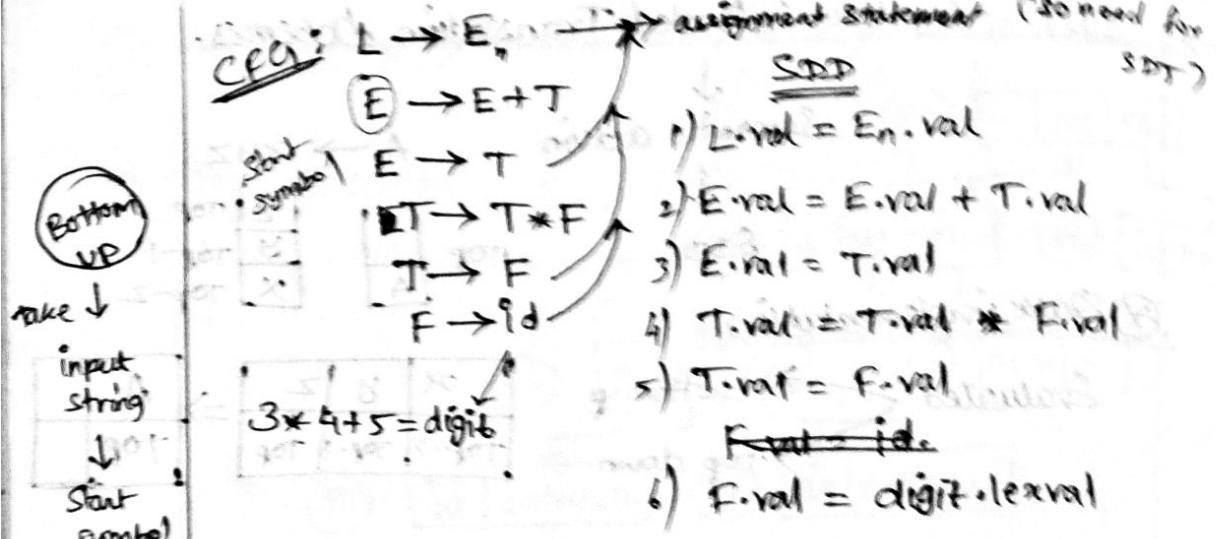
Evaluation of Translation Schemes

* SDT can be evaluated by bottom up evaluation and top down evaluation for S-attributed and L-attributed definitions.

Gramman

- ↳ Bottom up (Attributed) $\xrightarrow{\text{LR}}$ (Parent can take the value of child node)
- ↳ Top down (Attributed) $\xrightarrow{\text{LL}}$ (Child can take the value of sibling along with parent)

- * Bottom up evaluation for S-attributed definition implementation.
- * Bottom up evaluation is done by stack evaluation.
- * When there is a reduction semantic action is carried out by using a double stack.



- using SDT to obtain partial eval
- 1) $\{ print(val[Top]) \}$ 
 - 2) $\{ val[Top] = val[Top-2] + val[Top] \}$ 
 - 3) $\{ val[Top] = val[Top-2] * val[Top] \}$ 

8-attribute $3 * 4 + 5 = digit$
 $id_1 * id_2 * id_3$
 $T * F \quad E = E + T$

$$\begin{array}{c|c} E & 12+5 \\ \hline E_2 & = 17 \\ \hline E & 17 \end{array}$$

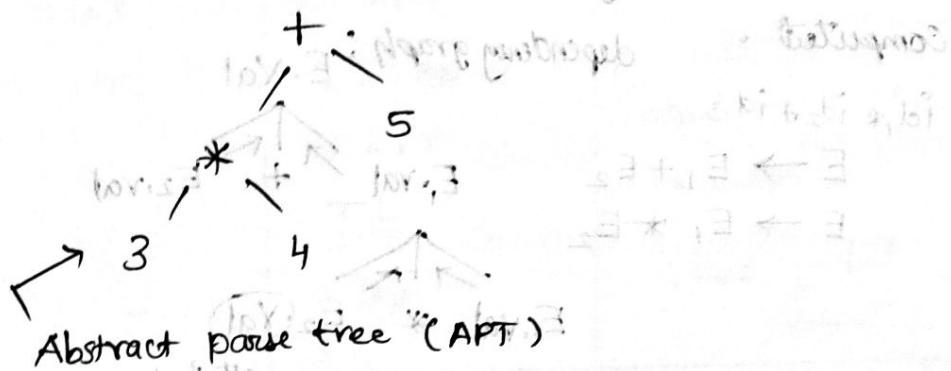
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$(top-2 + top-3 = top-3)$ about $T + E_{n-2} = E_{n-3}$

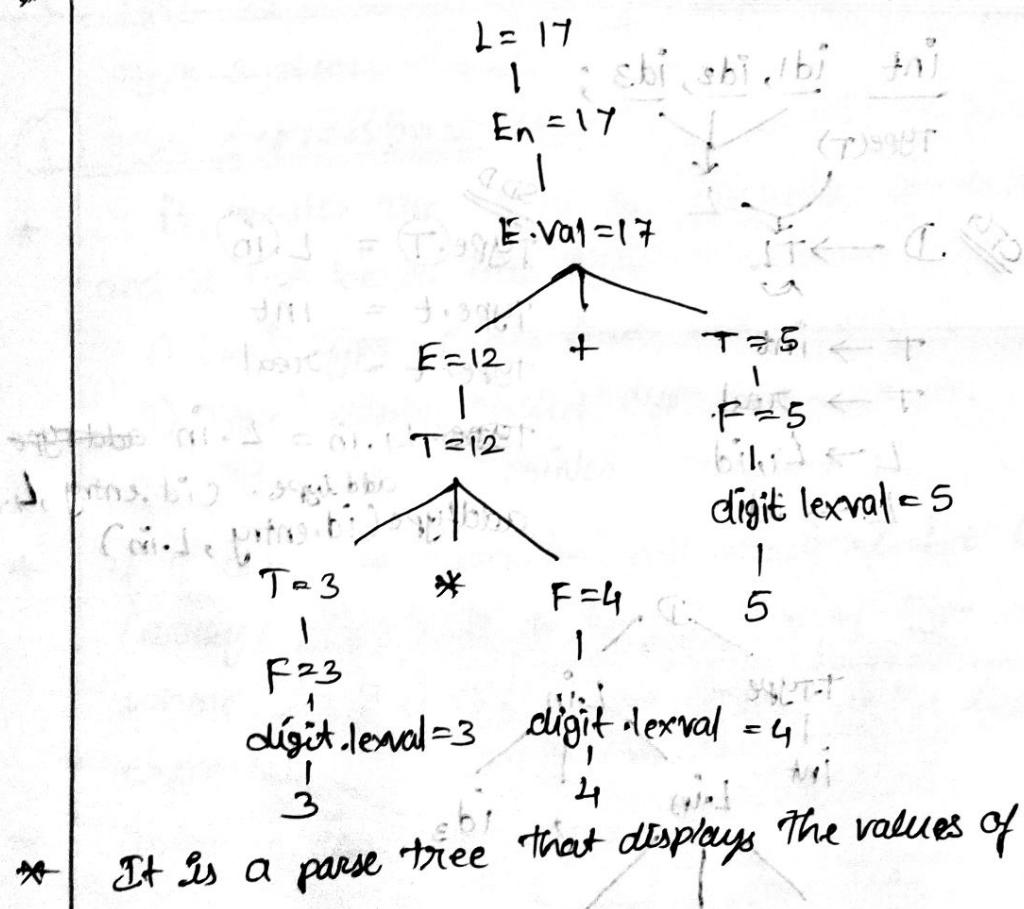
Stack implementation.

input	Stack	value	Action
$3 * 4 + 5 \$$	-	-	-
$* 4 + 5 \$$	3	3	Reduce $F \rightarrow 3$ ($F \rightarrow id$)
$* 4 + 5 \$$	F	3	-
$* 4 + 5 \$$	T	3	Reduce $T \rightarrow F$ ($F \rightarrow id$)
$4 + 5 \$$	T *	3	-
$+ 5 \$$	T * 4	3, 4	Reduce $F \rightarrow 4$ ($F \rightarrow id$)
$+ 5 \$$	T * F	12	-
$+ 5 \$$	T	12	Reduce $T \rightarrow T * F$
$+ 5 \$$	E	12	Reduce $E \rightarrow T$
$5 \$$	E +	12	-
$\$$	E + 5	12	-

\$	$E + F$	12,5	$F \rightarrow S$ ($F \rightarrow id$)
\$	$E + T$	12,5	$T \rightarrow F$
\$	E	17	$E \rightarrow E + T$
\$	E	17	-
\$	E_n	17	$L \rightarrow E_n$



Annotated parse tree



- * It is a parse tree that displays the values of attributes at each node.
- * The process of computing attributes values at the nodes is called annotating or decorating the parse tree.
- * Information for annotated parse tree:
 - 1) attributes, inherited attribute.
 - 2) Semantic phase - deals with SDD, SDT.

3) Dependency graph - a dependency graph determines how the values of the attributes can be computed.

* ④ The order of computations that depends on dependency graph is induced by semantic rules.

* ⑤ Annotated parse tree shows the values of the attributes whereas dependency graph determine how the values are computed.

dependency graph:

$\text{id}, * \text{id}_2 + \text{id}_3$

$E \rightarrow E_1 + E_2$

$E \rightarrow E_1 * E_2$

$E \cdot \text{Val}$

$E_1 \cdot \text{val} + E_2 \cdot \text{val}$

$E_1 \cdot \text{val} * E_2 \cdot \text{Val}$

attribute.

Bottom-up
inherited

Inherited attribute

int $\text{id}_1, \text{id}_2, \text{id}_3$;

TYPE(T)

CFU D \rightarrow TL

T \rightarrow int

T \rightarrow real

L \rightarrow L_1, id

C $L_1 \rightarrow \text{id}_1$

~~SD~~ TYPE(T) = L.in

type.t = int

type.t = real

~~TYPE~~ $L_1.in = L.in$ ~~add type~~

add type. (id.entry, L.in)
add type. (id.entry, L.in)

T-type
int
L.in
L.in
id₃

L.in
L.in

id₁
id₂

int

S/P string	Stack	Action
int P, q, r	int	Reduce T → int
p, q, r	T	- copy final
, q, r	TP	Reduce L → id
. q, r	TL,	-
, r	TL, q	Reduce L → id
r	TL, q	→ L → id (3)
-	TL, r	reduce → L, id (2)
-	TL	→ TL
-	D/	Start symbol.

Q2m

Type expressions and type systems

~~Type systems~~

(1) Type expressions

- * it denotes the type of language construction and it can be of two type
 - 1) basic type (int, float, char, boolean)
 - 2) Type name, called type construction.
eg:- Array, pointer, structure :
- * if T is a type expression then array of T, t (array[I, T]) is a type system of type expression where I is the index and T is the type expression
- * notation for Type expression.

$i(\text{array}(1, \dots, 10), T)$

$TE \Leftarrow (\text{array}[1, \dots, 10], T)$

\downarrow
TE
 \downarrow
TS

\downarrow
TS
 \downarrow
TS

(2)

Type systems

it is the collection of rules for assigning type expressions.

Components of type systems.

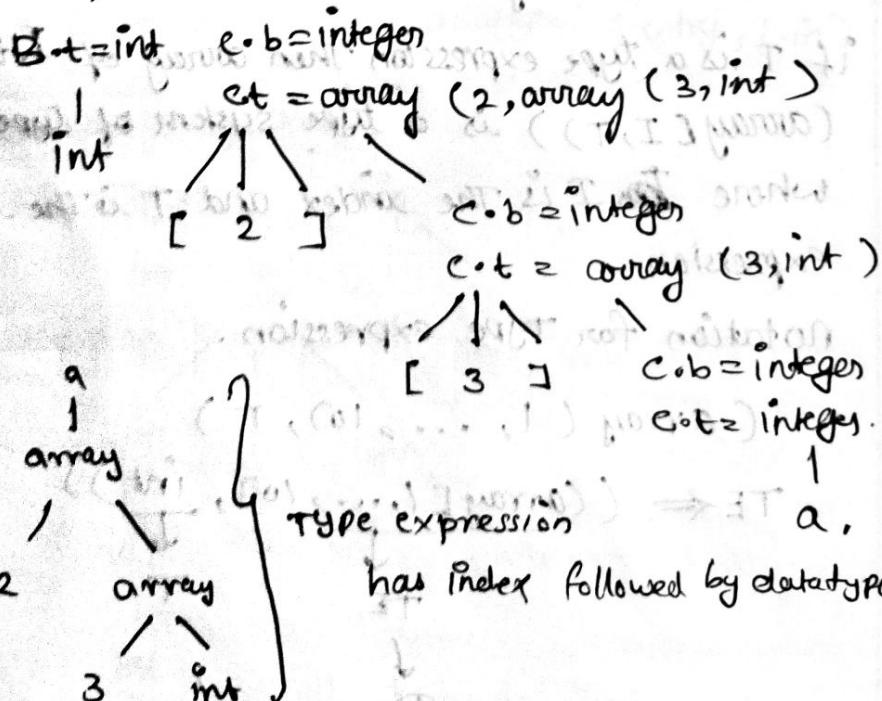
- 1) Basic types
- 2) Type construction
- 3) Type equivalence
 - \leftarrow structured equivalence e.g. struct a, b
char a;
a = A;
b = a;
 - \rightarrow name equivalence
- 4) type checker - it is a implementation of type system.

→ Arrays int [2][3]

production	Semantic Rules	SDP
$T \rightarrow Bc$ <small>type</small> Array	$T \cdot t = C \cdot t [2][3]$	
$B \rightarrow \text{int}$ $B \rightarrow \text{float}$ $C \rightarrow [\text{num}] C_1$ $c \rightarrow a$	$e \cdot b = B \cdot t$ $B \cdot t = \text{integer}$ $B \cdot t = \text{float}$ $C \cdot t = \text{array}(\text{num} \cdot \text{val}, C_1 \cdot t)$ $C_1 \cdot b = C \cdot b$ $e \cdot t = C \cdot b$	
		array and float to be same

→ Annotated parse tree.

$T \cdot t = \text{array}(2, \text{array}(3, \text{integer}))$



TYPE expression

has index followed by datatype.

EQUIVALENCE OF TYPE EXPRESSIONS

- * "If two type expressions are equal then return a certain type else return type-error".
- * "Therefore we should have a precise definition of when two type expressions are equivalent."
- * Ambiguities
- * two types of type expression

- ① STRUCTURAL EQUIVALENCE OF TYPE EXPRESSIONS.
- ② NAME EQUIVALENCE

(1) STRUCTURAL EQUIVALENCE

- * as long as type expressions are built from the basic structure equivalence \rightarrow structural equivalence



Eg: 1. Function $\text{sequiv}(s, t)$: boolean;
begin
2. if s and t are the same basic type then
3. Return true . \uparrow \rightarrow datatype
4. Else if $s = \text{array}(s_1, s_2)$ and $t = \text{array}(t_1, t_2)$ then
5. Return $\text{sequiv}(s_1, t_1)$ and $\text{sequiv}(s_2, t_2)$
6. Else if $s = s_1 * s_2$ and $t = t_1 * t_2$ then
7. Return $\text{sequiv}(s_1, t_1)$ and $\text{sequiv}(s_2, t_2)$
8. Else if $s = \text{pointer}(s_1)$ and $t = \text{pointer}(t_1)$ then
9. Return $\text{sequiv}(s_1, t_1)$
10. Elseif $s = s_1 \rightarrow s_2$ and $t = t_1 \rightarrow t_2$, then
11. Return $\text{sequiv}(s_1, t_1)$ and $\text{sequiv}(s_2, t_2)$

else

12. Return false

end.

* Example

Type constructors.

encoding

pointer

01

array

10

freturns

11

②

Name EQUIVALENCEVARIABLE AND TYPE EXPRESSION

NEXT	link
last	link
p	pointer ((cell))
q	pointer ((cell))
r	pointer ((cell))

* Confusion arise in passed from the fact that many implementation associate an implicit type name with each declared identifier. If the declaration contains a type expression that is not a name, a fresh implicit name is created every time a type expression appears in a variable declaration.

Ex:- type link = ↑ cell;

np = ↑ cell;

nqr = ↑ cell;



Var next : link;

last : link;

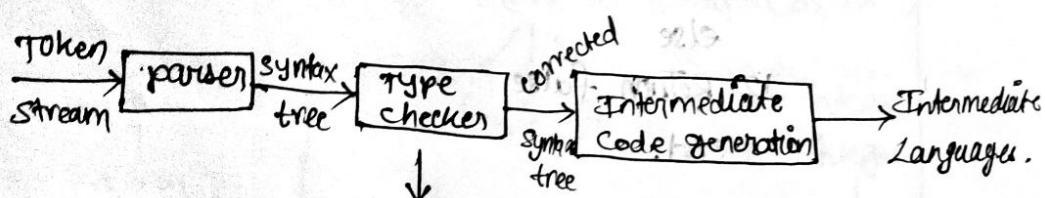
P : np;

q : nqr;

r : nqr;

→ Type checking

Implementation of type systems.



⊗ Verifies types of constructs & matches
ptr, array, func, records.

* One of the most important semantic aspects of compilation.

* Allows the programmer to limit what types may be used in certain circumstances.

- * Assigns types to values.
- * Determine whether these values are used in an appropriate manner.
- * Simplest situation: check types of objects and report a type-error in case of a ~~violation~~ violation.

- * Complex situation: incorrect type of objects

Static checking	Dynamic checking
* type checking done at compile time	performed during program execution
* properties can be verified before program run	permits programmers to be less concerned with types.
* can catch many common errors	mandatory in some situations such as, array bounds check.
* Desirable when faster execution is important	more robust and clearer code.

- ① rules for type checking.
- ② type conversion.
 - ③ overloading of functions and operators.
 - ④ type interface and polymorphic functions.
 - ⑤ An Algorithm for unification.

① Rules for type checking.

Type checking can take on two forms:

- 1) synthesis
- 2) inference.

1) synthesis. builds up the type of an expression from the type of its subexpressions. It requires names to be declared before they are used.

The type of $E_1 + E_2$, is defined in terms of the type of E_1 and E_2 . A typical rule for synthesis

rule:

if if has type $s \rightarrow t$ and x has type s ,

then expression $f(x)$ has type t .

Here, f and x denote expression, and $s \rightarrow t$ denotes a function from s to t .

This rule for function with one argument carries the function.

- * A typical rule for type inference has the form

rule:

if $f(x)$ is an expression,

Then for some a and b , f has type $a \rightarrow b$ and x has type a .

eg:- $\text{int id1, id2, id3;}$

variables. type $a \rightarrow b$
int + has int

- * type inference is needed for language ML.

Type Checking of Expressions.

- * use synthesized attribute 'type' for the non-terminal E representing an expression

Expression	Action (secondary action)
$E \rightarrow id$	$E.type \leftarrow \text{lookup}(id.entry)$
$E \rightarrow E_1, op E_2$	$E.type \leftarrow \text{if } E_1.type = E_2.type \text{ then } E_1.type \text{ else type.error}$
$E \rightarrow E_1, relop E_2$	$E.type \leftarrow \text{if } E_1.type = E_2.type \text{ then boolean } \text{ else type.error.}$
$E \rightarrow E_1[E_2]$	$E.type \leftarrow \text{if } E_2.type = \text{integer} \text{ and } E_1.type = \text{array}(s,t) \text{ then } t \text{ else type.error.}$
$E \rightarrow E_1.t$	$E.type \leftarrow \text{if } E_1.type = \text{pointer}(t) \text{ then } t \text{ else type.error.}$

- * statements normally do not have any value, hence of type void.
- * for propagating type error occurring in some statement nested deep inside a block, a set of rules needed.

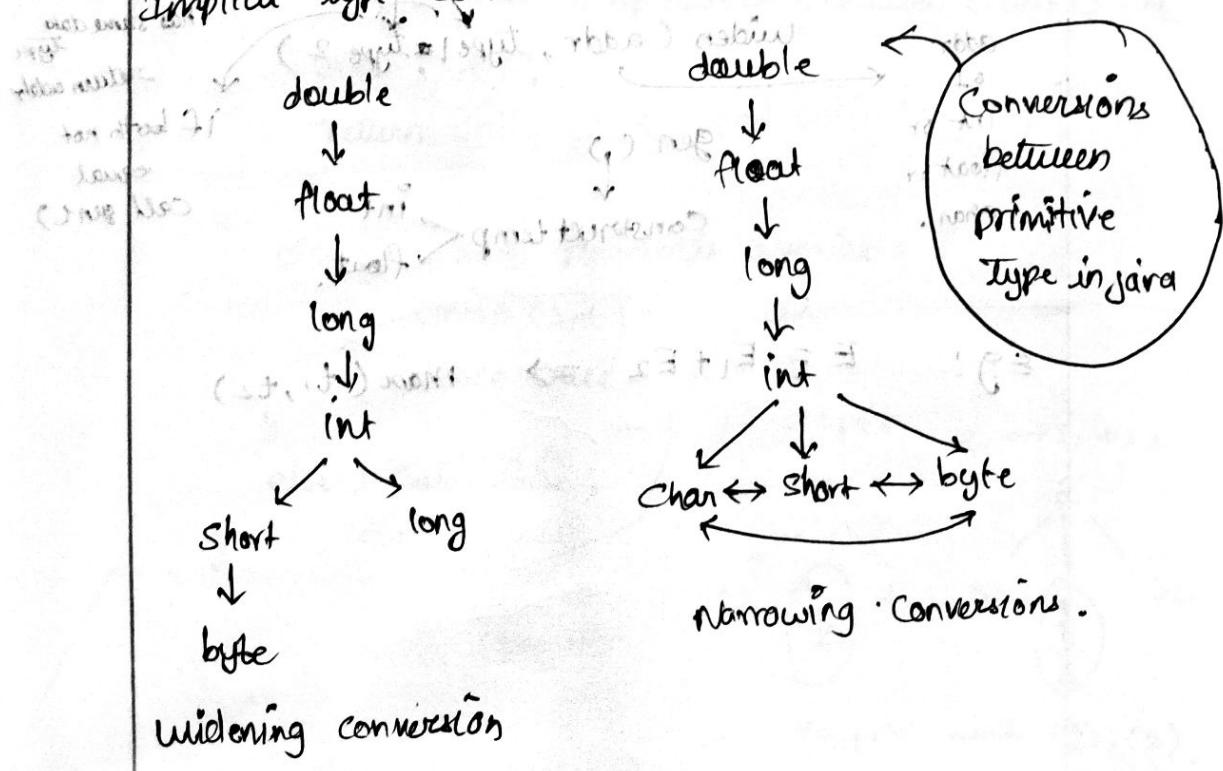
$S \rightarrow id = E$	$s.type \leftarrow$ if $id.type = E.type$, then void else type.error.
$S \rightarrow if E then S_1$	$s.type \leftarrow$ if $E.type = boolean$, then $S_1.type$ else type.error.
$S \rightarrow while E do S$	$s.type \leftarrow$ if $E.type = boolean$ then $S_1.type$ else type.error.
$S \rightarrow S_1 ; S_2$	$s.type \leftarrow$ if $S_1.type = void$ and $S_2.type = void$ then void else type.error.

- * A function call is equivalent to the application of one expression to another.

$(E) \rightarrow E_1(E_2)$	$E.type \leftarrow$ if $E_2.type = s$ and $E_1.type = s$ \rightarrow then t else type.error.
----------------------------	---

② Type conversion (specification of expression)

- * Conversion from one type to another is said to be implicit if it is done automatically by the compiler.
- * Implicit type conversions, also called coercions.



* It is also called type casting
Type casting is basically the conversion of one data type to another.

→ types: ① Implicit type conversion (type coercion)

If the compiler converts the one type of data to another automatically with no data loss.

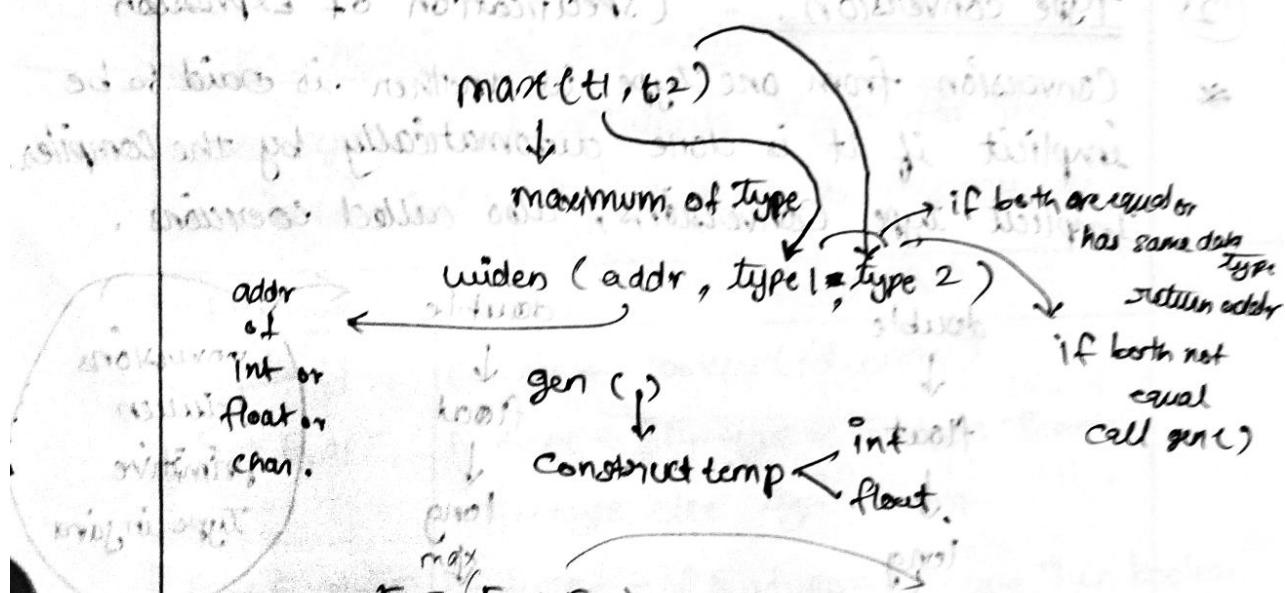
Eg:- char c = 'A'

printf("%d", c)

② Explicit type conversion

* When data of one type is converted explicitly to another type with the help of programming constraints is called explicit type conversion it has data loss.

* Higher data type cannot be converted to the lower data type.



Eg:- $E = (E_1 + E_2) \Rightarrow \max(t_1, t_2)$

Addr widen (Addr a, type t, type w)

if ($t = w$) return a;

else if ('t = integer and w = float') {

temp = new Temp();

gen.('temp' = ''(float)' a);

return temp; ('int')

}

else error;

}

return

$E \rightarrow E_1 + E_2 \quad \{ E.type = max(E_1.type, E_2.type); \}$

$a_1 = widen(E_1.addr, E_1.type, E.type);$

$a_2 = widen(E_2.addr, E_2.type, E.type);$

$E.addr = new Temp();$

gen.('E.addr' = 'a_1 + a_2'); }

Semantic action



Algorithm : unification Algorithm.

boolean unify (Node m, Node n) {

s = find (m); t = find (n);

if ($s = t$) return true;

else if (nodes s and t represent the same basic type)

elseif (s is an op-node with children s_1 and s_2 and t is an op-node with children t_1 and t_2) {

and t_1 and t_2 are op-nodes with children t_1 and t_2) {

union (s,t);

return unify (s_1, t_1) and unify (s_2, t_2);

}

else if s or t represents a variable {

union (s,t);

return true;

else return false;

}

$T \rightarrow t_1 + t_2 \quad E \rightarrow s_1 + s_2$

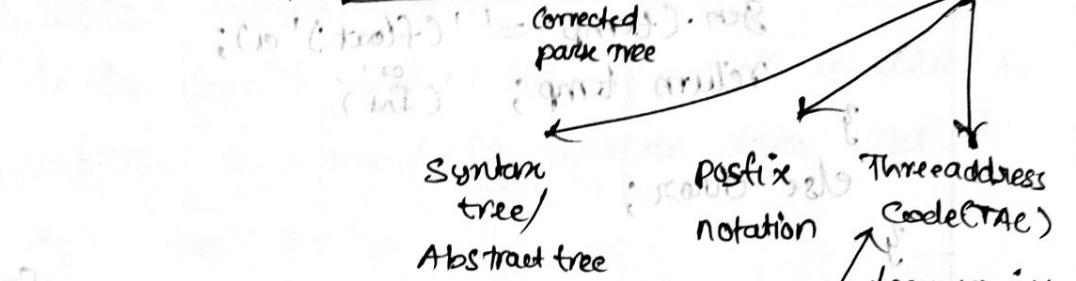
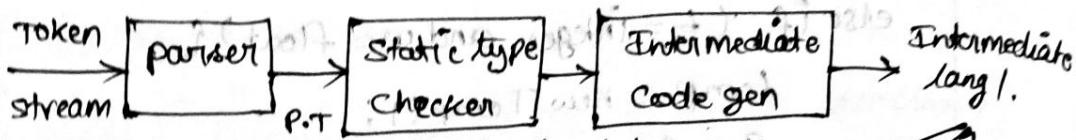
$t_1 + t_2 \quad s_1 + s_2$

$t_1 + t_2 \quad s_1 + s_2$

$t_1 + t_2 \quad s_1 + s_2$

unify (' s_1, t_1 ') unify (' s_2, t_2 ').

Intermediate code generation



Syntax tree

$$1) a * (b + c) / d$$

$$2) a = b * c + b * -c$$

$$\textcircled{1} : (a + b) * c / d$$

$$\begin{array}{c} * \\ \diagdown \quad \diagup \\ a \quad b \\ + \quad \quad \quad c \\ \diagup \quad \quad \quad \diagdown \\ d \end{array}$$

variables: a, b, c, d
constants: +, *, /

$$(a + b) * c / d = 8$$

\textcircled{2}

$$(a * b) + (c * d)$$

$$a * b + c * d$$

Postfix

$$1) (a + b) * c \Rightarrow abc+$$

$$2) a + (b * c) \Rightarrow abc*$$

$$3) (a - b) * (c / d) \Rightarrow abcd/*$$



(a + b) * c (abc)*

* Three address code should contain atmost 3 address and atmost one operator on RHS

Types of Three address code (3 types)

Quadruple

Triple

Indirect Triple

$$1) a = x + y * z$$

$$t_1 = x$$

$$t_2 = t_1 + y$$

$$t_3 = t_2 * z$$

$$(4) \quad d \quad + \quad t_1 \quad t_2 \quad t_3$$

$$x \quad * \quad t_1 \quad t_2 \quad t_3$$

$$y \quad * \quad t_2 \quad t_3 \quad a = t_2$$

$$z \quad * \quad a = t_2$$

$$2) a = b * -c + b * -c$$

$$t_1 = -c$$

$$t_2 = b * t_1$$

$$t_3 = -c$$

$$t_4 = b * t_3$$

$$t_5 = t_2 + t_4$$

$$a = t_5$$

① Quadruple has four fields: 1) OP

2) arg1

3) arg2

4) result

Forms of Intermediate Language

	OP	arg1	arg2	result
$a = b * -c + b * -c$	(0) uminus	c	-	t1
$t_1 = -c$	(1) *	b	t1	t2
$t_2 = b * t_1$	(2) uminus	c	-	t3
$t_3 = -c$	(3) *	b	t3	t4
$t_4 = b * t_3$	(4) +	t2	t4	t5
$t_5 = t_2 + t_4$	(5) =	t5	-	a

2) triple

	OP	arg1	arg2
(0)	uminus	c	-
(1)	*	b	(0)
(2)	uminus	c	-
(3)	*	b	(2)
(4)	+	(1)	(3)
(5)	=	(4)	a

3) Indirect triple

ptr table

	ptr	OP	arg1	arg2
100	(0)	100	uminus	* d
101	(1)	101	*	b
102	(2)	102	uminus	c
103	(3)	103	*	b
104	(4)	104	+	(10)
105	(5)	105	=	(10)

ptr	OP	arg1	arg2
100	uminus	* d	-
101	*	b	(100)
102	uminus	c	-
103	*	b	(102)
104	+	(10)	(103)
105	=	(10)	a

TRANSLATION EXPRESSION (OR) THREE ADDRESS CODE

FOR EXPRESSION (OR) SDD - SYNTAX DIRECTED

DEFINITION OF THREE ADDRESS CODE FOR EXPRESSION



AUG

①

Assignment statement

②

Boolean statement

③

Array reference statement

①

Assignment Statement

$S \rightarrow id : * = E$ (1)

$E \rightarrow E_1 + E_2$ (2)

$E \rightarrow E_1 * E_2$ (3)

$E \rightarrow (E)$ (4)

$E \rightarrow id$ (5)

production :- $S \rightarrow id := E$

{ P = lookup (id-name);

if P ≠ NIL then

Emit (P = 'E.place');

else

error; }

$E \rightarrow E_1 + E_2$ { $E \cdot \text{place} = \text{newTemp}();$

Emit ($E \cdot \text{place} = 'E_1 \cdot \text{place}' + 'E_2 \cdot \text{place}'$);

$E \rightarrow E_1 * E_2$ { $E \cdot \text{place} = \text{newTemp}();$

Emit ($E \cdot \text{place} = 'E_1 \cdot \text{place}' * 'E_2 \cdot \text{place}'$);

$E \rightarrow (E)$ { $E \cdot \text{place} = '(E_1 \cdot \text{place})';$

$E \rightarrow \text{id}$ { $p = \text{lookup}(\text{id_name});$

if $p \neq \text{NIL}$ then

Emit ($p = E \cdot \text{place}$);

else Error;

Eg : $a + b * c \Rightarrow x = id + id * id$

* Where p returns the entry for id_name in symbol table.

* Emit is a function appends 3 address code to be output file, or it appends error.

* newTemp : constructor that generate new temporary variable.

* $E \cdot \text{place}$ gives the value of E in symbol table.

Eg : $a + b * c \Rightarrow x = id + id * id$

$t_1 = b * c$ (value b) \rightarrow $t_1 = b * c$ ($t_1 = E$)

$t_2 = a + t_1$ ($t_1 = b * c$) \rightarrow $t_2 = a + t_1$ ($t_2 = E$)

($t_2 = E$) \rightarrow $t_2 = E$

($t_2 = E$) \rightarrow $t_2 = E$ ($t_2 = E$)

($t_2 = E$) \rightarrow $t_2 = E$ ($t_2 = E$)

($t_2 = E$) \rightarrow $t_2 = E$ ($t_2 = E$)

($t_2 = E$) \rightarrow $t_2 = E$ ($t_2 = E$)

($t_2 = E$) \rightarrow $t_2 = E$ ($t_2 = E$)

($t_2 = E$) \rightarrow $t_2 = E$ ($t_2 = E$)

($t_2 = E$) \rightarrow $t_2 = E$ ($t_2 = E$)

② Boolean Expression

- $E \rightarrow E_1 \text{ OR } E_2$
- $E \rightarrow E_1 \text{ AND } E_2$
- $E \rightarrow \text{NOT } E$
- $E \rightarrow (E)$
- $E \rightarrow \text{id relop id}$
- $E \rightarrow \text{True}$
- $E \rightarrow \text{False}$
- $E \rightarrow E \text{ OR } E \{ E.\text{place} = \text{newTemp}();$
 $\quad \quad \quad \text{emit}(E.\text{place}) = E_1.\text{place}' \text{ OR } E_2.\text{place}); \}$

$E \rightarrow E_1 \text{ AND } E_2$ & $E \cdot \text{place} = \text{newTemp}();$
 Emit ($E \cdot \text{place} = 'E_1 \cdot \text{place}$ AND $E_2 \cdot \text{place}');$

$E \rightarrow \text{NOTE}$ { $E \cdot \text{place} = \text{newTemp}();$
 Emit ($E \cdot \text{place} = 'NOT' E_1 \cdot \text{place}');$

$E \rightarrow (E_1)$ { $E \cdot \text{place} = 'L_i \cdot \text{place}'$
 $E \rightarrow \text{True}$ { $E \cdot \text{place} = \text{newTemp}();$
 Emit ($E \cdot \text{place} = '1'');$

$E \rightarrow \text{False}$ { $E \cdot \text{place} = \text{newTemp}();$
 Emit ($E \cdot \text{place} = '0'');$

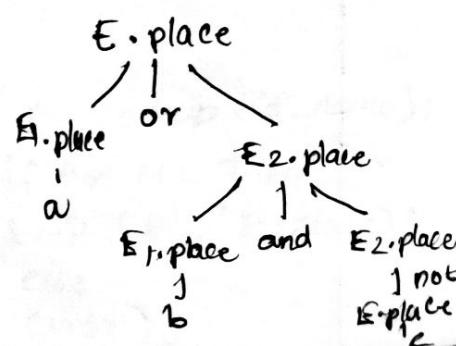
$E \rightarrow \text{id} \circ \text{loopid} \circ \text{id}$ { $E \cdot \text{place} = \text{newTemp}();$
 Emit (if ($\text{id}_1 \cdot \text{place} \circ \text{loop} \cdot \text{op} \text{id}_2 \cdot \text{place}$
 'goto' next state 3));
 Emit ($t \cdot \text{place} = '0'');$
 Emit ('goto' next state 2);
 Emit ($E \cdot \text{place} = '1'');$

Eg:- a or b and not c

$t_1 = \text{not } e$

$$t_2 = b \text{ and } t_1$$

$$t_{\beta} = \alpha \text{ or } t_{\beta 2}$$



eg if $a < b$ then 1 else 0

100 : if $a < b$ goto 103

101 : $t_1 = 0$

102 : goto 104

103 : $t_1 = 1$

104 :

eg $a < b$ or $c < d$ and $e < f$

100 : if $a < b$ goto 103

101 : $t_1 = 0$

102 : goto 104

103 : $t_1 = 1$

104 : if $c < d$ goto 107

105 : $t_2 = 0$

106 : goto 108

107 : $t_2 = 1$

108 : if $e < f$ goto 111

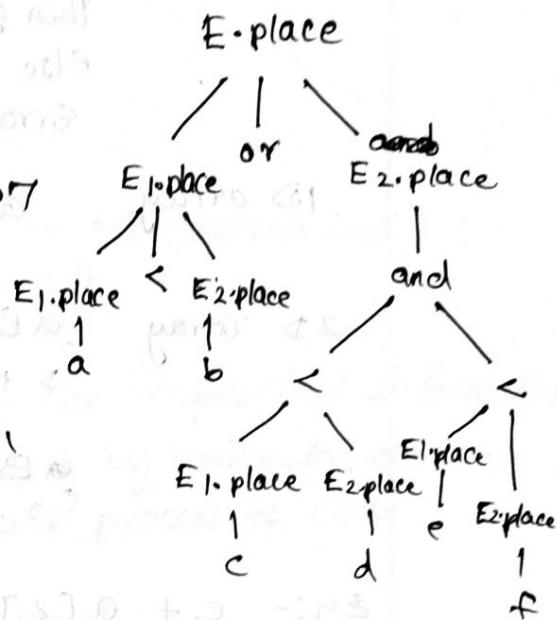
109 : $t_3 = 0$

110 : goto 112

111 : $t_3 = 1$

112 : $t_4 = t_2$ and t_3

113 : $t_5 = t_1$ or t_4



sign
man
③

Array references

emit()
gen()

$S \rightarrow id = E$

$S \rightarrow L = E$

$L \rightarrow L[E]$

$L \rightarrow id[E]$

$E \rightarrow L$

$E \rightarrow E_1 + E_2$

$E \rightarrow id$

array

$S \rightarrow id = E$ gen if $P = \text{lookup}(id_entry)$

if $P \neq \text{nil}$

then gen(E.place)

else

error ; y

$S \rightarrow L = E$ { gen & L.base
 $[L.\text{addr}] = E.\text{place}$

$L \rightarrow L[E]$ { L.array = L1.array

L.type = L1.type ;

t = newTemp();

L.addr = newTemp();

gen ($t^C = 't', \text{addr}' \Rightarrow L.\text{type}.\text{width}'$);
gen ($L.\text{addr}' = L1.\text{addr}' + 't'$); } }

$E \rightarrow L$ & $E.\text{addr} = \text{newTemp}(1);$
gen ($E.\text{addr}' = 'L.\text{array}.base' [L.\text{addr}']$); }

$E \rightarrow t1+t2$ & $t.\text{place} = \text{newTemp}(1);$
gen ($E.\text{place}' = E1.\text{place}' + E2.\text{place}'$); }

$E \rightarrow id$ gen if $p = \text{lookup}(id_entry)$

if $p \neq \text{nil}$

then gen ($E.\text{places}$)

else

error;

$\nearrow \text{base} \quad \nearrow \text{width} \quad \nearrow \text{introduce} \quad \nearrow \text{lower}$

bound

$a[i] = B + w * (i - LB)$

$a[i][j] = B + w * (N * (i - LR) + j - LC)$

↳ Row major

$a[i][j] = B + w * ((i - LR) + (M * (j - LC)))$

↳ Column major

eg:- $c + a[2][3]$

$$t_1 = i * 12$$

$$t_2 = j * 4 \quad (4 \text{ because } 1 \text{ int} \Rightarrow 4 \text{ bits. included in row}).$$

$$t_3 = t1 + t2$$

$$t_4 = a[t_3]$$

$$t_5 = c + t_4$$

$a[2][3]$
array (2, array (3, integers))

$$E.\text{addr} = c$$

$$S$$

$$|$$

$$+ \quad |$$

$$E.\text{addr} = t_5$$

$$E.\text{addr} = t_4$$

$$1$$

$$L.\text{array} = a$$

$$L.\text{type} = \text{int}$$

$$E.\text{addr} = t_3$$

$$L.\text{array} = a$$

$$L.\text{type} = \text{array}(3, \text{integers})$$

$$L.\text{addr} = t_1$$

$$a.\text{type} = \text{array}(2, \text{array}(3, \text{integers}))$$

$$[\quad]$$

$$c \quad E.\text{addr} = t_1$$

BACKPATCHING

Q1

Ans

$x < 100 \text{ || } y > 200 \text{ then } x \neq y$

TAC (act pass)

Labels

- 100 : if $x < 100$ then 106
- 101 : goto 102
- 102 : $y > 200$ then 104
- 103 : goto 107
- 104 : if $x \neq y$ then 106
- 105 : goto 107
- 106 : true
- 107 : false.

II pass (Backpatch)

/ / ← M (marker inst)
 backpatch merge() true list
 false list

- * the process of fulfilling the unspecified information by TAC (Three addr code) by using labels and semantic actions during the process of code generation.
- * It is also defined as by the process of filling the graph in an incomplete transformations and information
- * Backpatching is applied on boolean expression, flow of control
 - ① boolean expression
 - ② flow of control statement.
 - ③ Label and goto
- * in the given example if $x < 100$ is true then the entire expression is true. It is called short circuit evaluation.

$$\text{Eg: } x < 100 \text{ } \text{U} \text{ } y > 200 \quad \text{z} \text{ } \text{er} \cdot \text{z}! = y$$

Tac (Fastpass)

100 : if $x < 100$ then —
101 : goto —
102 : if $y > 200$ then —
103 : goto —
104 : if $x! = y$ then —
105 : goto —
106 : true
107 : false

(ii pass)

100 : if $x < 100$ then 106
 101 : goto 102
 102 : if $y > 200$ then 104
 103 : goto 107
 104 : if $x \neq y$ then 106
 105 : goto 107
 106 : true
 107 : false

address
 of
 next instruction

fl - false list
 tl - true list
 marker \rightarrow stores address
 of B2

1) $B \rightarrow B_1 || MB_2$ { Backpatch ($B_1 : fL, M : iSt$) };

$$\downarrow \text{marker} \quad B \cdot t_1 = \text{merge} \cdot (B_1 \cdot t_1, B_2 \cdot t_2); \\ B \cdot f_1 = B_2 \cdot f_2; y$$

2) $B \rightarrow B_1 \& MB_2 \quad \{ \text{Backpatch}(B1.tl, M.\text{inst}) \};$

$B \cdot f1 = B2 \cdot f1;$
 $B \cdot f1 = \text{merge} \cdot (B1 \cdot f1, B2 \cdot f1);$?

$$3) B \rightarrow !B_1 \quad \& \quad B + !I = B_1 \cdot fI;$$

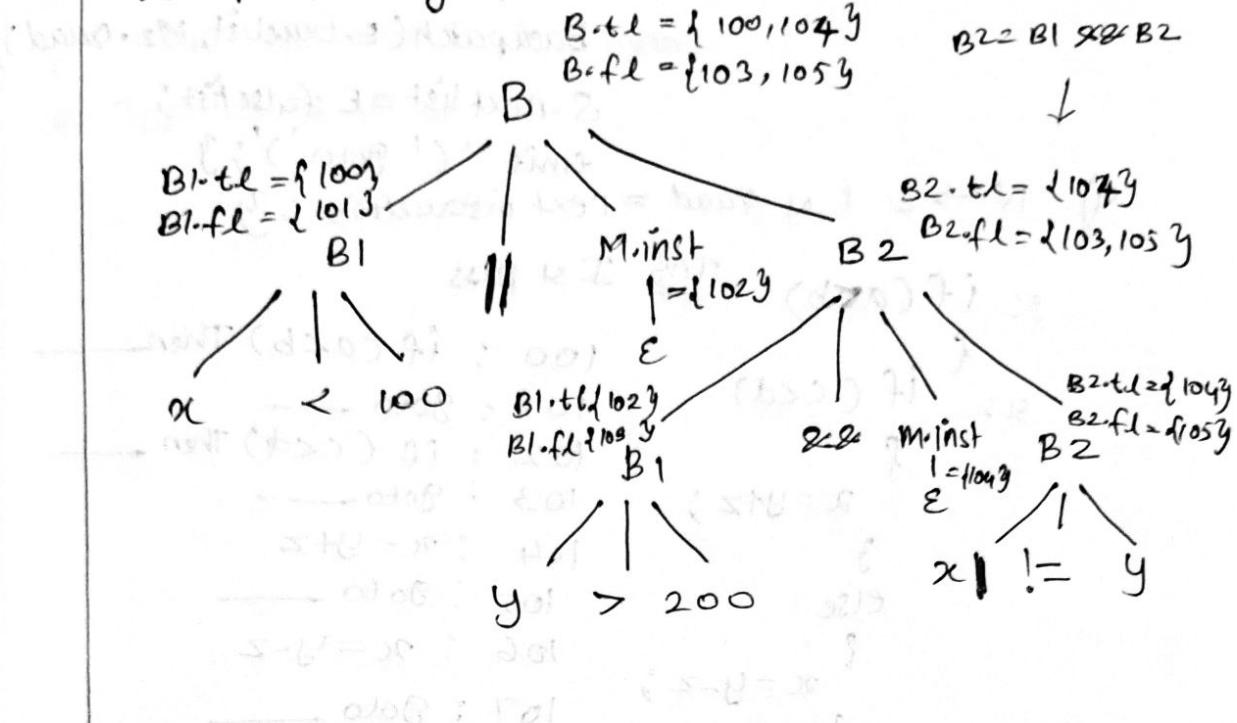
$$B \cdot f_1 = B I \cdot t_1; y$$

$$4) B \rightarrow (B_1) \cdot \{ B \cdot t \ell = B_1 \cdot t \ell \};$$

$$B_0 \cdot f_{\lambda} = B_1 \cdot f_{\lambda}; \quad y$$

5) $M \rightarrow E \cdot \& m.\text{inst} = \text{nextinst}; y$

1) Where M = non-terminal marker and address of B_2 is specified by m .



→ Flow of Control structures.

$S \rightarrow \text{if } E \text{ then } S$

| if E then S else S

| while E do S

| begin L end

| A

$L \rightarrow L ; S$

| S

1) $S \rightarrow A \quad \& S.\text{nextlist} = \text{nil}$

2) $S \rightarrow \text{begin } L \text{ end} \quad \& S.\text{nextlist} = L.\text{nextlist}$

3) $S \rightarrow \text{if } E \text{ then } M_1 \quad \& \text{backpatch}(E.\text{true list}, M_1.\text{quad})$

$S.\text{nextlist} = \text{merge}(E.\text{false list},$

$S_1.\text{nextlist})$

4) $L \rightarrow L_1 ; M_1 \quad \& \text{backpatch}(L_1.\text{nextlist}, M_1.\text{quad})$

$L.\text{nextlist} := S.\text{nextlist}$

5) $L \rightarrow S \quad \& L.\text{nextlist} = S.\text{nextlist}$

6) $M \rightarrow E \quad \& M.\text{quad} = \text{next instruction}$

7) $S \rightarrow \text{if } E \text{ then } M_1, S_1 \text{ else } M_2, S_2$

$\& \text{backpatch}(E.\text{true list}, M_1.\text{quad})$

$\& \text{backpatch}(E.\text{false list}, M_2.\text{quad})$

$S.\text{nextlist} = \text{merge}(S_1.\text{nextlist}, \text{merge}(N.\text{nextlist}, S_2.\text{nextlist}))$

→ Types and declaration.

- * Type expressions (TE)
- * Type equivalence (2 types)
- * Type declaration.