

Gowtham Ramarajan

No.13a, 1st Street Ganapathipuram, Pallikaranai • Chennai, Tamil Nadu
+91 9597775668 • rgowthamgauti@gmail.com
[LinkedIn](#) • [GitHub](#)



SUMMARY

Professional HIL Systems level ECUs Modeling and Automation Tools developer. Involved in Model based development with HIL Validation, System Rig debugging and Process Automation.

EXPERIENCE | 3+ YEARS

FCA Engineering India Private limited

EE & SW Engineer | GET | Intern

FCA Engineering India Private limited

Engineer – ADAS – HIL

Chennai, Tamil Nadu

Feb 2018 – Apr 2021

Chennai, Tamil Nadu

May 2018 – Present

WORK SUMMARY

- **Developed Utility Tool** for Virtual Test Platform Generation of Powertrain **Hybrid Motor Control Processor**.
- **Assisted** BSW Files Compilations and debugging in GPIO, CAN, ADC, Digital & PWM Systems.
- **Defined Timeline and Workflow** for CAN Data validation in Virtual Test Platform OSA Projects.
- Worked in **Test Suite Development scripting** for Power Train HIL **Model Verification** using PROVETECH - TA.
- Worked as a team member for Proof of Concept on **Active Chassis System Bench** development with ASM and HIL System **Model Integration** for Real Time Brake **Testing** within short span of time.
- **Improvised Legacy Processes** in file handling and New NM Panel creation with code generation task using Python.
- **Model Based development** and **RTI Configurations testing** for HIL System Testing which incorporates ECU – Power Relays Sensors | Actuators | Network Management | Rest Bus Simulation of CAN and LIN | In Plant Logics.
- Developed **Framework Dashboard** and **Interface Sheet** using Vehicle Architecture, Device Transmittal, Signal List, IOList, Hardware Interface Specs, Performance Standards, DBCs & LDFs, Fuse Tree, Vehicle Functionalities.
- **UI_Panel Development** using Python and **Functionality Validation** for Brake Systems | Indication Strategies | Switch Modules | Diagnostic – Maturation and De-Maturation Testing.
- 3D CAD Model Design Verification using UG-NX for **Headliner, Dashboard Console** and **Body Electronics** Harnesses.
- **Test Case development, Validation** and **Report Generation** of Instrument Clusters using Vector CANoe | Congex.

EDUCATION

- PSG Institute of Technology Applied Research • Bachelor of Electrical and Electronics Engineering (**CGPA:8.00/10.00**)
- Bharatiya Vidhya Bhavan Matriculation Higher Secondary School • HSC (**Score 95%**) • SSLC (**Score 93%**)

SOFTWARE & TOOLS

- **dSPACE Products** - ControlDesk | ModelDesk | MotionDesk | VEOS | SystemDesk | BusManager
- **General** – MATLAB | DET | JIRA | PyWin | Visual Studio | Provotech-TA | UG-NX | Workbench | SWCompositor | CANoe | CDA | CarMaker
- **Office Tools** - MS Office | G-SUITE | IBM Lotus Notes | ATHENA | GALILEO | VSEM
- **Protocols** – CAN | LIN | SPI | MODBUS | UART
- **Programming** – MATLAB – M-Scripting | Python | Embedded C | WWB-COM(Basic) | Assembly language

EXPOSURE

- Hardware Setup Schematics Development | PIM & GPEC Controllers | HIL PowerTrain Modeling | Flashing ECU | ConfigurationDesk | SCALEXIO | RENESAS R8C2527 | AUTOSAR | StateFlow | Altia | AutomationDesk | QGIS | DFSS

ADDITIONAL SKILLS

- Event Management, Requirement Writing, Public Relations and Asset Tracking.
- Languages known: English, Tamil and Telugu.

CERTIFICATIONS & HONORS

[Achievements](#) • [Competitive Programmer](#)

HOBBIES

- Travelling | Sports | Share Market Tipping