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1  /* Keypad.c - implemetation of Keypad.h
2  *
3  *   Copyright 2020 Graeme Judge, Sean Berkvens
4  *   Change Log:
5  *       May 3, 2020: Source file created
6  */
7
8
9  #include "Keypad.h"
10
11 #define KEYDELAY for(int i =0; i < 8000; i++)
12
13 #define DELLY for(int i =0; i < 100000; i++)
14
15 uint8_t rows[4] = {ROW1, ROW2, ROW3, ROW4};
16
17 void GPIOInit(){
18     GPIOInitRow();
19     GPIOInitCol();
20 }
21
22
23 void GPIOInitRow(){
24     //Clock init
25     RCC -> AHB2ENR |= RCC_AHB2ENR_GPIOEEN;
26     //using PE12 to PE15
27     for(int i = 12; i < 16; i++){ //for loop to go through all of the pins being used and initializes them
28         // GPIO Mode
29         //00 input mode
30         //01 output mode
31         //10 alternate mode
32         //11 analog mode
33         GPIOE->MODER &= ~(3UL<<(2*i));
34         GPIOE->MODER |= (1UL<<(2*i));          // Output(01)
35
36         //GPIO Speed
37         GPIOE->OSPEEDR &= ~(3UL<<(2*i));
38         GPIOE->OSPEEDR |= (3UL<<(2*i)); // Low speed
39
40         //00 none
41         //01 pullup
42         //10 pulldown
43         //11 reserved
44         GPIOE->PUPDR |= (1UL<<(2*i)); // pull-up
45
46         //GPIO Output Type: Output push-pull (0, reset), Output open drain (1)
47         GPIOE->OTYPER |= (1UL<<(1*i)); // Push-pull open drain
48     }
49
50     //clears the output data register for port E
51     GPIOE->ODR &= (uint32_t)0x0000;
52 }
53
54
55
56 void GPIOInitCol(){
57     //Clock init
58     RCC -> AHB2ENR |= RCC_AHB2ENR_GPIOAEN;
59
60     //using PA1 to PA5 excluding 4
61     for(int i = 1; i < 4; i++){ //for loop to go through all of the pins being used and initializes them
62         // GPIO Mode
63         //00 input mode
64         //01 output mode
65         //10 alternate mode
66         //11 analog mode
67         GPIOA->MODER &= ~(3UL<<(2*i));
68
69         //00 none
70         //01 pullup
71         //10 pulldown
72         //11 reserved

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73     GPIOA->PUPDR |= (1UL<<(2*i)); // pull-up
74 }
75
76 // GPIO Pin 5
77 //00 input mode
78 //01 output mode
79 //10 alternate mode
80 //11 analog mode
81 GPIOA->MODER &= ~(3UL<<(2*5));
82
83 //00 none
84 //01 pullup
85 //10 pulldown
86 //11 reserved
87 GPIOA->PUPDR |= (1UL<<(2*5)); // pull-up
88 }
89
90
91
92 enum Keys scan(){
93     GPIOE->ODR &= 0b0000 << 12;
94     DELLY;
95     uint8_t button = debouncedKey();
96
97     if((button & 0xFF) == 0x2E){
98         return Key_None;
99     }
100
101     for(int i = 0; i < 4; i++){
102         GPIOE->ODR = (rows[i])<<12;
103         DELLY;
104         button = debouncedKey();
105         if(button != 0x2E){
106             if(button == COL1){
107                 return Matrix[i][0];
108             }
109             if(button == COL2){
110                 return Matrix[i][1];
111             }
112             if(button == COL3){
113                 return Matrix[i][2];
114             }
115             if(button == COL4){
116                 return Matrix[i][3];
117             }
118         }
119     }
120 }
121
122 uint8_t getInput(){
123     volatile uint8_t IDR = (GPIOA->IDR & 0x2E);
124     return IDR;
125 }
126
127 uint8_t debouncedKey(){
128     uint8_t read = getInput();
129
130     for(int i = 0; i < DEBOUNCETIME; i++){
131         uint8_t newRead = getInput();
132         if(read != newRead || read == 0x2E){
133             read = 0x2E;
134             return read;
135         }
136     }
137     KEYDELAY;
138     return read;
139 }
140
141
142
143
144

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