

Lab (30%) and project (30%) arrangement

Week	Lab	Contents	Demo	Due Date	Report	Due Date
1	Lab1	Tutorial	No	-	No	-
2	Lab2, Ex1	Full Adder Simulation	Yes (2)	Week 3	Full report (4)	Week 4
3	Lab2, Ex2	Simulation of Process	Yes (2)	Week 4	No	-
4	Lab2, Ex3	Design a tree priority encoder	No	-	Simple report (4)	Week 6
5	Lab2, Ex4	Design a 3-digit decimal counter	Yes (2)	Week 6	Simple report (4)	Week 7
6	Lab3	7-segment LED display	Yes (2)	Week 7	No	-
7	Lab2, Ex5	FSM and FSMD	No	-	Full report (4)	Week 9
8	Project	Proposal and presentation	Yes (5)	Week 8	Yes (5)	Week 10
9	Lab4	Step motor	Yes (2)	Week 10	No	-
10	Lab2, Ex6		No	-	Full report (4)	Week 12
11-15	Project	Working on the project	Yes (5)	Week 15	Full report (10)	Week 16
16	Project	Presentation	Yes (5)	Week 16	-	-