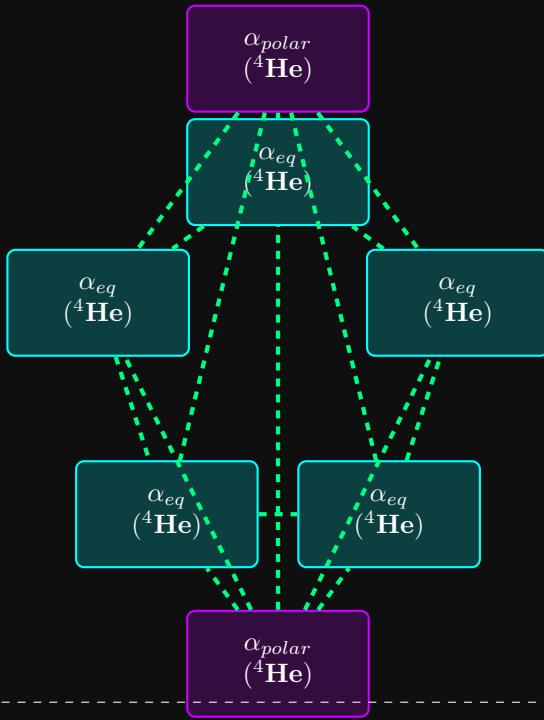


# Silicon-28 ( $7\alpha$ Subcircuit Architecture)



## IC-Equivalent Topology Level

Each  $\alpha$  macro-block encapsulates a synchronized 4-nucleon LC mesh.  
Bus lines represent 16 cross-coupled inductive pairs between blocks.

Total Capacitive Noise Network: 2700 discrete connections