

GRANT D POWELL | Computer Engineer

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Summary

Computer Engineering graduate with experience in software development, embedded systems, circuit design, and microcontroller programming. Skilled in problem-solving and adapting to new challenges in fast-paced environments. Possesses a strong foundation in various programming languages and technical tools. Seeking a role where I can apply my technical skills, contribute to real-world projects, and continue to grow as an engineer.

Experience

Parleii - LLC 09/2024 - Present

CO-Founder / Lead Developer

docs.parleii.com/about/aboutus

- Co-founded Parleii to commercialize DigiLab One, a senior capstone project turned educational product adopted by the University of Tennessee at Chattanooga.
- Led firmware development in C for ATMEGA microcontrollers, implementing efficient serial communication protocols.
- Built a modern browser-based frontend using React and TypeScript, interfacing with hardware through the Web Serial API.
- Engineered reliable, modular PCBs using KiCad and EasyEDA, integrating LEDs, switches, and a 7-segment display.
- Created automated testing and grading features tailored to electrical and computer engineering labs.
- Collaborated with faculty to ensure educational alignment, resulting in integration into UTC's engineering curriculum.
- Researched and integrated modern tools to enhance performance, user experience, and classroom scalability.

Walgreens 08/2021 - Present

Shift Manager

- Managed shifts, handled cash drawer reconciliation, led truck deliveries, and oversaw inventory management.
- Supported pharmacy operations through prescription filling, sales, and various administrative tasks.
- Coached and trained team members, facilitating the onboarding process for new employees.
- Supervised a team to ensure seamless daily operations, maintaining high levels of customer service and operational efficiency.
- References are available upon request.

Education

University of Tennessee at Chattanooga 08/2020 - 12/2024

Bachelor of Science in Computer Engineering, Minor in Electrical Engineering:

Graduated: December 2024

- Organized study halls and refresher courses for peers, fostering a collaborative learning environment.
- Gained a robust understanding of computer engineering principles, with coursework focusing on embedded systems, circuit design, and software development.
- Senior Capstone: Led development of the Integrated Lab Companion, a digital logic lab toolkit. Rebranded as DigiLab One and launched under Parleii LLC; now used in the UTC engineering curriculum.
- [CeDiD](#): 258R-QVTZ-G2LS – For online diploma verification.

Interests

When I'm not programming or working on hardware projects, I enjoy reading, building custom computers, 3D printing, and spending time outdoors.

Skills

Programming: Python | PHP | MySQL | Html5 | Java | JavaScript | TypeScript | C++ | C# | C | React | NodeJS | Next.js |

Technical: Microcontrollers | PCB Design | Circuit Design | Embedded Systems | Phone repair | PCBuilding/Repair |

Software: Linux | Windows | MS Office | SolidWorks | Unity | KiCad | EasyEDA | GIT | Simulink | MatLab |

Soft Skills: Verbal/Written Communication | Time management | Planning | Organization | Customer service | Presentation |