

GRANT D POWELL | Computer Engineer

931-214-9549 | GrantPowell911@gmail.com | Chattanooga, TN
[linkedin.com/in/GrantDPowell](https://www.linkedin.com/in/GrantDPowell) | github.com/GrantDPowell | GrantDPowell.github.io

Summary

Computer Engineering graduate with expertise in software development, embedded systems, circuit design, and microcontroller programming. Skilled in problem-solving and adapting to new challenges in fast-paced environments. Possesses a strong foundation in various programming languages and technical tools. Seeking a role in a forward-thinking tech company to leverage technical skills and drive innovation while fostering professional growth.

Experience

Parleii - LLC 06/2024 - Present

CO-Founder / Lead Developer

- Led a development team in integrating hardware and software components to create a cohesive, reliable product.
- Directed firmware development for ATMEGA microcontrollers, establishing core functionalities and a communication protocol that links the hardware to a web application.
- Designed a web-based application to enable interaction with the hardware, facilitating real-time control and data monitoring.
- Researched and implemented new technologies to enhance the performance, reliability, and user experience of the product.
- Ensured that the DigiLab One platform aligned with educational needs and lab automation, supporting its adoption into the UTC engineering curriculum

Walgreens 08/2021 - Present

Shift Manager

- Managed shifts, handled cash drawer reconciliation, led truck deliveries, and oversaw inventory management.
- Supported pharmacy operations through prescription filling, sales, and various administrative tasks.
- Coached and trained team members, facilitating the onboarding process for new employees.
- Supervised a team to ensure seamless daily operations, maintaining high levels of customer service and operational efficiency.
- References are available upon request.

Education

University of Tennessee at Chattanooga 08/2020 - 12/2024

BS in Computer Engineering, Minor in Electrical Engineering:

- Organized study halls and refresher courses for peers, fostering a collaborative learning environment.
- Gained a robust understanding of computer engineering principles, with coursework focusing on embedded systems, circuit design, and software development.
- Graduation Date: December 13, 2024.

Projects

Digilab One:

- Designed and developed the DigiLab One using C for firmware, React and TypeScript for the user interface, and TypeScript for backend processing and serial communication.
- Created an embedded system that enables real-time data transfer between ATMEGA microcontrollers and the web app, allowing for responsive user interactions.
- Implemented PCB design principles and serial communication protocols to integrate hardware components smoothly.
- Leveraged a combination of software and embedded systems design to create a reliable, user-friendly experience.
- for more information on this project, docs.parleii.com/about/aboutus

Skills

Programming: Python | PHP | MySQL | HTML5 | Java | JavaScript | TypeScript | C++ | C# | C | React | NodeJS | Next.js |

Technical: Microcontrollers | PCB Design | Circuit Design | Embedded Systems | Phone repair | PC Building/Repair |

Software: Linux | Windows | MS Office | SolidWorks | Unity | KiCad | EasyEDA | GIT |

Soft Skills: Verbal/Written Communication | Time management | Planning | Organization | Customer service | Presentation |