

ECE271, Final Project

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November 23, 2019

Contents

| | | |
|----------|--------------------------------|----------|
| 1 | Introduction | 2 |
| 2 | High Level Descriptions | 2 |
| 3 | Controller Descriptions | 2 |
| 4 | HDL Components | 2 |
| 5 | Appendix | 2 |

1 Introduction

The purpose of this project is to create a digital logic design that uses an NES controller to control various kinds of output through an FPGA.

2 High Level Descriptions

3 Controller Descriptions

4 HDL Components

5 Appendix