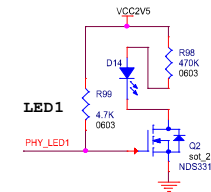
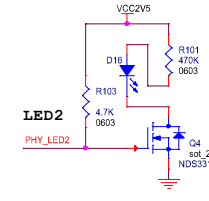
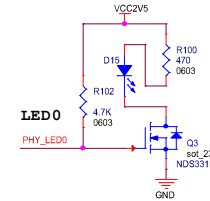
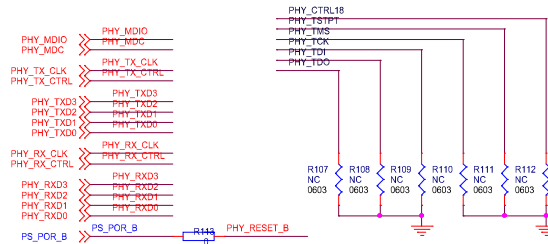
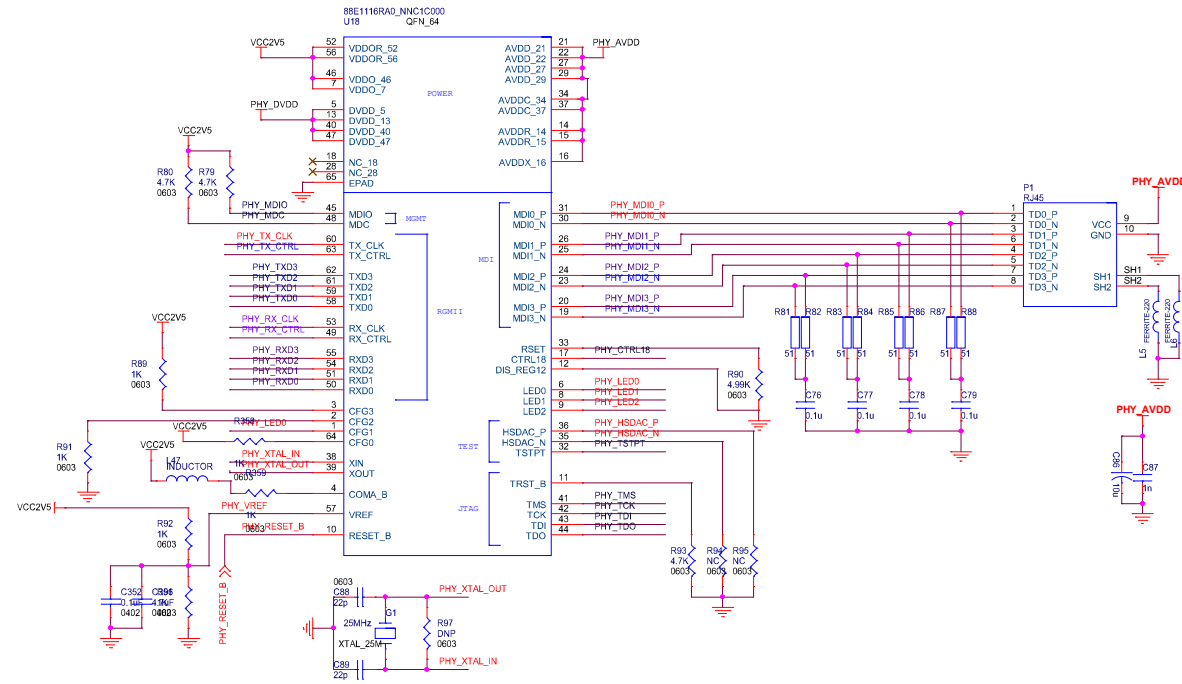
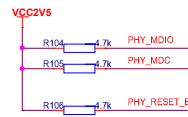
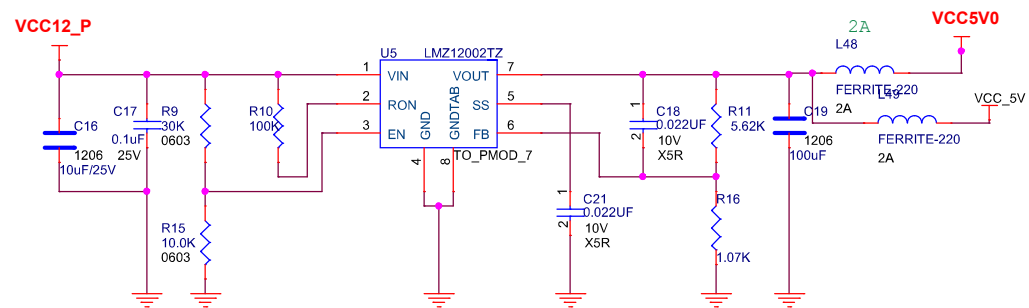
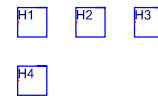
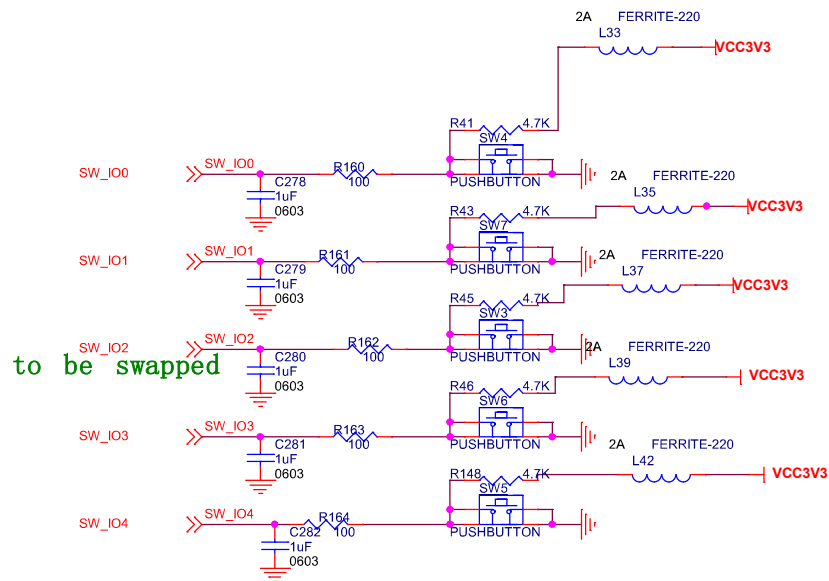
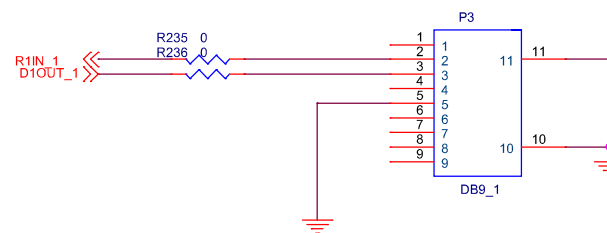
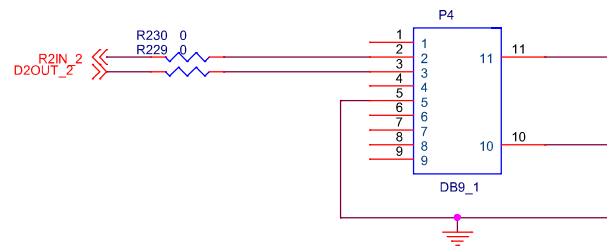
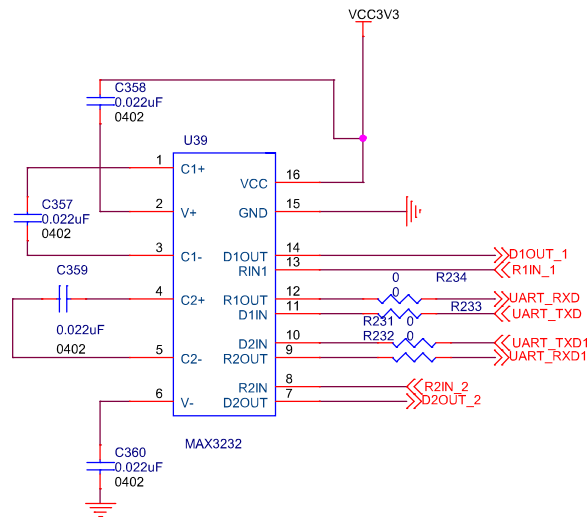


PIN	SETTING	CONFIGURATION	
CONFIG0	VCCO_MIO1	PHYAD [1]=1	PHYAD [0]=1
CONFIG1	EPHY_LED0	PHYAD [3]=0	PHYAD [2]=1
CONFIG2	GND	ENA_XC=0	PHYAD [4]=0
	EPHY_LED0	ENA_XC=0	PHYAD [4]=1
	VCCO_MIO1	ENA_XC=1	PHYAD [4]=1
CONFIG3	GND	RGMII_TX=0	RGMII_RX=0
	EPHY_LED0	RGMII_TX=0	RGMII_RX=1
	EPHY_LED1	RGMII_TX=1	RGMII_RX=0
	VCCO_MIO1	RGMII_TX=1	RGMII_RX=1

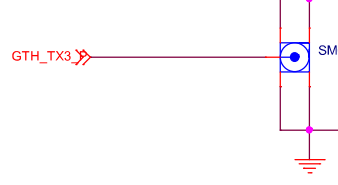
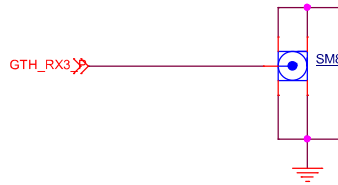
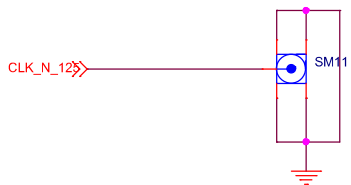
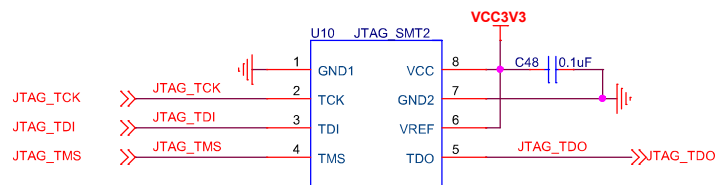
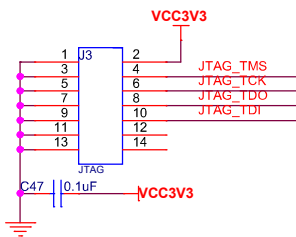


Title <Title>			
Size C	Document Number <Doc>		Rev <Rev Code>
Date: Friday, November 27, 2020	Sheet	1 of 1	





Title			<Title>
Size	Document Number	Rev	<RevCode>
B	<Doc>		
Date:	Tuesday, December 15, 2020	Sheet	1 of 1



Title			<Title>
Size	Document Number	Rev	<RevCode>
B	<Doc>		
Date:	Tuesday, December 15, 2020	Sheet	1 of 1

