

Grayson Hollaway

<https://graysonhollaway.github.io/>

OBJECTIVE

An electrical or computer engineering internship, gaining experience in electrical design or software design; open to relocation.

EDUCATION

The Ohio State University, Columbus, OH
B.S. ECE, Computer Engineering Focus

Expected Graduation: Dec. 2022
Overall GPA: 3.736 (4.00 scale)

The Ohio State University, Columbus, OH
M.S. ECE, Computer Engineering Focus

Expected Graduation: Dec. 2023

ENGINEERING CO-OP/INTERNSHIP EXPERIENCE

Lutron Electronics, Coopersburg, PA

RF Engineer Co-op, May - August 2022

- Conducted RF new product testing for redesign of a product due to part shortages (Sub-GHz)
- Measured antenna impedance using Vector Network Analyzer
- Matched antenna impedance to the 50 Ohm point on the Smith chart using discrete SMT components
- Tested antenna gain in an anechoic chamber using a spectrum analyzer
- Calculated allowable path loss with Friis Equation
- Automated RSSI test for Wake on Radio using Python to send SCPI commands to a signal generator and receiving data from the SoC's RTT buffer via J-Link

Vertiv Co., Delaware, OH

Electrical Compliance Intern, May - August 2021

- Developed a mobile application that calculates creepage and clearance distances for design engineers
- Refined programming skills in C# and XAML using Xamarin Forms to create the creepage/clearance application
- Created and deployed a MySQL database to the cloud via web service for login verification and data storage
- Studied electrical safety standards for UPS's and STS's, mainly UL62368 and IEC60950
- Streamlined form submission and user experience on team SharePoint site using Power Automate

RELEVANT SKILLS

- **Programming:** Verilog HDL, C, C++, C#, Java, Python, MATLAB, SQL, and Git version control
- **Hardware:** SOLIDWORKS, AutoCAD, FPGAs, oscilloscopes, function generators, VNAs, spectrum analyzers and digital design
- **Technical Communication:** Writing, presenting, and speaking

PERSONAL PROJECTS

- **Mortarboard Decorator Finite State Machine:** Custom PCB used as a decoration on a graduation cap
- **UART Implementation in Verilog for GoBoard FPGA:** 8-bit 115200 Baud rate serial communication protocol using 2 finite state machines
- **Discrete Fourier Transform Direct Computation in C:** Script that calculates Fourier coefficients