# Modeling Structured vs Unstructured and Dual vs Single Sided Sparsity in DNNs

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### Motivation:

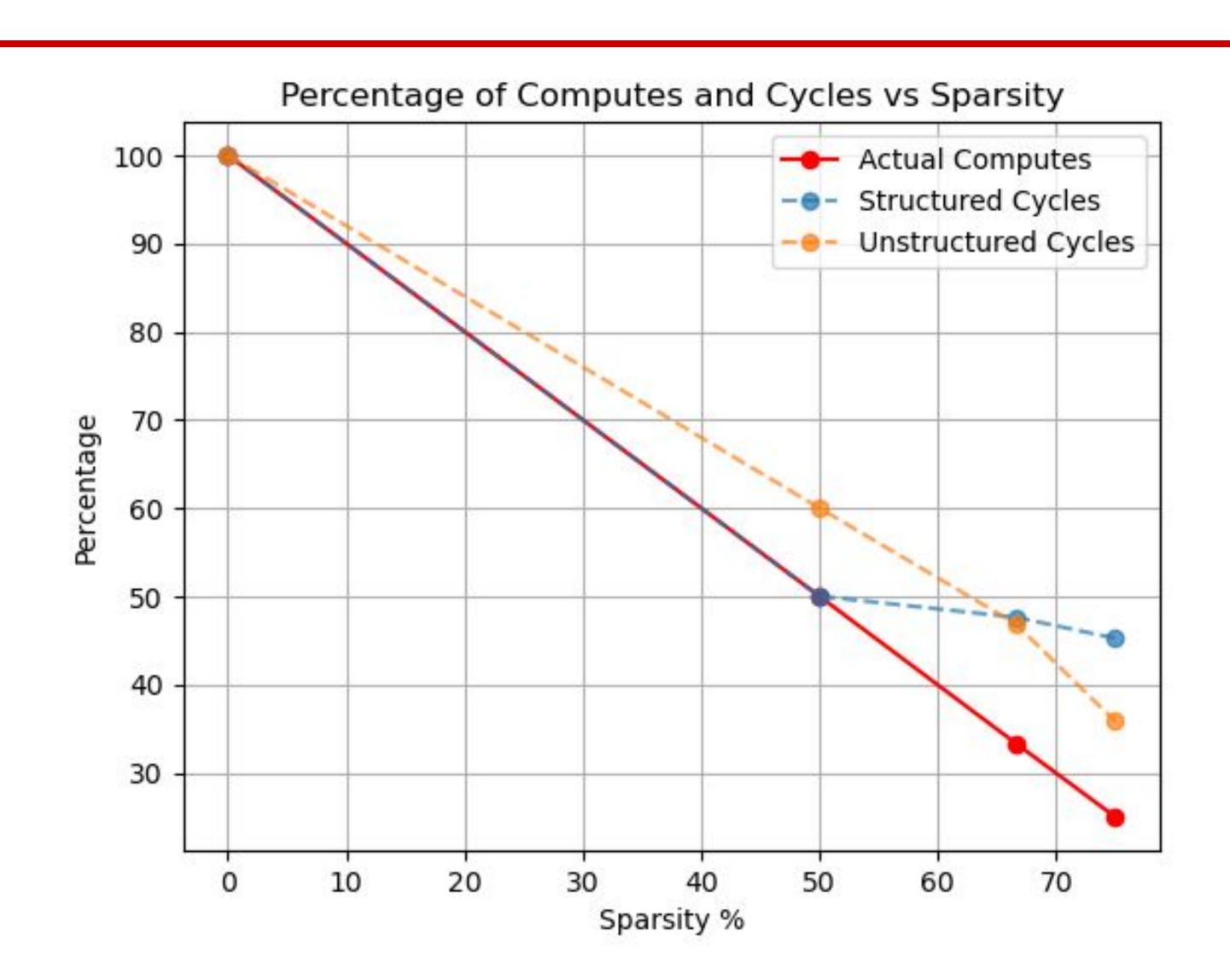
 Many approaches to exploit sparsity to save energy and reduce computation:

	Unstructured	Structured
Single -Sided	<ul> <li>Skip weights only</li> <li>Random pattern</li> <li>Flexible but higher overhead</li> <li>Example: Deep Compression (Han et al.)</li> </ul>	<ul> <li>Skip weights only</li> <li>Structured pattern (e.g., 2:4)</li> <li>Hardware-friendly, low overhead</li> <li>Example: Nvidia STC</li> </ul>
Dual- Sided	<ul> <li>Skip weights + activations</li> <li>Random pattern in both</li> <li>Maximum flexibility, high overhead</li> <li>Example: Dual-Side Sparse Tensor Core</li> </ul>	<ul> <li>Skip weights + activations</li> <li>Regular pattern in both</li> <li>High savings, more complex</li> <li>Example: Structured DSTC extension</li> </ul>

**Goal:** Identify which type yields best performance under various workflows

## Structured v.s. Unstructured:

- Structured outperforms unstructured in energy across most cases.
- Structured ~2× energy savings vs. unstructured up to ~70% sparsity.
- Unstructured needs ≥75% sparsity to beat baseline (single-sided).
- Dual-sided unstructured requires ~40% sparsity to be effective.
- At extreme sparsity, both converge in performance.
- Both skip the same number of computations.
- Unstructured faster in cycles at sparsity ≥66%.

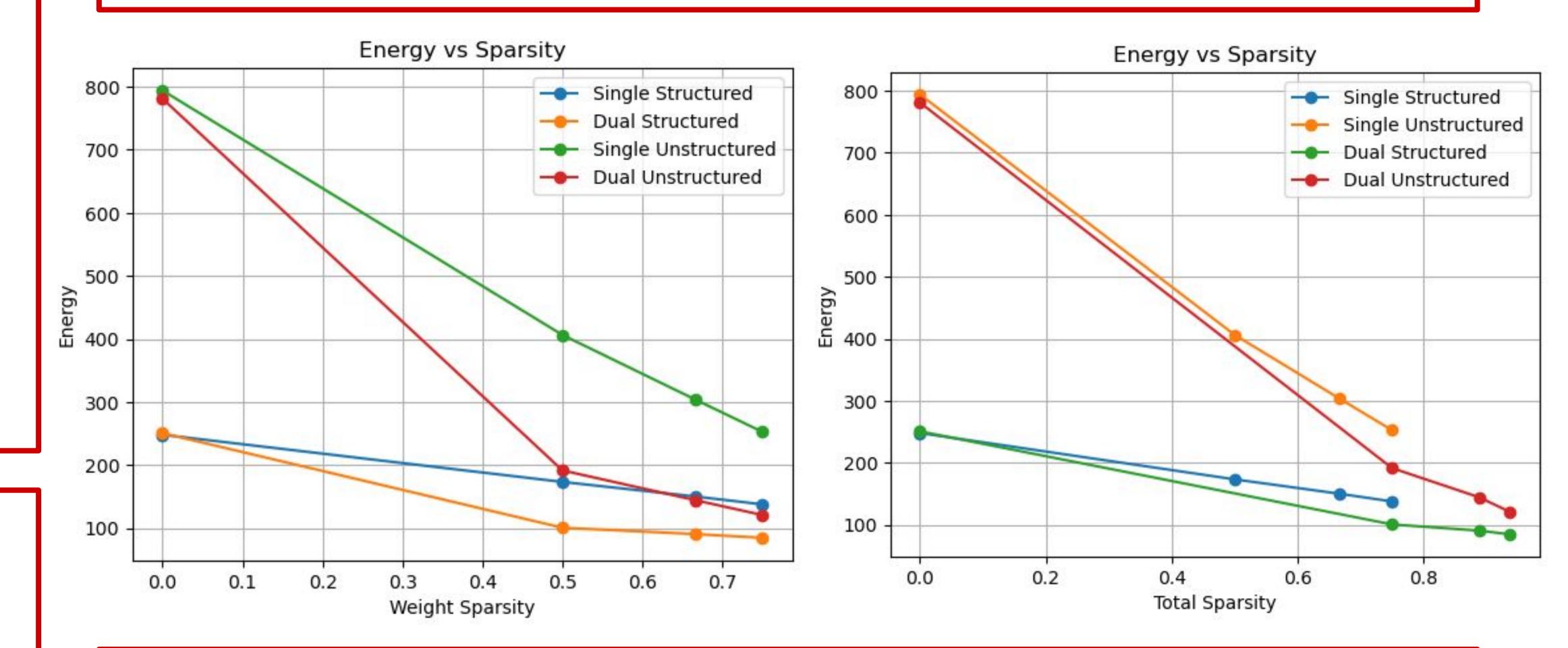


#### References:

- 1. Song Han et al., "Deep Compression," arXiv, 2016.
- Yang Wang et al., "Dual-Side Sparse Tensor Core," arXiv, 2021.
   Jeff Pool, "Accelerating Sparsity in the Nvidia Ampere Architecture," Nvidia, 2021.

## Experimental Setup:

- Simulated ResNet 50 on:
  - o DRAM, local DRAM buffer (4 cores), SRAM, MAC units.
  - 8-bit precision.
- Sparsities tested:
  - o [0%, 50%, 66.66%, 75%] sparse weights and activations
- Measured energy, cycles, and computations

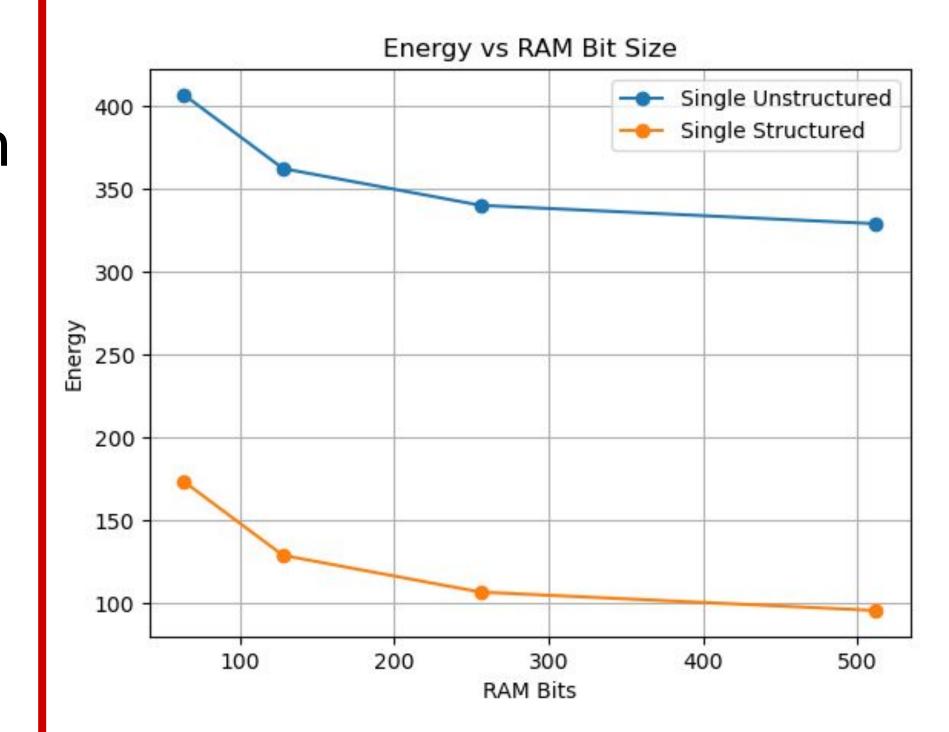


## Dual-sided v.s. Single-Sided:

- Dual-sided generally outperforms single-sided.
- Dual-sided overhead dominates at low sparsity, especially for unstructured.
- After adjusting for combined input + weight sparsity, dual-sided still leads but advantage shrinks.

## Memory Impact:

- Increasing RAM reduces energy for both structured and unstructured equally.
- Increasing only metadata RAM has no energy/cycle effect.
- Increasing register file meta-data storage capacity decreases energy usage by a nearly insignificant amount.



#### Limitations:

- Structured pruning (e.g., 2:4, 2:6) requires heavy preprocessing.
- Weight pruning is a one-time cost; input pruning incurs per-inference cost.
- Structured pruning can hurt accuracy.
- In practice, unstructured sparsity's flexibility may outweigh its hardware overhead.