

Circuit diagrams Fig.5-5-12 ECM, DRM, Check Connector PA029 0.5 R EuroIV HB012A 0.5 B/Y HB002Å^{HD} 0.5 B/Y EuroIV+LHD IA001 0.5 R FUSE_RELAY_BLOCK \(\int_{27}^{27}\) ROOM_LAMP CONN; CHECK HB012 0.5 B/Y INST_TO_FRAME_1_YSJ FUSE_RELAY_BLOCK Λ^{47} CA001 0.5 G/Y CA002B 0.5 G IC127 0.5 B/Y FUSE_RELAY_BLOCK 122 4 TF001B 0.5 LA030 0.5 B/W FUSE_RELAY_BLOCK 1 15 FUSE_RELAY_BLOCK \$\int_43\$ ICOO1A 0.5 EuroIV+LHD IZ033 1.25 EuroIV RELAY; STARTER_CUT IC003A 0.5 L/R RELAY; ECM_MAIN RELAY_BOX FUSE; ECM_B1 20A IC124A 0.85 R/L IC027 0.5 R/B EuroIV IC227 0.5 R IC001A 0.5 B/Y 1 + <u>_</u> HB001A 5.0 HB001E 5.0