

Base Addr			
0x4009_8000			
0x00	CTRL		Control Register
0	CRCEN		1: Enable
1	DATAINV		0: LSB first, 1: MSB first
2	DMAEN		1: Enable req to DMA
3..4	DLSIZE		Load Data Size
			b00 - Byte, CRC_DATAI[7:0]
			b01 - 16bit, CRC_DATAI[15:0]
			b10 - 32bit, CRC_DATAI[31:0]
5..6	DCSIZE		Calc Data Size
			b00 - Byte, DLSIZE = [0,1,3]
			b01 - 16bit, DLSIZE = [1,3]
			b10 - 32bit, DLSIZE = [3]
	0x00000000		ResetValue
	0x0000003F		MaskValue
0x04	STAT		Status Register
0	CONVCOMP	RO	0: Calculating 1: Completed & FIFO is Empty
1	FIFOFULL	RO	1: Full (4 word)
2	FIFOEMPTY	RO	1: Empty
3	FIFOOVER	RW	1: Write to Full FIFO ocured. Clear by WR 1.
	0x00000000		ResetValue
	0x0000000F		MaskValue
0x08	DATAI		Input Data
0..31	DATA_IN	WO	FIFO Input
	0x00000000		ResetValue
	0xFFFFFFFF		MaskValue
0x0C	CRC_VAL		Output Data
0..15	CRCOUT		Result/StartValue
	0x00000000		ResetValue
	0x0000FFFF		MaskValue
0x10	CRC_POL		Input Data
0..16	CRC_POL		Polinom Field Select, b0=b16=1 always!
	0x00000000		ResetValue
	0x0001FFFF		MaskValue