

<b>0x40038000</b>	<b>BaseAddr</b>	
<b>0x00</b>	<b>CR</b>	<b>Control Register 0</b>
0..1	M	0 - Static, Mux: 1/2, 1/3, 1/4
2	B	Bias: 0 - 1/3, 1 - 1/2
3	E	Enable
4..5	BF	Мерцание: 0 - Off, 1 - 2Hz, 2 - 1Hz, 3 - 0,5Hz
6	A	Alter мерцание: 1 - from alt mem, 0 - by BF
7	VLCDON	Power select: 0 - Int V_LCD, 1 - External V_LCD
8..23	DIV	F_frame = 32768Hz/DIV*Mux - ????
24	CLK_SEL	Clock: 0 - LSE, 1 - LSI
0x00FFFFFF		
<b>0x04</b>	<b>ROW1</b>	BP0 - DataString base mem [29..0]
<b>0x08</b>	<b>ROW2</b>	BP1 - DataString base mem
<b>0x0C</b>	<b>ROW3</b>	BP2 - DataString base mem
<b>0x10</b>	<b>ROW4</b>	BP3 - DataString base mem
<b>0x14</b>	<b>AROW1</b>	BP0 - DataString alter mem
<b>0x18</b>	<b>AROW2</b>	BP1 - DataString alter mem
<b>0x1C</b>	<b>AROW3</b>	BP2 - DataString alter mem
<b>0x20</b>	<b>AROW4</b>	BP3 - DataString alter mem