

	Regs	Access	BE1	BE3	BE9x	BL1901	BE4	BK214	BE234
Base Addr	PVDCS	RW	0x4005_8000	0x4005_8000	0x4005_8000	0x4005_8000	0x4005_8000	0x4005_8000	0x4005_8000

0x00	PVDCS	Contorl-Status Register				
0	PVDEN	1: Enable Ucc (Ena All for VE9x, VC1)				
1..2	PBLS	0: 1,8V 2,2V 2,6V 3.0V - BUcc Level				
3..5	PLS	2,0 2,2 2,4 2,6 2,8 3,0 3,2 3,4 V - Ucc Level				
6	PVBD	1: Event BUcc > PBLS				
7	PVD	1: Event Ucc > PLS				
8	IEPVBD	1: Ena Irq by PVBD event				
9	IEPVD	1: Ena Irq by PVD event				
10	INVB	1: Inverse PVDB event				
11	INV	1: Inverse PVD event				
12	PVDBEN	1: Enable Bucc detector (Reserved in VE9x, VC1)				
	0x00000000	ResetValue				
	0x00001FFF	MaskValue				
	0x00000FFF					