Addr Shift	Regs		BE1	BE3	BE9x	BE4	BK214	BE234	ВЦ1901	
0x00	EEPROM_CMD	13..0 / rw	+	+	+	+	+	+	+	Control
0x04	EEPROM_ADDR	32 bits / rw	+	+	+	+	+	+	+	Address, [1..0] - unused, 32 bit only
0x08	EEPROM_DI	32 bits / rw	+	+	+	+	+	+	+	Write data
0x0C	EEPROM_DO	32 bits / rw	+	+	+	+	+	+	+	Read data
0x10	EEPROM_KEY	32 bits / rw	+	+	+	+	+	+	+	Unlock key = 0x8AAA5551
			0x40018000	0x40018000	0x40018000	0x40018000	0x40018000	0x40018000	0x40018000	
Bits	Fields	Access	BE1	BE3	BE9x	BE4	BK214	BE234	ВЦ1901	
x00	EEPROM_CMD									
0	CON	rw	+	+	+	+	+	+	+	0 - Norm, 1 - ProgMode
1	WR	rw	+	+	+	+	+	+	+	1 - write
2	RD	rw	+	+	+	+	+	+	+	1 - read
5..3	Delay	rw	b100	b100	b100	-	-	-	b100	0..7 - Mem access delay cycles, 125MHz
3	Delay	rw	-	-	-	bxx1	bxx1	bxx1		0 - le 18MHz, 1 - le 36MHz
6	XE	rw	+	+	+	+	+	+	+	1 - ena ADDR[16:9]
7	YE	rw	+	+	+	+	+	+	+	1 - ena ADDR[8:2]
8	SE	rw	+	+	+	+	+	+	+	1 - RD amplifier ON
9	IFREN	rw	+	+	+	+	+	+	+	0 - Base mem, 1 - Info Mem
10	ERASE	rw	+	+	+	+	+	+	+	1 - Erase string Addr[16:0]
11	MAS1	rw	+	+	+	+	+	+	+	1 - Erase whole block
12	PROG	rw	+	+	+	+	+	+	+	1 - Prog Addr[16:2] = EEROM_DI
13	NVSTR	rw	+	+	+	+	+	+	+	0 - for read, 1 - for Prog and Erase
			0x20	0x20	0x20	0x8	0x8	0x8	0x20	ResetValye
			0x3FF	0x3FF	0x3FF	0x3FF	0x3FF	0x3FF	0x3FF	MaskValue

	Main			Info		
EEPROM	BE1,BE3,BE9x, VC1	BE4	BK214, BK234	BE1,BE3,BE9x, VC1	BE4	BK214, BK234
TotalSize	128KB	128KB	64KB	4KB	8KB	4KB
Sectors	4	-	-	4	-	-
PageSize	4KB	512B	512B	4KB	512B	512B
PageCount	16	256	128	1	16	8
Blocks	-	4	2	-	4	2
BlockSize	-	32KB	32KB	-	2KB	2KB
Page in Block	-	64	64	-	4	4
RAM Size	32KB	16KB	16KB			
Block Addr		0x0_0000	0x0_0000		0x0000	0x0000
		0x0_8000	0x0_8000		0x0800	0x0800
		0x1_0000	0x1_0000		0x1000	0x1000
		0x1_8000			0x1800	
		0x2_0000			0x2000	