
Worksheet 5 - Logic Circuits

1. AND, OR, XOR, NOT

2. AND

INPUT	INPUT	OUTPUT
0	0	0
0	1	0
1	0	0
1	1	1

OR

INPUT	INPUT	OUTPUT
0	0	0
0	1	1
1	0	1
1	1	1

XOR

INPUT	INPUT	OUTPUT
0	0	0
0	1	1
1	0	1
1	1	0

NOT

True → False False → True

AB	CD	Output
0	0	0
0	1	1
1	0	1
1	1	1

- 3.
4. False. It uses a hardware description language or as a schematic.
5. True.
6. False. We have studied SR, D, JK, and T flip flops.
7. True.
8. True. *add this to our notes of flip flops*
9. True.
10. False. The inputs for a SR flip flop are S and R
11. True,
12. False. There is only one input and the clock
13. True.
14. False. It will go high.
15. True.

External Links

[Conversion of Flip Flops](#)