## **Assignment 3**

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In the previous assignment we initially set the RSA\_NUMBER to a constant to improve the speed of all threads. We encountered a warp scheduling problem where the more blocks we added the slower the program performed. The following shows the increase in calculation time when the number of blocks is set to 655350 and the threads are 1024.

```
Brute Factor
Name: Test 20
Digits: 20
Digit Sum: 62
16522060140317034287 = 2832673253 X 5832674179

Calculation time = 9433
Enter File Name :
```

Fig 1 Warp causes increase in calculation time

This can also be verified by checking the occupancy calculator for the "Active Thread Blocks per Multiprocessor" field. It states that for the current device there are 4 ATBM. This means that at any time the maximum simultaneous threads that can be scheduled is (threads per warp) 32×4 (ATBM) = 128.

```
/oid BruteFactor::getPrimesCuda(){
  bool foundprime = false;
  cudaError(cudaMemcpyToSymbol(d_RSA_NUMBER, &RSA_NUMBER, sizeof(__int64), 0, cudaMemcpyHostToDevice));
   unsigned __int64 *d_a, *d_b;
   cudaError(cudaMalloc((void**)&d_a, sizeof(__int64)));
   cudaError(cudaMalloc((void**)&d_b, sizeof(__int64)));
   start = chrono::high resolution clock::now();
   int blks = (RSA NUMBER / NTPB) < 65535 ? (RSA NUMBER / NTPB) : 65535;</pre>
   while (!foundprime && i < RSA_NUMBER){</pre>
      unsigned __int64 h_a = i;
      cudaGetPrimes << <blks, NTPB >> >(d_a, d_b, i);
       cudaError(cudaMemcpy(&p1, d_a, sizeof(__int64), cudaMemcpyDeviceToHost));
      cudaError(cudaMemcpy(&p2, d_b, sizeof(__int64), cudaMemcpyDeviceToHost));
       if (p1 != 0 && p2 != 0)
           foundprime = true;
       i = i + blks * NTPB;
   cudaError(cudaFree(d_a));
   cudaError(cudaFree(d_b));
   finish = chrono::high_resolution_clock::now();
   totalduration = chrono::duration_cast<chrono::milliseconds>(finish - start).count();
```

Fig Updated Kernel

Slight improvements were made to the way the program calculates the number of blocks. This increased performance by \_\_\_ because it minimized the number of kernel calls needed to factorize the number. This only benefited numbers with 17 or more digits. Also the starting number constant from the previous assignment was removed as we later realised this gave no real benefit and actual caused some decreases in performance.

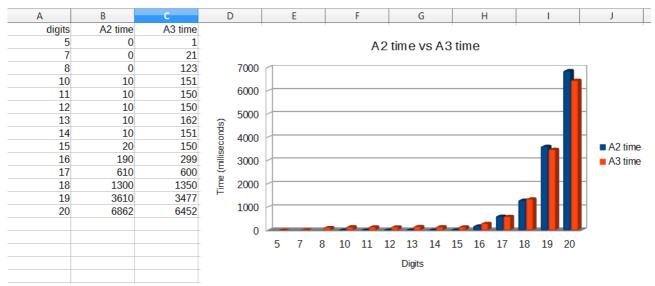


Fig 3 Slight improvements for lager digit numbers

For these results the block size was calculated using RSA\_NUMBER / 1024 to get the blocks to be used. If the result was lager than 65535 the default value of 65535 would be used instead of the static block size of 1500 used in the previous assignment.