## **ASIC**

## **FPGA**

EXPENSIVE
FAST, MULTI-TASKING
MODERATE POWER
LOW-RISK DEVELOPMENT

## MICRO-CONTROLLER

LOWEST COST (HIGH VOLUME) HIGHEST SPEED LOWEST POWER SINGLE PURPOSE, INFLEXIBLE LOW COST (HIGH VOLUME)
SOFTWARE, OS
MODERATE POWER
GENERAL PURPOSE COMPUTE
VIRTUAL MULTI-TASKING
SLOWEST FOR SPECIFIC TASK