LLVM-IR

```
%15 = load i32, i32* %nian, align 4, !tbaa !1
%rem38 = and i32 %15, 3
%rem11 = srem i32 %15, 100
```

Tokenized

5000
vocabulary indices,
padded & truncated

Embeddings

5000 x 64
embedding vectors

Grouped Embeddings

num_statements x 64
Grouped embedding vectors

Auxiliary Inputs

num_segments
1-hot "selector" vector

Model inputs

num_segments x (64+2)
concatenated embedding
and selector vectors

LSTM

Fully-connected

Model outputs

num_segments x 2
1-hot outputs

t_1 t_2 t_3 $t_{num\_segments}$