

Defines and Type Definitions

Nested Vectored Interrupt
Controller (NVIC)

```
graph LR; A[Nested Vectored Interrupt Controller (NVIC)] --> B[Defines and Type Definitions]
```

The diagram consists of two rectangular boxes. The box on the right is shaded gray and contains the text 'Nested Vectored Interrupt Controller (NVIC)'. The box on the left is white with a black border and contains the text 'Defines and Type Definitions'. A black arrow points from the right side of the gray box to the left side of the white box, indicating a dependency or relationship between the two components.