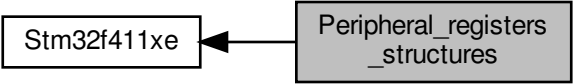


Stm32f411xe

Peripheral_registers
_structures



```
graph LR; A[Peripheral_registers_structures] --> B[Stm32f411xe];
```

The diagram consists of two rectangular boxes. The box on the left is white with a black border and contains the text 'Stm32f411xe'. The box on the right is gray with a black border and contains the text 'Peripheral_registers' on the top line and '_structures' on the bottom line. A black arrow points from the right side of the gray box to the left side of the white box, indicating a relationship or dependency from the peripheral registers/structures to the Stm32f411xe component.