



#### 40V 175°C N-CHANNEL ENHANCEMENT MODE MOSFET

### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>C</sub> = +25°C
40V	$7.3 \text{m}\Omega$ @ $V_{GS} = 10V$	70A
	9.8mΩ @ V <sub>GS</sub> = 4.5V	44A

### **Features**

- Low R<sub>DS(ON)</sub> Ensures On State Losses are Minimized
- Excellent Q<sub>gd</sub> x R<sub>DS(ON)</sub> Product (FOM)
- Advanced Technology for DC-DC Converters
- Small Form Factor Thermally Efficient Package Enables Higher Density End Products
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- An Automotive-Compliant Part is Available Under Separate Datasheet (DMTH4007LK3Q)

### **Description and Applications**

This new generation MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- Power Management Functions
- DC-DC Converters
- Backlighting

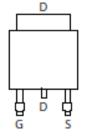
#### **Mechanical Data**

- Case: TO252
- Case Material: Molded Plastic, "Green" Molding Compound.
- UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe;
   Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.33 grams (Approximate)

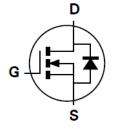




Top View



Pin Out Top View



**Equivalent Circuit** 

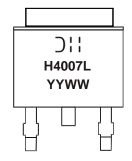
### **Ordering Information** (Note 4)

Part Number	Case	Packaging
DMTH4007LK3-13	TO252	2,500/Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

## **Marking Information**



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### **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	$V_{DSS}$	40	V	
Gate-Source Voltage		V <sub>GSS</sub>	±20	V
Continuous Drain Current, V <sub>GS</sub> = 10V (Note 5)	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	Ι <sub>D</sub>	16.8 13.9	Α
Continuous Drain Current, V <sub>GS</sub> = 10V (Note 6)	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	ΙD	70 51	Α
Maximum Continuous Body Diode Forward Current (Note 6)	Is	80	Α	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	80	Α	
Avalanche Current, L = 0.1mH		I <sub>AS</sub>	20	Α
Avalanche Energy, L = 0.1mH	E <sub>AS</sub>	20	mJ	

### Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	$P_{D}$	2.6	W
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	47	°C/W
Total Power Dissipation (Note 6)	P <sub>D</sub>	59	W
Thermal Resistance, Junction to Case (Note 6)	$R_{\theta JC}$	2.5	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +175	°C

# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	40	-	-	V	$V_{GS} = 0V$ , $I_D = 1mA$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	-	-	1	μA	$V_{DS} = 32V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	-	-	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1	1	3	٧	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance	2	-	5	7.3	mΩ	$V_{GS} = 10V, I_D = 20A$	
Static Diain-Source Off-Resistance	R <sub>DS(ON)</sub>	-	7	9.8		$V_{GS} = 4.5V, I_D = 20A$	
Diode Forward Voltage	V <sub>SD</sub>	-	-	1.2	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 20A	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C <sub>iss</sub>	-	1895	-		$V_{DS} = 30V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Output Capacitance	Coss	-	485	-	pF		
Reverse Transfer Capacitance	$C_{rss}$	-	20.9	-			
Gate Resistance	Rg	-	0.62	-	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Qq	-	12.4	-			
Total Gate Charge (V <sub>GS</sub> = 10V)	Qg	-	29.1	-	~C	V 00V I 00A	
Gate-Source Charge	Q <sub>qs</sub>	-	5.9	-	nC	$V_{DS} = 30V, I_{D} = 20A$	
Gate-Drain Charge	Q <sub>qd</sub>	-	3.5	-			
Turn-On Delay Time	t <sub>D(ON)</sub>	-	5.4	-		$V_{DD} = 30V, V_{GS} = 10V,$ $I_{D} = 20A, R_{G} = 3\Omega$	
Turn-On Rise Time	t <sub>R</sub>	-	4.5	-			
Turn-Off Delay Time	t <sub>D(OFF)</sub>	-	16.2	-	ns		
Turn-Off Fall Time	t <sub>F</sub>	-	3.5	-			
Body Diode Reverse Recovery Time	t <sub>RR</sub>	-	30.6	-	ns	1 004 11/11 4004/	
Body Diode Reverse Recovery Charge	Q <sub>RR</sub>	-	28.1	-	nC	$I_F = 20A$ , di/dt = 100A/ $\mu$ s	

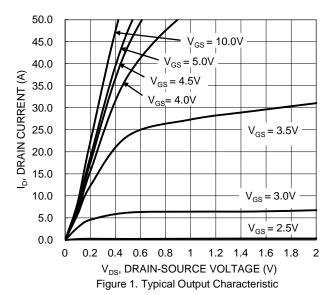
Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

6. Thermal resistance from junction to soldering point (on the exposed drain pad).

North duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.

DMTH4007LK3 Document number: DS37357 Rev. 4 - 2





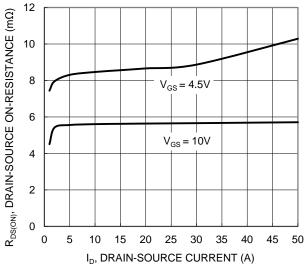


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

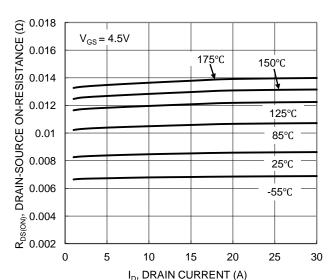


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

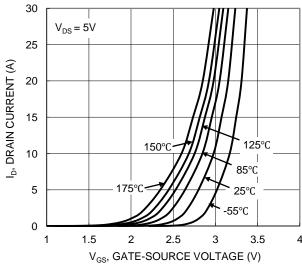


Figure 2. Typical Transfer Characteristic

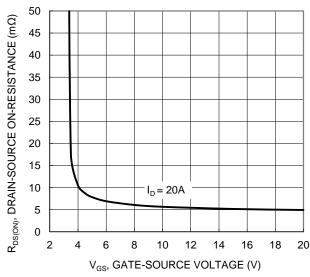


Figure 4. Typical Transfer Characteristic

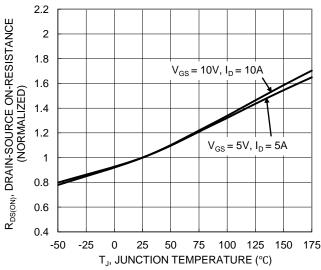


Figure 6. On-Resistance Variation with Temperature



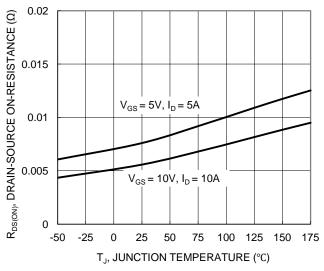


Figure 7. On-Resistance Variation with Temperature

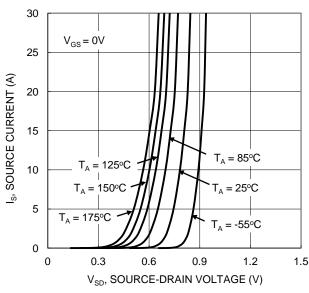


Figure 9. Diode Forward Voltage vs. Current

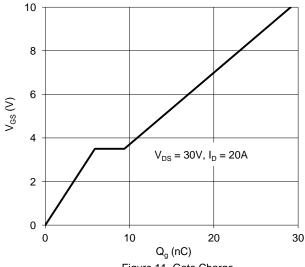


Figure 11. Gate Charge

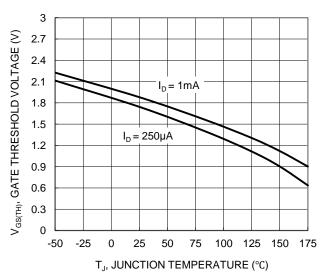
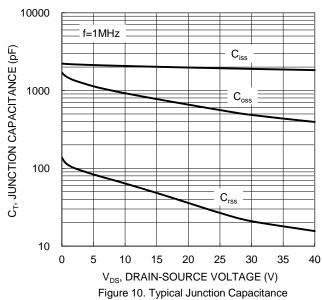


Figure 8. Gate Threshold Variation vs. Junction Temperature



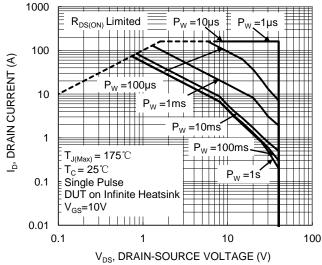


Figure 12. SOA, Safe Operation Area



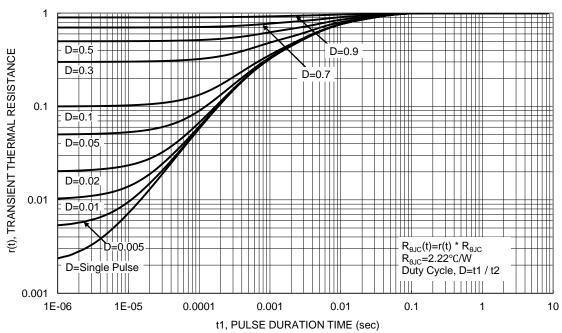
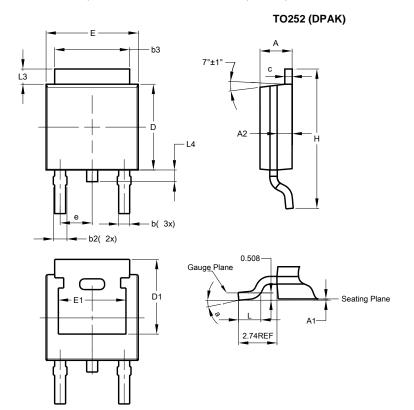


Figure 13. Transient Thermal Resistance



# **Package Outline Dimensions**

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

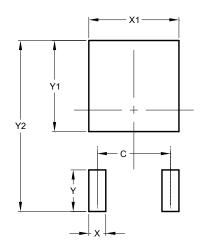


TO252 (DPAK)					
Dim	Min				
		Max	Тур		
Α	2.19	2.39	2.29		
<b>A</b> 1	0.00	0.13	0.08		
<b>A2</b>	0.97	1.17	1.07		
b	0.64	0.88	0.783		
b2	0.76	1.14	0.95		
b3	5.21	5.46	5.33		
С	0.45	0.58	0.531		
D	6.00	6.20	6.10		
D1	5.21	-	-		
е	-	-	2.286		
Е	6.45	6.70	6.58		
E1	4.32	-	-		
Н	9.40	10.41	9.91		
Г	1.40	1.78	1.59		
L3	0.88	1.27	1.08		
L4	0.64	1.02	0.83		
а	0°	10°	-		
All Dimensions in mm					

# **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

#### TO252 (DPAK)



Dimensions	Value (in mm)		
С	4.572		
Х	1.060		
X1	5.632		
Y	2.600		
Y1	5.700		
Y2	10.700		



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