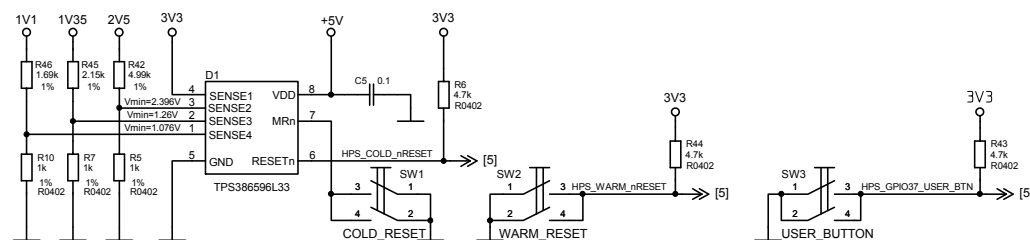
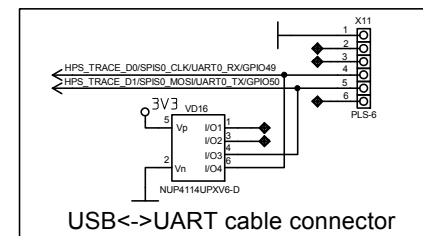
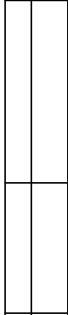


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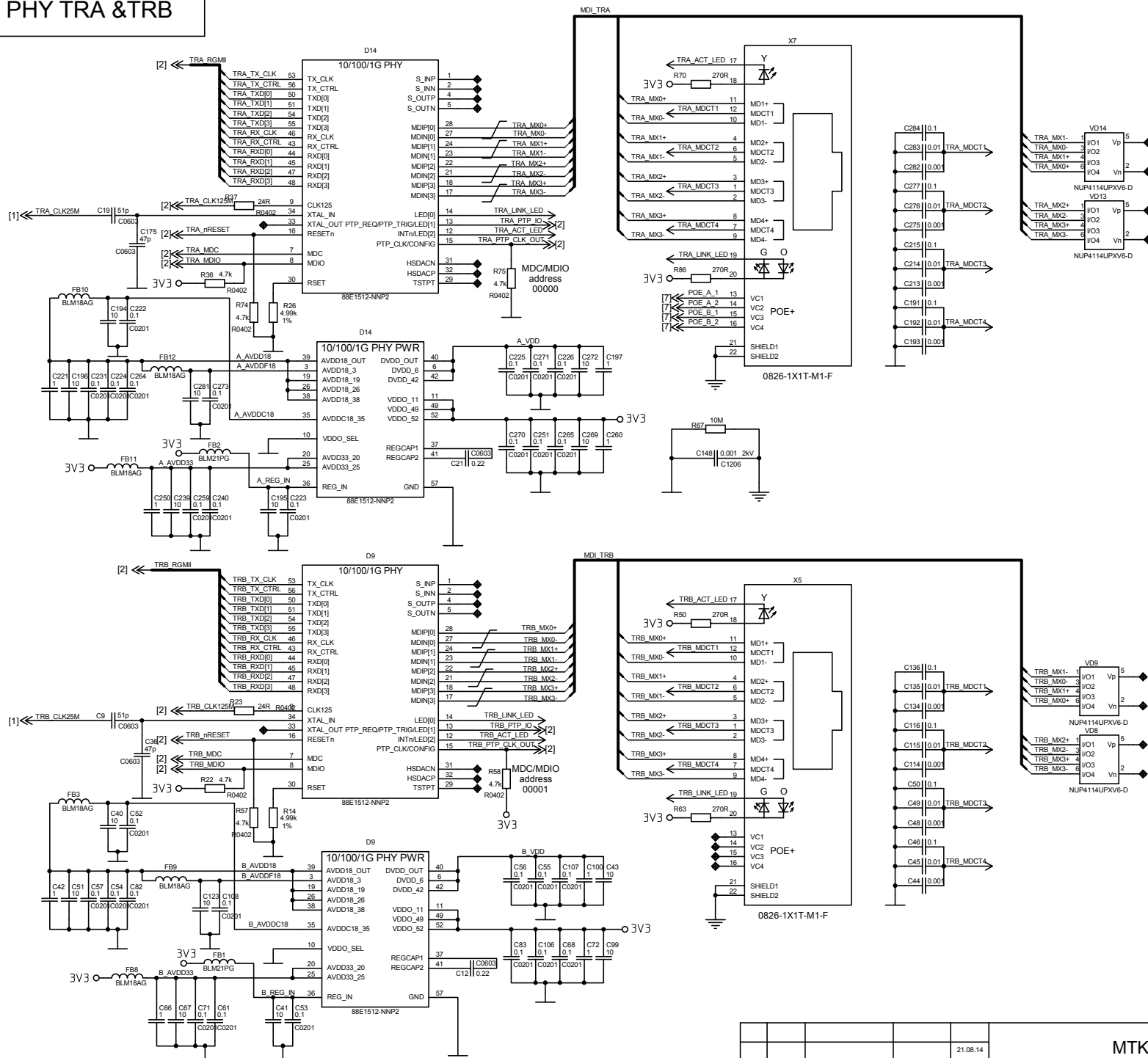


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| | | | | Подп. | Дата | | | | | | | | | | | | |
| Разраб. | | | | | 21.08.14 | | | | | | | | | | | | |
| Пров. | | | | | | | | | | | | | | | | | |
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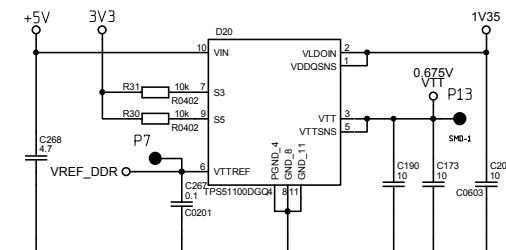
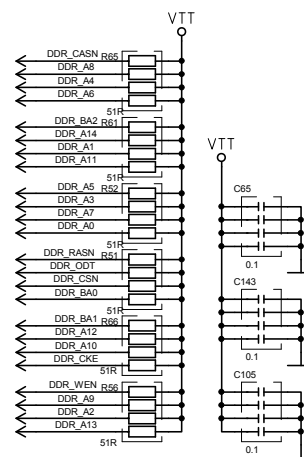
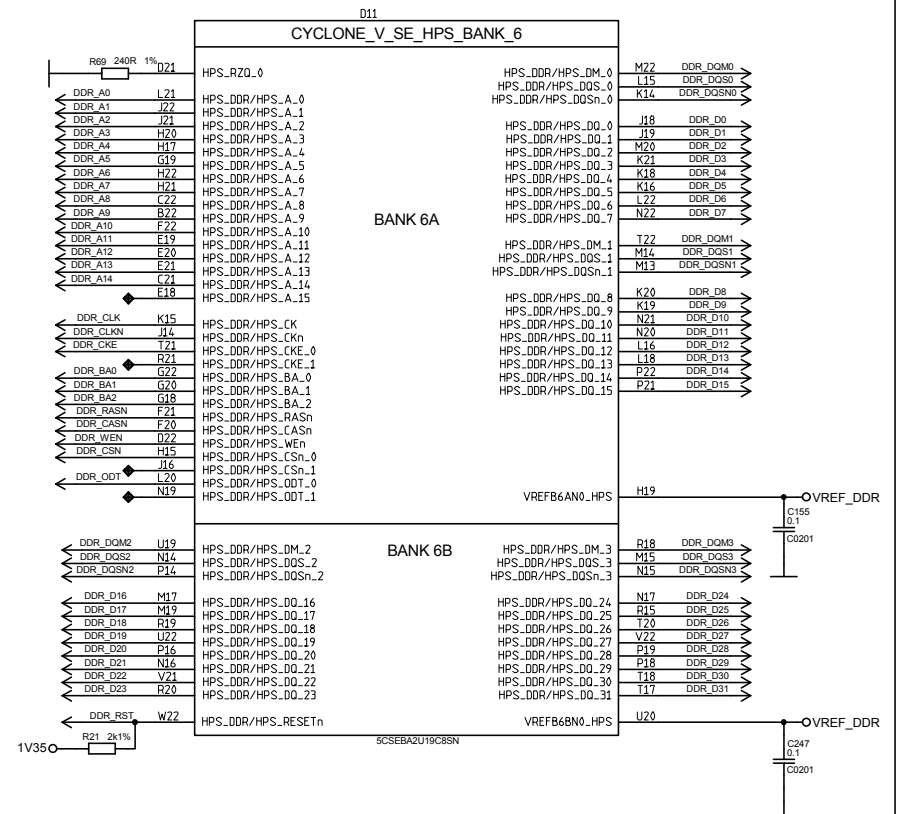
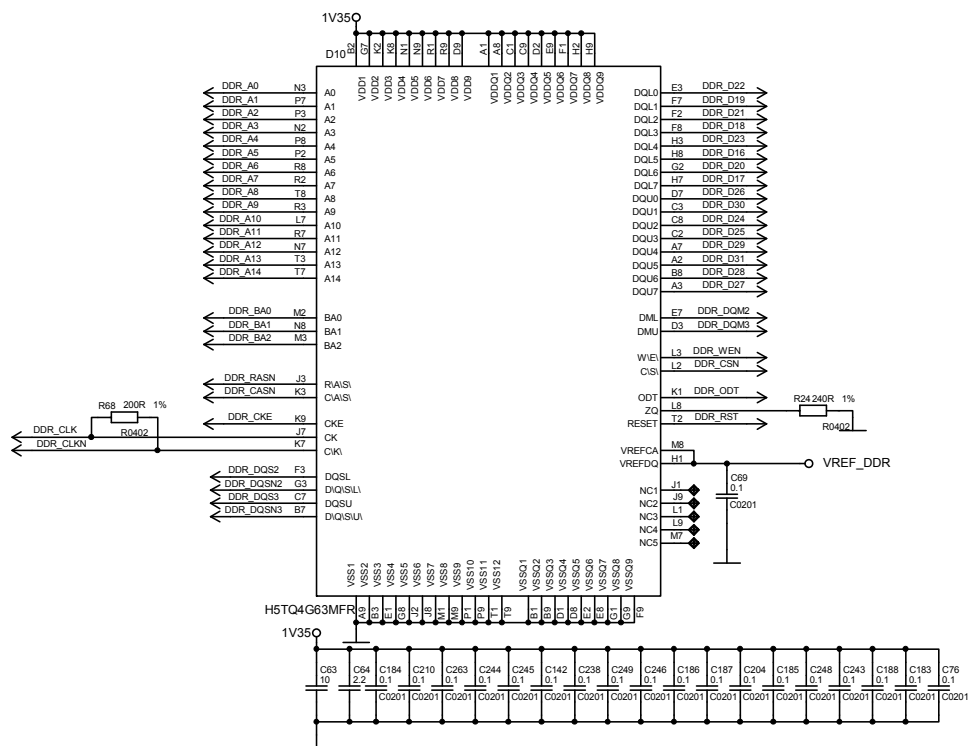
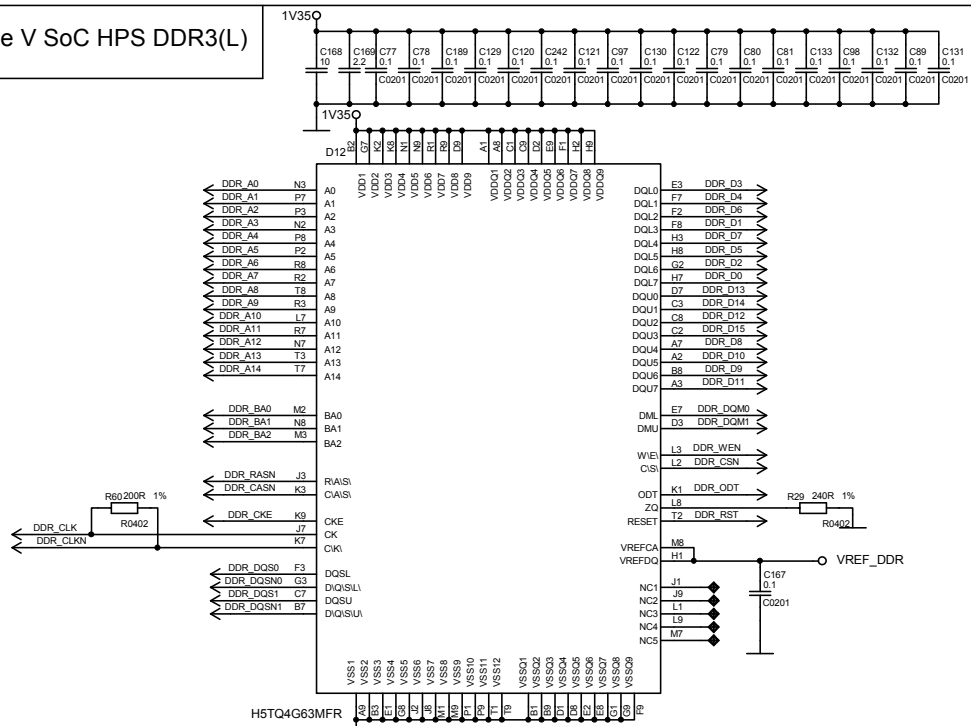
Подп. Дата

21.08.14

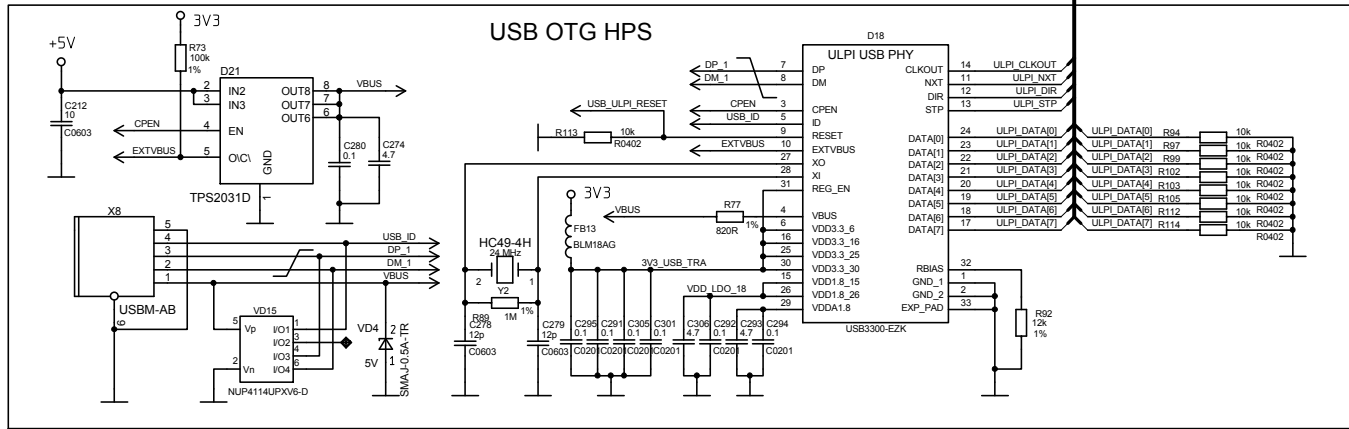
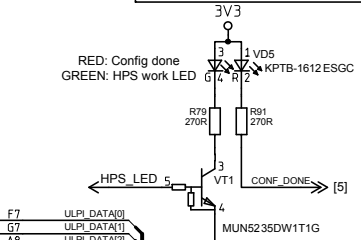
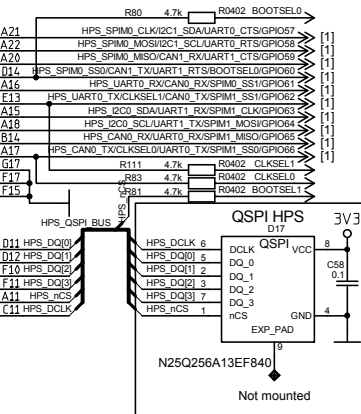
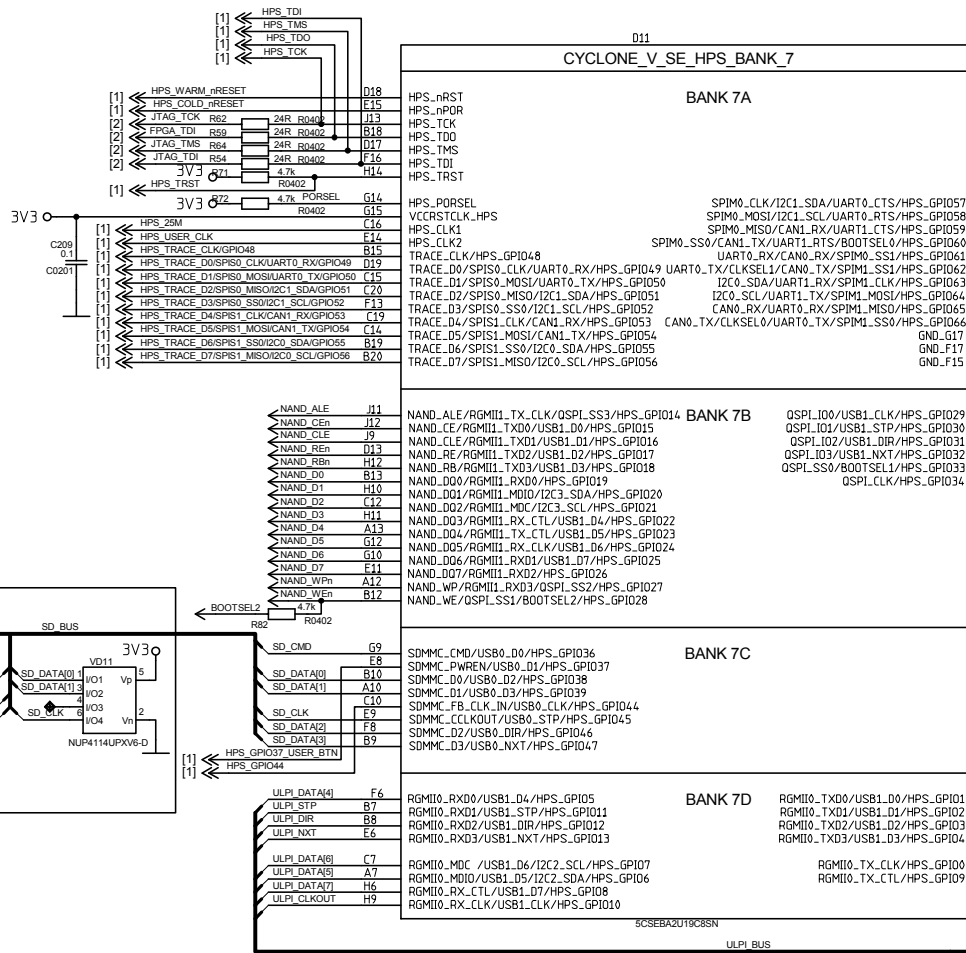
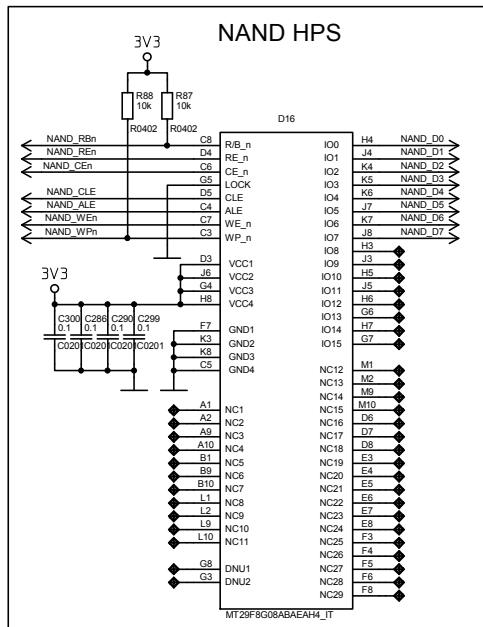
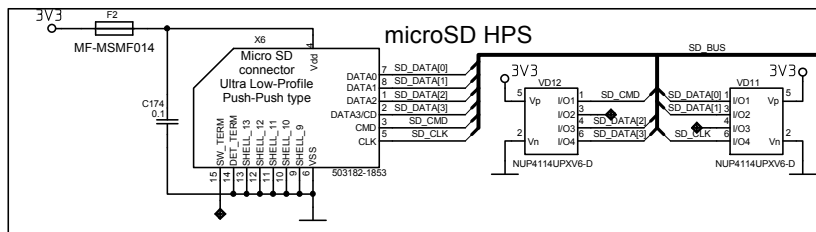
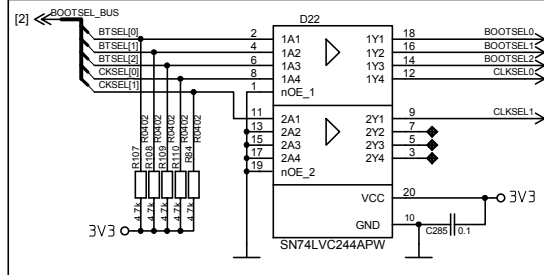
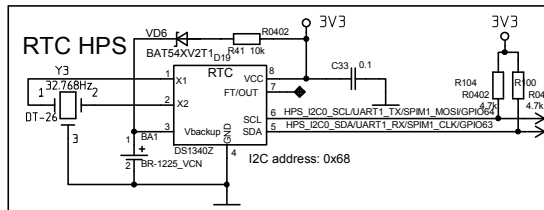
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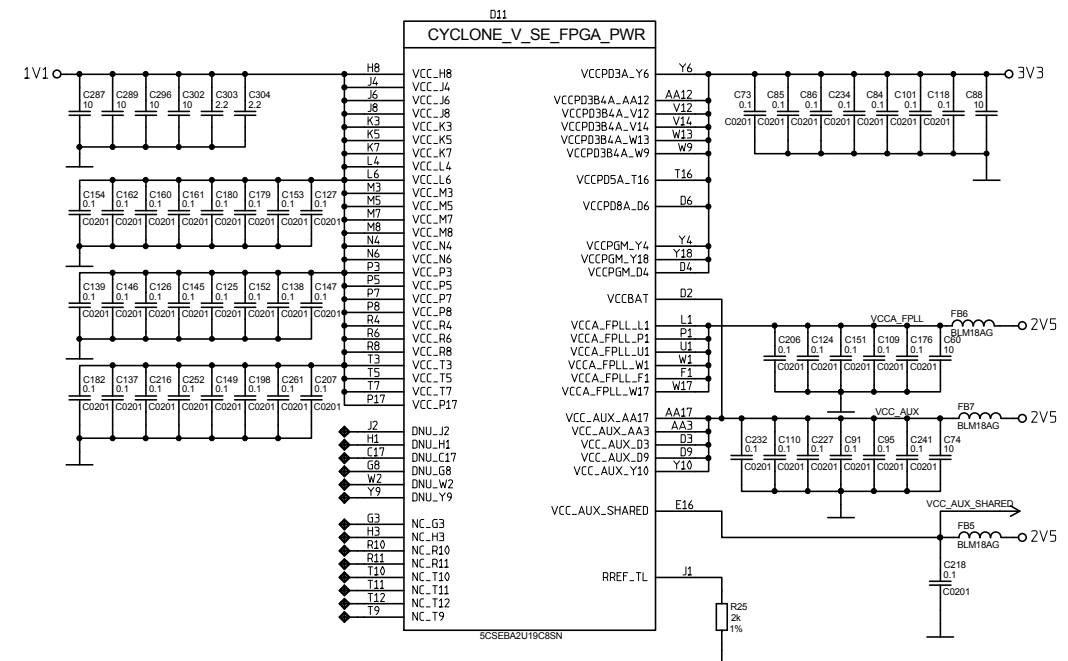
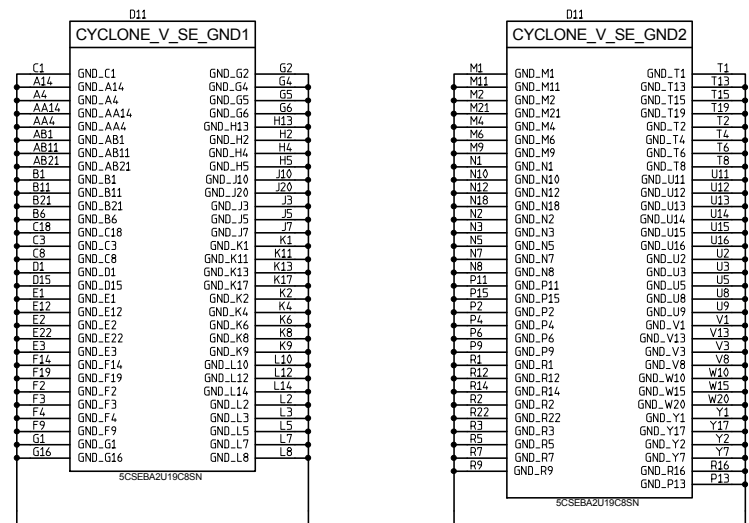
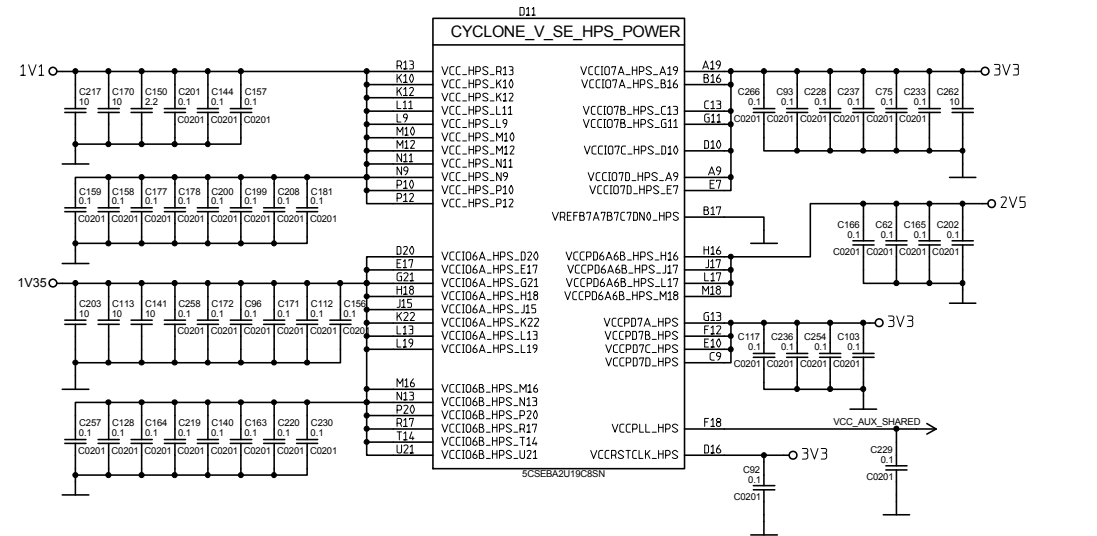
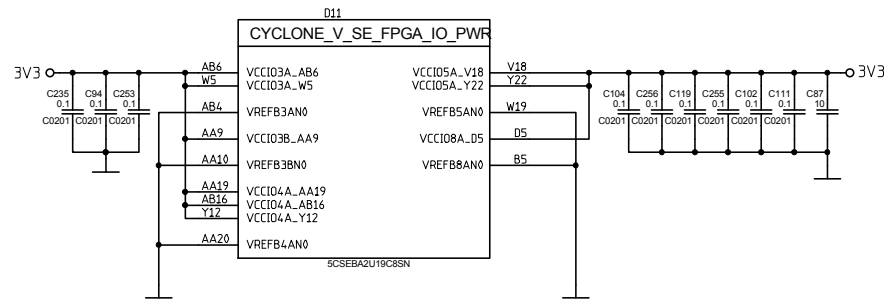
Cyclone V SoC HPS DDR3(L)



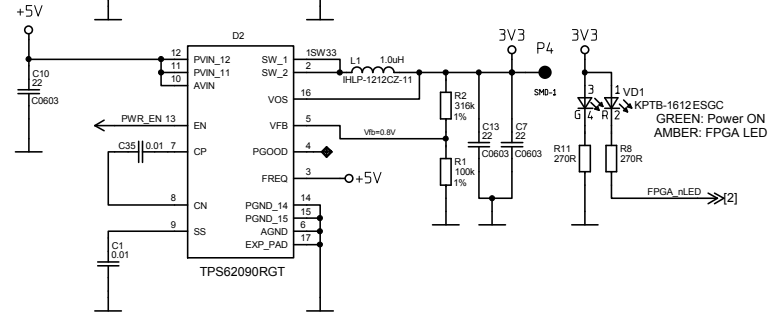
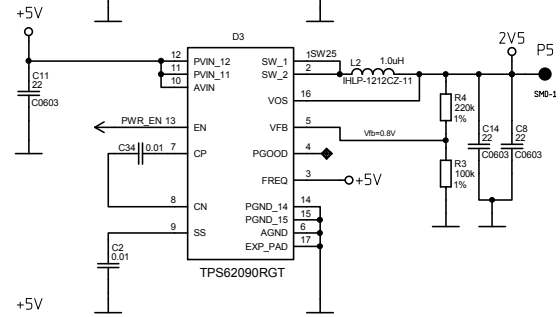
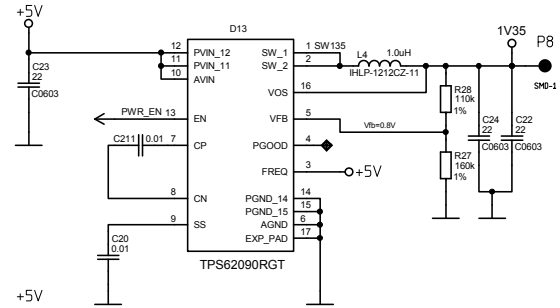
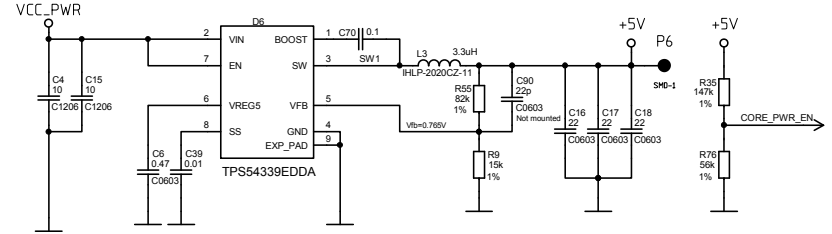
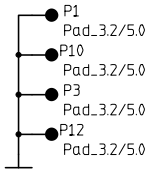
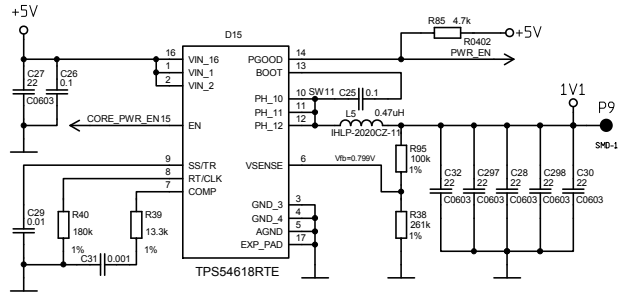
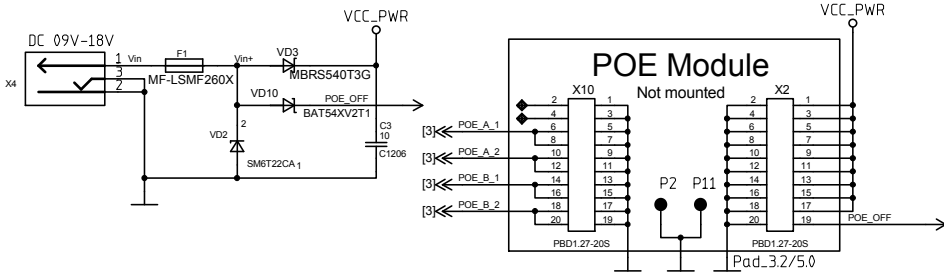
Cyclone V SoC HPS



Cyclone V SoC HPS&FPGA PWR



POWER



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