



CUPTI contains below changes as part of the CUDA Toolkit 9.0 release.

- CUPTI extends tracing and profiling support for devices with compute capability
   7.0
- ▶ Usage of compute device memory can now be tracked through CUPTI.

  A new activity record CUpti\_ActivityMemory and activity kind

  CUPTI\_ACTIVITY\_KIND\_MEMORY are added to track the allocation and freeing

  of memory. This activity record includes fields like virtual base address, size, PC

  (program counter), timestamps for memory allocation and free calls.
- ▶ Unified memory profiling adds new events for thrashing, throttling, remote map and device-to-device migration on 64 bit Linux platforms. New events are added under enum CUpti\_ActivityUnifiedMemoryCounterKind. Enum CUpti\_ActivityUnifiedMemoryRemoteMapCause lists possible causes for remote map events.
- ▶ PC sampling now supports wide range of sampling periods ranging from 2^5 cycles to 2^31 cycles per sample. This can be controlled through new field samplingPeriod2 in the PC sampling configuration struct CUpti ActivityPCSamplingConfig.
- ▶ Added API cuptiDeviceSupported() to check support for a compute device.
- Activity record CUpti\_ActivityKernel3 for kernel execution has been deprecated and replaced by new activity record CUpti\_ActivityKernel4. New record gives information about queued and submit timestamps which can help to determine software and hardware latencies associated with the kernel launch. These timestamps are not collected by default. Use API cuptiActivityEnableLatencyTimestamps() to enable collection. New field launchType of type CUpti\_ActivityLaunchType can be used to determine if it is a cooperative CUDA kernel launch.
- Activity record CUpti\_ActivityPCSampling2 for PC sampling has been deprecated and replaced by new activity record CUpti\_ActivityPCSampling3. New record accommodates 64-bit PC Offset supported on devices of compute capability 7.0 and higher.
- Activity record CUpti\_ActivityNvLink for NVLink attributes has been deprecated and replaced by new activity record CUpti\_ActivityNvLink2. New record accommodates increased port numbers between two compute devices.
- Activity record CUpti\_ActivityGlobalAccess2 for source level global accesses has been deprecated and replaced by new activity record CUpti\_ActivityGlobalAccess3. New record accommodates 64-bit PC Offset supported on devices of compute capability 7.0 and higher.

New attributes CUPTI\_ACTIVITY\_ATTR\_PROFILING\_SEMAPHORE\_POOL\_SIZE and CUPTI\_ACTIVITY\_ATTR\_PROFILING\_SEMAPHORE\_POOL\_LIMIT are added in the activity attribute enum CUpti\_ActivityAttribute to set and get the profiling semaphore pool size and the pool limit.

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# Chapter 1. USAGE

The *CUDA Profiling Tools Interface* (CUPTI) enables the creation of profiling and tracing tools that target CUDA applications. CUPTI provides four APIs: *the Activity API*, the *Callback API*, the *Event API*, and the *Metric API*. Using these APIs, you can develop profiling tools that give insight into the CPU and GPU behavior of CUDA applications. CUPTI is delivered as a dynamic library on all platforms supported by CUDA.

# 1.1. CUPTI Compatibility and Requirements

New versions of the CUDA driver are backwards compatible with older versions of CUPTI. For example, a developer using a profiling tool based on CUPTI 7.0 can update to a more recently released CUDA driver. However, new versions of CUPTI are not backwards compatible with older versions of the CUDA driver. For example, a developer using a profiling tool based on CUPTI 7.0 must have a version of the CUDA driver released with CUDA Toolkit 7.0 (or later) installed as well. CUPTI calls will fail with CUPTI\_ERROR\_NOT\_INITIALIZED if the CUDA driver version is not compatible with the CUPTI version.

# 1.2. CUPTI Initialization

CUPTI initialization occurs lazily the first time you invoke any CUPTI function. For the Activity, Event, Metric, and Callback APIs there are no requirements on when this initialization must occur (i.e. you can invoke the first CUPTI function at any point). See the CUPTI Activity API section for more information on CUPTI initialization requirements for the activity API.

# 1.3. CUPTI Activity API

The CUPTI Activity API allows you to asynchronously collect a trace of an application's CPU and GPU CUDA activity. The following terminology is used by the activity API.

### **Activity Record**

CPU and GPU activity is reported in C data structures called activity records. There is a different C structure type for each activity kind (e.g. CUpti\_ActivityMemcpy). Records are generically referred to using the CUpti\_Activity type. This type contains only a kind field that indicates the kind of the activity record. Using this kind, the object can be cast from the generic CUpti\_Activity type to the specific type representing the activity. See the printActivity function in the activity\_trace\_async sample for an example.

#### **Activity Buffer**

An activity buffer is used to transfer one or more activity records from CUPTI to the client. CUPTI fills activity buffers with activity records as the corresponding activities occur on the CPU and GPU. The CUPTI client is responsible for providing empty activity buffers as necessary to ensure that no records are dropped.

An asynchronous buffering API is implemented by cuptiActivityRegisterCallbacks and cuptiActivityFlushAll.

It is not required that the activity API be initalized before CUDA initialization. All related activities occuring after initializing the activity API are collected. You can force initialization of the activity API by enabling one or more activity kinds using cuptiActivityEnable or cuptiActivityEnableContext, as shown in the initTrace function of the activity\_trace\_async sample. Some activity kinds cannot be directly enabled, see the API documentation for for CUpti\_ActivityKind for details. Functions cuptiActivityEnable and cuptiActivityEnableContext will return CUPTI\_ERROR\_NOT\_COMPATIBLE if the requested activity kind cannot be enabled.

The activity buffer API uses callbacks to request and return buffers of activity records. To use the asynchronous buffering API you must first register two callbacks using <code>cuptiActivityRegisterCallbacks</code>. One of these callbacks will be invoked whenever CUPTI needs an empty activity buffer. The other callback is used to deliver a buffer containing one or more activity records to the client. To minimize profiling overhead the client should return as quickly as possible from these callbacks. Function <code>cuptiActivityFlushAll</code> can be used to force CUPTI to deliver any activity buffers that contain completed activity records. Functions <code>cuptiActivityGetAttribute</code> and <code>cuptiActivitySetAttribute</code> can be used to read and write attributes that control how the buffering API behaves. See the API documentation for more information.

The activity\_trace\_async sample shows how to use the activity buffer API to collect a trace of CPU and GPU activity for a simple application.

## 1.3.1. SASS Source Correlation

While high-level languages for GPU programming like CUDA C offer a useful level of abstraction, convenience, and maintainability, they inherently hide some of the details of the execution on the hardware. It is sometimes helpful to analyze performance problems

for a kernel at the assembly instruction level. Reading assembly language is tedious and challenging; CUPTI can help you to build the correlation between lines in your high-level source code and the executed assembly instructions.

Building SASS source correlation for a PC can be split into two parts -

- ► Correlation of the PC to SASS instruction subscribe to any one of CUPTI\_CBID\_RESOURCE\_MODULE\_LOADED or CUPTI\_CBID\_RESOURCE\_MODULE\_UNLOAD\_STARTING or CUPTI\_CBID\_RESOURCE\_MODULE\_PROFILED callbacks. This returns a CUpti\_ModuleResourceData structure having the CUDA binary. The binary can be disassembled using nvdisasm utility that comes with the CUDA toolkit. An application can have multiple functions and modules, to uniquely identify there is a functionId field in all source level activity records. This uniquely corresponds to a CUPTI\_ACTIVITY\_KIND\_FUNCTION which has the unique module ID and function ID in the module.
- ► Correlation of the SASS instruction to CUDA source line every source level activity has a sourcelocatorId field which uniquely maps to a record of kind CUPTI\_ACTIVITY\_KIND\_SOURCE\_LOCATOR containing the line and file name information. Please note that multiple PCs can correspond to single source line.

When any source level activity (global access, branch, PC Sampling etc) is enabled, source locator record is generated for the PCs that have the source level results. Record <code>CUpti\_ActivityInstructionCorrelation</code> can be used along with source level activities to generate SASS assembly instructions to CUDA C source code mapping for all the PCs of the function and not just the PCs that have the source level results. This can be enabled using activity kind <code>CUPTI ACTIVITY KIND INSTRUCTION CORRELATION</code>.

The sass\_source\_map sample shows how to map SASS assembly instructions to CUDA C source.

# 1.3.2. PC Sampling

CUPTI supports device-wide sampling of the program counter (PC). The PC Sampling gives the number of samples for each source and assembly line with various stall reasons. Using this information you can pinpoint portions of your kernel that are introducing latencies and the reason for the latency. Samples are taken in round robin order for all active warps at a fixed number of cycles regardless of whether the warp is issuing an instruction or not.

Devices with compute capability 6.0 and higher have a new feature that gives latency reasons. The latency samples indicate the reasons for holes in the issue pipeline. While collecting these samples, there is no instruction issued in the respective warp scheduler and hence these give the latency reasons. The latency reasons will be one of the stall

reasons listed in the enum CUpti\_ActivityPCSamplingStallReason except stall reason CUPTI\_ACTIVITY\_PC\_SAMPLING\_STALL\_NOT\_SELECTED.

Activity record CUpti\_ActivityPCSampling3 enabled using activity kind CUPTI\_ACTIVITY\_KIND\_PC\_SAMPLING outputs stall reason along with PC and other related information. Enum CUpti\_ActivityPCSamplingStallReason lists all the stall reasons. Sampling period is configurable and can be tuned using API cuptiActivityConfigurePCSampling. A wide range of sampling periods ranging from 2^5 cycles to 2^31 cycles per sample is supported. This can be controlled through field samplingPeriod2 in the PC sampling configuration struct CUpti\_ActivityPCSamplingConfig. Activity record CUpti\_ActivityPCSamplingRecordInfo provides the total and dropped samples for each kernel profiled for PC sampling.

This feature is available on devices with compute capability 5.2 and higher, excluding mobile devices.

The pc\_sampling sample shows how to use these APIs to collect PC Sampling profiling information for a kernel.

## 1.3.3. NVLink

NVIDIA NVLink is a high-bandwidth, energy-efficient interconnect that enables fast communication between the CPU and GPU, and between GPUs. CUPTI provides NVLink topology information and NVLink transmit/receive throughput metrics.

Activity record CUpti\_ActivityNVLink2 enabled using activity kind CUPTI\_ACTIVITY\_KIND\_NVLink outputs NVLink topology information in terms of logical NVLinks. A logical NVLink is connected between 2 devices, the device can be of type NPU (NVLink Processing Unit which can be CPU) or GPU. Each device can support upto 6 NVLinks hence one logical link can comprise of 1 to 6 physical NVLinks. Field physicalNvLinkCount gives number of physical links in this logical link. Fields portDev0 and portDev1 give information about the slot in which physical NVLinks are connected for a logical link. This port is same as instance of NVLink metrics profiled from a device. So port and instance information should be used to correlate the per-instance metric values with the physical NVLinks and in turn to the topology. Field flag gives the properties of a logical link, whether the link has access to system memory or peer device memory, and have capabilities to do system memory or peer memmory atomics. Field bandwidth gives the bandwidth of the logical link in kilobytes/sec.

CUPTI also provides some metrics for each physical links. Metrics are provided for data transmitted/received, transmit/receive throughput and header versus user data overhead for each physical NVLink. These metrics are also provided per packet type (read/write/ atomics/response) to get more detailed insight in the NVLink traffic.

This feature is available on devices with compute capability 6.0 and 7.0.

The nvlink\_bandwidth sample shows how to use these APIs to collect NVLink metrics and topology and how to correlate metrics with the topology.

# 1.3.4. OpenACC

On Linux x86\_64, CUPTI supports collecting information for OpenACC applications using the OpenACC tools interface implementation of the PGI runtime. In addition to being available only on 64bit Linux platforms, this feature also requires PGI runtime version 15.7 or higher.

Activity records CUpti\_ActivityOpenAccData, CUpti\_ActivityOpenAccLaunch and CUpti\_ActivityOpenAccOther are created, representing the three groups of callback events specified in the OpenACC tools interface. CUPTI\_ACTIVITY\_KIND\_OPENACC\_DATA, CUPTI\_ACTIVITY\_KIND\_OPENACC\_LAUNCH and CUPTI\_ACTIVITY\_KIND\_OPENACC\_OTHER can be enabled to collect the respective activity records.

Due to restrictions of the OpenACC tools interface, CUPTI cannot record OpenACC records from within the client application. Instead, a shared library that exports the acc\_register\_library function defined in the OpenACC tools interface specification must be implemented. Parameters passed into this function from the OpenACC runtime can be used to initialize CUPTI OpenACC measurement using cuptiOpenACCInitialize. Before starting the client application, the environment variable ACC PROFLIB must be set to point to this shared library.

cuptiOpenACCInitialize is defined in cupti\_openacc.h, which is included by cupti\_activity.h. Since the CUPTI OpenACC header is only available on supported platforms, CUPTI clients must define CUPTI\_OPENACC\_SUPPORT when compiling.

The openacc\_trace sample shows how to use CUPTI APIs for OpenACC data collection.

## 1.3.5. External Correlation

Starting with CUDA 8.0, CUPTI supports correlation of CUDA API activity records with external APIs. Such APIs include e.g. OpenACC, OpenMP and MPI. The correlation associates CUPTI correlation IDs with IDs provided by the external API. Both IDs are stored in a new activity record of type CUpti\_ActivityExternalCorrelation.

CUPTI maintains a stack of external correlation IDs per CPU thread and per CUpti\_ExternalCorrelationKind. Clients must use cuptiActivityPushExternalCorrelationId to push an external ID of a specific kind to this stack and cuptiActivityPopExternalCorrelationId to remove the latest ID. If a CUDA API activity record is generated while any CUpti\_ExternalCorrelationKind-stack on the same CPU thread is non-empty, one CUpti\_ActivityExternalCorrelation record per CUpti\_ExternalCorrelationKind-stack is inserted into the activity buffer before

the respective CUDA API activity record. The CUPTI client is responsible for tracking passed external API correlation IDs in order to eventually associate external API calls with CUDA API calls.

If both CUPTI\_ACTIVITY\_KIND\_EXTERNAL\_CORRELATION and any of CUPTI\_ACTIVITY\_KIND\_OPENACC\_\* activity kinds are enabled, CUPTI will generate external correlation activity records for OpenACC with externalKind CUPTI\_EXTERNAL\_CORRELATION\_KIND\_OPENACC.

## 1.4. CUPTI Callback API

The CUPTI Callback API allows you to register a callback into your own code. Your callback will be invoked when the application being profiled calls a CUDA runtime or driver function, or when certain events occur in the CUDA driver. The following terminology is used by the callback API.

#### Callback Domain

Callbacks are grouped into domains to make it easier to associate your callback functions with groups of related CUDA functions or events. There are currently four callback domains, as defined by CUpti\_CallbackDomain: a domain for CUDA runtime functions, a domain for CUDA driver functions, a domain for CUDA resource tracking, and a domain for CUDA synchronization notification.

#### Callback ID

Each callback is given a unique ID within the corresponding callback domain so that you can identify it within your callback function. The CUDA driver API IDs are defined in <code>cupti\_driver\_cbid.h</code> and the CUDA runtime API IDs are defined in <code>cupti\_runtime\_cbid.h</code>. Both of these headers are included for you when you include <code>cupti.h</code>. The CUDA resource callback IDs are defined by <code>CUpti\_CallbackIdResource</code> and the CUDA synchronization callback IDs are defined by <code>CUpti\_CallbackIdSync</code>.

#### Callback Function

Your callback function must be of type <code>CUpti\_CallbackFunc</code>. This function type has two arguments that specify the callback domain and ID so that you know why the callback is occurring. The type also has a <code>cbdata</code> argument that is used to pass data specific to the callback.

#### Subscriber

A subscriber is used to associate each of your callback functions with one or more CUDA API functions. There can be at most one subscriber initialized with cuptiSubscribe() at any time. Before initializing a new subscriber, the existing subscriber must be finalized with cuptiUnsubscribe().

Each callback domain is described in detail below. Unless explicitly stated, it is not supported to call any CUDA runtime or driver API from within a callback function. Doing so may cause the application to hang.

## 1.4.1. Driver and Runtime API Callbacks

Using the callback API with the CUPTI\_CB\_DOMAIN\_DRIVER\_API or CUPTI\_CB\_DOMAIN\_RUNTIME\_API domains, you can associate a callback function with one or more CUDA API functions. When those CUDA functions are invoked in the application, your callback function is invoked as well. For these domains, the cbdata argument to your callback function will be of the type CUpti CallbackData.

It is legal to call cudaThreadSynchronize(), cudaDeviceSynchronize(), cudaStreamSynchronize(), cuCtxSynchronize(), and cuStreamSynchronize() from within a driver or runtime API callback function.

The following code shows a typical sequence used to associate a callback function with one or more CUDA API functions. To simplify the presentation error checking code has been removed.

First, cuptiSubscribe is used to initialize a subscriber with the my\_callback callback function. Next, cuptiEnableDomain is used to associate that callback with all the CUDA runtime API functions. Using this code sequence will cause my\_callback to be called twice each time any of the CUDA runtime API functions are invoked, once on entry to the CUDA function and once just before exit from the CUDA function. CUPTI callback API functions cuptiEnableCallback and cuptiEnableAllDomains can also be used to associate CUDA API functions with a callback (see reference below for more information).

The following code shows a typical callback function.

In your callback function, you use the <code>CUpti\_CallbackDomain</code> and <code>CUpti\_CallbackID</code> parameters to determine which <code>CUDA</code> API function invocation is causing this callback. In the example above, we are checking for the <code>CUDA</code> runtime <code>cudaMemcpy</code> function. The <code>cbdata</code> parameter holds a structure of useful information that can be used within the callback. In this case we use the <code>callbackSite</code> member of the structure to detect that the callback is occurring on entry to <code>cudaMemcpy</code>, and we use the <code>functionParams</code> member to access the parameters that were passed to <code>cudaMemcpy</code>. To access the parameters we first cast <code>functionParams</code> to a structure type corresponding to the <code>cudaMemcpy</code> function. These parameter structures are contained in <code>generated\_cuda\_runtime\_api\_meta.h</code>, <code>generated\_cuda\_meta.h</code>, and a number of other files. When possible these files are included for you by <code>cupti.h</code>.

The **callback\_event** and **callback\_timestamp** samples described on the samples page both show how to use the callback API for the driver and runtime API domains.

## 1.4.2. Resource Callbacks

Using the callback API with the CUPTI\_CB\_DOMAIN\_RESOURCE domain, you can associate a callback function with some CUDA resource creation and destruction events. For example, when a CUDA context is created, your callback function will be invoked with a callback ID equal to CUPTI\_CBID\_RESOURCE\_CONTEXT\_CREATED. For this domain, the cbdata argument to your callback function will be of the type CUpti ResourceData.

Note that, APIs cuptiActivityFlush and cuptiActivityFlushAll will result in deadlock when called from stream destroy starting callback identified using callback ID CUPTI\_CBID\_RESOURCE\_STREAM\_DESTROY\_STARTING.

# 1.4.3. Synchronization Callbacks

Using the callback API with the CUPTI\_CB\_DOMAIN\_SYNCHRONIZE domain, you can associate a callback function with CUDA context and stream synchronizations. For example, when a CUDA context is synchronized, your callback function will be invoked with a callback ID equal to CUPTI\_CBID\_SYNCHRONIZE\_CONTEXT\_SYNCHRONIZED. For this domain, the cbdata argument to your callback function will be of the type CUpti SynchronizeData.

## 1.4.4. NVIDIA Tools Extension Callbacks

Using the callback API with the CUPTI\_CB\_DOMAIN\_NVTX domain, you can associate a callback function with NVIDIA Tools Extension (NVTX) API functions. When an NVTX function is invoked in the application, your callback function is invoked as well. For these domains, the cbdata argument to your callback function will be of the type CUpti NvtxData.

The NVTX library has its own convention for discovering the profiling library that will provide the implementation of the NVTX callbacks. To receive callbacks you must set the NVTX environment variables appropriately so that when the application calls an NVTX function, your profiling library recieve the callbacks. The following code sequence shows a typical initialization sequence to enable NVTX callbacks and activity records.

```
/* Set env so CUPTI-based profiling library loads on first nvtx call. */
char *inj32_path = "/path/to/32-bit/version/of/cupti/based/profiling/library";
char *inj64_path = "/path/to/64-bit/version/of/cupti/based/profiling/library";
setenv("NVTX_INJECTION32_PATH", inj32_path, 1);
setenv("NVTX_INJECTION64_PATH", inj64_path, 1);
```

The following code shows a typical sequence used to associate a callback function with one or more NVTX functions. To simplify the presentation error checking code has been removed.

First, cuptiSubscribe is used to initialize a subscriber with the my\_callback callback function. Next, cuptiEnableDomain is used to associate that callback with all the NVTX functions. Using this code sequence will cause my\_callback to be called once each time any of the NVTX functions are invoked. CUPTI callback API functions cuptiEnableCallback and cuptiEnableAllDomains can also be used to associate NVTX API functions with a callback (see reference below for more information).

The following code shows a typical callback function.

In your callback function, you use the <code>CUpti\_CallbackDomain</code> and <code>CUpti\_CallbackID</code> parameters to determine which NVTX API function invocation is causing this callback. In the example above, we are checking for the <code>nvtxNameOsThreadA</code> function. The <code>cbdata</code> parameter holds a structure of useful information that can be used within the callback. In this case, we use the <code>functionParams</code> member to access the parameters that were passed to <code>nvtxNameOsThreadA</code>. To access the parameters we first cast <code>functionParams</code> to a structure type corresponding to the <code>nvtxNameOsThreadA</code> function. These parameter structures are contained in <code>generated nvtx meta.h</code>.

# 1.5. CUPTI Event API

The CUPTI Event API allows you to query, configure, start, stop, and read the event counters on a CUDA-enabled device. The following terminology is used by the event API.

#### **Event**

An event is a countable activity, action, or occurrence on a device.

#### **Event ID**

Each event is assigned a unique identifier. A named event will represent the same activity, action, or occurrence on all device types. But the named event may have different IDs on different device families. Use <code>cuptiEventGetIdFromName</code> to get the ID for a named event on a particular device.

### **Event Category**

Each event is placed in one of the categories defined by CUpti\_EventCategory. The category indicates the general type of activity, action, or occurrence measured by the event.

#### **Event Domain**

A device exposes one or more event domains. Each event domain represents a group of related events available on that device. A device may have multiple instances of a domain, indicating that the device can simultaneously record multiple instances of each event within that domain.

### **Event Group**

An event group is a collection of events that are managed together. The number and type of events that can be added to an event group are subject to device-specific limits. At any given time, a device may be configured to count events from a limited number of event groups. All events in an event group must belong to the same event domain.

### **Event Group Set**

An event group set is a collection of event groups that can be enabled at the same time. Event group sets are created by <code>cuptiEventGroupSetsCreate</code> and <code>cuptiMetricCreateEventGroupSets</code>.

You can determine the events available on a device using the cuptiDeviceEnumEventDomains and cuptiEventDomainEnumEvents functions. The **cupti\_query** sample described on the samples page shows how to use these functions. You can also enumerate all the CUPTI events available on any device using the cuptiEnumEventDomains function.

Configuring and reading event counts requires the following steps. First, select your event collection mode. If you want to count events that occur during the execution of a kernel, use <code>cuptiSetEventCollectionMode</code> to set mode <code>CUPTI\_EVENT\_COLLECTION\_MODE\_KERNEL</code>. If you want to continuously sample the event counts, use mode <code>CUPTI\_EVENT\_COLLECTION\_MODE\_CONTINUOUS</code>.

Next determine the names of the events that you want to count, and then use the <code>cuptiEventGroupCreate</code>, <code>cuptiEventGetIdFromName</code>, and <code>cuptiEventGroupAddEvent</code> functions to create and initialize an event group with those events. If you are unable to add all the events to a single event group then you will need to create multiple event groups. Alternatively, you can use the <code>cuptiEventGroupSetsCreate</code> function to automatically create the event group(s) required for a set of events.

To begin counting a set of events, enable the event group or groups that contain those events by using the <code>cuptiEventGroupEnable</code> function. If your events are contained in multiple event groups you may be unable to enable all of the event groups at the same time, due to device limitations. In this case, you can gather the events across multiple executions of the application or you can enable kernel replay. If you enable kernel replay using <code>cuptiEnableKernelReplayMode</code> you will be able to enabled any number of event groups and all the contained events will be collect.

Use the cuptiEventGroupReadEvent and/or cuptiEventGroupReadAllEvents functions to read the event values. When you are done collecting events, use the cuptiEventGroupDisable function to stop counting of the events contained in an event group. The callback\_event sample described on the samples page shows how to use these functions to create, enable, and disable event groups, and how to read event counts.

In a system with multiple GPUs, events can be collected simultaneously on all the GPUs i.e. event profiling doesn't enforce any serialization of work across GPUs. The event\_multi\_gpu sample shows how to use the CUPTI event and CUDA APIs on such setups.

# 1.5.1. Collecting Kernel Execution Events

A common use of the event API is to count a set of events during the execution of a kernel (as demonstrated by the **callback\_event** sample). The following code shows a typical callback used for this purpose. Assume that the callback was enabled only for a kernel launch using the CUDA runtime (i.e. by cuptiEnableCallback (1, subscriber, CUPTI CB DOMAIN RUNTIME API,

CUPTI\_RUNTIME\_TRACE\_CBID\_cudaLaunch\_v3020). To simplify the presentation error checking code has been removed.

```
static void CUPTIAPI
getEventValueCallback(void *userdata,
                      CUpti_CallbackDomain domain,
                     CUpti_CallbackId cbid,
                     const void *cbdata)
 const CUpti CallbackData *cbData =
               (CUpti CallbackData *)cbdata;
 if (cbData->callbackSite == CUPTI API ENTER) {
   cudaDeviceSynchronize();
   cuptiSetEventCollectionMode(cbInfo->context,
                               CUPTI EVENT COLLECTION MODE KERNEL);
   cuptiEventGroupEnable(eventGroup);
 if (cbData->callbackSite == CUPTI_API_EXIT) {
   cudaDeviceSynchronize();
   cuptiEventGroupReadEvent(eventGroup,
                            CUPTI EVENT READ FLAG NONE,
                            eventId,
                            &bytesRead, &eventVal);
   cuptiEventGroupDisable(eventGroup);
 }
```

Two synchronization points are used to ensure that events are counted only for the execution of the kernel. If the application contains other threads that launch kernels, then additional thread-level synchronization must also be introduced to ensure that those threads do not launch kernels while the callback is collecting events. When the cudaLaunch API is entered (that is, before the kernel is actually launched on the device), cudaDeviceSynchronize is used to wait until the GPU is idle. The event collection mode is set to CUPTI\_EVENT\_COLLECTION\_MODE\_KERNEL so that the event counters are automatically started and stopped just before and after the kernel executes. Then event collection is enabled with cuptiEventGroupEnable.

When the cudaLaunch API is exited (that is, after the kernel is queued for execution on the GPU) another cudaDeviceSynchronize is used to cause the CPU thread to wait for the kernel to finish execution. Finally, the event counts are read with cuptiEventGroupReadEvent.

# 1.5.2. Sampling Events

The event API can also be used to sample event values while a kernel or kernels are executing (as demonstrated by the **event\_sampling** sample). The sample shows one possible way to perform the sampling. The event collection mode is set to CUPTI\_EVENT\_COLLECTION\_MODE\_CONTINUOUS so that the event counters run continuously. Two threads are used in **event\_sampling**: one thread schedules the kernels and memcpys that perform the computation, while another thread wakes up periodically to sample an event counter. In this sample there is no correlation of the event samples with what is happening on the GPU. To get some coarse correlation, you

can use cuptiDeviceGetTimestamp to collect the GPU timestamp at the time of the sample and also at other interesting points in your application.

# 1.6. CUPTI Metric API

The CUPTI Metric API allows you to collect application metrics calculated from one or more event values. The following terminology is used by the metric API.

#### Metric

An characteristic of an application that is calculated from one or more event values.

#### Metric ID

Each metric is assigned a unique identifier. A named metric will represent the same characteristic on all device types. But the named metric may have different IDs on different device families. Use <code>cuptiMetricGetIdFromName</code> to get the ID for a named metric on a particular device.

### **Metric Category**

Each metric is placed in one of the categories defined by <code>CUpti\_MetricCategory</code>. The category indicates the general type of the characteristic measured by the metric.

### **Metric Property**

Each metric is calculated from input values. These input values can be events or properties of the device or system. The available properties are defined by CUpti\_MetricPropertyID.

#### Metric Value

Each metric has a value that represents one of the kinds defined by CUpti\_MetricValueKind. For each value kind, there is a corresponding member of the CUpti MetricValue union that is used to hold the metric's value.

The tables included in this section list the metrics available for each device, as determined by the device's compute capability. You can also determine the metrics available on a device using the <code>cuptiDeviceEnumMetrics</code> function. The <code>cupti\_query</code> sample described on the samples page shows how to use this function. You can also enumerate all the CUPTI metrics available on any device using the <code>cuptiEnumMetrics</code> function.

CUPTI provides two functions for calculating a metric value. cuptiMetricGetValue2 can be used to calculate a metric value when the device is not available. All required event values and metric properties must be provided by the caller. cuptiMetricGetValue can be used to calculate a metric value when the device is available (as a CUdevice object). All required event values must be provided by the caller but CUPTI will determine the appropriate property values from the CUdevice object.

Configuring and calculating metric values requires the following steps. First, determine the name of the metric that you want to collect, and then use the <code>cuptiMetricGetIdFromName</code> to get the metric ID. Use <code>cuptiMetricEnumEvents</code>

to get the events required to calculate the metric and follow instructions in the CUPTI Event API section to create the event groups for those events. When creating event groups in this manner it is important to use the result of cuptiMetricGetRequiredEventGroupSets to properly group together events that must be collected in the same pass to ensure proper metric calculation.

Alternatively, you can use the <code>cuptiMetricCreateEventGroupSets</code> function to automatically create the event <code>group(s)</code> required for metric's events. When using this function events will be grouped as required to most accurately calculate the metric, as a result it is not necessary to use <code>cuptiMetricGetRequiredEventGroupSets</code>.

If you are using cuptiMetricGetValue2 then you must also collect the required metric property values using cuptiMetricEnumProperties.

Collect event counts as described in the CUPTI Event API section, and then use either cuptiMetricGetValue or cuptiMetricGetValue2 to calculate the metric value from the collected event and property values. The callback\_metric sample described on the samples page shows how to use the functions to calculate event values and calculate a metric using cuptiMetricGetValue. Note that, as shown in the example, you should collect event counts from all domain instances and normalize the counts to get the most accurate metric values. It is necessary to normalize the event counts because the number of event counter instances varies by device and by the event being counted.

For example, a device might have 8 multiprocessors but only have event counters for 4 of the multiprocessors, and might have 3 memory units and only have events counters for one memory unit. When calculating a metric that requires a multiprocessor event and a memory unit event, the 4 multiprocessor counters should be summed and multiplied by 2 to normalize the event count across the entire device. Similarly, the one memory unit counter should be multiplied by 3 to normalize the event count across the entire device. The normalized values can then be passed to cuptiMetricGetValue or cuptiMetricGetValue2 to calculate the metric value.

As described, the normalization assumes the kernel executes a sufficient number of blocks to completely load the device. If the kernel has only a small number of blocks, normalizing across the entire device may skew the result.

# 1.6.1. Metrics Reference

This section contains detailed descriptions of the metrics that can be collected by the CUPTI. A scope value of "Single-context" indicates that the metric can only be accurately collected when a single context (CUDA or graphics) is executing on the GPU. A scope value of "Multi-context" indicates that the metric can be accurately collected when multiple contexts are executing on the GPU. A scope value of "Device" indicates that the metric will be collected at device level, that is, it will include values for all the contexts executing on the GPU. The events for these metrics can be collected at device level using CUPTI\_EVENT\_COLLECTION\_MODE\_CONTINUOUS. When these metrics are collected for a kernel using CUPTI\_EVENT\_COLLECTION\_MODE\_KERNEL, they exhibit

the behavior of single-context. **Note that NVLink metrics collected for kernel mode exhibit the behavior of "Single-context".** 

## 1.6.1.1. Metrics for Capability 3.x

Devices with compute capability 3.x implement the metrics shown in the following table. Note that for some metrics the "Multi-context" scope is supported only for specific devices. Such metrics are marked with "Multi-context\*" under the "Scope" column. Refer to the note at the bottom of the table.

Table 1 Capability 3.x Metrics

Metric Name	Description	Scope
achieved_occupancy	Ratio of the average active warps per active cycle to the maximum number of warps supported on a multiprocessor	Multi-context
alu_fu_utilization	The utilization level of the multiprocessor function units that execute integer and floating-point arithmetic instructions on a scale of 0 to 10	Multi-context
atomic_replay_overhead	Average number of replays due to atomic and reduction bank conflicts for each instruction executed	Multi-context
atomic_throughput	Global memory atomic and reduction throughput	Multi-context
atomic_transactions	Global memory atomic and reduction transactions	Multi-context
atomic_transactions_per_request	Average number of global memory atomic and reduction transactions performed for each atomic and reduction instruction	Multi-context
branch_efficiency	Ratio of non-divergent branches to total branches expressed as percentage. This is available for compute capability 3.0.	Multi-context
cf_executed	Number of executed control-flow instructions	Multi-context
cf_fu_utilization	The utilization level of the multiprocessor function units that execute control-flow instructions on a scale of 0 to 10	Multi-context
cf_issued	Number of issued control-flow instructions	Multi-context
dram_read_throughput	Device memory read throughput. This is available for compute capability 3.0, 3.5 and 3.7.	Multi- context <sup>*</sup>
dram_read_transactions	Device memory read transactions. This is available for compute capability 3.0, 3.5 and 3.7.	Multi- context <sup>*</sup>
dram_utilization	The utilization level of the device memory relative to the peak utilization on a scale of 0 to 10	Multi- context <sup>*</sup>

Metric Name	Description	Scope
dram_write_throughput	Device memory write throughput. This is available for compute capability 3.0, 3.5 and 3.7.	Multi- context <sup>*</sup>
dram_write_transactions	Device memory write transactions. This is available for compute capability 3.0, 3.5 and 3.7.	Multi- context <sup>*</sup>
ecc_throughput	ECC throughput from L2 to DRAM. This is available for compute capability 3.5 and 3.7.	Multi- context <sup>*</sup>
ecc_transactions	Number of ECC transactions between L2 and DRAM. This is available for compute capability 3.5 and 3.7.	Multi- context
eligible_warps_per_cycle	Average number of warps that are eligible to issue per active cycle	Multi-context
flop_count_dp	Number of double-precision floating-point operations executed by non-predicated threads (add, multiply and multiply-accumulate). Each multiply-accumulate operation contributes 2 to the count.	Multi-context
flop_count_dp_add	Number of double-precision floating-point add operations executed by non-predicated threads	Multi-context
flop_count_dp_fma	Number of double-precision floating-point multiply-accumulate operations executed by non-predicated threads. Each multiply-accumulate operation contributes 1 to the count.	Multi-context
flop_count_dp_mul	Number of double-precision floating-point multiply operations executed by non-predicated threads	Multi-context
flop_count_sp	Number of single-precision floating-point operations executed by non-predicated threads (add, multiply and multiply-accumulate). Each multiply-accumulate operation contributes 2 to the count. The count does not include special operations.	Multi-context
flop_count_sp_add	Number of single-precision floating-point add operations executed by non-predicated threads	Multi-context
flop_count_sp_fma	Number of single-precision floating-point multiply-accumulate operations executed by non-predicated threads. Each multiply-accumulate operation contributes 1 to the count.	Multi-context
flop_count_sp_mul	Number of single-precision floating-point multiply operations executed by non-predicated threads	Multi-context
flop_count_sp_special	Number of single-precision floating-point special operations executed by non-predicated threads	Multi-context
flop_dp_efficiency	Ratio of achieved to peak double-precision floating-point operations	Multi-context

Metric Name	Description	Scope
flop_sp_efficiency	Ratio of achieved to peak single-precision floating-point operations	Multi-context
gld_efficiency	Ratio of requested global memory load throughput to required global memory load throughput expressed as percentage	Multi- context <sup>*</sup>
gld_requested_throughput	Requested global memory load throughput	Multi-context
gld_throughput	Global memory load throughput	Multi- context <sup>*</sup>
gld_transactions	Number of global memory load transactions	Multi- context <sup>*</sup>
gld_transactions_per_request	Average number of global memory load transactions performed for each global memory load	Multi- context <sup>*</sup>
global_cache_replay_overhead	Average number of replays due to global memory cache misses for each instruction executed	Multi-context
global_replay_overhead	Average number of replays due to global memory cache misses	Multi-context
gst_efficiency	Ratio of requested global memory store throughput to required global memory store throughput expressed as percentage	Multi- context <sup>*</sup>
gst_requested_throughput	Requested global memory store throughput	Multi-context
gst_throughput	Global memory store throughput	Multi- context <sup>*</sup>
gst_transactions	Number of global memory store transactions	Multi- context <sup>*</sup>
gst_transactions_per_request	Average number of global memory store transactions performed for each global memory store	Multi- context <sup>*</sup>
inst_bit_convert	Number of bit-conversion instructions executed by non-predicated threads	Multi-context
inst_compute_ld_st	Number of compute load/store instructions executed by non-predicated threads	Multi-context
inst_control	Number of control-flow instructions executed by non-predicated threads (jump, branch, etc.)	Multi-context
inst_executed	The number of instructions executed	Multi-context
inst_fp_32	Number of single-precision floating-point instructions executed by non-predicated threads (arithmetic, compare, etc.)	Multi-context
inst_fp_64	Number of double-precision floating-point instructions executed by non-predicated threads (arithmetic, compare, etc.)	Multi-context
inst_integer	Number of integer instructions executed by non-predicated threads	Multi-context

Metric Name	Description	Scope
inst_inter_thread_communication	Number of inter-thread communication instructions executed by non-predicated threads	Multi-context
inst_issued	The number of instructions issued	Multi-context
inst_misc	Number of miscellaneous instructions executed by non-predicated threads	Multi-context
inst_per_warp	Average number of instructions executed by each warp	Multi-context
inst_replay_overhead	Average number of replays for each instruction executed	Multi-context
ірс	Instructions executed per cycle	Multi-context
ipc_instance	Instructions executed per cycle for a single multiprocessor	Multi-context
issue_slot_utilization	Percentage of issue slots that issued at least one instruction, averaged across all cycles	Multi-context
issue_slots	The number of issue slots used	Multi-context
issued_ipc	Instructions issued per cycle	Multi-context
l1_cache_global_hit_rate	Hit rate in L1 cache for global loads	Multi- context <sup>*</sup>
l1_cache_local_hit_rate	Hit rate in L1 cache for local loads and stores	Multi- context <sup>*</sup>
l1_shared_utilization	The utilization level of the L1/shared memory relative to peak utilization on a scale of 0 to 10. This is available for compute capability 3.0, 3.5 and 3.7.	Multi- context <sup>*</sup>
l2_atomic_throughput	Memory read throughput seen at L2 cache for atomic and reduction requests	Multi- context <sup>*</sup>
l2_atomic_transactions	Memory read transactions seen at L2 cache for atomic and reduction requests	Multi- context <sup>*</sup>
l2_l1_read_hit_rate	Hit rate at L2 cache for all read requests from L1 cache. This is available for compute capability 3.0, 3.5 and 3.7.	Multi- context <sup>*</sup>
l2_l1_read_throughput	Memory read throughput seen at L2 cache for read requests from L1 cache. This is available for compute capability 3.0, 3.5 and 3.7.	Multi- context <sup>*</sup>
l2_l1_read_transactions	Memory read transactions seen at L2 cache for all read requests from L1 cache. This is available for compute capability 3.0, 3.5 and 3.7.	Multi- context <sup>*</sup>
l2_l1_write_throughput	Memory write throughput seen at L2 cache for write requests from L1 cache. This is available for compute capability 3.0, 3.5 and 3.7.	Multi- context <sup>*</sup>
l2_l1_write_transactions	Memory write transactions seen at L2 cache for all write requests from L1 cache. This is available for compute capability 3.0, 3.5 and 3.7.	Multi- context <sup>*</sup>

Metric Name	Description	Scope
l2_read_throughput	Memory read throughput seen at L2 cache for all read requests	Multi- context <sup>*</sup>
l2_read_transactions	Memory read transactions seen at L2 cache for all read requests	Multi- context <sup>*</sup>
l2_tex_read_transactions	Memory read transactions seen at L2 cache for read requests from the texture cache	Multi- context <sup>*</sup>
l2_tex_read_hit_rate	Hit rate at L2 cache for all read requests from texture cache. This is available for compute capability 3.0, 3.5 and 3.7.	Multi- context <sup>*</sup>
l2_tex_read_throughput	Memory read throughput seen at L2 cache for read requests from the texture cache	Multi- context <sup>*</sup>
l2_utilization	The utilization level of the L2 cache relative to the peak utilization on a scale of 0 to 10	Multi- context <sup>*</sup>
l2_write_throughput	Memory write throughput seen at L2 cache for all write requests	Multi- context <sup>*</sup>
l2_write_transactions	Memory write transactions seen at L2 cache for all write requests	Multi- context
ldst_executed	Number of executed local, global, shared and texture memory load and store instructions	Multi-context
ldst_fu_utilization	The utilization level of the multiprocessor function units that execute global, local and shared memory instructions on a scale of 0 to 10	Multi-context
ldst_issued	Number of issued local, global, shared and texture memory load and store instructions	Multi-context
local_load_throughput	Local memory load throughput	Multi- context <sup>*</sup>
local_load_transactions	Number of local memory load transactions	Multi- context <sup>*</sup>
local_load_transactions_per_request	Average number of local memory load transactions performed for each local memory load	Multi- context <sup>*</sup>
local_memory_overhead	Ratio of local memory traffic to total memory traffic between the L1 and L2 caches expressed as percentage. This is available for compute capability 3.0, 3.5 and 3.7.	Multi- context <sup>*</sup>
local_replay_overhead	Average number of replays due to local memory accesses for each instruction executed	Multi-context
local_store_throughput	Local memory store throughput	Multi- context <sup>*</sup>
local_store_transactions	Number of local memory store transactions	Multi- context <sup>*</sup>
local_store_transactions_per_request	Average number of local memory store transactions performed for each local memory store	Multi- context <sup>*</sup>

Metric Name	Description	Scope
nc_cache_global_hit_rate	Hit rate in non coherent cache for global loads	Multi- context <sup>*</sup>
nc_gld_efficiency	Ratio of requested non coherent global memory load throughput to required non coherent global memory load throughput expressed as percentage	Multi- context <sup>*</sup>
nc_gld_requested_throughput	Requested throughput for global memory loaded via non-coherent cache	Multi-context
nc_gld_throughput	Non coherent global memory load throughput	Multi- context <sup>*</sup>
nc_l2_read_throughput	Memory read throughput for non coherent global read requests seen at L2 cache	Multi- context <sup>*</sup>
nc_l2_read_transactions	Memory read transactions seen at L2 cache for non coherent global read requests	Multi- context <sup>*</sup>
shared_efficiency	Ratio of requested shared memory throughput to required shared memory throughput expressed as percentage	Multi- context <sup>*</sup>
shared_load_throughput	Shared memory load throughput	Multi- context <sup>*</sup>
shared_load_transactions	Number of shared memory load transactions	Multi- context <sup>*</sup>
shared_load_transactions_per_request	Average number of shared memory load transactions performed for each shared memory load	Multi- context <sup>*</sup>
shared_replay_overhead	Average number of replays due to shared memory conflicts for each instruction executed	Multi-context
shared_store_throughput	Shared memory store throughput	Multi- context <sup>*</sup>
shared_store_transactions	Number of shared memory store transactions	Multi- context <sup>*</sup>
shared_store_transactions_per_request	Average number of shared memory store transactions performed for each shared memory store	Multi- context <sup>*</sup>
sm_efficiency	The percentage of time at least one warp is active on a multiprocessor averaged over all multiprocessors on the GPU	Multi- context <sup>*</sup>
sm_efficiency_instance	The percentage of time at least one warp is active on a specific multiprocessor	Multi- context <sup>*</sup>
stall_constant_memory_dependency	Percentage of stalls occurring because of immediate constant cache miss. This is available for compute capability 3.2, 3.5 and 3.7.	Multi-context
stall_exec_dependency	Percentage of stalls occurring because an input required by the instruction is not yet available	Multi-context

Metric Name	Description	Scope
stall_inst_fetch	Percentage of stalls occurring because the next assembly instruction has not yet been fetched	Multi-context
stall_memory_dependency	Percentage of stalls occurring because a memory operation cannot be performed due to the required resources not being available or fully utilized, or because too many requests of a given type are outstanding.	Multi-context
stall_memory_throttle	Percentage of stalls occurring because of memory throttle.	Multi-context
stall_not_selected	Percentage of stalls occurring because warp was not selected.	Multi-context
stall_other	Percentage of stalls occurring due to miscellaneous reasons	Multi-context
stall_pipe_busy	Percentage of stalls occurring because a compute operation cannot be performed because the compute pipeline is busy. This is available for compute capability 3.2, 3.5 and 3.7.	Multi-context
stall_sync	Percentage of stalls occurring because the warp is blocked at asyncthreads() call	Multi-context
stall_texture	Percentage of stalls occurring because the texture sub-system is fully utilized or has too many outstanding requests	Multi-context
sysmem_read_throughput	System memory read throughput. This is available for compute capability 3.0, 3.5 and 3.7.	Multi- context <sup>*</sup>
sysmem_read_transactions	System memory read transactions. This is available for compute capability 3.0, 3.5 and 3.7.	Multi- context <sup>*</sup>
sysmem_read_utilization	The read utilization level of the system memory relative to the peak utilization on a scale of 0 to 10. This is available for compute capability 3.0, 3.5 and 3.7.	Multi-context
sysmem_utilization	The utilization level of the system memory relative to the peak utilization on a scale of 0 to 10. This is available for compute capability 3.0, 3.5 and 3.7.	Multi- context <sup>*</sup>
sysmem_write_throughput	System memory write throughput. This is available for compute capability 3.0, 3.5 and 3.7.	Multi- context <sup>*</sup>
sysmem_write_transactions	System memory write transactions. This is available for compute capability 3.0, 3.5 and 3.7.	Multi- context <sup>*</sup>
sysmem_write_utilization	The write utilization level of the system memory relative to the peak utilization on a scale of 0 to 10. This is available for compute capability 3.0, 3.5 and 3.7.	Multi-context

Metric Name	Description	Scope
tex_cache_hit_rate	Texture cache hit rate	Multi- context <sup>*</sup>
tex_cache_throughput	Texture cache throughput	Multi- context <sup>*</sup>
tex_cache_transactions	Texture cache read transactions	Multi- context <sup>*</sup>
tex_fu_utilization	The utilization level of the multiprocessor function units that execute texture instructions on a scale of 0 to 10	Multi-context
tex_utilization	The utilization level of the texture cache relative to the peak utilization on a scale of 0 to 10	Multi- context <sup>*</sup>
warp_execution_efficiency	Ratio of the average active threads per warp to the maximum number of threads per warp supported on a multiprocessor expressed as percentage	Multi-context
warp_nonpred_execution_efficiency	Ratio of the average active threads per warp executing non-predicated instructions to the maximum number of threads per warp supported on a multiprocessor expressed as percentage	Multi-context

<sup>\*</sup> The "Multi-context" scope for this metric is supported only for devices with compute capability 3.0, 3.5 and 3.7.

## 1.6.1.2. Metrics for Capability 5.x

Devices with compute capability 5.x implement the metrics shown in the following table. Note that for some metrics the "Multi-context" scope is supported only for specific devices. Such metrics are marked with "Multi-context\*" under the "Scope" column. Refer to the note at the bottom of the table.

Table 2 Capability 5.x Metrics

Metric Name	Description	Scope
achieved_occupancy	Ratio of the average active warps per active cycle to the maximum number of warps supported on a multiprocessor	Multi-context
atomic_transactions	Global memory atomic and reduction transactions	Multi-context
atomic_transactions_per_request	Average number of global memory atomic and reduction transactions performed for each atomic and reduction instruction	Multi-context
branch_efficiency	Ratio of non-divergent branches to total branches expressed as percentage	Multi-context
cf_executed	Number of executed control-flow instructions	Multi-context

Metric Name	Description	Scope
cf_fu_utilization	The utilization level of the multiprocessor function units that execute control-flow instructions on a scale of 0 to 10	Multi-context
cf_issued	Number of issued control-flow instructions	Multi-context
double_precision_fu_utilization	The utilization level of the multiprocessor function units that execute double-precision floating-point instructions and integer instructions on a scale of 0 to 10	Multi-context
dram_read_throughput	Device memory read throughput. This is available for compute capability 5.0 and 5.2.	Multi- context <sup>*</sup>
dram_read_transactions	Device memory read transactions. This is available for compute capability 5.0 and 5.2.	Multi- context <sup>*</sup>
dram_utilization	The utilization level of the device memory relative to the peak utilization on a scale of 0 to 10	Multi- context <sup>*</sup>
dram_write_throughput	Device memory write throughput. This is available for compute capability 5.0 and 5.2.	Multi- context <sup>*</sup>
dram_write_transactions	Device memory write transactions. This is available for compute capability 5.0 and 5.2.	Multi- context <sup>*</sup>
ecc_throughput	ECC throughput from L2 to DRAM. This is available for compute capability 5.0 and 5.2.	Multi- context <sup>*</sup>
ecc_transactions	Number of ECC transactions between L2 and DRAM. This is available for compute capability 5.0 and 5.2.	Multi- context <sup>*</sup>
eligible_warps_per_cycle	Average number of warps that are eligible to issue per active cycle	Multi-context
flop_count_dp	Number of double-precision floating-point operations executed by non-predicated threads (add, multiply and multiply-accumulate). Each multiply-accumulate operation contributes 2 to the count.	Multi-context
flop_count_dp_add	Number of double-precision floating-point add operations executed by non-predicated threads	Multi-context
flop_count_dp_fma	Number of double-precision floating-point multiply-accumulate operations executed by non-predicated threads. Each multiply-accumulate operation contributes 1 to the count.	Multi-context
flop_count_dp_mul	Number of double-precision floating-point multiply operations executed by non-predicated threads	Multi-context
flop_count_hp	Number of half-precision floating-point operations executed by non-predicated threads (add, multiply and multiply-accumulate). Each multiply-accumulate operation contributes 2 to the count. This is available for compute capability 5.3.	Multi- context

Metric Name	Description	Scope
flop_count_hp_add	Number of half-precision floating-point add operations executed by non-predicated threads. This is available for compute capability 5.3.	Multi- context <sup>*</sup>
flop_count_hp_fma	Number of half-precision floating-point multiply-accumulate operations executed by non-predicated threads. Each multiply-accumulate operation contributes 1 to the count. This is available for compute capability 5.3.	Multi- context <sup>*</sup>
flop_count_hp_mul	Number of half-precision floating-point multiply operations executed by non-predicated threads. This is available for compute capability 5.3.	Multi- context <sup>*</sup>
flop_count_sp	Number of single-precision floating-point operations executed by non-predicated threads (add, multiply and multiply-accumulate). Each multiply-accumulate operation contributes 2 to the count. The count does not include special operations.	Multi-context
flop_count_sp_add	Number of single-precision floating-point add operations executed by non-predicated threads	Multi-context
flop_count_sp_fma	Number of single-precision floating-point multiply-accumulate operations executed by non-predicated threads. Each multiply-accumulate operation contributes 1 to the count.	Multi-context
flop_count_sp_mul	Number of single-precision floating-point multiply operations executed by non-predicated threads	Multi-context
flop_count_sp_special	Number of single-precision floating-point special operations executed by non-predicated threads	Multi-context
flop_dp_efficiency	Ratio of achieved to peak double-precision floating-point operations	Multi-context
flop_hp_efficiency	Ratio of achieved to peak half-precision floating-point operations. This is available for compute capability 5.3.	Multi- context
flop_sp_efficiency	Ratio of achieved to peak single-precision floating-point operations	Multi-context
gld_efficiency	Ratio of requested global memory load throughput to required global memory load throughput expressed as percentage	Multi- context <sup>*</sup>
gld_requested_throughput	Requested global memory load throughput	Multi-context
gld_throughput	Global memory load throughput	Multi- context <sup>*</sup>
gld_transactions	Number of global memory load transactions	Multi- context <sup>*</sup>

Metric Name	Description	Scope
gld_transactions_per_request	Average number of global memory load transactions performed for each global memory load	Multi- context <sup>*</sup>
global_hit_rate	Hit rate for global loads	Multi- context <sup>*</sup>
gst_efficiency	Ratio of requested global memory store throughput to required global memory store throughput expressed as percentage	Multi- context <sup>*</sup>
gst_requested_throughput	Requested global memory store throughput	Multi-context
gst_throughput	Global memory store throughput	Multi- context <sup>*</sup>
gst_transactions	Number of global memory store transactions	Multi- context <sup>*</sup>
gst_transactions_per_request	Average number of global memory store transactions performed for each global memory store	Multi- context <sup>*</sup>
half_precision_fu_utilization	The utilization level of the multiprocessor function units that execute 16 bit floating-point instructions and integer instructions on a scale of 0 to 10. This is available for compute capability 5.3.	Multi- context <sup>*</sup>
inst_bit_convert	Number of bit-conversion instructions executed by non-predicated threads	Multi-context
inst_compute_ld_st	Number of compute load/store instructions executed by non-predicated threads	Multi-context
inst_control	Number of control-flow instructions executed by non-predicated threads (jump, branch, etc.)	Multi-context
inst_executed	The number of instructions executed	Multi-context
inst_fp_16	Number of half-precision floating-point instructions executed by non-predicated threads (arithmetic, compare, etc.) This is available for compute capability 5.3.	Multi- context <sup>*</sup>
inst_fp_32	Number of single-precision floating-point instructions executed by non-predicated threads (arithmetic, compare, etc.)	Multi-context
inst_fp_64	Number of double-precision floating-point instructions executed by non-predicated threads (arithmetic, compare, etc.)	Multi-context
inst_integer	Number of integer instructions executed by non-predicated threads	Multi-context
inst_inter_thread_communication	Number of inter-thread communication instructions executed by non-predicated threads	Multi-context
inst_issued	The number of instructions issued	Multi-context
inst_misc	Number of miscellaneous instructions executed by non-predicated threads	Multi-context

Metric Name	Description	Scope
inst_per_warp	Average number of instructions executed by each warp	Multi-context
inst_replay_overhead	Average number of replays for each instruction executed	Multi-context
ipc	Instructions executed per cycle	Multi-context
issue_slot_utilization	Percentage of issue slots that issued at least one instruction, averaged across all cycles	Multi-context
issue_slots	The number of issue slots used	Multi-context
issued_ipc	Instructions issued per cycle	Multi-context
l2_atomic_throughput	Memory read throughput seen at L2 cache for atomic and reduction requests	Multi-context
l2_atomic_transactions	Memory read transactions seen at L2 cache for atomic and reduction requests	Multi- context <sup>*</sup>
l2_read_throughput	Memory read throughput seen at L2 cache for all read requests	Multi- context <sup>*</sup>
l2_read_transactions	Memory read transactions seen at L2 cache for all read requests	Multi- context <sup>*</sup>
l2_tex_read_hit_rate	Hit rate at L2 cache for all read requests from texture cache. This is available for compute capability 5.0 and 5.2.	Multi- context <sup>*</sup>
l2_tex_read_throughput	Memory read throughput seen at L2 cache for read requests from the texture cache	Multi- context <sup>*</sup>
l2_tex_read_transactions	Memory read transactions seen at L2 cache for read requests from the texture cache	Multi- context <sup>*</sup>
l2_tex_write_hit_rate	Hit Rate at L2 cache for all write requests from texture cache. This is available for compute capability 5.0 and 5.2.	Multi- context <sup>*</sup>
l2_tex_write_throughput	Memory write throughput seen at L2 cache for write requests from the texture cache	Multi- context <sup>*</sup>
l2_tex_write_transactions	Memory write transactions seen at L2 cache for write requests from the texture cache	Multi- context <sup>*</sup>
l2_utilization	The utilization level of the L2 cache relative to the peak utilization on a scale of 0 to 10	Multi- context <sup>*</sup>
l2_write_throughput	Memory write throughput seen at L2 cache for all write requests	Multi- context <sup>*</sup>
l2_write_transactions	Memory write transactions seen at L2 cache for all write requests	Multi- context <sup>*</sup>
ldst_executed	Number of executed local, global, shared and texture memory load and store instructions	Multi-context
ldst_fu_utilization	The utilization level of the multiprocessor function units that execute global, local and shared memory instructions on a scale of 0 to 10	Multi-context

Metric Name	Description	Scope
ldst_issued	Number of issued local, global, shared and texture memory load and store instructions	Multi-context
local_hit_rate	Hit rate for local loads and stores	Multi- context <sup>*</sup>
local_load_throughput	Local memory load throughput	Multi- context <sup>*</sup>
local_load_transactions	Number of local memory load transactions	Multi- context <sup>*</sup>
local_load_transactions_per_request	Average number of local memory load transactions performed for each local memory load	Multi- context <sup>*</sup>
local_memory_overhead	Ratio of local memory traffic to total memory traffic between the L1 and L2 caches expressed as percentage	Multi- context <sup>*</sup>
local_store_throughput	Local memory store throughput	Multi- context <sup>*</sup>
local_store_transactions	Number of local memory store transactions	Multi- context <sup>*</sup>
local_store_transactions_per_request	Average number of local memory store transactions performed for each local memory store	Multi- context
shared_efficiency	Ratio of requested shared memory throughput to required shared memory throughput expressed as percentage	Multi- context <sup>*</sup>
shared_load_throughput	Shared memory load throughput	Multi- context <sup>*</sup>
shared_load_transactions	Number of shared memory load transactions	Multi- context <sup>*</sup>
shared_load_transactions_per_request	Average number of shared memory load transactions performed for each shared memory load	Multi- context <sup>*</sup>
shared_store_throughput	Shared memory store throughput	Multi- context <sup>*</sup>
shared_store_transactions	Number of shared memory store transactions	Multi- context <sup>*</sup>
shared_store_transactions_per_request	Average number of shared memory store transactions performed for each shared memory store	Multi- context <sup>*</sup>
shared_utilization	The utilization level of the shared memory relative to peak utilization on a scale of 0 to 10	Multi- context <sup>*</sup>
single_precision_fu_utilization	The utilization level of the multiprocessor function units that execute single-precision floating-point instructions and integer instructions on a scale of 0 to 10	Multi-context

Metric Name	Description	Scope
sm_efficiency	The percentage of time at least one warp is active on a multiprocessor	Multi- context <sup>*</sup>
special_fu_utilization	The utilization level of the multiprocessor function units that execute sin, cos, ex2, popc, flo, and similar instructions on a scale of 0 to 10	Multi-context
stall_constant_memory_dependency	Percentage of stalls occurring because of immediate constant cache miss	Multi-context
stall_exec_dependency	Percentage of stalls occurring because an input required by the instruction is not yet available	Multi-context
stall_inst_fetch	Percentage of stalls occurring because the next assembly instruction has not yet been fetched	Multi-context
stall_memory_dependency	Percentage of stalls occurring because a memory operation cannot be performed due to the required resources not being available or fully utilized, or because too many requests of a given type are outstanding	Multi-context
stall_memory_throttle	Percentage of stalls occurring because of memory throttle	Multi-context
stall_not_selected	Percentage of stalls occurring because warp was not selected	Multi-context
stall_other	Percentage of stalls occurring due to miscellaneous reasons	Multi-context
stall_pipe_busy	Percentage of stalls occurring because a compute operation cannot be performed because the compute pipeline is busy	Multi-context
stall_sync	Percentage of stalls occurring because the warp is blocked at asyncthreads() call	Multi-context
stall_texture	Percentage of stalls occurring because the texture sub-system is fully utilized or has too many outstanding requests	Multi-context
sysmem_read_throughput	System memory read throughput	Multi- context <sup>*</sup>
sysmem_read_transactions	System memory read transactions	Multi- context <sup>*</sup>
sysmem_read_utilization	The read utilization level of the system memory relative to the peak utilization on a scale of 0 to 10. This is available for compute capability 5.0 and 5.2.	Multi-context
sysmem_utilization	The utilization level of the system memory relative to the peak utilization on a scale of 0 to 10. This is available for compute capability 5.0 and 5.2.	Multi- context <sup>*</sup>
sysmem_write_throughput	System memory write throughput	Multi- context <sup>*</sup>
sysmem_write_transactions	System memory write transactions	Multi- context <sup>*</sup>

Metric Name	Description	Scope
sysmem_write_utilization	The write utilization level of the system memory relative to the peak utilization on a scale of 0 to 10. This is available for compute capability 5.0 and 5.2.	Multi-context
tex_cache_hit_rate	Texture cache hit rate	Multi- context <sup>*</sup>
tex_cache_throughput	Texture cache throughput	Multi- context <sup>*</sup>
tex_cache_transactions	Texture cache read transactions	Multi- context <sup>*</sup>
tex_fu_utilization	The utilization level of the multiprocessor function units that execute global, local and texture memory instructions on a scale of 0 to 10	Multi-context
tex_utilization	The utilization level of the texture cache relative to the peak utilization on a scale of 0 to 10	Multi- context <sup>*</sup>
warp_execution_efficiency	Ratio of the average active threads per warp to the maximum number of threads per warp supported on a multiprocessor expressed as percentage	Multi-context
warp_nonpred_execution_efficiency	Ratio of the average active threads per warp executing non-predicated instructions to the maximum number of threads per warp supported on a multiprocessor	Multi-context

<sup>\*</sup> The "Multi-context" scope for this metric is supported only for devices with compute capability 5.0 and 5.2.

# 1.6.1.3. Metrics for Capability 6.x

Devices with compute capability 6.x implement the metrics shown in the following table.

Table 3 Capability 6.x Metrics

Metric Name	Description	Scope
achieved_occupancy	Ratio of the average active warps per active cycle to the maximum number of warps supported on a multiprocessor	Multi-context
atomic_transactions	Global memory atomic and reduction transactions	Multi-context
atomic_transactions_per_request	Average number of global memory atomic and reduction transactions performed for each atomic and reduction instruction	Multi-context
branch_efficiency	Ratio of non-divergent branches to total branches expressed as percentage	Multi-context

Metric Name	Description	Scope
cf_executed	Number of executed control-flow instructions	Multi-context
cf_fu_utilization	The utilization level of the multiprocessor function units that execute control-flow instructions on a scale of 0 to 10	Multi-context
cf_issued	Number of issued control-flow instructions	Multi-context
double_precision_fu_utilization	The utilization level of the multiprocessor function units that execute double-precision floating-point instructions and integer instructions on a scale of 0 to 10	Multi-context
dram_read_throughput	Device memory read throughput. This is available for compute capability 6.0 and 6.1.	Multi-context
dram_read_transactions	Device memory read transactions. This is available for compute capability 6.0 and 6.1.	Multi-context
dram_utilization	The utilization level of the device memory relative to the peak utilization on a scale of 0 to 10	Multi-context
dram_write_throughput	Device memory write throughput. This is available for compute capability 6.0 and 6.1.	Multi-context
dram_write_transactions	Device memory write transactions. This is available for compute capability 6.0 and 6.1.	Multi-context
ecc_throughput	ECC throughput from L2 to DRAM. This is available for compute capability 6.1.	Multi-context
ecc_transactions	Number of ECC transactions between L2 and DRAM. This is available for compute capability 6.1.	Multi-context
eligible_warps_per_cycle	Average number of warps that are eligible to issue per active cycle	Multi-context
executed_ipc	Instructions executed per cycle	Multi-context
flop_count_dp	Number of double-precision floating-point operations executed by non-predicated threads (add, multiply and multiply-accumulate). Each multiply-accumulate operation contributes 2 to the count.	Multi-context
flop_count_dp_add	Number of double-precision floating-point add operations executed by non-predicated threads	Multi-context
flop_count_dp_fma	Number of double-precision floating-point multiply-accumulate operations executed by non-predicated threads. Each multiply-accumulate operation contributes 1 to the count.	Multi-context
flop_count_dp_mul	Number of double-precision floating-point multiply operations executed by non-predicated threads	Multi-context
flop_count_hp	Number of half-precision floating-point operations executed by non-predicated threads (add, multiply and multiply-accumulate). Each	Multi-context

Metric Name	Description	Scope
	multiply-accumulate operation contributes 2 to the count.	
flop_count_hp_add	Number of half-precision floating-point add operations executed by non-predicated threads	Multi-context
flop_count_hp_fma	Number of half-precision floating-point multiply-accumulate operations executed by non-predicated threads. Each multiply-accumulate operation contributes 1 to the count.	Multi-context
flop_count_hp_mul	Number of half-precision floating-point multiply operations executed by non-predicated threads	Multi-context
flop_count_sp	Number of single-precision floating-point operations executed by non-predicated threads (add, multiply and multiply-accumulate). Each multiply-accumulate operation contributes 2 to the count. The count does not include special operations.	Multi-context
flop_count_sp_add	Number of single-precision floating-point add operations executed by non-predicated threads	Multi-context
flop_count_sp_fma	Number of single-precision floating-point multiply-accumulate operations executed by non-predicated threads. Each multiply-accumulate operation contributes 1 to the count.	Multi-context
flop_count_sp_mul	Number of single-precision floating-point multiply operations executed by non-predicated threads	Multi-context
flop_count_sp_special	Number of single-precision floating-point special operations executed by non-predicated threads	Multi-context
flop_dp_efficiency	Ratio of achieved to peak double-precision floating-point operations	Multi-context
flop_hp_efficiency	Ratio of achieved to peak half-precision floating-point operations	Multi-context
flop_sp_efficiency	Ratio of achieved to peak single-precision floating-point operations	Multi-context
gld_efficiency	Ratio of requested global memory load throughput to required global memory load throughput expressed as percentage	Multi-context
gld_requested_throughput	Requested global memory load throughput	Multi-context
gld_throughput	Global memory load throughput	Multi-context
gld_transactions	Number of global memory load transactions	Multi-context
gld_transactions_per_request	Average number of global memory load transactions performed for each global memory load	Multi-context
global_hit_rate	Hit rate for global loads	Multi-context

Metric Name	Description	Scope
gst_efficiency	Ratio of requested global memory store throughput to required global memory store throughput expressed as percentage	Multi-context
gst_requested_throughput	Requested global memory store throughput	Multi-context
gst_throughput	Global memory store throughput	Multi-context
gst_transactions	Number of global memory store transactions	Multi-context
gst_transactions_per_request	Average number of global memory store transactions performed for each global memory store	Multi-context
half_precision_fu_utilization	The utilization level of the multiprocessor function units that execute 16 bit floating-point instructions and integer instructions on a scale of 0 to 10	Multi-context
inst_bit_convert	Number of bit-conversion instructions executed by non-predicated threads	Multi-context
inst_compute_ld_st	Number of compute load/store instructions executed by non-predicated threads	Multi-context
inst_control	Number of control-flow instructions executed by non-predicated threads (jump, branch, etc.)	Multi-context
inst_executed	The number of instructions executed	Multi-context
inst_fp_16	Number of half-precision floating-point instructions executed by non-predicated threads (arithmetic, compare, etc.)	Multi-context
inst_fp_32	Number of single-precision floating-point instructions executed by non-predicated threads (arithmetic, compare, etc.)	Multi-context
inst_fp_64	Number of double-precision floating-point instructions executed by non-predicated threads (arithmetic, compare, etc.)	Multi-context
inst_integer	Number of integer instructions executed by non-predicated threads	Multi-context
inst_inter_thread_communication	Number of inter-thread communication instructions executed by non-predicated threads	Multi-context
inst_issued	The number of instructions issued	Multi-context
inst_misc	Number of miscellaneous instructions executed by non-predicated threads	Multi-context
inst_per_warp	Average number of instructions executed by each warp	Multi-context
inst_replay_overhead	Average number of replays for each instruction executed	Multi-context
issue_slot_utilization	Percentage of issue slots that issued at least one instruction, averaged across all cycles	Multi-context
issue_slots	The number of issue slots used	Multi-context

Metric Name	Description	Scope
issued_ipc	Instructions issued per cycle	Multi-context
l2_atomic_throughput	Memory read throughput seen at L2 cache for atomic and reduction requests	Multi-context
l2_atomic_transactions	Memory read transactions seen at L2 cache for atomic and reduction requests	Multi-context
l2_read_throughput	Memory read throughput seen at L2 cache for all read requests	Multi-context
l2_read_transactions	Memory read transactions seen at L2 cache for all read requests	Multi-context
l2_tex_read_hit_rate	Hit rate at L2 cache for all read requests from texture cache. This is available for compute capability 6.0 and 6.1.	Multi-context
l2_tex_read_throughput	Memory read throughput seen at L2 cache for read requests from the texture cache	Multi-context
l2_tex_read_transactions	Memory read transactions seen at L2 cache for read requests from the texture cache	Multi-context
l2_tex_write_hit_rate	Hit Rate at L2 cache for all write requests from texture cache. This is available for compute capability 6.0 and 6.1.	Multi-context
l2_tex_write_throughput	Memory write throughput seen at L2 cache for write requests from the texture cache	Multi-context
l2_tex_write_transactions	Memory write transactions seen at L2 cache for write requests from the texture cache	Multi-context
l2_utilization	The utilization level of the L2 cache relative to the peak utilization on a scale of 0 to 10	Multi-context
l2_write_throughput	Memory write throughput seen at L2 cache for all write requests	Multi-context
l2_write_transactions	Memory write transactions seen at L2 cache for all write requests	Multi-context
ldst_executed	Number of executed local, global, shared and texture memory load and store instructions	Multi-context
ldst_fu_utilization	The utilization level of the multiprocessor function units that execute global, local and shared memory instructions on a scale of 0 to 10	Multi-context
ldst_issued	Number of issued local, global, shared and texture memory load and store instructions	Multi-context
local_hit_rate	Hit rate for local loads and stores	Multi-context
local_load_throughput	Local memory load throughput	Multi-context
local_load_transactions	Number of local memory load transactions	Multi-context
local_load_transactions_per_request	Average number of local memory load transactions performed for each local memory load	Multi-context

Metric Name	Description	Scope
local_memory_overhead	Ratio of local memory traffic to total memory traffic between the L1 and L2 caches expressed as percentage	Multi-context
local_store_throughput	Local memory store throughput	Multi-context
local_store_transactions	Number of local memory store transactions	Multi-context
local_store_transactions_per_request	Average number of local memory store transactions performed for each local memory store	Multi-context
nvlink_overhead_data_received	Ratio of overhead data to the total data, received through NVLink. This is available for compute capability 6.0.	Device
nvlink_overhead_data_transmitted	Ratio of overhead data to the total data, transmitted through NVLink. This is available for compute capability 6.0.	Device
nvlink_receive_throughput	Number of bytes received per second through NVLinks. This is available for compute capability 6.0.	Device
nvlink_total_data_received	Total data bytes received through NVLinks including headers. This is available for compute capability 6.0.	Device
nvlink_total_data_transmitted	Total data bytes transmitted through NVLinks including headers. This is available for compute capability 6.0.	Device
nvlink_total_nratom_data_transmitted	Total non-reduction atomic data bytes transmitted through NVLinks. This is available for compute capability 6.0.	Device
nvlink_total_ratom_data_transmitted	Total reduction atomic data bytes transmitted through NVLinks This is available for compute capability 6.0.	Device
nvlink_total_response_data_received	Total response data bytes received through NVLink, response data includes data for read requests and result of non-reduction atomic requests. This is available for compute capability 6.0.	Device
nvlink_total_write_data_transmitted	Total write data bytes transmitted through NVLinks. This is available for compute capability 6.0.	Device
nvlink_transmit_throughput	Number of Bytes Transmitted per second through NVLinks. This is available for compute capability 6.0.	Device
nvlink_user_data_received	User data bytes received through NVLinks, doesn't include headers. This is available for compute capability 6.0.	Device
nvlink_user_data_transmitted	User data bytes transmitted through NVLinks, doesn't include headers. This is available for compute capability 6.0.	Device

Metric Name	Description	Scope
nvlink_user_nratom_data_transmitted	Total non-reduction atomic user data bytes transmitted through NVLinks. This is available for compute capability 6.0.	Device
nvlink_user_ratom_data_transmitted	Total reduction atomic user data bytes transmitted through NVLinks. This is available for compute capability 6.0.	Device
nvlink_user_response_data_received	Total user response data bytes received through NVLink, response data includes data for read requests and result of non-reduction atomic requests. This is available for compute capability 6.0.	Device
nvlink_user_write_data_transmitted	User write data bytes transmitted through NVLinks. This is available for compute capability 6.0.	Device
shared_efficiency	Ratio of requested shared memory throughput to required shared memory throughput expressed as percentage	Multi-context
shared_load_throughput	Shared memory load throughput	Multi-context
shared_load_transactions	Number of shared memory load transactions	Multi-context
shared_load_transactions_per_request	Average number of shared memory load transactions performed for each shared memory load	Multi-context
shared_store_throughput	Shared memory store throughput	Multi-context
shared_store_transactions	Number of shared memory store transactions	Multi-context
shared_store_transactions_per_request	Average number of shared memory store transactions performed for each shared memory store	Multi-context
shared_utilization	The utilization level of the shared memory relative to peak utilization on a scale of 0 to 10	Multi-context
single_precision_fu_utilization	The utilization level of the multiprocessor function units that execute single-precision floating-point instructions and integer instructions on a scale of 0 to 10	Multi-context
sm_activity	The percentage of time at least one warp is active on a multiprocessor	Multi-context
special_fu_utilization	The utilization level of the multiprocessor function units that execute sin, cos, ex2, popc, flo, and similar instructions on a scale of 0 to 10	Multi-context
stall_constant_memory_dependency	Percentage of stalls occurring because of immediate constant cache miss	Multi-context
stall_exec_dependency	Percentage of stalls occurring because an input required by the instruction is not yet available	Multi-context
stall_inst_fetch	Percentage of stalls occurring because the next assembly instruction has not yet been fetched	Multi-context
stall_memory_dependency	Percentage of stalls occurring because a memory operation cannot be performed due to	Multi-context

Metric Name	Description	Scope
	the required resources not being available or fully utilized, or because too many requests of a given type are outstanding	
stall_memory_throttle	Percentage of stalls occurring because of memory throttle	Multi-context
stall_not_selected	Percentage of stalls occurring because warp was not selected	Multi-context
stall_other	Percentage of stalls occurring due to miscellaneous reasons	Multi-context
stall_pipe_busy	Percentage of stalls occurring because a compute operation cannot be performed due to the required resources not being available	Multi-context
stall_sync	Percentage of stalls occurring because the warp is blocked at asyncthreads() call	Multi-context
stall_texture	Percentage of stalls occurring because the texture sub-system is fully utilized or has too many outstanding requests	Multi-context
sysmem_read_throughput	System memory read throughput	Multi-context
sysmem_read_transactions	System memory read transactions	Multi-context
sysmem_read_utilization	The read utilization level of the system memory relative to the peak utilization on a scale of 0 to 10. This is available for compute capability 6.0 and 6.1.	Multi-context
sysmem_utilization	The utilization level of the system memory relative to the peak utilization on a scale of 0 to 10. This is available for compute capability 6.0 and 6.1.	Multi-context
sysmem_write_throughput	System memory write throughput	Multi-context
sysmem_write_transactions	System memory write transactions	Multi-context
sysmem_write_utilization	The write utilization level of the system memory relative to the peak utilization on a scale of 0 to 10. This is available for compute capability 6.0 and 6.1.	Multi-context
tex_cache_hit_rate	Texture cache hit rate	Multi-context
tex_cache_throughput	Texture cache throughput	Multi-context
tex_cache_transactions	Texture cache read transactions	Multi-context
tex_fu_utilization	The utilization level of the multiprocessor function units that execute global, local and texture memory instructions on a scale of 0 to 10	Multi-context
tex_utilization	The utilization level of the texture cache relative to the peak utilization on a scale of 0 to 10	Multi-context
warp_execution_efficiency	Ratio of the average active threads per warp to the maximum number of threads per warp	Multi-context

Metric Name	Description	Scope
	supported on a multiprocessor expressed as percentage	
warp_nonpred_execution_efficiency	Ratio of the average active threads per warp executing non-predicated instructions to the maximum number of threads per warp supported on a multiprocessor	Multi-context

# 1.6.1.4. Metrics for Capability 7.0

Devices with compute capability 7.0 implement the metrics shown in the following table.

Table 4 Capability 7.0 Metrics

Metric Name	Description	Scope
achieved_occupancy	Ratio of the average active warps per active cycle to the maximum number of warps supported on a multiprocessor	Multi-context
atomic_transactions	Global memory atomic and reduction transactions	Multi-context
atomic_transactions_per_request	Average number of global memory atomic and reduction transactions performed for each atomic and reduction instruction	Multi-context
branch_efficiency	Ratio of non-divergent branches to total branches expressed as percentage	Multi-context
cf_executed	Number of executed control-flow instructions	Multi-context
cf_fu_utilization	The utilization level of the multiprocessor function units that execute control-flow instructions on a scale of 0 to 10	Multi-context
cf_issued	Number of issued control-flow instructions	Multi-context
double_precision_fu_utilization	The utilization level of the multiprocessor function units that execute double-precision floating-point instructions and integer instructions on a scale of 0 to 10	Multi-context
dram_read_throughput	Device memory read throughput.	Multi-context
dram_read_transactions	Device memory read transactions.	Multi-context
dram_utilization	The utilization level of the device memory relative to the peak utilization on a scale of 0 to 10	Multi-context
dram_write_throughput	Device memory write throughput.	Multi-context
dram_write_transactions	Device memory write transactions.	Multi-context
eligible_warps_per_cycle	Average number of warps that are eligible to issue per active cycle	Multi-context
flop_count_dp	Number of double-precision floating-point operations executed by non-predicated threads	Multi-context

Metric Name	Description	Scope
	(add, multiply and multiply-accumulate). Each multiply-accumulate operation contributes 2 to the count.	
flop_count_dp_add	Number of double-precision floating-point add operations executed by non-predicated threads	Multi-context
flop_count_dp_fma	Number of double-precision floating-point multiply-accumulate operations executed by non-predicated threads. Each multiply-accumulate operation contributes 1 to the count.	Multi-context
flop_count_dp_mul	Number of double-precision floating-point multiply operations executed by non-predicated threads	Multi-context
flop_count_hp	Number of half-precision floating-point operations executed by non-predicated threads (add, multiply and multiply-accumulate). Each multiply-accumulate operation contributes 2 or 4 to the count based on the number of inputs.	Multi-context
flop_count_hp_add	Number of half-precision floating-point add operations executed by non-predicated threads	Multi-context
flop_count_hp_fma	Number of half-precision floating-point multiply-accumulate operations executed by non-predicated threads. Each multiply-accumulate operation contributes 2 or 4 to the count based on the number of inputs.	Multi-context
flop_count_hp_mul	Number of half-precision floating-point multiply operations executed by non-predicated threads	Multi-context
flop_count_sp	Number of single-precision floating-point operations executed by non-predicated threads (add, multiply and multiply-accumulate). Each multiply-accumulate operation contributes 2 to the count. The count does not include special operations.	Multi-context
flop_count_sp_add	Number of single-precision floating-point add operations executed by non-predicated threads	Multi-context
flop_count_sp_fma	Number of single-precision floating-point multiply-accumulate operations executed by non-predicated threads. Each multiply-accumulate operation contributes 1 to the count.	Multi-context
flop_count_sp_mul	Number of single-precision floating-point multiply operations executed by non-predicated threads	Multi-context
flop_count_sp_special	Number of single-precision floating-point special operations executed by non-predicated threads	Multi-context
flop_dp_efficiency	Ratio of achieved to peak double-precision floating-point operations	Multi-context

Metric Name	Description	Scope
flop_hp_efficiency	Ratio of achieved to peak half-precision floating-point operations	Multi-context
flop_sp_efficiency	Ratio of achieved to peak single-precision floating-point operations	Multi-context
gld_efficiency	Ratio of requested global memory load throughput to required global memory load throughput expressed as percentage	Multi-context
gld_requested_throughput	Requested global memory load throughput	Multi-context
gld_throughput	Global memory load throughput	Multi-context
gld_transactions	Number of global memory load transactions	Multi-context
gld_transactions_per_request	Average number of global memory load transactions performed for each global memory load	Multi-context
global_hit_rate	Hit rate for global loads	Multi-context
gst_efficiency	Ratio of requested global memory store throughput to required global memory store throughput expressed as percentage	Multi-context
gst_requested_throughput	Requested global memory store throughput	Multi-context
gst_throughput	Global memory store throughput	Multi-context
gst_transactions	Number of global memory store transactions	Multi-context
gst_transactions_per_request	Average number of global memory store transactions performed for each global memory store	Multi-context
half_precision_fu_utilization	The utilization level of the multiprocessor function units that execute 16 bit floating-point instructions and integer instructions on a scale of 0 to 10	Multi-context
inst_bit_convert	Number of bit-conversion instructions executed by non-predicated threads	Multi-context
inst_compute_ld_st	Number of compute load/store instructions executed by non-predicated threads	Multi-context
inst_control	Number of control-flow instructions executed by non-predicated threads (jump, branch, etc.)	Multi-context
inst_executed	The number of instructions executed	Multi-context
inst_fp_16	Number of half-precision floating-point instructions executed by non-predicated threads (arithmetic, compare, etc.)	Multi-context
inst_fp_32	Number of single-precision floating-point instructions executed by non-predicated threads (arithmetic, compare, etc.)	Multi-context
inst_fp_64	Number of double-precision floating-point instructions executed by non-predicated threads (arithmetic, compare, etc.)	Multi-context

Metric Name	Description	Scope
inst_integer	Number of integer instructions executed by non-predicated threads	Multi-context
inst_inter_thread_communication	Number of inter-thread communication instructions executed by non-predicated threads	Multi-context
inst_issued	The number of instructions issued	Multi-context
inst_misc	Number of miscellaneous instructions executed by non-predicated threads	Multi-context
inst_per_warp	Average number of instructions executed by each warp	Multi-context
inst_replay_overhead	Average number of replays for each instruction executed	Multi-context
ipc	Instructions executed per cycle	Multi-context
issue_slot_utilization	Percentage of issue slots that issued at least one instruction, averaged across all cycles	Multi-context
issue_slots	The number of issue slots used	Multi-context
issued_ipc	Instructions issued per cycle	Multi-context
l2_atomic_throughput	Memory read throughput seen at L2 cache for atomic and reduction requests	Multi-context
l2_atomic_transactions	Memory read transactions seen at L2 cache for atomic and reduction requests	Multi-context
l2_read_throughput	Memory read throughput seen at L2 cache for all read requests	Multi-context
l2_read_transactions	Memory read transactions seen at L2 cache for all read requests	Multi-context
l2_tex_read_hit_rate	Hit rate at L2 cache for all read requests from texture cache.	Multi-context
l2_tex_read_throughput	Memory read throughput seen at L2 cache for read requests from the texture cache	Multi-context
l2_tex_read_transactions	Memory read transactions seen at L2 cache for read requests from the texture cache	Multi-context
l2_tex_write_hit_rate	Hit Rate at L2 cache for all write requests from texture cache.	Multi-context
l2_tex_write_throughput	Memory write throughput seen at L2 cache for write requests from the texture cache	Multi-context
l2_tex_write_transactions	Memory write transactions seen at L2 cache for write requests from the texture cache	Multi-context
l2_utilization	The utilization level of the L2 cache relative to the peak utilization on a scale of 0 to 10	Multi-context
l2_write_throughput	Memory write throughput seen at L2 cache for all write requests	Multi-context
l2_write_transactions	Memory write transactions seen at L2 cache for all write requests	Multi-context

Metric Name	Description	Scope
ldst_executed	Number of executed local, global, shared and texture memory load and store instructions	Multi-context
ldst_fu_utilization	The utilization level of the multiprocessor function units that execute global, local and shared memory instructions on a scale of 0 to 10	Multi-context
ldst_issued	Number of issued local, global, shared and texture memory load and store instructions	Multi-context
local_hit_rate	Hit rate for local loads and stores	Multi-context
local_load_throughput	Local memory load throughput	Multi-context
local_load_transactions	Number of local memory load transactions	Multi-context
local_load_transactions_per_request	Average number of local memory load transactions performed for each local memory load	Multi-context
local_memory_overhead	Ratio of local memory traffic to total memory traffic between the L1 and L2 caches expressed as percentage	Multi-context
local_store_throughput	Local memory store throughput	Multi-context
local_store_transactions	Number of local memory store transactions	Multi-context
local_store_transactions_per_request	Average number of local memory store transactions performed for each local memory store	Multi-context
nvlink_overhead_data_received	Ratio of overhead data to the total data, received through NVLink.	Device
nvlink_overhead_data_transmitted	Ratio of overhead data to the total data, transmitted through NVLink.	Device
nvlink_receive_throughput	Number of bytes received per second through NVLinks.	Device
nvlink_total_data_received	Total data bytes received through NVLinks including headers.	Device
nvlink_total_data_transmitted	Total data bytes transmitted through NVLinks including headers.	Device
nvlink_total_nratom_data_transmitted	Total non-reduction atomic data bytes transmitted through NVLinks.	Device
nvlink_total_ratom_data_transmitted	Total reduction atomic data bytes transmitted through NVLinks	Device
nvlink_total_response_data_received	Total response data bytes received through NVLink, response data includes data for read requests and result of non-reduction atomic requests.	Device
nvlink_total_write_data_transmitted	Total write data bytes transmitted through NVLinks.	Device
nvlink_transmit_throughput	Number of Bytes Transmitted per second through NVLinks.	Device

Metric Name	Description	Scope
nvlink_user_data_received	User data bytes received through NVLinks, doesn't include headers.	Device
nvlink_user_data_transmitted	User data bytes transmitted through NVLinks, doesn't include headers.	Device
nvlink_user_nratom_data_transmitted	Total non-reduction atomic user data bytes transmitted through NVLinks.	Device
nvlink_user_ratom_data_transmitted	Total reduction atomic user data bytes transmitted through NVLinks.	Device
nvlink_user_response_data_received	Total user response data bytes received through NVLink, response data includes data for read requests and result of non-reduction atomic requests.	Device
nvlink_user_write_data_transmitted	User write data bytes transmitted through NVLinks.	Device
shared_efficiency	Ratio of requested shared memory throughput to required shared memory throughput expressed as percentage	Multi-context
shared_load_throughput	Shared memory load throughput	Multi-context
shared_load_transactions	Number of shared memory load transactions	Multi-context
shared_load_transactions_per_request	Average number of shared memory load transactions performed for each shared memory load	Multi-context
shared_store_throughput	Shared memory store throughput	Multi-context
shared_store_transactions	Number of shared memory store transactions	Multi-context
shared_store_transactions_per_request	Average number of shared memory store transactions performed for each shared memory store	Multi-context
shared_utilization	The utilization level of the shared memory relative to peak utilization on a scale of 0 to 10	Multi-context
single_precision_fu_utilization	The utilization level of the multiprocessor function units that execute single-precision floating-point instructions and integer instructions on a scale of 0 to 10	Multi-context
sm_efficiency	The percentage of time at least one warp is active on a multiprocessor	Multi-context
special_fu_utilization	The utilization level of the multiprocessor function units that execute sin, cos, ex2, popc, flo, and similar instructions on a scale of 0 to 10	Multi-context
stall_constant_memory_dependency	Percentage of stalls occurring because of immediate constant cache miss	Multi-context
stall_exec_dependency	Percentage of stalls occurring because an input required by the instruction is not yet available	Multi-context
stall_inst_fetch	Percentage of stalls occurring because the next assembly instruction has not yet been fetched	Multi-context

Metric Name	Description	Scope
stall_memory_dependency	Percentage of stalls occurring because a memory operation cannot be performed due to the required resources not being available or fully utilized, or because too many requests of a given type are outstanding	Multi-context
stall_memory_throttle	Percentage of stalls occurring because of memory throttle	Multi-context
stall_not_selected	Percentage of stalls occurring because warp was not selected	Multi-context
stall_other	Percentage of stalls occurring due to miscellaneous reasons	Multi-context
stall_pipe_busy	Percentage of stalls occurring because a compute operation cannot be performed because the compute pipeline is busy	Multi-context
stall_sleeping	Percentage of stalls occurring because warp was sleeping	Multi-context
stall_sync	Percentage of stalls occurring because the warp is blocked at asyncthreads() call	Multi-context
stall_texture	Percentage of stalls occurring because the texture sub-system is fully utilized or has too many outstanding requests	Multi-context
sysmem_read_throughput	System memory read throughput	Multi-context
sysmem_read_transactions	System memory read transactions	Multi-context
sysmem_read_utilization	The read utilization level of the system memory relative to the peak utilization on a scale of 0 to 10.	Multi-context
sysmem_utilization	The utilization level of the system memory relative to the peak utilization on a scale of 0 to 10.	Multi-context
sysmem_write_throughput	System memory write throughput	Multi-context
sysmem_write_transactions	System memory write transactions	Multi-context
sysmem_write_utilization	The write utilization level of the system memory relative to the peak utilization on a scale of 0 to 10.	Multi-context
tex_cache_hit_rate	Texture cache hit rate	Multi-context
tex_cache_throughput	Texture cache throughput	Multi-context
tex_cache_transactions	Texture cache read transactions	Multi-context
tex_fu_utilization	The utilization level of the multiprocessor function units that execute global, local and texture memory instructions on a scale of 0 to 10	Multi-context
tex_utilization	The utilization level of the texture cache relative to the peak utilization on a scale of 0 to 10	Multi-context

Metric Name	Description	Scope
warp_execution_efficiency	Ratio of the average active threads per warp to the maximum number of threads per warp supported on a multiprocessor expressed as percentage	Multi-context
warp_nonpred_execution_efficiency	Ratio of the average active threads per warp executing non-predicated instructions to the maximum number of threads per warp supported on a multiprocessor	Multi-context

# 1.7. Samples

The CUPTI installation includes several samples that demonstrate the use of the CUPTI APIs. The samples are:

### activity\_trace\_async

This sample shows how to collect a trace of CPU and GPU activity using the new asynchronous activity buffer APIs.

### callback\_event

This sample shows how to use both the callback and event APIs to record the events that occur during the execution of a simple kernel. The sample shows the required ordering for synchronization, and for event group enabling, disabling and reading.

## callback\_metric

This sample shows how to use both the callback and metric APIs to record the metric's events during the execution of a simple kernel, and then use those events to calculate the metric value.

### callback\_timestamp

This sample shows how to use the callback API to record a trace of API start and stop times.

### cupti\_query

This sample shows how to query CUDA-enabled devices for their event domains, events, and metrics.

### event\_sampling

This sample shows how to use the event APIs to sample events using a separate host thread.

## event\_multi\_gpu

This sample shows how to use the CUPTI event and CUDA APIs to sample events on a setup with multiple GPUs. The sample shows the required ordering for synchronization, and for event group enabling, disabling and reading.

### sass\_source\_map

This sample shows how to generate CUpti\_ActivityInstructionExecution records and how to map SASS assembly instructions to CUDA C source.

#### unified\_memory

This sample shows how to collect information about page transfers for unified memory.

#### pc\_sampling

This sample shows how to collect PC Sampling profiling information for a kernel.

#### nvlink\_bandwidth

This sample shows how to collect NVLink topology and NVLink throughput metrics in continuous mode.

#### openacc\_trace

This sample shows how to use CUPTI APIs for OpenACC data collection.

# Chapter 2. MODULES

#### Here is a list of all modules:

- CUPTI Version
- CUPTI Result Codes
- ► CUPTI Activity API
- ► CUPTI Callback API
- ► CUPTI Event API
- ► CUPTI Metric API

## 2.1. CUPTI Version

Function and macro to determine the CUPTI version.

## CUptiResult cuptiGetVersion (uint32\_t \*version)

Get the CUPTI API version.

#### **Parameters**

#### version

Returns the version

#### **Returns**

► CUPTI\_SUCCESS

on success

CUPTI\_ERROR\_INVALID\_PARAMETER

if version is NULL

#### Description

Return the API version in \*version.

#### See also:

CUPTI API VERSION

## #define CUPTI\_API\_VERSION 10

The API version for this implementation of CUPTI.

The API version for this implementation of CUPTI. This define along with cuptiGetVersion can be used to dynamically detect if the version of CUPTI compiled against matches the version of the loaded CUPTI library.

v1 : CUDAToolsSDK 4.0 v2 : CUDAToolsSDK 4.1 v3 : CUDA Toolkit 5.0 v4 : CUDA Toolkit 5.5 v5 : CUDA Toolkit 6.0 v6 : CUDA Toolkit 6.5 v7 : CUDA Toolkit 6.5(with sm\_52 support) v8 : CUDA Toolkit 7.0 v9 : CUDA Toolkit 8.0 v10 : CUDA Toolkit 9.0

## 2.2. CUPTI Result Codes

Error and result codes returned by CUPTI functions.

## enum CUptiResult

CUPTI result codes.

Error and result codes returned by CUPTI functions.

#### **Values**

 $CUPTI_SUCCESS = 0$ 

No error.

CUPTI\_ERROR\_INVALID\_PARAMETER = 1

One or more of the parameters is invalid.

CUPTI\_ERROR\_INVALID\_DEVICE = 2

The device does not correspond to a valid CUDA device.

CUPTI\_ERROR\_INVALID\_CONTEXT = 3

The context is NULL or not valid.

CUPTI\_ERROR\_INVALID\_EVENT\_DOMAIN\_ID = 4

The event domain id is invalid.

CUPTI\_ERROR\_INVALID\_EVENT\_ID = 5

The event id is invalid.

**CUPTI ERROR INVALID EVENT NAME = 6** 

The event name is invalid.

CUPTI\_ERROR\_INVALID\_OPERATION = 7

The current operation cannot be performed due to dependency on other factors.

#### CUPTI\_ERROR\_OUT\_OF\_MEMORY = 8

Unable to allocate enough memory to perform the requested operation.

#### CUPTI\_ERROR\_HARDWARE = 9

An error occurred on the performance monitoring hardware.

#### CUPTI\_ERROR\_PARAMETER\_SIZE\_NOT\_SUFFICIENT = 10

The output buffer size is not sufficient to return all requested data.

#### CUPTI\_ERROR\_API\_NOT\_IMPLEMENTED = 11

API is not implemented.

#### CUPTI\_ERROR\_MAX\_LIMIT\_REACHED = 12

The maximum limit is reached.

#### CUPTI\_ERROR\_NOT\_READY = 13

The object is not yet ready to perform the requested operation.

#### **CUPTI ERROR NOT COMPATIBLE = 14**

The current operation is not compatible with the current state of the object

#### CUPTI\_ERROR\_NOT\_INITIALIZED = 15

CUPTI is unable to initialize its connection to the CUDA driver.

#### CUPTI\_ERROR\_INVALID\_METRIC\_ID = 16

The metric id is invalid.

#### **CUPTI ERROR INVALID METRIC NAME = 17**

The metric name is invalid.

#### CUPTI\_ERROR\_QUEUE\_EMPTY = 18

The queue is empty.

#### CUPTI\_ERROR\_INVALID\_HANDLE = 19

Invalid handle (internal?).

#### CUPTI\_ERROR\_INVALID\_STREAM = 20

Invalid stream.

#### CUPTI\_ERROR\_INVALID\_KIND = 21

Invalid kind.

#### CUPTI\_ERROR\_INVALID\_EVENT\_VALUE = 22

Invalid event value.

#### **CUPTI ERROR DISABLED = 23**

CUPTI is disabled due to conflicts with other enabled profilers

#### CUPTI\_ERROR\_INVALID\_MODULE = 24

Invalid module.

#### CUPTI\_ERROR\_INVALID\_METRIC\_VALUE = 25

Invalid metric value.

#### **CUPTI ERROR HARDWARE BUSY = 26**

The performance monitoring hardware is in use by other client.

#### CUPTI\_ERROR\_NOT\_SUPPORTED = 27

The attempted operation is not supported on the current system or device.

#### CUPTI\_ERROR\_UM\_PROFILING\_NOT\_SUPPORTED = 28

Unified memory profiling is not supported on the system. Potential reason could be unsupported OS or architecture.

#### CUPTI\_ERROR\_UM\_PROFILING\_NOT\_SUPPORTED\_ON\_DEVICE = 29

Unified memory profiling is not supported on the device

## CUPTI\_ERROR\_UM\_PROFILING\_NOT\_SUPPORTED\_ON\_NON\_P2P\_DEVICES = 30

Unified memory profiling is not supported on a multi-GPU configuration without P2P support between any pair of devices

#### CUPTI\_ERROR\_UM\_PROFILING\_NOT\_SUPPORTED\_WITH\_MPS = 31

Unified memory profiling is not supported under the Multi-Process Service (MPS) environment. CUDA 7.5 removes this restriction.

#### CUPTI\_ERROR\_CDP\_TRACING\_NOT\_SUPPORTED = 32

In CUDA 9.0, devices with compute capability 7.0 don't support CDP tracing

#### **CUPTI ERROR UNKNOWN = 999**

An unknown internal error has occurred.

CUPTI\_ERROR\_FORCE\_INT = 0x7ffffffff

# CUptiResult cuptiGetResultString (CUptiResult result, const char \*\*str)

Get the descriptive string for a CUptiResult.

#### **Parameters**

#### result

The result to get the string for

str

Returns the string

#### Returns

CUPTI SUCCESS

on success

CUPTI\_ERROR\_INVALID\_PARAMETER

if str is NULL or result is not a valid CUptiResult

#### Description

Return the descriptive string for a CUptiResult in \*str.



Thread-safety: this function is thread safe.

## 2.3. CUPTI Activity API

Functions, types, and enums that implement the CUPTI Activity API.

## struct CUpti\_Activity

The base activity record.

## struct CUpti\_ActivityAPI

The activity record for a driver or runtime API invocation.

## struct CUpti\_ActivityAutoBoostState

Device auto boost state structure.

## struct CUpti\_ActivityBranch

The activity record for source level result branch. (deprecated).

## struct CUpti\_ActivityBranch2

The activity record for source level result branch.

## struct CUpti\_ActivityCdpKernel

The activity record for CDP (CUDA Dynamic Parallelism) kernel.

## struct CUpti\_ActivityContext

The activity record for a context.

## struct CUpti\_ActivityCudaEvent

The activity record for CUDA event.

## struct CUpti\_ActivityDevice

The activity record for a device. (deprecated).

## struct CUpti\_ActivityDevice2

The activity record for a device. (CUDA 7.0 onwards).

## struct CUpti\_ActivityDeviceAttribute

The activity record for a device attribute.

## struct CUpti\_ActivityEnvironment

The activity record for CUPTI environmental data.

## struct CUpti\_ActivityEvent

The activity record for a CUPTI event.

## struct CUpti\_ActivityEventInstance

The activity record for a CUPTI event with instance information.

## struct CUpti\_ActivityExternalCorrelation

The activity record for correlation with external records.

## struct CUpti\_ActivityFunction

The activity record for global/device functions.

## struct CUpti\_ActivityGlobalAccess

The activity record for source-level global access. (deprecated).

## struct CUpti\_ActivityGlobalAccess2

The activity record for source-level global access. (deprecated in CUDA 9.0).

## struct CUpti\_ActivityGlobalAccess3

The activity record for source-level global access.

## struct CUpti\_ActivityInstantaneousEvent

The activity record for an instantaneous CUPTI event.

## struct CUpti\_ActivityInstantaneousEventInstance

The activity record for an instantaneous CUPTI event with event domain instance information.

## struct CUpti\_ActivityInstantaneousMetric

The activity record for an instantaneous CUPTI metric.

## struct CUpti\_ActivityInstantaneousMetricInstance

The instantaneous activity record for a CUPTI metric with instance information.

## struct CUpti\_ActivityInstructionCorrelation

The activity record for source-level sass/source line-by-line correlation.

## struct CUpti\_ActivityInstructionExecution

The activity record for source-level instruction execution.

## struct CUpti\_ActivityKernel

The activity record for kernel. (deprecated).

## struct CUpti\_ActivityKernel2

The activity record for kernel. (deprecated).

## struct CUpti\_ActivityKernel3

The activity record for a kernel (CUDA 6.5(with sm\_52 support) onwards). (deprecated in CUDA 9.0).

## struct CUpti\_ActivityKernel4

The activity record for a kernel.

## struct CUpti\_ActivityMarker

The activity record providing a marker which is an instantaneous point in time. (deprecated in CUDA 8.0).

## struct CUpti\_ActivityMarker2

The activity record providing a marker which is an instantaneous point in time.

## struct CUpti\_ActivityMarkerData

The activity record providing detailed information for a marker.

## struct CUpti\_ActivityMemcpy

The activity record for memory copies.

## struct CUpti\_ActivityMemcpy2

The activity record for peer-to-peer memory copies.

## struct CUpti\_ActivityMemory

The activity record for memory.

## struct CUpti\_ActivityMemset

The activity record for memset.

## struct CUpti\_ActivityMetric

The activity record for a CUPTI metric.

## struct CUpti\_ActivityMetricInstance

The activity record for a CUPTI metric with instance information.

## struct CUpti\_ActivityModule

The activity record for a CUDA module.

## struct CUpti\_ActivityName

The activity record providing a name.

## struct CUpti\_ActivityNvLink

NVLink information. (deprecated in CUDA 9.0).

## struct CUpti\_ActivityNvLink2

NVLink information.

## union CUpti\_ActivityObjectKindId

Identifiers for object kinds as specified by CUpti\_ActivityObjectKind.

## struct CUpti\_ActivityOpenAcc

The base activity record for OpenAcc records.

## struct CUpti\_ActivityOpenAccData

The activity record for OpenACC data.

## struct CUpti\_ActivityOpenAccLaunch

The activity record for OpenACC launch.

## struct CUpti\_ActivityOpenAccOther

The activity record for OpenACC other.

## struct CUpti\_ActivityOverhead

The activity record for CUPTI and driver overheads.

## struct CUpti\_ActivityPCSampling

The activity record for PC sampling. (deprecated in CUDA 8.0).

## struct CUpti\_ActivityPCSampling2

The activity record for PC sampling. (deprecated in CUDA 9.0).

## struct CUpti\_ActivityPCSampling3

The activity record for PC sampling.

## struct CUpti\_ActivityPCSamplingConfig

PC sampling configuration structure.

## struct CUpti\_ActivityPCSamplingRecordInfo

The activity record for record status for PC sampling.

## struct CUpti\_ActivityPreemption

The activity record for a preemption of a CDP kernel.

## struct CUpti\_ActivitySharedAccess

The activity record for source-level shared access.

## struct CUpti\_ActivitySourceLocator

The activity record for source locator.

## struct CUpti\_ActivityStream

The activity record for CUDA stream.

## struct CUpti\_ActivitySynchronization

The activity record for synchronization management.

## struct CUpti\_ActivityUnifiedMemoryCounter

The activity record for Unified Memory counters (deprecated in CUDA 7.0).

## struct CUpti\_ActivityUnifiedMemoryCounter2

The activity record for Unified Memory counters (CUDA 7.0 and beyond).

## struct CUpti\_ActivityUnifiedMemoryCounterConfig

Unified Memory counters configuration structure.

## enum CUpti\_ActivityAttribute

Activity attributes.

These attributes are used to control the behavior of the activity API.

#### **Values**

#### CUPTI\_ACTIVITY\_ATTR\_DEVICE\_BUFFER\_SIZE = 0

The device memory size (in bytes) reserved for storing profiling data for non-CDP operations, especially for concurrent kernel tracing, for each buffer on a context. The value is a size\_t. Having larger buffer size means less flush operations but consumes more device memory. Having smaller buffer size increases the risk of dropping timestamps for kernel records if too many kernels are launched/replayed at one time. This value only applies to new buffer allocations. Set this value before initializing

CUDA or before creating a context to ensure it is considered for the following allocations. The default value is 8388608 (8MB). Note: The actual amount of device memory per buffer reserved by CUPTI might be larger.

#### CUPTI\_ACTIVITY\_ATTR\_DEVICE\_BUFFER\_SIZE\_CDP = 1

The device memory size (in bytes) reserved for storing profiling data for CDP operations for each buffer on a context. The value is a size\_t.Having larger buffer size means less flush operations but consumes more device memory. This value only applies to new allocations.Set this value before initializing CUDA or before creating a context to ensure it is considered for the following allocations.The default value is 8388608 (8MB).Note: The actual amount of device memory per context reserved by CUPTI might be larger.

#### CUPTI\_ACTIVITY\_ATTR\_DEVICE\_BUFFER\_POOL\_LIMIT = 2

The maximum number of memory buffers per context. The value is a size\_t.Buffers can be reused by the context. Increasing this value reduces the number of times CUPTI needs to flush the buffers. Setting this value will not modify the number of memory buffers currently stored. Set this value before initializing CUDA to ensure the limit is not exceeded. The default value is 100.

#### CUPTI\_ACTIVITY\_ATTR\_PROFILING\_SEMAPHORE\_POOL\_SIZE = 3

The profiling semaphore pool size reserved for storing profiling data for serialized kernels and memory operations for each context. The value is a size\_t.Having larger pool size means less semaphore query operations but consumes more device resources. Having smaller pool size increases the risk of dropping timestamps for kernel and memcpy records if too many kernels or memcpy are launched/replayed at one time. This value only applies to new pool allocations.Set this value before initializing CUDA or before creating a context to ensure it is considered for the following allocations.The default value is 65536.

#### CUPTI\_ACTIVITY\_ATTR\_PROFILING\_SEMAPHORE\_POOL\_LIMIT = 4

The maximum number of profiling semaphore pools per context. The value is a size\_t.Profiling semaphore pool can be reused by the context. Increasing this value reduces the number of times CUPTI needs to query semaphores in the pool. Setting this value will not modify the number of semaphore pools currently stored. Set this value before initializing CUDA to ensure the limit is not exceeded. The default value is 100.

CUPTI\_ACTIVITY\_ATTR\_DEVICE\_BUFFER\_FORCE\_INT = 0x7fffffff

## enum CUpti\_ActivityComputeApiKind

The kind of a compute API.

#### **Values**

CUPTI\_ACTIVITY\_COMPUTE\_API\_UNKNOWN = 0
The compute API is not known.

CUPTI\_ACTIVITY\_COMPUTE\_API\_CUDA = 1

The compute APIs are for CUDA.

#### CUPTI\_ACTIVITY\_COMPUTE\_API\_CUDA\_MPS = 2

The compute APIs are for CUDA running in MPS (Multi-Process Service) environment.

CUPTI\_ACTIVITY\_COMPUTE\_API\_FORCE\_INT = 0x7fffffff

## enum CUpti\_ActivityEnvironmentKind

The kind of environment data. Used to indicate what type of data is being reported by an environment activity record.

#### **Values**

#### **CUPTI ACTIVITY ENVIRONMENT UNKNOWN = 0**

Unknown data.

#### CUPTI\_ACTIVITY\_ENVIRONMENT\_SPEED = 1

The environment data is related to speed.

#### CUPTI\_ACTIVITY\_ENVIRONMENT\_TEMPERATURE = 2

The environment data is related to temperature.

#### CUPTI\_ACTIVITY\_ENVIRONMENT\_POWER = 3

The environment data is related to power.

#### CUPTI\_ACTIVITY\_ENVIRONMENT\_COOLING = 4

The environment data is related to cooling.

#### CUPTI\_ACTIVITY\_ENVIRONMENT\_COUNT

CUPTI\_ACTIVITY\_ENVIRONMENT\_KIND\_FORCE\_INT = 0x7fffffff

## enum CUpti\_ActivityFlag

Flags associated with activity records.

Activity record flags. Flags can be combined by bitwise OR to associated multiple flags with an activity record. Each flag is specific to a certain activity kind, as noted below.

#### **Values**

#### CUPTI\_ACTIVITY\_FLAG\_NONE = 0

Indicates the activity record has no flags.

#### CUPTI\_ACTIVITY\_FLAG\_DEVICE\_CONCURRENT\_KERNELS = 1<<0

Indicates the activity represents a device that supports concurrent kernel execution. Valid for CUPTI ACTIVITY KIND DEVICE.

#### CUPTI\_ACTIVITY\_FLAG\_DEVICE\_ATTRIBUTE\_CUDEVICE = 1<<0

Indicates if the activity represents a CUdevice\_attribute

value or a CUpti\_DeviceAttribute value. Valid for

CUPTI\_ACTIVITY\_KIND\_DEVICE\_ATTRIBUTE.

CUPTI\_ACTIVITY\_FLAG\_MEMCPY\_ASYNC = 1<<0

Indicates the activity represents an asynchronous memcpy operation. Valid for CUPTI\_ACTIVITY\_KIND\_MEMCPY.

#### CUPTI\_ACTIVITY\_FLAG\_MARKER\_INSTANTANEOUS = 1<<0

Indicates the activity represents an instantaneous marker. Valid for CUPTI\_ACTIVITY\_KIND\_MARKER.

#### CUPTI\_ACTIVITY\_FLAG\_MARKER\_START = 1<<1

Indicates the activity represents a region start marker. Valid for CUPTI\_ACTIVITY\_KIND\_MARKER.

#### CUPTI\_ACTIVITY\_FLAG\_MARKER\_END = 1<<2

Indicates the activity represents a region end marker. Valid for CUPTI\_ACTIVITY\_KIND\_MARKER.

#### CUPTI\_ACTIVITY\_FLAG\_MARKER\_SYNC\_ACQUIRE = 1<<3

Indicates the activity represents an attempt to acquire a user defined synchronization object. Valid for CUPTI ACTIVITY KIND MARKER.

#### CUPTI\_ACTIVITY\_FLAG\_MARKER\_SYNC\_ACQUIRE\_SUCCESS = 1<<4

Indicates the activity represents success in acquiring the user defined synchronization object. Valid for CUPTI\_ACTIVITY\_KIND\_MARKER.

#### CUPTI\_ACTIVITY\_FLAG\_MARKER\_SYNC\_ACQUIRE\_FAILED = 1<<5

Indicates the activity represents failure in acquiring the user defined synchronization object. Valid for CUPTI ACTIVITY KIND MARKER.

#### CUPTI\_ACTIVITY\_FLAG\_MARKER\_SYNC\_RELEASE = 1<<6

Indicates the activity represents releasing a reservation on user defined synchronization object. Valid for CUPTI ACTIVITY KIND MARKER.

#### CUPTI\_ACTIVITY\_FLAG\_MARKER\_COLOR\_NONE = 1<<0

Indicates the activity represents a marker that does not specify a color. Valid for CUPTI\_ACTIVITY\_KIND\_MARKER\_DATA.

#### CUPTI\_ACTIVITY\_FLAG\_MARKER\_COLOR\_ARGB = 1<<1

Indicates the activity represents a marker that specifies a color in alpha-red-green-blue format. Valid for CUPTI\_ACTIVITY\_KIND\_MARKER\_DATA.

#### CUPTI\_ACTIVITY\_FLAG\_GLOBAL\_ACCESS\_KIND\_SIZE\_MASK = 0xFF<<0

The number of bytes requested by each thread Valid for CUpti ActivityGlobalAccess3.

#### CUPTI\_ACTIVITY\_FLAG\_GLOBAL\_ACCESS\_KIND\_LOAD = 1<<8

If bit in this flag is set, the access was load, else it is a store access. Valid for CUpti\_ActivityGlobalAccess3.

#### CUPTI\_ACTIVITY\_FLAG\_GLOBAL\_ACCESS\_KIND\_CACHED = 1<<9

If this bit in flag is set, the load access was cached else it is uncached. Valid for CUpti\_ActivityGlobalAccess3.

#### CUPTI\_ACTIVITY\_FLAG\_METRIC\_OVERFLOWED = 1<<0

If this bit in flag is set, the metric value overflowed. Valid for CUpti\_ActivityMetric and CUpti\_ActivityMetricInstance.

#### CUPTI\_ACTIVITY\_FLAG\_METRIC\_VALUE\_INVALID = 1<<1

If this bit in flag is set, the metric value couldn't be calculated. This occurs when a value(s) required to calculate the metric is missing. Valid for CUpti\_ActivityMetric and CUpti\_ActivityMetricInstance.

#### CUPTI\_ACTIVITY\_FLAG\_INSTRUCTION\_VALUE\_INVALID = 1<<0

If this bit in flag is set, the source level metric value couldn't be calculated. This occurs when a value(s) required to calculate the source level metric cannot be evaluated. Valid for CUpti\_ActivityInstructionExecution.

#### CUPTI\_ACTIVITY\_FLAG\_INSTRUCTION\_CLASS\_MASK = 0xFF<<1

The mask for the instruction class, CUpti\_ActivityInstructionClass Valid for CUpti\_ActivityInstructionExecution and CUpti\_ActivityInstructionCorrelation

#### CUPTI\_ACTIVITY\_FLAG\_FLUSH\_FORCED = 1<<0

When calling cuptiActivityFlushAll, this flag can be set to force CUPTI to flush all records in the buffer, whether finished or not

#### CUPTI\_ACTIVITY\_FLAG\_SHARED\_ACCESS\_KIND\_SIZE\_MASK = 0xFF<<0

The number of bytes requested by each thread Valid for CUpti\_ActivitySharedAccess.

#### CUPTI\_ACTIVITY\_FLAG\_SHARED\_ACCESS\_KIND\_LOAD = 1<<8

If bit in this flag is set, the access was load, else it is a store access. Valid for CUpti ActivitySharedAccess.

#### CUPTI\_ACTIVITY\_FLAG\_MEMSET\_ASYNC = 1<<0

Indicates the activity represents an asynchronous memset operation. Valid for CUPTI\_ACTIVITY\_KIND\_MEMSET.

#### CUPTI\_ACTIVITY\_FLAG\_THRASHING\_IN\_CPU = 1<<0

Indicates the activity represents thrashing in CPU. Valid for counter of kind CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_THRASHING in CUPTI\_ACTIVITY\_KIND\_UNIFIED\_MEMORY\_COUNTER

#### CUPTI\_ACTIVITY\_FLAG\_THROTTLING\_IN\_CPU = 1<<0

Indicates the activity represents page throttling in CPU. Valid for counter of kind CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_THROTTLING in CUPTI\_ACTIVITY\_KIND\_UNIFIED\_MEMORY\_COUNTER

CUPTI\_ACTIVITY\_FLAG\_FORCE\_INT = 0x7fffffff

### enum CUpti\_ActivityInstructionClass

SASS instruction classification.

The sass instruction are broadly divided into different class. Each enum represents a classification.

#### **Values**

#### CUPTI\_ACTIVITY\_INSTRUCTION\_CLASS\_UNKNOWN = 0

The instruction class is not known.

#### CUPTI\_ACTIVITY\_INSTRUCTION\_CLASS\_FP\_32 = 1

Represents a 32 bit floating point operation.

CUPTI\_ACTIVITY\_INSTRUCTION\_CLASS\_FP\_64 = 2

Represents a 64 bit floating point operation.

#### CUPTI\_ACTIVITY\_INSTRUCTION\_CLASS\_INTEGER = 3

Represents an integer operation.

#### CUPTI\_ACTIVITY\_INSTRUCTION\_CLASS\_BIT\_CONVERSION = 4

Represents a bit conversion operation.

#### CUPTI\_ACTIVITY\_INSTRUCTION\_CLASS\_CONTROL\_FLOW = 5

Represents a control flow instruction.

#### CUPTI\_ACTIVITY\_INSTRUCTION\_CLASS\_GLOBAL = 6

Represents a global load-store instruction.

#### CUPTI\_ACTIVITY\_INSTRUCTION\_CLASS\_SHARED = 7

Represents a shared load-store instruction.

#### CUPTI\_ACTIVITY\_INSTRUCTION\_CLASS\_LOCAL = 8

Represents a local load-store instruction.

#### **CUPTI ACTIVITY INSTRUCTION CLASS GENERIC = 9**

Represents a generic load-store instruction.

#### CUPTI\_ACTIVITY\_INSTRUCTION\_CLASS\_SURFACE = 10

Represents a surface load-store instruction.

#### CUPTI\_ACTIVITY\_INSTRUCTION\_CLASS\_CONSTANT = 11

Represents a constant load instruction.

#### CUPTI\_ACTIVITY\_INSTRUCTION\_CLASS\_TEXTURE = 12

Represents a texture load-store instruction.

#### CUPTI\_ACTIVITY\_INSTRUCTION\_CLASS\_GLOBAL\_ATOMIC = 13

Represents a global atomic instruction.

#### CUPTI\_ACTIVITY\_INSTRUCTION\_CLASS\_SHARED\_ATOMIC = 14

Represents a shared atomic instruction.

#### CUPTI\_ACTIVITY\_INSTRUCTION\_CLASS\_SURFACE\_ATOMIC = 15

Represents a surface atomic instruction.

### $\hbox{\tt CUPTI\_ACTIVITY\_INSTRUCTION\_CLASS\_INTER\_THREAD\_COMMUNICATION}$

= 16

Represents a inter-thread communication instruction.

#### CUPTI\_ACTIVITY\_INSTRUCTION\_CLASS\_BARRIER = 17

Represents a barrier instruction.

#### CUPTI\_ACTIVITY\_INSTRUCTION\_CLASS\_MISCELLANEOUS = 18

Represents some miscellaneous instructions which do not fit in the above classification.

#### CUPTI\_ACTIVITY\_INSTRUCTION\_CLASS\_FP\_16 = 19

Represents a 16 bit floating point operation.

#### CUPTI\_ACTIVITY\_INSTRUCTION\_CLASS\_KIND\_FORCE\_INT = 0x7fffffff

## enum CUpti\_ActivityKind

The kinds of activity records.

Each activity record kind represents information about a GPU or an activity occurring on a CPU or GPU. Each kind is associated with a activity record structure that holds the information associated with the kind.

#### See also:

CUpti\_Activity

CUpti\_ActivityAPI

CUpti\_ActivityContext

CUpti\_ActivityDevice

CUpti\_ActivityDevice2

CUpti\_ActivityDeviceAttribute

CUpti\_ActivityEvent

CUpti\_ActivityEventInstance

CUpti\_ActivityKernel

CUpti\_ActivityKernel2

CUpti\_ActivityKernel3

CUpti\_ActivityKernel4

CUpti\_ActivityCdpKernel

CUpti\_ActivityPreemption

CUpti\_ActivityMemcpy

CUpti\_ActivityMemcpy2

CUpti\_ActivityMemset

CUpti\_ActivityMetric

CUpti\_ActivityMetricInstance

CUpti\_ActivityName

CUpti\_ActivityMarker

CUpti\_ActivityMarker2

CUpti\_ActivityMarkerData

CUpti\_ActivitySourceLocator

CUpti\_ActivityGlobalAccess

CUpti\_ActivityGlobalAccess2

CUpti\_ActivityGlobalAccess3

CUpti\_ActivityBranch

CUpti\_ActivityBranch2

CUpti\_ActivityOverhead

CUpti\_ActivityEnvironment

CUpti\_ActivityInstructionExecution

CUpti\_ActivityUnifiedMemoryCounter

CUpti\_ActivityFunction

CUpti\_ActivityModule

CUpti\_ActivitySharedAccess

CUpti\_ActivityPCSampling

CUpti\_ActivityPCSampling2

CUpti\_ActivityPCSampling3

 $CUpti\_Activity PCS ampling Record Info$ 

CUpti\_ActivityCudaEvent

CUpti\_ActivityStream

CUpti\_ActivitySynchronization

CUpti\_ActivityInstructionCorrelation

CUpti\_ActivityExternalCorrelation

CUpti\_ActivityUnifiedMemoryCounter2

CUpti\_ActivityOpenAccData

CUpti\_ActivityOpenAccLaunch

CUpti\_ActivityOpenAccOther

CUpti\_ActivityNvLink

CUpti\_ActivityNvLink2

CUpti\_ActivityMemory

#### **Values**

CUPTI\_ACTIVITY\_KIND\_INVALID = 0

The activity record is invalid.

#### CUPTI\_ACTIVITY\_KIND\_MEMCPY = 1

A host<->host, host<->device, or device<->device memory copy. The corresponding activity record structure is CUpti\_ActivityMemcpy.

#### CUPTI\_ACTIVITY\_KIND\_MEMSET = 2

A memory set executing on the GPU. The corresponding activity record structure is CUpti\_ActivityMemset.

#### CUPTI\_ACTIVITY\_KIND\_KERNEL = 3

A kernel executing on the GPU. The corresponding activity record structure is CUpti\_ActivityKernel4.

#### CUPTI\_ACTIVITY\_KIND\_DRIVER = 4

A CUDA driver API function execution. The corresponding activity record structure is CUpti\_ActivityAPI.

#### CUPTI\_ACTIVITY\_KIND\_RUNTIME = 5

A CUDA runtime API function execution. The corresponding activity record structure is CUpti\_ActivityAPI.

#### CUPTI\_ACTIVITY\_KIND\_EVENT = 6

An event value. The corresponding activity record structure is CUpti\_ActivityEvent.

#### CUPTI\_ACTIVITY\_KIND\_METRIC = 7

A metric value. The corresponding activity record structure is CUpti\_ActivityMetric.

#### CUPTI\_ACTIVITY\_KIND\_DEVICE = 8

Information about a device. The corresponding activity record structure is CUpti\_ActivityDevice2.

#### CUPTI\_ACTIVITY\_KIND\_CONTEXT = 9

Information about a context. The corresponding activity record structure is CUpti\_ActivityContext.

#### CUPTI\_ACTIVITY\_KIND\_CONCURRENT\_KERNEL = 10

A (potentially concurrent) kernel executing on the GPU. The corresponding activity record structure is CUpti\_ActivityKernel4.

#### CUPTI\_ACTIVITY\_KIND\_NAME = 11

Thread, device, context, etc. name. The corresponding activity record structure is CUpti\_ActivityName.

#### CUPTI\_ACTIVITY\_KIND\_MARKER = 12

Instantaneous, start, or end marker. The corresponding activity record structure is CUpti\_ActivityMarker2.

#### CUPTI\_ACTIVITY\_KIND\_MARKER\_DATA = 13

Extended, optional, data about a marker. The corresponding activity record structure is CUpti\_ActivityMarkerData.

#### CUPTI\_ACTIVITY\_KIND\_SOURCE\_LOCATOR = 14

Source information about source level result. The corresponding activity record structure is CUpti\_ActivitySourceLocator.

#### CUPTI\_ACTIVITY\_KIND\_GLOBAL\_ACCESS = 15

Results for source-level global access. The corresponding activity record structure is CUpti\_ActivityGlobalAccess3.

#### CUPTI\_ACTIVITY\_KIND\_BRANCH = 16

Results for source-level branch. The corresponding activity record structure is CUpti\_ActivityBranch2.

#### CUPTI\_ACTIVITY\_KIND\_OVERHEAD = 17

Overhead activity records. The corresponding activity record structure is CUpti\_ActivityOverhead.

#### CUPTI\_ACTIVITY\_KIND\_CDP\_KERNEL = 18

A CDP (CUDA Dynamic Parallel) kernel executing on the GPU. The corresponding activity record structure is CUpti\_ActivityCdpKernel. This activity can not be directly enabled or disabled. It is enabled and disabled through concurrent kernel activity i.e. \_CONCURRENT\_KERNEL

#### **CUPTI ACTIVITY KIND PREEMPTION = 19**

Preemption activity record indicating a preemption of a CDP (CUDA Dynamic Parallel) kernel executing on the GPU. The corresponding activity record structure is CUpti\_ActivityPreemption.

#### CUPTI\_ACTIVITY\_KIND\_ENVIRONMENT = 20

Environment activity records indicating power, clock, thermal, etc. levels of the GPU. The corresponding activity record structure is CUpti\_ActivityEnvironment.

#### CUPTI\_ACTIVITY\_KIND\_EVENT\_INSTANCE = 21

An event value associated with a specific event domain instance. The corresponding activity record structure is CUpti ActivityEventInstance.

#### CUPTI\_ACTIVITY\_KIND\_MEMCPY2 = 22

A peer to peer memory copy. The corresponding activity record structure is CUpti\_ActivityMemcpy2.

#### CUPTI\_ACTIVITY\_KIND\_METRIC\_INSTANCE = 23

A metric value associated with a specific metric domain instance. The corresponding activity record structure is CUpti\_ActivityMetricInstance.

#### CUPTI\_ACTIVITY\_KIND\_INSTRUCTION\_EXECUTION = 24

Results for source-level instruction execution. The corresponding activity record structure is CUpti\_ActivityInstructionExecution.

#### CUPTI\_ACTIVITY\_KIND\_UNIFIED\_MEMORY\_COUNTER = 25

Unified Memory counter record. The corresponding activity record structure is CUpti\_ActivityUnifiedMemoryCounter2.

#### CUPTI\_ACTIVITY\_KIND\_FUNCTION = 26

Device global/function record. The corresponding activity record structure is CUpti\_ActivityFunction.

#### CUPTI\_ACTIVITY\_KIND\_MODULE = 27

CUDA Module record. The corresponding activity record structure is CUpti\_ActivityModule.

#### CUPTI\_ACTIVITY\_KIND\_DEVICE\_ATTRIBUTE = 28

A device attribute value. The corresponding activity record structure is CUpti\_ActivityDeviceAttribute.

#### CUPTI\_ACTIVITY\_KIND\_SHARED\_ACCESS = 29

Results for source-level shared access. The corresponding activity record structure is CUpti\_ActivitySharedAccess.

#### CUPTI\_ACTIVITY\_KIND\_PC\_SAMPLING = 30

Enable PC sampling for kernels. This will serialize kernels. The corresponding activity record structure is CUpti\_ActivityPCSampling3.

#### CUPTI\_ACTIVITY\_KIND\_PC\_SAMPLING\_RECORD\_INFO = 31

Summary information about PC sampling records. The corresponding activity record structure is CUpti\_ActivityPCSamplingRecordInfo.

#### CUPTI\_ACTIVITY\_KIND\_INSTRUCTION\_CORRELATION = 32

SASS/Source line-by-line correlation record. This will generate sass/source correlation for functions that have source level analysis or pc sampling results.

The records will be generated only when either of source level analysis or pc sampling activity is enabled. The corresponding activity record structure is CUpti\_ActivityInstructionCorrelation.

#### CUPTI\_ACTIVITY\_KIND\_OPENACC\_DATA = 33

OpenACC data events. The corresponding activity record structure is CUpti\_ActivityOpenAccData.

#### CUPTI\_ACTIVITY\_KIND\_OPENACC\_LAUNCH = 34

OpenACC launch events. The corresponding activity record structure is CUpti ActivityOpenAccLaunch.

#### CUPTI\_ACTIVITY\_KIND\_OPENACC\_OTHER = 35

OpenACC other events. The corresponding activity record structure is CUpti\_ActivityOpenAccOther.

#### CUPTI\_ACTIVITY\_KIND\_CUDA\_EVENT = 36

Information about a CUDA event. The corresponding activity record structure is CUpti\_ActivityCudaEvent.

#### CUPTI\_ACTIVITY\_KIND\_STREAM = 37

Information about a CUDA stream. The corresponding activity record structure is CUpti\_ActivityStream.

#### CUPTI\_ACTIVITY\_KIND\_SYNCHRONIZATION = 38

Records for synchronization management. The corresponding activity record structure is CUpti\_ActivitySynchronization.

#### CUPTI\_ACTIVITY\_KIND\_EXTERNAL\_CORRELATION = 39

Records for correlation of different programming APIs. The corresponding activity record structure is CUpti\_ActivityExternalCorrelation.

#### CUPTI\_ACTIVITY\_KIND\_NVLINK = 40

NVLink information. The corresponding activity record structure is CUpti\_ActivityNvLink2.

#### CUPTI\_ACTIVITY\_KIND\_INSTANTANEOUS\_EVENT = 41

Instantaneous Event information. The corresponding activity record structure is CUpti\_ActivityInstantaneousEvent.

#### CUPTI\_ACTIVITY\_KIND\_INSTANTANEOUS\_EVENT\_INSTANCE = 42

Instantaneous Event information for a specific event domain instance. The corresponding activity record structure is CUpti\_ActivityInstantaneousEventInstance

#### CUPTI\_ACTIVITY\_KIND\_INSTANTANEOUS\_METRIC = 43

Instantaneous Metric information The corresponding activity record structure is CUpti\_ActivityInstantaneousMetric.

#### CUPTI\_ACTIVITY\_KIND\_INSTANTANEOUS\_METRIC\_INSTANCE = 44

Instantaneous Metric information for a specific metric domain instance. The corresponding activity record structure is CUpti\_ActivityInstantaneousMetricInstance.

CUPTI\_ACTIVITY\_KIND\_MEMORY = 45 CUPTI\_ACTIVITY\_KIND\_FORCE\_INT = 0x7fffffff

## enum CUpti\_ActivityLaunchType

The type of the CUDA kernel launch.

#### **Values**

#### CUPTI\_ACTIVITY\_LAUNCH\_TYPE\_REGULAR = 0

The kernel was launched via a regular kernel call

#### CUPTI\_ACTIVITY\_LAUNCH\_TYPE\_COOPERATIVE\_SINGLE\_DEVICE = 1

The kernel was launched via API cudaLaunchCooperativeKernel() or cuLaunchCooperativeKernel()

#### CUPTI\_ACTIVITY\_LAUNCH\_TYPE\_COOPERATIVE\_MULTI\_DEVICE = 2

The kernel was launched via API cudaLaunchCooperativeKernelMultiDevice() or cuLaunchCooperativeKernelMultiDevice()

## enum CUpti\_ActivityMemcpyKind

The kind of a memory copy, indicating the source and destination targets of the copy.

Each kind represents the source and destination targets of a memory copy. Targets are host, device, and array.

#### **Values**

#### CUPTI\_ACTIVITY\_MEMCPY\_KIND\_UNKNOWN = 0

The memory copy kind is not known.

#### CUPTI\_ACTIVITY\_MEMCPY\_KIND\_HTOD = 1

A host to device memory copy.

#### CUPTI\_ACTIVITY\_MEMCPY\_KIND\_DTOH = 2

A device to host memory copy.

CUPTI\_ACTIVITY\_MEMCPY\_KIND\_HTOA = 3

A host to device array memory copy.

#### CUPTI\_ACTIVITY\_MEMCPY\_KIND\_ATOH = 4

A device array to host memory copy.

#### CUPTI\_ACTIVITY\_MEMCPY\_KIND\_ATOA = 5

A device array to device array memory copy.

#### CUPTI\_ACTIVITY\_MEMCPY\_KIND\_ATOD = 6

A device array to device memory copy.

#### CUPTI\_ACTIVITY\_MEMCPY\_KIND\_DTOA = 7

A device to device array memory copy.

#### CUPTI\_ACTIVITY\_MEMCPY\_KIND\_DTOD = 8

A device to device memory copy on the same device.

#### CUPTI\_ACTIVITY\_MEMCPY\_KIND\_HTOH = 9

A host to host memory copy.

#### **CUPTI ACTIVITY MEMCPY KIND PTOP = 10**

A peer to peer memory copy across different devices.

CUPTI\_ACTIVITY\_MEMCPY\_KIND\_FORCE\_INT = 0x7fffffff

## enum CUpti\_ActivityMemoryKind

The kinds of memory accessed by a memory operation/copy.

Each kind represents the type of the memory accessed by a memory operation/copy.

#### **Values**

#### CUPTI\_ACTIVITY\_MEMORY\_KIND\_UNKNOWN = 0

The memory kind is unknown.

#### CUPTI\_ACTIVITY\_MEMORY\_KIND\_PAGEABLE = 1

The memory is pageable.

#### CUPTI\_ACTIVITY\_MEMORY\_KIND\_PINNED = 2

The memory is pinned.

#### CUPTI\_ACTIVITY\_MEMORY\_KIND\_DEVICE = 3

The memory is on the device.

#### CUPTI\_ACTIVITY\_MEMORY\_KIND\_ARRAY = 4

The memory is an array.

#### CUPTI\_ACTIVITY\_MEMORY\_KIND\_MANAGED = 5

The memory is managed

#### CUPTI\_ACTIVITY\_MEMORY\_KIND\_DEVICE\_STATIC = 6

The memory is device static

#### CUPTI\_ACTIVITY\_MEMORY\_KIND\_MANAGED\_STATIC = 7

The memory is managed static

CUPTI\_ACTIVITY\_MEMORY\_KIND\_FORCE\_INT = 0x7fffffff

## enum CUpti\_ActivityObjectKind

The kinds of activity objects.

#### See also:

CUpti\_ActivityObjectKindId

#### **Values**

CUPTI\_ACTIVITY\_OBJECT\_UNKNOWN = 0

The object kind is not known.

CUPTI\_ACTIVITY\_OBJECT\_PROCESS = 1

A process.

CUPTI\_ACTIVITY\_OBJECT\_THREAD = 2

A thread.

CUPTI\_ACTIVITY\_OBJECT\_DEVICE = 3

A device.

CUPTI\_ACTIVITY\_OBJECT\_CONTEXT = 4

A context.

CUPTI\_ACTIVITY\_OBJECT\_STREAM = 5

A stream.

CUPTI\_ACTIVITY\_OBJECT\_FORCE\_INT = 0x7fffffff

## enum CUpti\_ActivityOverheadKind

The kinds of activity overhead.

#### **Values**

CUPTI ACTIVITY OVERHEAD UNKNOWN = 0

The overhead kind is not known.

CUPTI\_ACTIVITY\_OVERHEAD\_DRIVER\_COMPILER = 1

Compiler(JIT) overhead.

CUPTI\_ACTIVITY\_OVERHEAD\_CUPTI\_BUFFER\_FLUSH = 1<<16

Activity buffer flush overhead.

CUPTI\_ACTIVITY\_OVERHEAD\_CUPTI\_INSTRUMENTATION = 2<<16

CUPTI instrumentation overhead.

CUPTI\_ACTIVITY\_OVERHEAD\_CUPTI\_RESOURCE = 3<<16

CUPTI resource creation and destruction overhead.

CUPTI\_ACTIVITY\_OVERHEAD\_FORCE\_INT = 0x7fffffff

## enum CUpti\_ActivityPartitionedGlobalCacheConfig

Partitioned global caching option.

#### **Values**

CUPTI\_ACTIVITY\_PARTITIONED\_GLOBAL\_CACHE\_CONFIG\_UNKNOWN = 0
Partitioned global cache config unknown.

CUPTI\_ACTIVITY\_PARTITIONED\_GLOBAL\_CACHE\_CONFIG\_NOT\_SUPPORTED = 1

Partitioned global cache not supported.

**CUPTI\_ACTIVITY\_PARTITIONED\_GLOBAL\_CACHE\_CONFIG\_OFF = 2**Partitioned global cache config off.

CUPTI\_ACTIVITY\_PARTITIONED\_GLOBAL\_CACHE\_CONFIG\_ON = 3
Partitioned global cache config on.

CUPTI\_ACTIVITY\_PARTITIONED\_GLOBAL\_CACHE\_CONFIG\_FORCE\_INT = 0x7fffffff

## enum CUpti\_ActivityPCSamplingPeriod

Sampling period for PC sampling method Sampling period can be set using /ref cuptiActivityConfigurePCSampling.

#### **Values**

CUPTI\_ACTIVITY\_PC\_SAMPLING\_PERIOD\_INVALID = 0

The PC sampling period is not set.

CUPTI\_ACTIVITY\_PC\_SAMPLING\_PERIOD\_MIN = 1

Minimum sampling period available on the device.

CUPTI\_ACTIVITY\_PC\_SAMPLING\_PERIOD\_LOW = 2

Sampling period in lower range.

CUPTI\_ACTIVITY\_PC\_SAMPLING\_PERIOD\_MID = 3

Medium sampling period.

CUPTI\_ACTIVITY\_PC\_SAMPLING\_PERIOD\_HIGH = 4

Sampling period in higher range.

CUPTI\_ACTIVITY\_PC\_SAMPLING\_PERIOD\_MAX = 5

Maximum sampling period available on the device.

CUPTI\_ACTIVITY\_PC\_SAMPLING\_PERIOD\_FORCE\_INT = 0x7ffffffff

## enum CUpti\_ActivityPCSamplingStallReason

The stall reason for PC sampling activity.

#### **Values**

CUPTI\_ACTIVITY\_PC\_SAMPLING\_STALL\_INVALID = 0

Invalid reason

CUPTI\_ACTIVITY\_PC\_SAMPLING\_STALL\_NONE = 1

No stall, instruction is selected for issue

CUPTI\_ACTIVITY\_PC\_SAMPLING\_STALL\_INST\_FETCH = 2

Warp is blocked because next instruction is not yet available, because of instruction cache miss, or because of branching effects

#### CUPTI\_ACTIVITY\_PC\_SAMPLING\_STALL\_EXEC\_DEPENDENCY = 3

Instruction is waiting on an arithmatic dependency

#### CUPTI\_ACTIVITY\_PC\_SAMPLING\_STALL\_MEMORY\_DEPENDENCY = 4

Warp is blocked because it is waiting for a memory access to complete.

#### CUPTI\_ACTIVITY\_PC\_SAMPLING\_STALL\_TEXTURE = 5

Texture sub-system is fully utilized or has too many outstanding requests.

#### CUPTI\_ACTIVITY\_PC\_SAMPLING\_STALL\_SYNC = 6

Warp is blocked as it is waiting at \_\_syncthreads() or at memory barrier.

## CUPTI\_ACTIVITY\_PC\_SAMPLING\_STALL\_CONSTANT\_MEMORY\_DEPENDENCY = 7

Warp is blocked waiting for \_\_constant\_\_ memory and immediate memory access to complete.

#### CUPTI\_ACTIVITY\_PC\_SAMPLING\_STALL\_PIPE\_BUSY = 8

Compute operation cannot be performed due to the required resources not being available.

#### CUPTI\_ACTIVITY\_PC\_SAMPLING\_STALL\_MEMORY\_THROTTLE = 9

Warp is blocked because there are too many pending memory operations. In Kepler architecture it often indicates high number of memory replays.

#### CUPTI\_ACTIVITY\_PC\_SAMPLING\_STALL\_NOT\_SELECTED = 10

Warp was ready to issue, but some other warp issued instead.

#### **CUPTI ACTIVITY PC SAMPLING STALL OTHER = 11**

Miscellaneous reasons

## CUPTI\_ACTIVITY\_PC\_SAMPLING\_STALL\_SLEEPING = 12 Sleeping.

CUPTI\_ACTIVITY\_PC\_SAMPLING\_STALL\_FORCE\_INT = 0x7fffffff

## enum CUpti\_ActivityPreemptionKind

The kind of a preemption activity.

#### **Values**

#### CUPTI\_ACTIVITY\_PREEMPTION\_KIND\_UNKNOWN = 0

The preemption kind is not known.

#### CUPTI\_ACTIVITY\_PREEMPTION\_KIND\_SAVE = 1

Preemption to save CDP block.

#### CUPTI\_ACTIVITY\_PREEMPTION\_KIND\_RESTORE = 2

Preemption to restore CDP block.

CUPTI\_ACTIVITY\_PREEMPTION\_KIND\_FORCE\_INT = 0x7fffffff

## enum CUpti\_ActivityStreamFlag

stream type.

The types of stream to be used with CUpti\_ActivityStream.

#### **Values**

CUPTI\_ACTIVITY\_STREAM\_CREATE\_FLAG\_UNKNOWN = 0 Unknown data.

CUPTI\_ACTIVITY\_STREAM\_CREATE\_FLAG\_DEFAULT = 1
Default stream.

CUPTI\_ACTIVITY\_STREAM\_CREATE\_FLAG\_NON\_BLOCKING = 2 Non-blocking stream.

CUPTI\_ACTIVITY\_STREAM\_CREATE\_FLAG\_NULL = 3
Null stream.

CUPTI\_ACTIVITY\_STREAM\_CREATE\_MASK = 0xFFFF
Stream create Mask

CUPTI\_ACTIVITY\_STREAM\_CREATE\_FLAG\_FORCE\_INT = 0x7fffffff

## enum CUpti\_ActivitySynchronizationType

Synchronization type.

The types of synchronization to be used with CUpti\_ActivitySynchronization.

#### **Values**

CUPTI\_ACTIVITY\_SYNCHRONIZATION\_TYPE\_UNKNOWN = 0 Unknown data.

CUPTI\_ACTIVITY\_SYNCHRONIZATION\_TYPE\_EVENT\_SYNCHRONIZE = 1 Event synchronize API.

CUPTI\_ACTIVITY\_SYNCHRONIZATION\_TYPE\_STREAM\_WAIT\_EVENT = 2
Stream wait event API.

CUPTI\_ACTIVITY\_SYNCHRONIZATION\_TYPE\_STREAM\_SYNCHRONIZE = 3
Stream synchronize API.

CUPTI\_ACTIVITY\_SYNCHRONIZATION\_TYPE\_CONTEXT\_SYNCHRONIZE = 4
Context synchronize API.

CUPTI\_ACTIVITY\_SYNCHRONIZATION\_TYPE\_FORCE\_INT = 0x7fffffff

## enum CUpti\_ActivityThreadIdType

Thread-Id types.

CUPTI uses different methods to obtain the thread-id depending on the support and the underlying platform. This enum documents these methods for each type. APIs cuptiSetThreadIdType and cuptiGetThreadIdType can be used to set and get the thread-id type.

#### **Values**

#### CUPTI\_ACTIVITY\_THREAD\_ID\_TYPE\_DEFAULT = 0

Default type Windows uses API GetCurrentThreadId() Linux/Mac/Android/QNX use POSIX pthread API pthread\_self()

#### CUPTI\_ACTIVITY\_THREAD\_ID\_TYPE\_SYSTEM = 1

This type is based on the system API available on the underlying platform and thread-id obtained is supposed to be unique for the process lifetime. Windows uses API GetCurrentThreadId() Linux uses syscall SYS\_gettid Mac uses syscall SYS\_thread\_selfid Android/QNX use gettid()

CUPTI\_ACTIVITY\_THREAD\_ID\_TYPE\_FORCE\_INT = 0x7ffffffff

## enum CUpti\_ActivityUnifiedMemoryAccessType

Memory access type for unified memory page faults.

This is valid for

CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_GPU\_PAGE\_FAULT and CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_CPU\_PAGE\_FAULT\_COUNT

#### **Values**

#### CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_ACCESS\_TYPE\_UNKNOWN = 0

The unified memory access type is not known

#### CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_ACCESS\_TYPE\_READ = 1

The page fault was triggered by read memory instruction

#### CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_ACCESS\_TYPE\_WRITE = 2

The page fault was triggered by write memory instruction

#### CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_ACCESS\_TYPE\_ATOMIC = 3

The page fault was triggered by atomic memory instruction

#### CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_ACCESS\_TYPE\_PREFETCH = 4

The page fault was triggered by memory prefetch operation

## enum CUpti\_ActivityUnifiedMemoryCounterKind

Kind of the Unified Memory counter.

Many activities are associated with Unified Memory mechanism; among them are transfer from host to device, device to host, page fault at host side.

#### **Values**

#### CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_UNKNOWN = 0

The unified memory counter kind is not known.

## CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_BYTES\_TRANSFER\_HTOD = 1

Number of bytes transfered from host to device

## CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_BYTES\_TRANSFER\_DTOH = 2

Number of bytes transfered from device to host

## CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_CPU\_PAGE\_FAULT\_COUNT = 3

Number of CPU page faults, this is only supported on 64 bit Linux and Mac platforms

## CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_GPU\_PAGE\_FAULT = 4

Number of GPU page faults, this is only supported on devices with compute capability 6.0 and higher and 64 bit Linux platforms

#### CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_THRASHING = 5

Thrashing occurs when data is frequently accessed by multiple processors and has to be constantly migrated around to achieve data locality. In this case the overhead of migration may exceed the benefits of locality. This is only supported on 64 bit Linux platforms.

#### CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_THROTTLING = 6

Throttling is a prevention technique used by the driver to avoid further thrashing. Here, the driver doesn't service the fault for one of the contending processors for a specific period of time, so that the other processor can run at full-speed. This is only supported on 64 bit Linux platforms.

#### CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_REMOTE\_MAP = 7

In case throttling does not help, the driver tries to pin the memory to a processor for a specific period of time. One of the contending processors will have slow access to the memory, while the other will have fast access. This is only supported on 64 bit Linux platforms.

## CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_BYTES\_TRANSFER\_DTOD = 8

Number of bytes transferred from one device to another device. This is only supported on 64 bit Linux platforms.

CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_COUNT CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_FORCE\_INT = 0x7fffffff

## enum CUpti\_ActivityUnifiedMemoryCounterScope

Scope of the unified memory counter (deprecated in CUDA 7.0).

#### **Values**

## ${\bf CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_SCOPE\_UNKNOWN=0}$

The unified memory counter scope is not known.

## CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_SCOPE\_PROCESS\_SINGLE\_DEVICE = 1

Collect unified memory counter for single process on one device

## CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_SCOPE\_PROCESS\_ALL\_DEVICES = 2

Collect unified memory counter for single process across all devices

CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_SCOPE\_COUNT

CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_SCOPE\_FORCE\_INT =

0x7fffffff

## enum CUpti\_ActivityUnifiedMemoryMigrationCause

Migration cause of the Unified Memory counter.

This is valid for

CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_BYTES\_TRANSFER\_HTOD and

CUPTI ACTIVITY UNIFIED MEMORY COUNTER KIND BYTES TRANSFER DTOH

#### **Values**

## CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_MIGRATION\_CAUSE\_UNKNOWN = 0 The unified memory migration cause is not known

# CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_MIGRATION\_CAUSE\_USER = 1 The unified memory migrated due to an explicit call from the user e.g. cudaMemPrefetchAsync

- CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_MIGRATION\_CAUSE\_COHERENCE = 2
  The unified memory migrated to guarantee data coherence e.g. CPU/GPU faults on Pascal+ and kernel launch on pre-Pascal GPUs
- CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_MIGRATION\_CAUSE\_PREFETCH = 3

  The unified memory was speculatively migrated by the UVM driver before being accessed by the destination processor to improve performance
- CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_MIGRATION\_CAUSE\_EVICTION = 4

  The unified memory migrated to the CPU because it was evicted to make room for another block of memory on the GPU

## enum CUpti\_DevType

The device type for device connected to NVLink.

#### **Values**

CUPTI\_DEV\_TYPE\_INVALID = 0

CUPTI\_DEV\_TYPE\_GPU = 1

The device type is GPU.

CUPTI\_DEV\_TYPE\_NPU = 2

The device type is NVLink processing unit in CPU.

CUPTI\_DEV\_TYPE\_FORCE\_INT = 0x7fffffff

## enum CUpti\_EnvironmentClocksThrottleReason

Reasons for clock throttling.

The possible reasons that a clock can be throttled. There can be more than one reason that a clock is being throttled so these types can be combined by bitwise OR. These are used in the clocksThrottleReason field in the Environment Activity Record.

#### **Values**

#### CUPTI\_CLOCKS\_THROTTLE\_REASON\_GPU\_IDLE = 0x00000001

Nothing is running on the GPU and the clocks are dropping to idle state.

CUPTI\_CLOCKS\_THROTTLE\_REASON\_USER\_DEFINED\_CLOCKS = 0x000000002 The GPU clocks are limited by a user specified limit.

#### CUPTI\_CLOCKS\_THROTTLE\_REASON\_SW\_POWER\_CAP = 0x00000004

A software power scaling algorithm is reducing the clocks below requested clocks.

#### CUPTI\_CLOCKS\_THROTTLE\_REASON\_HW\_SLOWDOWN = 0x00000008

Hardware slowdown to reduce the clock by a factor of two or more is engaged. This is an indicator of one of the following: 1) Temperature is too high, 2) External power brake assertion is being triggered (e.g. by the system power supply), 3) Change in power state.

## CUPTI\_CLOCKS\_THROTTLE\_REASON\_UNKNOWN = 0x80000000

Some unspecified factor is reducing the clocks.

#### CUPTI\_CLOCKS\_THROTTLE\_REASON\_UNSUPPORTED = 0x40000000 Throttle reason is not supported for this GPU.

CUPTI\_CLOCKS\_THROTTLE\_REASON\_NONE = 0x000000000
No clock throttling.

CUPTI\_CLOCKS\_THROTTLE\_REASON\_FORCE\_INT = 0x7fffffff

### enum CUpti\_ExternalCorrelationKind

The kind of external APIs supported for correlation.

Custom correlation kinds are reserved for usage in external tools.

#### See also:

CUpti\_ActivityExternalCorrelation

#### **Values**

CUPTI\_EXTERNAL\_CORRELATION\_KIND\_INVALID = 0
CUPTI\_EXTERNAL\_CORRELATION\_KIND\_UNKNOWN = 1
CUPTI\_EXTERNAL\_CORRELATION\_KIND\_OPENACC = 2
CUPTI\_EXTERNAL\_CORRELATION\_KIND\_CUSTOM0 = 3
CUPTI\_EXTERNAL\_CORRELATION\_KIND\_CUSTOM1 = 4
CUPTI\_EXTERNAL\_CORRELATION\_KIND\_CUSTOM2 = 5

#### CUPTI\_EXTERNAL\_CORRELATION\_KIND\_SIZE CUPTI\_EXTERNAL\_CORRELATION\_KIND\_FORCE\_INT = 0x7fffffff

## enum CUpti\_LinkFlag

Link flags.

Describes link properties, to be used with CUpti\_ActivityNvLink.

#### **Values**

CUPTI\_LINK\_FLAG\_INVALID = 0
CUPTI\_LINK\_FLAG\_PEER\_ACCESS = (1<<1)
 Is peer to peer access supported by this link.

CUPTI\_LINK\_FLAG\_SYSMEM\_ACCESS = (1<<2)
 Is system memory access supported by this link.

CUPTI\_LINK\_FLAG\_PEER\_ATOMICS = (1<<3)
 Is peer atomic access supported by this link.

CUPTI\_LINK\_FLAG\_SYSMEM\_ATOMICS = (1<<4)
 Is system memory atomic access supported by this link.

CUPTI\_LINK\_FLAG\_FORCE\_INT = 0x7ffffffff

## enum CUpti\_OpenAccConstructKind

The OpenAcc parent construct kind for OpenAcc activity records.

#### **Values**

CUPTI\_OPENACC\_CONSTRUCT\_KIND\_UNKNOWN = 0 CUPTI OPENACC CONSTRUCT KIND PARALLEL = 1 CUPTI\_OPENACC\_CONSTRUCT\_KIND\_KERNELS = 2 CUPTI\_OPENACC\_CONSTRUCT\_KIND\_LOOP = 3 CUPTI\_OPENACC\_CONSTRUCT\_KIND\_DATA = 4 CUPTI\_OPENACC\_CONSTRUCT\_KIND\_ENTER\_DATA = 5 CUPTI\_OPENACC\_CONSTRUCT\_KIND\_EXIT\_DATA = 6 CUPTI OPENACC CONSTRUCT KIND HOST DATA = 7 CUPTI\_OPENACC\_CONSTRUCT\_KIND\_ATOMIC = 8 CUPTI\_OPENACC\_CONSTRUCT\_KIND\_DECLARE = 9 CUPTI OPENACC CONSTRUCT KIND INIT = 10 CUPTI\_OPENACC\_CONSTRUCT\_KIND\_SHUTDOWN = 11 CUPTI\_OPENACC\_CONSTRUCT\_KIND\_SET = 12 CUPTI OPENACC CONSTRUCT KIND UPDATE = 13 CUPTI\_OPENACC\_CONSTRUCT\_KIND\_ROUTINE = 14 CUPTI\_OPENACC\_CONSTRUCT\_KIND\_WAIT = 15 CUPTI\_OPENACC\_CONSTRUCT\_KIND\_RUNTIME\_API = 16 CUPTI\_OPENACC\_CONSTRUCT\_KIND\_FORCE\_INT = 0x7fffffff

## enum CUpti\_OpenAccEventKind

The OpenAcc event kind for OpenAcc activity records.

#### See also:

CUpti\_ActivityKindOpenAcc

#### **Values**

```
CUPTI OPENACC EVENT KIND INVALID = 0
CUPTI_OPENACC_EVENT_KIND_DEVICE_INIT = 1
CUPTI_OPENACC_EVENT_KIND_DEVICE_SHUTDOWN = 2
CUPTI OPENACC EVENT KIND RUNTIME SHUTDOWN = 3
CUPTI_OPENACC_EVENT_KIND_ENQUEUE_LAUNCH = 4
CUPTI_OPENACC_EVENT_KIND_ENQUEUE_UPLOAD = 5
CUPTI_OPENACC_EVENT_KIND_ENQUEUE_DOWNLOAD = 6
CUPTI_OPENACC_EVENT_KIND_WAIT = 7
CUPTI_OPENACC_EVENT_KIND_IMPLICIT_WAIT = 8
CUPTI_OPENACC_EVENT_KIND_COMPUTE_CONSTRUCT = 9
CUPTI_OPENACC_EVENT_KIND_UPDATE = 10
CUPTI_OPENACC_EVENT_KIND_ENTER_DATA = 11
CUPTI OPENACC EVENT KIND EXIT DATA = 12
CUPTI_OPENACC_EVENT_KIND_CREATE = 13
CUPTI_OPENACC_EVENT_KIND_DELETE = 14
CUPTI_OPENACC_EVENT_KIND_ALLOC = 15
CUPTI_OPENACC_EVENT_KIND_FREE = 16
CUPTI_OPENACC_EVENT_KIND_FORCE_INT = 0x7fffffff
```

# typedef (\*CUpti\_BuffersCallbackCompleteFunc) (CUcontext context, uint32\_t streamId, uint8\_t\* buffer, size\_t size, size\_t validSize)

Function type for callback used by CUPTI to return a buffer of activity records.

This callback function returns to the CUPTI client a buffer containing activity records. The buffer contains <code>validSize</code> bytes of activity records which should be read using cuptiActivityGetNextRecord. The number of dropped records can be read using cuptiActivityGetNumDroppedRecords. After this call CUPTI relinquished ownership of the buffer and will not use it anymore. The client may return the buffer to CUPTI using the CUpti\_BuffersCallbackRequestFunc callback. Note: CUDA 6.0 onwards, all buffers returned by this callback are global buffers i.e. there is no context/stream specific buffer. User needs to parse the global buffer to extract the context/stream specific activity records.

# typedef (\*CUpti\_BuffersCallbackRequestFunc) (uint8\_t\* \*buffer, size\_t\* size, size\_t\* maxNumRecords)

Function type for callback used by CUPTI to request an empty buffer for storing activity records.

This callback function signals the CUPTI client that an activity buffer is needed by CUPTI. The activity buffer is used by CUPTI to store activity records. The callback function can decline the request by setting \*buffer to NULL. In this case CUPTI may drop activity records.

# CUptiResult cuptiActivityConfigurePCSampling (CUcontext ctx, CUpti\_ActivityPCSamplingConfig\*config)

Set PC sampling configuration.

#### **Parameters**

ctx

The context

#### config

A pointer to CUpti\_ActivityPCSamplingConfig structure containing PC sampling configuration.

#### Returns

- CUPTI SUCCESS
- CUPTI\_ERROR\_INVALID\_OPERATION

if this api is called while some valid event collection method is set.

CUPTI ERROR INVALID PARAMETER

if config is NULL or any parameter in the config structures is not a valid value

CUPTI\_ERROR\_NOT\_SUPPORTED

Indicates that the system/device does not support the unified memory counters

# CUptiResult cuptiActivityConfigureUnifiedMemoryCounter

# (CUpti\_ActivityUnifiedMemoryCounterConfig \*config, uint32\_t count)

Set Unified Memory Counter configuration.

#### **Parameters**

#### config

A pointer to CUpti\_ActivityUnifiedMemoryCounterConfig structures containing Unified Memory counter configuration.

#### count

Number of Unified Memory counter configuration structures

#### Returns

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_INVALID\_PARAMETER

if config is NULL or any parameter in the config structures is not a valid value

- ► CUPTI\_ERROR\_UM\_PROFILING\_NOT\_SUPPORTED
  - One potential reason is that platform (OS/arch) does not support the unified memory counters
- CUPTI\_ERROR\_UM\_PROFILING\_NOT\_SUPPORTED\_ON\_DEVICE
  - Indicates that the device does not support the unified memory counters
- CUPTI\_ERROR\_UM\_PROFILING\_NOT\_SUPPORTED\_ON\_NON\_P2P\_DEVICES

Indicates that multi-GPU configuration without P2P support between any pair of devices does not support the unified memory counters

# CUptiResult cuptiActivityDisable (CUpti\_ActivityKind kind)

Disable collection of a specific kind of activity record.

#### **Parameters**

#### kind

The kind of activity record to stop collecting

#### Returns

- CUPTI SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED

#### CUPTI\_ERROR\_INVALID\_KIND

if the activity kind is not supported

#### Description

Disable collection of a specific kind of activity record. Multiple kinds can be disabled by calling this function multiple times. By default all activity kinds are disabled for collection.

# CUptiResult cuptiActivityDisableContext (CUcontext context, CUpti\_ActivityKind kind)

Disable collection of a specific kind of activity record for a context.

#### **Parameters**

#### context

The context for which activity is to be disabled

#### kind

The kind of activity record to stop collecting

#### Returns

- CUPTI SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_INVALID\_KIND

if the activity kind is not supported

#### Description

Disable collection of a specific kind of activity record for a context. This setting done by this API will supersede the global settings for activity records. Multiple kinds can be enabled by calling this function multiple times.

# CUptiResult cuptiActivityEnable (CUpti\_ActivityKind kind)

Enable collection of a specific kind of activity record.

#### **Parameters**

#### kind

The kind of activity record to collect

#### Returns

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_NOT\_COMPATIBLE
  - if the activity kind cannot be enabled
- CUPTI\_ERROR\_INVALID\_KIND
  - if the activity kind is not supported

#### Description

Enable collection of a specific kind of activity record. Multiple kinds can be enabled by calling this function multiple times. By default all activity kinds are disabled for collection.

# CUptiResult cuptiActivityEnableContext (CUcontext context, CUpti\_ActivityKind kind)

Enable collection of a specific kind of activity record for a context.

#### **Parameters**

#### context

The context for which activity is to be enabled

#### kind

The kind of activity record to collect

#### Returns

- CUPTI\_SUCCESS
- CUPTI ERROR NOT INITIALIZED
- CUPTI\_ERROR\_NOT\_COMPATIBLE
  - if the activity kind cannot be enabled
- CUPTI\_ERROR\_INVALID\_KIND
  - if the activity kind is not supported

#### Description

Enable collection of a specific kind of activity record for a context. This setting done by this API will supersede the global settings for activity records enabled by cuptiActivityEnable. Multiple kinds can be enabled by calling this function multiple times.

# CUptiResult cuptiActivityEnableLatencyTimestamps (uint8\_t enable)

Controls the collection of queued and submitted timestamps for kernels.

#### **Parameters**

#### enable

is a boolean, denoting whether these timestamps should be collected

#### Returns

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED

#### Description

This API is used to control the collection of queued and submitted timestamps for kernels whose records are provided through the struct CUpti\_ActivityKernel4. Default value is 0, i.e. these timestamps are not collected. This API needs to be called before initialization of CUDA and this setting should not be changed during the profiling session.

# CUptiResult cuptiActivityFlush (CUcontext context, uint32\_t streamId, uint32\_t flag)

Wait for all activity records are delivered via the completion callback.

#### **Parameters**

#### context

A valid CUcontext or NULL.

#### streamId

The stream ID.

#### flag

The flag can be set to indicate a forced flush. See CUpti\_ActivityFlag

#### **Returns**

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_CUPTI\_ERROR\_INVALID\_OPERATION
   if not preceded by a successful call to cuptiActivityRegisterCallbacks

#### CUPTI\_ERROR\_UNKNOWN

an internal error occurred

#### Description

This function does not return until all activity records associated with the specified context/stream are returned to the CUPTI client using the callback registered in cuptiActivityRegisterCallbacks. To ensure that all activity records are complete, the requested stream(s), if any, are synchronized.

If context is NULL, the global activity records (i.e. those not associated with a particular stream) are flushed (in this case no streams are synchonized). If context is a valid CUcontext and streamId is 0, the buffers of all streams of this context are flushed. Otherwise, the buffers of the specified stream in this context is flushed.

Before calling this function, the buffer handling callback api must be activated by calling cuptiActivityRegisterCallbacks.

\*\*DEPRECATED\*\* This method is deprecated CONTEXT and STREAMID will be ignored. Use cuptiActivityFlushAll to flush all data.

## CUptiResult cuptiActivityFlushAll (uint32\_t flag)

Wait for all activity records are delivered via the completion callback.

#### **Parameters**

#### flag

The flag can be set to indicate a forced flush. See CUpti\_ActivityFlag

#### Returns

- CUPTI SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_INVALID\_OPERATION

if not preceded by a successful call to cuptiActivityRegisterCallbacks

CUPTI\_ERROR\_UNKNOWN

an internal error occurred

#### Description

This function does not return until all activity records associated with all contexts/ streams (and the global buffers not associated with any stream) are returned to the CUPTI client using the callback registered in cuptiActivityRegisterCallbacks. To ensure that all activity records are complete, the requested stream(s), if any, are synchronized. Before calling this function, the buffer handling callback api must be activated by calling cuptiActivityRegisterCallbacks.

# CUptiResult cuptiActivityGetAttribute (CUpti\_ActivityAttribute attr, size\_t \*valueSize, void \*value)

Read an activity API attribute.

#### **Parameters**

#### attr

The attribute to read

#### valueSize

Size of buffer pointed by the value, and returns the number of bytes written to value **value** 

Returns the value of the attribute

#### Returns

- CUPTI SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_INVALID\_PARAMETER

if valueSize or value is NULL, or if attr is not an activity attribute

► CUPTI\_ERROR\_PARAMETER\_SIZE\_NOT\_SUFFICIENT

Indicates that the value buffer is too small to hold the attribute value.

#### Description

Read an activity API attribute and return it in \*value.

# CUptiResult cuptiActivityGetNextRecord (uint8\_t \*buffer, size\_t validBufferSizeBytes, CUpti\_Activity \*\*record)

Iterate over the activity records in a buffer.

#### **Parameters**

#### buffer

The buffer containing activity records

#### validBufferSizeBytes

The number of valid bytes in the buffer.

#### record

Inputs the previous record returned by cuptiActivityGetNextRecord and returns the next activity record from the buffer. If input value is NULL, returns the first activity record in the buffer. Records of kind CUPTI\_ACTIVITY\_KIND\_CONCURRENT\_KERNEL may contain invalid (0) timestamps, indicating that no timing information could be collected for lack of device memory.

#### Returns

- CUPTI SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_MAX\_LIMIT\_REACHED

if no more records in the buffer

CUPTI\_ERROR\_INVALID\_PARAMETER

if buffer is NULL.

#### Description

This is a helper function to iterate over the activity records in a buffer. A buffer of activity records is typically obtained by receiving a CUpti\_BuffersCallbackCompleteFunc callback.

An example of typical usage:

```
CUpti_Activity *record = NULL;
CUptiResult status = CUPTI_SUCCESS;
do {
    status = cuptiActivityGetNextRecord(buffer, validSize, &record);
    if(status == CUPTI_SUCCESS) {
        // Use record here...
}
else if (status == CUPTI_ERROR_MAX_LIMIT_REACHED)
        break;
else {
        goto Error;
}
while (1);
```

# CUptiResult cuptiActivityGetNumDroppedRecords (CUcontext context, uint32\_t streamId, size\_t \*dropped)

Get the number of activity records that were dropped of insufficient buffer space.

#### **Parameters**

#### context

The context, or NULL to get dropped count from global queue

#### streamId

The stream ID

#### dropped

The number of records that were dropped since the last call to this function.

#### Returns

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_INVALID\_PARAMETER

if dropped is NULL

#### Description

Get the number of records that were dropped because of insufficient buffer space. The dropped count includes records that could not be recorded because CUPTI did not have activity buffer space available for the record (because the CUpti\_BuffersCallbackRequestFunc callback did not return an empty buffer of sufficient size) and also CDP records that could not be record because the device-size buffer was full (size is controlled by the CUPTI\_ACTIVITY\_ATTR\_DEVICE\_BUFFER\_SIZE\_CDP attribute). The dropped count maintained for the queue is reset to zero when this function is called.

# CUptiResult cuptiActivityPopExternalCorrelationId (CUpti\_ExternalCorrelationKind kind, uint64\_t \*lastId)

Pop an external correlation id for the calling thread.

#### **Parameters**

#### kind

The kind of external API activities should be correlated with.

#### lastId

If the function returns successful, contains the last external correlation id for this kind, can be NULL.

#### Returns

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_INVALID\_PARAMETER

The external API kind is invalid.

CUPTI\_ERROR\_QUEUE\_EMPTY

No external id is currently associated with kind.

#### Description

This function notifies CUPTI that the calling thread is leaving an external API region.

# CUptiResult cuptiActivityPushExternalCorrelationId (CUpti\_ExternalCorrelationKind kind, uint64\_t id)

Push an external correlation id for the calling thread.

#### **Parameters**

#### kind

The kind of external API activities should be correlated with.

id

External correlation id.

#### Returns

- CUPTI SUCCESS
- CUPTI\_ERROR\_INVALID\_PARAMETER

The external API kind is invalid

#### Description

This function notifies CUPTI that the calling thread is entering an external API region. When a CUPTI activity API record is created while within an external API region and CUPTI\_ACTIVITY\_KIND\_EXTERNAL\_CORRELATION is enabled, the activity API record will be preceded by a CUpti\_ActivityExternalCorrelation record for each CUpti\_ExternalCorrelationKind.

CUptiResult cuptiActivityRegisterCallbacks (CUpti\_BuffersCallbackRequestFunc funcBufferRequested, CUpti\_BuffersCallbackCompleteFunc funcBufferCompleted)

Registers callback functions with CUPTI for activity buffer handling.

#### **Parameters**

#### funcBufferRequested

callback which is invoked when an empty buffer is requested by CUPTI

#### funcBufferCompleted

callback which is invoked when a buffer containing activity records is available from CUPTI

#### **Returns**

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_INVALID\_PARAMETER

if either funcBufferRequested or funcBufferCompleted is NULL

#### Description

This function registers two callback functions to be used in asynchronous buffer handling. If registered, activity record buffers are handled using asynchronous requested/completed callbacks from CUPTI.

Registering these callbacks prevents the client from using CUPTI's blocking enqueue/dequeue functions.

# CUptiResult cuptiActivitySetAttribute (CUpti\_ActivityAttribute attr, size\_t \*valueSize, void \*value)

Write an activity API attribute.

#### **Parameters**

#### attr

The attribute to write

#### valueSize

The size, in bytes, of the value

#### value

The attribute value to write

#### Returns

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_INVALID\_PARAMETER

if valueSize or value is NULL, or if attr is not an activity attribute

► CUPTI\_ERROR\_PARAMETER\_SIZE\_NOT\_SUFFICIENT

Indicates that the value buffer is too small to hold the attribute value.

#### Description

Write an activity API attribute.

# CUptiResult cuptiComputeCapabilitySupported (int major, int minor, int \*support)

Check support for a compute capability.

#### **Parameters**

#### major

The major revision number of the compute capability

#### minor

The minor revision number of the compute capability

#### support

Pointer to an integer to return the support status

#### **Returns**

- CUPTI\_SUCCESS
- CUPTI ERROR INVALID PARAMETER

if support is NULL

#### Description

This function is used to check the support for a device based on it's compute capability. It sets the support when the compute capability is supported by the current version of CUPTI, and clears it otherwise. This version of CUPTI might not support all GPUs sharing the same compute capability. It is suggested to use API cuptiDeviceSupported which provides correct information.

#### See also:

cuptiDeviceSupported

# CUptiResult cuptiDeviceSupported (CUdevice dev, int \*support)

Check support for a compute device.

#### **Parameters**

#### dev

The device handle returned by CUDA Driver API cuDeviceGet

#### support

Pointer to an integer to return the support status

#### Returns

- CUPTI\_SUCCESS
- ► CUPTI\_ERROR\_INVALID\_PARAMETER

if support is NULL

CUPTI\_ERROR\_INVALID\_DEVICE

if dev is not a valid device

#### Description

This function is used to check the support for a compute device. It sets the support when the device is supported by the current version of CUPTI, and clears it otherwise.

#### See also:

cuptiComputeCapabilitySupported

## CUptiResult cuptiFinalize (void)

Cleanup CUPTI.

#### Description

Explicitly destroys and cleans up all resources associated with CUPTI in the current process. Any subsequent CUPTI API call will reinitialize CUPTI. The CUPTI client needs to make sure that required CUDA synchronization and CUPTI activity buffer flush is done before calling cuptiFinalize.

# CUptiResult cuptiGetAutoBoostState (CUcontext context, CUpti\_ActivityAutoBoostState \*state)

Get auto boost state.

#### **Parameters**

#### context

A valid CUcontext.

#### state

A pointer to CUpti\_ActivityAutoBoostState structure which contains the current state and the id of the process that has requested the current state

#### Returns

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_INVALID\_PARAMETER

if CUcontext or state is NULL

CUPTI\_ERROR\_NOT\_SUPPORTED

Indicates that the device does not support auto boost

CUPTI\_ERROR\_UNKNOWN

an internal error occurred

#### Description

The profiling results can be inconsistent in case auto boost is enabled. CUPTI tries to disable auto boost while profiling. It can fail to disable in cases where user does not have the permissions or CUDA\_AUTO\_BOOST env variable is set. The function can be used to query whether auto boost is enabled.

# CUptiResult cuptiGetContextId (CUcontext context, uint32\_t \*contextId)

Get the ID of a context.

#### **Parameters**

#### context

The context

#### contextId

Returns a process-unique ID for the context

#### Returns

- CUPTI SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI ERROR INVALID CONTEXT

The context is NULL or not valid.

► CUPTI\_ERROR\_INVALID\_PARAMETER

if contextId is NULL

#### Description

Get the ID of a context.

# CUptiResult cuptiGetDeviceId (CUcontext context, uint32\_t \*deviceId)

Get the ID of a device.

#### **Parameters**

#### context

The context, or NULL to indicate the current context.

#### deviceId

Returns the ID of the device that is current for the calling thread.

#### Returns

- ► CUPTI\_SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI ERROR INVALID DEVICE

if unable to get device ID

► CUPTI\_ERROR\_INVALID\_PARAMETER

if deviceId is NULL

#### Description

If context is NULL, returns the ID of the device that contains the currently active context. If context is non-NULL, returns the ID of the device which contains that context. Operates in a similar manner to cudaGetDevice() or cuCtxGetDevice() but may be called from within callback functions.

## CUptiResult cuptiGetLastError (void)

Returns the last error from a cupti call or callback.

#### Description

Returns the last error that has been produced by any of the cupti api calls or the callback in the same host thread and resets it to CUPTI\_SUCCESS.

# CUptiResult cuptiGetStreamId (CUcontext context, CUstream stream, uint32\_t \*streamId)

Get the ID of a stream.

#### **Parameters**

#### context

If non-NULL then the stream is checked to ensure that it belongs to this context. Typically this parameter should be null.

#### stream

The stream

#### streamId

Returns a context-unique ID for the stream

#### Returns

- CUPTI SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_INVALID\_STREAM

if unable to get stream ID, or if context is non-NULL and stream does not belong to the context

► CUPTI\_ERROR\_INVALID\_PARAMETER

if streamId is NULL

#### Description

Get the ID of a stream. The stream ID is unique within a context (i.e. all streams within a context will have unique stream IDs).

\*\*DEPRECATED\*\* This method is deprecated as of CUDA 8.0. Use method cuptiGetStreamIdEx instead.

# CUptiResult cuptiGetStreamIdEx (CUcontext context, CUstream stream, uint8\_t perThreadStream, uint32\_t \*streamId)

Get the ID of a stream.

#### **Parameters**

#### context

If non-NULL then the stream is checked to ensure that it belongs to this context. Typically this parameter should be null.

#### stream

The stream

#### perThreadStream

Flag to indicate if program is compiled for per-thread streams

#### streamId

Returns a context-unique ID for the stream

#### Returns

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_INVALID\_STREAM

if unable to get stream ID, or if context is non-NULL and stream does not belong to the context

► CUPTI\_ERROR\_INVALID\_PARAMETER

if streamId is NULL

#### Description

Get the ID of a stream. The stream ID is unique within a context (i.e. all streams within a context will have unique stream IDs).

# CUptiResult cuptiGetThreadIdType (CUpti\_ActivityThreadIdType \*type)

Get the thread-id type.

#### **Returns**

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_INVALID\_PARAMETER

if type is NULL

#### Description

Returns the thread-id type used in CUPTI

## CUptiResult cuptiGetTimestamp (uint64\_t \*timestamp)

Get the CUPTI timestamp.

#### **Parameters**

#### timestamp

Returns the CUPTI timestamp

#### Returns

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_INVALID\_PARAMETER

if timestamp is NULL

#### Description

Returns a timestamp normalized to correspond with the start and end timestamps reported in the CUPTI activity records. The timestamp is reported in nanoseconds.

# CUptiResult cuptiSetThreadIdType (CUpti\_ActivityThreadIdType type)

Set the thread-id type.

#### Returns

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_NOT\_SUPPORTED

if type is not supported on the platform

#### Description

CUPTI uses the method corresponding to set type to generate the thread-id. See enum / ref CUpti\_ActivityThreadIdType for the list of methods. Activity records having thread-id field contain the same value. Thread id type must not be changed during the profiling session to avoid thread-id value mismatch across activity records.

## #define CUPTI\_AUTO\_BOOST\_INVALID\_CLIENT\_PID 0

An invalid/unknown process id.

### #define CUPTI\_CORRELATION\_ID\_UNKNOWN 0

An invalid/unknown correlation ID. A correlation ID of this value indicates that there is no correlation for the activity record.

## #define CUPTI\_GRID\_ID\_UNKNOWN OLL

An invalid/unknown grid ID.

### #define CUPTI\_MAX\_NVLINK\_PORTS 16

Maximum NVLink port numbers.

## #define CUPTI\_NVLINK\_INVALID\_PORT -1

Invalid/unknown NVLink port number.

## #define CUPTI\_SOURCE\_LOCATOR\_ID\_UNKNOWN 0

The source-locator ID that indicates an unknown source location. There is not an actual CUpti\_ActivitySourceLocator object corresponding to this value.

## #define CUPTI\_SYNCHRONIZATION\_INVALID\_VALUE -1

An invalid/unknown value.

## #define CUPTI\_TIMESTAMP\_UNKNOWN OLL

An invalid/unknown timestamp for a start, end, queued, submitted, or completed time.

### 2.4. CUPTI Callback API

Functions, types, and enums that implement the CUPTI Callback API.

## struct CUpti\_CallbackData

Data passed into a runtime or driver API callback function.

## struct CUpti\_ModuleResourceData

Module data passed into a resource callback function.

### struct CUpti\_NvtxData

Data passed into a NVTX callback function.

### struct CUpti\_ResourceData

Data passed into a resource callback function.

## struct CUpti\_SynchronizeData

Data passed into a synchronize callback function.

## enum CUpti\_ApiCallbackSite

Specifies the point in an API call that a callback is issued.

Specifies the point in an API call that a callback is issued. This value is communicated to the callback function via CUpti\_CallbackData::callbackSite.

#### **Values**

#### $CUPTI\_API\_ENTER = 0$

The callback is at the entry of the API call.

#### $CUPTI\_API\_EXIT = 1$

The callback is at the exit of the API call.

CUPTI\_API\_CBSITE\_FORCE\_INT = 0x7fffffff

### enum CUpti\_CallbackDomain

Callback domains.

Callback domains. Each domain represents callback points for a group of related API functions or CUDA driver activity.

#### **Values**

#### CUPTI\_CB\_DOMAIN\_INVALID = 0

Invalid domain.

#### CUPTI\_CB\_DOMAIN\_DRIVER\_API = 1

Domain containing callback points for all driver API functions.

#### CUPTI\_CB\_DOMAIN\_RUNTIME\_API = 2

Domain containing callback points for all runtime API functions.

#### **CUPTI CB DOMAIN RESOURCE = 3**

Domain containing callback points for CUDA resource tracking.

#### CUPTI\_CB\_DOMAIN\_SYNCHRONIZE = 4

Domain containing callback points for CUDA synchronization.

#### CUPTI\_CB\_DOMAIN\_NVTX = 5

Domain containing callback points for NVTX API functions.

CUPTI\_CB\_DOMAIN\_SIZE = 6

CUPTI\_CB\_DOMAIN\_FORCE\_INT = 0x7fffffff

# enum CUpti\_CallbackIdResource

Callback IDs for resource domain.

Callback IDs for resource domain, CUPTI\_CB\_DOMAIN\_RESOURCE. This value is communicated to the callback function via the cbid parameter.

#### **Values**

#### CUPTI\_CBID\_RESOURCE\_INVALID = 0

Invalid resource callback ID.

#### CUPTI\_CBID\_RESOURCE\_CONTEXT\_CREATED = 1

A new context has been created.

#### CUPTI\_CBID\_RESOURCE\_CONTEXT\_DESTROY\_STARTING = 2

A context is about to be destroyed.

#### CUPTI\_CBID\_RESOURCE\_STREAM\_CREATED = 3

A new stream has been created.

#### CUPTI\_CBID\_RESOURCE\_STREAM\_DESTROY\_STARTING = 4

A stream is about to be destroyed.

#### CUPTI\_CBID\_RESOURCE\_CU\_INIT\_FINISHED = 5

The driver has finished initializing.

#### CUPTI\_CBID\_RESOURCE\_MODULE\_LOADED = 6

A module has been loaded.

#### CUPTI\_CBID\_RESOURCE\_MODULE\_UNLOAD\_STARTING = 7

A module is about to be unloaded.

#### CUPTI CBID RESOURCE MODULE PROFILED = 8

The current module which is being profiled.

CUPTI\_CBID\_RESOURCE\_SIZE

CUPTI CBID RESOURCE FORCE INT = 0x7fffffff

# enum CUpti\_CallbackIdSync

Callback IDs for synchronization domain.

Callback IDs for synchronization domain, CUPTI\_CB\_DOMAIN\_SYNCHRONIZE. This value is communicated to the callback function via the cbid parameter.

#### **Values**

#### CUPTI\_CBID\_SYNCHRONIZE\_INVALID = 0

Invalid synchronize callback ID.

#### CUPTI\_CBID\_SYNCHRONIZE\_STREAM\_SYNCHRONIZED = 1

Stream synchronization has completed for the stream.

#### CUPTI\_CBID\_SYNCHRONIZE\_CONTEXT\_SYNCHRONIZED = 2

Context synchronization has completed for the context.

CUPTI\_CBID\_SYNCHRONIZE\_SIZE

CUPTI\_CBID\_SYNCHRONIZE\_FORCE\_INT = 0x7fffffff

# typedef (\*CUpti\_CallbackFunc) (void\* userdata, CUpti\_CallbackDomain domain, CUpti\_CallbackId cbid, const void\* cbdata)

Function type for a callback.

Function type for a callback. The type of the data passed to the callback in cbdata depends on the domain. If domain is CUPTI\_CB\_DOMAIN\_DRIVER\_API or CUPTI\_CB\_DOMAIN\_RUNTIME\_API the type of cbdata will be CUpti\_CallbackData. If domain is CUPTI\_CB\_DOMAIN\_RESOURCE the type of cbdata will be CUpti\_ResourceData. If domain is CUPTI\_CB\_DOMAIN\_SYNCHRONIZE the type of cbdata will be CUpti\_SynchronizeData. If domain is CUPTI\_CB\_DOMAIN\_NVTX the type of cbdata will be CUpti\_NvtxData.

### typedef uint32\_t CUpti\_CallbackId

An ID for a driver API, runtime API, resource or synchronization callback.

An ID for a driver API, runtime API, resource or synchronization callback. Within a driver API callback this should be interpreted as a CUpti\_driver\_api\_trace\_cbid value (these values are defined in cupti\_driver\_cbid.h). Within a runtime API callback this should be interpreted as a CUpti\_runtime\_api\_trace\_cbid value (these values are defined in cupti\_runtime\_cbid.h). Within a resource API callback this should be interpreted as a CUpti\_CallbackIdResource value. Within a synchronize API callback this should be interpreted as a CUpti\_CallbackIdSync value.

### typedef CUpti\_DomainTable

Pointer to an array of callback domains.

# typedef struct CUpti\_Subscriber\_st \*CUpti SubscriberHandle

A callback subscriber.

# CUptiResult cuptiEnableAllDomains (uint32\_t enable, CUpti\_SubscriberHandle subscriber)

Enable or disable all callbacks in all domains.

#### **Parameters**

#### enable

New enable state for all callbacks in all domain. Zero disables all callbacks, non-zero enables all callbacks.

#### subscriber

- Handle to callback subscription

#### Returns

CUPTI SUCCESS

on success

CUPTI\_ERROR\_NOT\_INITIALIZED

if unable to initialized CUPTI

► CUPTI\_ERROR\_INVALID\_PARAMETER

if subscriber is invalid

#### Description

Enable or disable all callbacks in all domains.



**Thread-safety:** a subscriber must serialize access to cuptiGetCallbackState, cuptiEnableCallback, cuptiEnableDomain, and cuptiEnableAllDomains. For example, if cuptiGetCallbackState(sub, d, \*) and cuptiEnableAllDomains(sub) are called concurrently, the results are undefined.

# CUptiResult cuptiEnableCallback (uint32\_t enable, CUpti\_SubscriberHandle subscriber, CUpti\_CallbackDomain domain, CUpti\_CallbackId cbid)

Enable or disabled callbacks for a specific domain and callback ID.

#### **Parameters**

#### enable

New enable state for the callback. Zero disables the callback, non-zero enables the callback.

#### subscriber

- Handle to callback subscription

#### domain

The domain of the callback

#### cbid

The ID of the callback

#### Returns

CUPTI\_SUCCESS

on success

CUPTI\_ERROR\_NOT\_INITIALIZED

if unable to initialized CUPTI

► CUPTI\_ERROR\_INVALID\_PARAMETER

if subscriber, domain or cbid is invalid.

#### Description

Enable or disabled callbacks for a subscriber for a specific domain and callback ID.



**Thread-safety:** a subscriber must serialize access to cuptiGetCallbackState, cuptiEnableCallback, cuptiEnableDomain, and cuptiEnableAllDomains. For example, if cuptiGetCallbackState(sub, d, c) and cuptiEnableCallback(sub, d, c) are called concurrently, the results are undefined.

# CUptiResult cuptiEnableDomain (uint32\_t enable, CUpti\_SubscriberHandle subscriber, CUpti\_CallbackDomain domain)

Enable or disabled all callbacks for a specific domain.

#### **Parameters**

#### enable

New enable state for all callbacks in the domain. Zero disables all callbacks, non-zero enables all callbacks.

#### subscriber

- Handle to callback subscription

#### domain

The domain of the callback

#### Returns

CUPTI\_SUCCESS

on success

CUPTI\_ERROR\_NOT\_INITIALIZED

if unable to initialized CUPTI

► CUPTI\_ERROR\_INVALID\_PARAMETER

if subscriber or domain is invalid

#### Description

Enable or disabled all callbacks for a specific domain.



Thread-safety: a subscriber must serialize access to cuptiGetCallbackState, cuptiEnableCallback, cuptiEnableDomain, and cuptiEnableAllDomains. For example, if cuptiGetCallbackEnabled(sub, d, \*) and cuptiEnableDomain(sub, d) are called concurrently, the results are undefined.

# CUptiResult cuptiGetCallbackName (CUpti\_CallbackDomain domain, uint32\_t cbid, const char \*\*name)

Get the name of a callback for a specific domain and callback ID.

#### **Parameters**

#### domain

The domain of the callback

#### cbid

The ID of the callback

#### name

Returns pointer to the name string on success, NULL otherwise

#### Returns

CUPTI SUCCESS

on success

► CUPTI\_ERROR\_INVALID\_PARAMETER

if name is NULL, or if domain or cbid is invalid.

#### Description

Returns a pointer to the name c\_string in \*\*name.



Names are available only for the DRIVER and RUNTIME domains.

# CUptiResult cuptiGetCallbackState (uint32\_t \*enable, CUpti\_SubscriberHandle subscriber, CUpti\_CallbackId cbid)

Get the current enabled/disabled state of a callback for a specific domain and function ID.

#### **Parameters**

#### enable

Returns non-zero if callback enabled, zero if not enabled

#### subscriber

Handle to the initialize subscriber

#### domain

The domain of the callback

#### cbid

The ID of the callback

#### Returns

CUPTI\_SUCCESS

on success

CUPTI\_ERROR\_NOT\_INITIALIZED

if unable to initialized CUPTI

► CUPTI\_ERROR\_INVALID\_PARAMETER

if enabled is NULL, or if subscriber, domain or cbid is invalid.

#### Description

Returns non-zero in \*enable if the callback for a domain and callback ID is enabled, and zero if not enabled.



Thread-safety: a subscriber must serialize access to cuptiGetCallbackState, cuptiEnableCallback, cuptiEnableDomain, and cuptiEnableAllDomains. For example, if cuptiGetCallbackState(sub, d, c) and cuptiEnableCallback(sub, d, c) are called concurrently, the results are undefined.

# CUptiResult cuptiSubscribe (CUpti\_SubscriberHandle \*subscriber, CUpti\_CallbackFunc callback, void \*userdata)

Initialize a callback subscriber with a callback function and user data.

#### **Parameters**

#### subscriber

Returns handle to initialize subscriber

#### callback

The callback function

#### userdata

A pointer to user data. This data will be passed to the callback function via the userdata paramater.

#### **Returns**

CUPTI\_SUCCESS

on success

CUPTI\_ERROR\_NOT\_INITIALIZED

if unable to initialize CUPTI

CUPTI\_ERROR\_MAX\_LIMIT\_REACHED

if there is already a CUPTI subscriber

CUPTI\_ERROR\_INVALID\_PARAMETER

if subscriber is NULL

#### Description

Initializes a callback subscriber with a callback function and (optionally) a pointer to user data. The returned subscriber handle can be used to enable and disable the callback for specific domains and callback IDs.



- Only a single subscriber can be registered at a time.
- This function does not enable any callbacks.
- Thread-safety: this function is thread safe.

# CUptiResult cuptiSupportedDomains (size\_t \*domainCount, CUpti\_DomainTable \*domainTable)

Get the available callback domains.

#### **Parameters**

#### domainCount

Returns number of callback domains

#### domainTable

Returns pointer to array of available callback domains

#### Returns

► CUPTI\_SUCCESS

on success

CUPTI\_ERROR\_NOT\_INITIALIZED

if unable to initialize CUPTI

► CUPTI\_ERROR\_INVALID\_PARAMETER

if domainCount or domainTable are NULL

#### Description

Returns in \*domainTable an array of size \*domainCount of all the available callback domains.



Thread-safety: this function is thread safe.

# CUptiResult cuptiUnsubscribe (CUpti\_SubscriberHandle subscriber)

Unregister a callback subscriber.

#### **Parameters**

#### subscriber

Handle to the initialize subscriber

#### Returns

CUPTI SUCCESS

on success

CUPTI\_ERROR\_NOT\_INITIALIZED

if unable to initialized CUPTI

► CUPTI ERROR INVALID PARAMETER

if subscriber is NULL or not initialized

#### Description

Removes a callback subscriber so that no future callbacks will be issued to that subscriber.



Thread-safety: this function is thread safe.

### 2.5. CUPTI Event API

Functions, types, and enums that implement the CUPTI Event API.

## struct CUpti\_EventGroupSet

A set of event groups.

## struct CUpti\_EventGroupSets

A set of event group sets.

### enum CUpti\_DeviceAttribute

Device attributes.

CUPTI device attributes. These attributes can be read using cuptiDeviceGetAttribute.

#### **Values**

#### CUPTI\_DEVICE\_ATTR\_MAX\_EVENT\_ID = 1

Number of event IDs for a device. Value is a uint32\_t.

#### CUPTI\_DEVICE\_ATTR\_MAX\_EVENT\_DOMAIN\_ID = 2

Number of event domain IDs for a device. Value is a uint32\_t.

#### CUPTI\_DEVICE\_ATTR\_GLOBAL\_MEMORY\_BANDWIDTH = 3

Get global memory bandwidth in Kbytes/sec. Value is a uint64\_t.

#### CUPTI\_DEVICE\_ATTR\_INSTRUCTION\_PER\_CYCLE = 4

Get theoretical maximum number of instructions per cycle. Value is a uint32\_t.

#### CUPTI\_DEVICE\_ATTR\_INSTRUCTION\_THROUGHPUT\_SINGLE\_PRECISION = 5

Get theoretical maximum number of single precision instructions that can be executed per second. Value is a uint64\_t.

#### CUPTI\_DEVICE\_ATTR\_MAX\_FRAME\_BUFFERS = 6

Get number of frame buffers for device. Value is a uint64\_t.

#### **CUPTI DEVICE ATTR PCIE LINK RATE = 7**

Get PCIE link rate in Mega bits/sec for device. Return 0 if bus-type is non-PCIE. Value is a uint64\_t.

#### CUPTI\_DEVICE\_ATTR\_PCIE\_LINK\_WIDTH = 8

Get PCIE link width for device. Return 0 if bus-type is non-PCIE. Value is a uint64\_t.

#### CUPTI\_DEVICE\_ATTR\_PCIE\_GEN = 9

Get PCIE generation for device. Return 0 if bus-type is non-PCIE. Value is a uint64\_t.

#### CUPTI\_DEVICE\_ATTR\_DEVICE\_CLASS = 10

Get the class for the device. Value is a CUpti\_DeviceAttributeDeviceClass.

#### CUPTI\_DEVICE\_ATTR\_FLOP\_SP\_PER\_CYCLE = 11

Get the peak single precision flop per cycle. Value is a uint64\_t.

#### CUPTI\_DEVICE\_ATTR\_FLOP\_DP\_PER\_CYCLE = 12

Get the peak double precision flop per cycle. Value is a uint64\_t.

#### **CUPTI DEVICE ATTR MAX L2 UNITS = 13**

Get number of L2 units. Value is a uint64\_t.

# CUPTI\_DEVICE\_ATTR\_MAX\_SHARED\_MEMORY\_CACHE\_CONFIG\_PREFER\_SHARED = 14

Get the maximum shared memory for the CU\_FUNC\_CACHE\_PREFER\_SHARED preference. Value is a uint64\_t.

# CUPTI\_DEVICE\_ATTR\_MAX\_SHARED\_MEMORY\_CACHE\_CONFIG\_PREFER\_L1 = 15

Get the maximum shared memory for the CU\_FUNC\_CACHE\_PREFER\_L1 preference. Value is a uint64 t.

# CUPTI\_DEVICE\_ATTR\_MAX\_SHARED\_MEMORY\_CACHE\_CONFIG\_PREFER\_EQUAL = 16

Get the maximum shared memory for the CU\_FUNC\_CACHE\_PREFER\_EQUAL preference. Value is a uint64\_t.

#### CUPTI\_DEVICE\_ATTR\_FLOP\_HP\_PER\_CYCLE = 17

Get the peak half precision flop per cycle. Value is a uint64\_t.

#### CUPTI\_DEVICE\_ATTR\_NVLINK\_PRESENT = 18

Check if Nvlink is connected to device. Returns 1, if at least one Nvlink is connected to the device, returns 0 otherwise. Value is a uint32\_t.

#### CUPTI\_DEVICE\_ATTR\_GPU\_CPU\_NVLINK\_BW = 19

Check if Nvlink is present between GPU and CPU. Returns Bandwidth, in Bytes/sec, if Nvlink is present, returns 0 otherwise. Value is a uint64\_t.

#### CUPTI\_DEVICE\_ATTR\_FORCE\_INT = 0x7fffffff

## enum CUpti\_DeviceAttributeDeviceClass

Device class.

Enumeration of device classes for device attribute CUPTI\_DEVICE\_ATTR\_DEVICE\_CLASS.

#### **Values**

CUPTI\_DEVICE\_ATTR\_DEVICE\_CLASS\_TESLA = 0 CUPTI\_DEVICE\_ATTR\_DEVICE\_CLASS\_QUADRO = 1 CUPTI\_DEVICE\_ATTR\_DEVICE\_CLASS\_GEFORCE = 2 CUPTI\_DEVICE\_ATTR\_DEVICE\_CLASS\_TEGRA = 3

## enum CUpti\_EventAttribute

Event attributes.

Event attributes. These attributes can be read using cuptiEventGetAttribute.

#### **Values**

#### $CUPTI_EVENT_ATTR_NAME = 0$

Event name. Value is a null terminated const c-string.

#### CUPTI\_EVENT\_ATTR\_SHORT\_DESCRIPTION = 1

Short description of event. Value is a null terminated const c-string.

#### CUPTI\_EVENT\_ATTR\_LONG\_DESCRIPTION = 2

Long description of event. Value is a null terminated const c-string.

#### CUPTI\_EVENT\_ATTR\_CATEGORY = 3

Category of event. Value is CUpti\_EventCategory.

CUPTI\_EVENT\_ATTR\_FORCE\_INT = 0x7fffffff

### enum CUpti\_EventCategory

An event category.

Each event is assigned to a category that represents the general type of the event. A event's category is accessed using cuptiEventGetAttribute and the CUPTI\_EVENT\_ATTR\_CATEGORY attribute.

#### **Values**

#### CUPTI\_EVENT\_CATEGORY\_INSTRUCTION = 0

An instruction related event.

#### CUPTI\_EVENT\_CATEGORY\_MEMORY = 1

A memory related event.

#### CUPTI\_EVENT\_CATEGORY\_CACHE = 2

A cache related event.

#### CUPTI\_EVENT\_CATEGORY\_PROFILE\_TRIGGER = 3

A profile-trigger event.

CUPTI\_EVENT\_CATEGORY\_FORCE\_INT = 0x7fffffff

## enum CUpti\_EventCollectionMethod

The collection method used for an event.

The collection method indicates how an event is collected.

#### **Values**

#### CUPTI\_EVENT\_COLLECTION\_METHOD\_PM = 0

Event is collected using a hardware global performance monitor.

#### CUPTI\_EVENT\_COLLECTION\_METHOD\_SM = 1

Event is collected using a hardware SM performance monitor.

#### CUPTI\_EVENT\_COLLECTION\_METHOD\_INSTRUMENTED = 2

Event is collected using software instrumentation.

#### CUPTI\_EVENT\_COLLECTION\_METHOD\_NVLINK\_TC = 3

Event is collected using NvLink throughput counter method.

CUPTI\_EVENT\_COLLECTION\_METHOD\_FORCE\_INT = 0x7ffffffff

### enum CUpti\_EventCollectionMode

Event collection modes.

The event collection mode determines the period over which the events within the enabled event groups will be collected.

#### **Values**

#### CUPTI\_EVENT\_COLLECTION\_MODE\_CONTINUOUS = 0

Events are collected for the entire duration between the cuptiEventGroupEnable and cuptiEventGroupDisable calls. For devices with compute capability less than 2.0, event values are reset when a kernel is launched. For all other devices event values are only reset when the events are read. For CUDA toolkit v6.0 and older this was the default mode. From CUDA toolkit v6.5 this mode is supported on Tesla devices only.

#### CUPTI\_EVENT\_COLLECTION\_MODE\_KERNEL = 1

Events are collected only for the durations of kernel executions that occur between the cuptiEventGroupEnable and cuptiEventGroupDisable calls. Event collection begins when a kernel execution begins, and stops when kernel execution completes. Event values are reset to zero when each kernel execution begins. If multiple kernel executions occur between the cuptiEventGroupEnable and cuptiEventGroupDisable calls then the event values must be read after each kernel launch if those events need to be associated with the specific kernel launch. This is the default mode from CUDA toolkit v6.5, and it is the only supported mode for non-Tesla (Quadro, GeForce etc.) devices.

CUPTI\_EVENT\_COLLECTION\_MODE\_FORCE\_INT = 0x7fffffff

## enum CUpti\_EventDomainAttribute

Event domain attributes.

Event domain attributes. Except where noted, all the attributes can be read using either cuptiDeviceGetEventDomainAttribute or cuptiEventDomainGetAttribute.

#### **Values**

#### CUPTI\_EVENT\_DOMAIN\_ATTR\_NAME = 0

Event domain name. Value is a null terminated const c-string.

#### CUPTI\_EVENT\_DOMAIN\_ATTR\_INSTANCE\_COUNT = 1

Number of instances of the domain for which event counts will be collected. The domain may have additional instances that cannot be profiled (see CUPTI\_EVENT\_DOMAIN\_ATTR\_TOTAL\_INSTANCE\_COUNT). Can be read only with cuptiDeviceGetEventDomainAttribute. Value is a uint32\_t.

#### CUPTI\_EVENT\_DOMAIN\_ATTR\_TOTAL\_INSTANCE\_COUNT = 3

Total number of instances of the domain, including instances that cannot be profiled. Use CUPTI\_EVENT\_DOMAIN\_ATTR\_INSTANCE\_COUNT to get the number of instances that can be profiled. Can be read only with cuptiDeviceGetEventDomainAttribute. Value is a uint32\_t.

#### CUPTI\_EVENT\_DOMAIN\_ATTR\_COLLECTION\_METHOD = 4

Collection method used for events contained in the event domain. Value is a CUpti EventCollectionMethod.

CUPTI\_EVENT\_DOMAIN\_ATTR\_FORCE\_INT = 0x7fffffff

## enum CUpti\_EventGroupAttribute

Event group attributes.

Event group attributes. These attributes can be read using cuptiEventGroupGetAttribute. Attributes marked [rw] can also be written using cuptiEventGroupSetAttribute.

#### **Values**

#### CUPTI\_EVENT\_GROUP\_ATTR\_EVENT\_DOMAIN\_ID = 0

The domain to which the event group is bound. This attribute is set when the first event is added to the group. Value is a CUpti\_EventDomainID.

#### CUPTI\_EVENT\_GROUP\_ATTR\_PROFILE\_ALL\_DOMAIN\_INSTANCES = 1

[rw] Profile all the instances of the domain for this eventgroup. This feature can be used to get load balancing across all instances of a domain. Value is an integer.

#### CUPTI\_EVENT\_GROUP\_ATTR\_USER\_DATA = 2

[rw] Reserved for user data.

#### CUPTI\_EVENT\_GROUP\_ATTR\_NUM\_EVENTS = 3

Number of events in the group. Value is a uint32 t.

#### **CUPTI EVENT GROUP ATTR EVENTS = 4**

Enumerates events in the group. Value is a pointer to buffer of size sizeof(CUpti\_EventID) \* num\_of\_events in the eventgroup. num\_of\_events can be queried using CUPTI\_EVENT\_GROUP\_ATTR\_NUM\_EVENTS.

#### CUPTI\_EVENT\_GROUP\_ATTR\_INSTANCE\_COUNT = 5

Number of instances of the domain bound to this event group that will be counted. Value is a uint32 t.

CUPTI\_EVENT\_GROUP\_ATTR\_FORCE\_INT = 0x7fffffff

## enum CUpti\_ReadEventFlags

 $Flags\ for\ cupti Event Group Read Event\ an\ cupti Event Group Read All Events.$ 

Flags for cuptiEventGroupReadEvent an cuptiEventGroupReadAllEvents.

#### **Values**

CUPTI\_EVENT\_READ\_FLAG\_NONE = 0
No flags.
CUPTI\_EVENT\_READ\_FLAG\_FORCE\_INT = 0x7fffffff

## typedef uint32\_t CUpti\_EventDomainID

ID for an event domain.

ID for an event domain. An event domain represents a group of related events. A device may have multiple instances of a domain, indicating that the device can simultaneously record multiple instances of each event within that domain.

## typedef void \*CUpti\_EventGroup

A group of events.

An event group is a collection of events that are managed together. All events in an event group must belong to the same domain.

## typedef uint32\_t CUpti\_EventID

ID for an event.

An event represents a countable activity, action, or occurrence on the device.

# typedef (\*CUpti\_KernelReplayUpdateFunc) (const char\* kernelName, int numReplaysDone, void\* customData)

Function type for getting updates on kernel replay.

# CUptiResult cuptiDeviceEnumEventDomains (CUdevice device, size\_t \*arraySizeBytes, CUpti\_EventDomainID \*domainArray)

Get the event domains for a device.

#### **Parameters**

#### device

The CUDA device

#### arraySizeBytes

The size of domainArray in bytes, and returns the number of bytes written to domainArray

#### domainArray

Returns the IDs of the event domains for the device

#### Returns

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_INVALID\_DEVICE
- CUPTI ERROR INVALID PARAMETER

if arraySizeBytes or domainArray are NULL

#### Description

Returns the event domains IDs in domainArray for a device. The size of the domainArray buffer is given by \*arraySizeBytes. The size of the domainArray buffer must be at least numdomains \* sizeof(CUpti\_EventDomainID) or else all domains will not be returned. The value returned in \*arraySizeBytes contains the number of bytes returned in domainArray.



Thread-safety: this function is thread safe.

# CUptiResult cuptiDeviceGetAttribute (CUdevice device, CUpti\_DeviceAttribute attrib, size\_t \*valueSize, void \*value)

Read a device attribute.

#### **Parameters**

#### device

The CUDA device

#### attrib

The attribute to read

#### valueSize

Size of buffer pointed by the value, and returns the number of bytes written to value value

Returns the value of the attribute

#### Returns

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI ERROR INVALID DEVICE
- CUPTI\_ERROR\_INVALID\_PARAMETER

if valueSize or value is NULL, or if attrib is not a device attribute

► CUPTI\_ERROR\_PARAMETER\_SIZE\_NOT\_SUFFICIENT

For non-c-string attribute values, indicates that the value buffer is too small to hold the attribute value.

#### Description

Read a device attribute and return it in \*value.



Thread-safety: this function is thread safe.

# CUptiResult cuptiDeviceGetEventDomainAttribute (CUdevice device, CUpti\_EventDomainID eventDomain,

# CUpti\_EventDomainAttribute attrib, size\_t \*valueSize, void \*value)

Read an event domain attribute.

#### **Parameters**

#### device

The CUDA device

#### eventDomain

ID of the event domain

#### attrib

The event domain attribute to read

#### valueSize

The size of the value buffer in bytes, and returns the number of bytes written to value

#### value

Returns the attribute's value

#### Returns

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_INVALID\_DEVICE
- CUPTI\_ERROR\_INVALID\_EVENT\_DOMAIN\_ID
- CUPTI ERROR INVALID PARAMETER

if valueSize or value is NULL, or if attrib is not an event domain attribute

► CUPTI ERROR PARAMETER SIZE NOT SUFFICIENT

For non-c-string attribute values, indicates that the value buffer is too small to hold the attribute value.

#### Description

Returns an event domain attribute in \*value. The size of the value buffer is given by \*valueSize. The value returned in \*valueSize contains the number of bytes returned in value.

If the attribute value is a c-string that is longer than \*valueSize, then only the first \*valueSize characters will be returned and there will be no terminating null byte.



Thread-safety: this function is thread safe.

# CUptiResult cuptiDeviceGetNumEventDomains (CUdevice device, uint32\_t \*numDomains)

Get the number of domains for a device.

#### **Parameters**

#### device

The CUDA device

#### numDomains

Returns the number of domains

#### **Returns**

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_INVALID\_DEVICE
- ► CUPTI\_ERROR\_INVALID\_PARAMETER

if numDomains is NULL

#### Description

Returns the number of domains in numDomains for a device.



Thread-safety: this function is thread safe.

# CUptiResult cuptiDeviceGetTimestamp (CUcontext context, uint64\_t \*timestamp)

Read a device timestamp.

#### **Parameters**

#### context

A context on the device from which to get the timestamp

#### timestamp

Returns the device timestamp

#### Returns

- CUPTI SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED

- CUPTI\_ERROR\_INVALID\_CONTEXT
- ► CUPTI\_ERROR\_INVALID\_PARAMETER is timestamp is NULL

#### Description

Returns the device timestamp in \*timestamp. The timestamp is reported in nanoseconds and indicates the time since the device was last reset.



Thread-safety: this function is thread safe.

# CUptiResult cuptiDisableKernelReplayMode (CUcontext context)

Disable kernel replay mode.

#### **Parameters**

#### context

The context

#### Returns

► CUPTI\_SUCCESS

#### Description

Set profiling mode for the context to non-replay (default) mode. Event collection mode will be set to CUPTI\_EVENT\_COLLECTION\_MODE\_KERNEL. All previously enabled event groups and event group sets will be disabled.



Thread-safety: this function is thread safe.

# CUptiResult cuptiEnableKernelReplayMode (CUcontext context)

Enable kernel replay mode.

#### **Parameters**

#### context

The context

#### Returns

CUPTI\_SUCCESS

#### Description

Set profiling mode for the context to replay mode. In this mode, any number of events can be collected in one run of the kernel. The event collection mode will automatically switch to CUPTI\_EVENT\_COLLECTION\_MODE\_KERNEL. In this mode, cuptiSetEventCollectionMode will return CUPTI\_ERROR\_INVALID\_OPERATION.



- Kernels might take longer to run if many events are enabled.
- ► Thread-safety: this function is thread safe.

### CUptiResult cuptiEnumEventDomains (size\_t \*arraySizeBytes, CUpti\_EventDomainID \*domainArray)

Get the event domains available on any device.

#### **Parameters**

#### arraySizeBytes

The size of domainArray in bytes, and returns the number of bytes written to domainArray

#### domainArray

Returns all the event domains

#### Returns

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_INVALID\_PARAMETER

if arraySizeBytes or domainArray are NULL

#### Description

Returns all the event domains available on any CUDA-capable device. Event domain IDs are returned in domainArray. The size of the domainArray buffer is given by \*arraySizeBytes. The size of the domainArray buffer must be at least numDomains \* sizeof(CUpti\_EventDomainID) or all domains will not be returned. The value returned in \*arraySizeBytes contains the number of bytes returned in domainArray.



Thread-safety: this function is thread safe.

# CUptiResult cuptiEventDomainEnumEvents (CUpti\_EventDomainID eventDomain, size\_t \*arraySizeBytes, CUpti\_EventID \*eventArray)

Get the events in a domain.

#### **Parameters**

#### eventDomain

ID of the event domain

#### arraySizeBytes

The size of eventArray in bytes, and returns the number of bytes written to eventArray

#### eventArray

Returns the IDs of the events in the domain

#### Returns

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_INVALID\_EVENT\_DOMAIN\_ID
- CUPTI ERROR INVALID PARAMETER

if arraySizeBytes or eventArray are NULL

#### Description

Returns the event IDs in eventArray for a domain. The size of the eventArray buffer is given by \*arraySizeBytes. The size of the eventArray buffer must be at least numdomainevents \* sizeof(CUpti\_EventID) or else all events will not be returned. The value returned in \*arraySizeBytes contains the number of bytes returned in eventArray.



Thread-safety: this function is thread safe.

### CUptiResult cuptiEventDomainGetAttribute (CUpti\_EventDomainID eventDomain,

### CUpti\_EventDomainAttribute attrib, size\_t \*valueSize, void \*value)

Read an event domain attribute.

#### **Parameters**

#### eventDomain

ID of the event domain

#### attrib

The event domain attribute to read

#### valueSize

The size of the value buffer in bytes, and returns the number of bytes written to value

#### value

Returns the attribute's value

#### Returns

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_INVALID\_EVENT\_DOMAIN\_ID
- CUPTI\_ERROR\_INVALID\_PARAMETER

if valueSize or value is NULL, or if attrib is not an event domain attribute

► CUPTI\_ERROR\_PARAMETER\_SIZE\_NOT\_SUFFICIENT

For non-c-string attribute values, indicates that the value buffer is too small to hold the attribute value.

#### Description

Returns an event domain attribute in \*value. The size of the value buffer is given by \*valueSize. The value returned in \*valueSize contains the number of bytes returned in value.

If the attribute value is a c-string that is longer than \*valueSize, then only the first \*valueSize characters will be returned and there will be no terminating null byte.



Thread-safety: this function is thread safe.

# CUptiResult cuptiEventDomainGetNumEvents (CUpti\_EventDomainID eventDomain, uint32\_t \*numEvents)

Get number of events in a domain.

#### **Parameters**

#### eventDomain

ID of the event domain

#### numEvents

Returns the number of events in the domain

#### Returns

- ► CUPTI\_SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_INVALID\_EVENT\_DOMAIN\_ID
- ► CUPTI ERROR INVALID PARAMETER

if numEvents is NULL

#### Description

Returns the number of events in numEvents for a domain.



Thread-safety: this function is thread safe.

# CUptiResult cuptiEventGetAttribute (CUpti\_EventID event, CUpti\_EventAttribute attrib, size\_t \*valueSize, void \*value)

Get an event attribute.

#### **Parameters**

#### event

ID of the event

#### attrib

The event attribute to read

#### valueSize

The size of the value buffer in bytes, and returns the number of bytes written to value

#### value

Returns the attribute's value

#### Returns

- CUPTI SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_INVALID\_EVENT\_ID
- ► CUPTI\_ERROR\_INVALID\_PARAMETER

if valueSize or value is NULL, or if attrib is not an event attribute

► CUPTI\_ERROR\_PARAMETER\_SIZE\_NOT\_SUFFICIENT

For non-c-string attribute values, indicates that the value buffer is too small to hold the attribute value.

#### Description

Returns an event attribute in \*value. The size of the value buffer is given by \*valueSize. The value returned in \*valueSize contains the number of bytes returned in value.

If the attribute value is a c-string that is longer than \*valueSize, then only the first \*valueSize characters will be returned and there will be no terminating null byte.



Thread-safety: this function is thread safe.

### CUptiResult cuptiEventGetIdFromName (CUdevice device, const char \*eventName, CUpti\_EventID \*event)

Find an event by name.

#### **Parameters**

#### device

The CUDA device

#### eventName

The name of the event to find

#### event

Returns the ID of the found event or undefined if unable to find the event

#### Returns

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_INVALID\_DEVICE
- ► CUPTI ERROR INVALID EVENT NAME

if unable to find an event with name eventName. In this case \*event is undefined

CUPTI ERROR INVALID PARAMETER

if eventName or event are NULL

#### Description

Find an event by name and return the event ID in \*event.



Thread-safety: this function is thread safe.

### CUptiResult cuptiEventGroupAddEvent (CUpti\_EventGroup eventGroup, CUpti\_EventID event)

Add an event to an event group.

#### **Parameters**

#### eventGroup

The event group

#### event

The event to add to the group

#### Returns

- CUPTI\_SUCCESS
- CUPTI ERROR NOT INITIALIZED
- CUPTI\_ERROR\_INVALID\_EVENT\_ID
- CUPTI\_ERROR\_OUT\_OF\_MEMORY
- ► CUPTI\_ERROR\_INVALID\_OPERATION

if eventGroup is enabled

► CUPTI\_ERROR\_NOT\_COMPATIBLE

if event belongs to a different event domain than the events already in eventGroup, or if a device limitation prevents event from being collected at the same time as the events already in eventGroup

CUPTI\_ERROR\_MAX\_LIMIT\_REACHED

if eventGroup is full

CUPTI\_ERROR\_INVALID\_PARAMETER

if eventGroup is NULL

#### Description

Add an event to an event group. The event add can fail for a number of reasons:

- ▶ The event group is enabled
- ► The event does not belong to the same event domain as the events that are already in the event group
- Device limitations on the events that can belong to the same group
- The event group is full



Thread-safety: this function is thread safe.

### CUptiResult cuptiEventGroupCreate (CUcontext context, CUpti\_EventGroup \*eventGroup, uint32\_t flags)

Create a new event group for a context.

#### **Parameters**

#### context

The context for the event group

#### eventGroup

Returns the new event group

#### flags

Reserved - must be zero

#### **Returns**

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI ERROR INVALID CONTEXT
- CUPTI\_ERROR\_OUT\_OF\_MEMORY
- CUPTI\_ERROR\_INVALID\_PARAMETER

if eventGroup is NULL

#### Description

Creates a new event group for context and returns the new group in \*eventGroup.



- flags are reserved for future use and should be set to zero.
- Thread-safety: this function is thread safe.

### CUptiResult cuptiEventGroupDestroy (CUpti\_EventGroup eventGroup)

Destroy an event group.

#### **Parameters**

#### eventGroup

The event group to destroy

#### Returns

- ► CUPTI\_SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_INVALID\_OPERATION
  - if the event group is enabled
- ► CUPTI\_ERROR\_INVALID\_PARAMETER

if eventGroup is NULL

#### Description

Destroy an eventGroup and free its resources. An event group cannot be destroyed if it is enabled.



Thread-safety: this function is thread safe.

### CUptiResult cuptiEventGroupDisable (CUpti\_EventGroup eventGroup)

Disable an event group.

#### **Parameters**

#### eventGroup

The event group

#### Returns

- ► CUPTI\_SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- ► CUPTI\_ERROR\_HARDWARE
- CUPTI\_ERROR\_INVALID\_PARAMETER

if eventGroup is NULL

#### Description

Disable an event group. Disabling an event group stops collection of events contained in the group.



Thread-safety: this function is thread safe.

### CUptiResult cuptiEventGroupEnable (CUpti\_EventGroup eventGroup)

Enable an event group.

#### **Parameters**

#### eventGroup

The event group

#### Returns

- CUPTI SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_HARDWARE
- CUPTI\_ERROR\_NOT\_READY

if eventGroup does not contain any events

► CUPTI\_ERROR\_NOT\_COMPATIBLE

if eventGroup cannot be enabled due to other already enabled event groups

► CUPTI\_ERROR\_INVALID\_PARAMETER

if eventGroup is NULL

CUPTI\_ERROR\_HARDWARE\_BUSY

if another client is profiling and hardware is busy

#### Description

Enable an event group. Enabling an event group zeros the value of all the events in the group and then starts collection of those events.



Thread-safety: this function is thread safe.

# CUptiResult cuptiEventGroupGetAttribute (CUpti\_EventGroup eventGroup, CUpti\_EventGroupAttribute attrib, size\_t \*valueSize, void \*value)

Read an event group attribute.

#### **Parameters**

#### eventGroup

The event group

#### attrib

The attribute to read

#### valueSize

Size of buffer pointed by the value, and returns the number of bytes written to value value

Returns the value of the attribute

#### Returns

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_INVALID\_PARAMETER

if valueSize or value is NULL, or if attrib is not an eventgroup attribute

► CUPTI\_ERROR\_PARAMETER\_SIZE\_NOT\_SUFFICIENT

For non-c-string attribute values, indicates that the value buffer is too small to hold the attribute value.

#### Description

Read an event group attribute and return it in \*value.



Thread-safety: this function is thread safe but client must guard against simultaneous destruction or modification of eventGroup (for example, client must guard against simultaneous calls to cuptiEventGroupDestroy, cuptiEventGroupAddEvent, etc.), and must guard against simultaneous destruction of the context in which eventGroup was created (for example, client must guard against simultaneous calls to cudaDeviceReset, cuCtxDestroy, etc.).

CUptiResult cuptiEventGroupReadAllEvents
(CUpti\_EventGroup eventGroup, CUpti\_ReadEventFlags
flags, size\_t \*eventValueBufferSizeBytes, uint64\_t
\*eventValueBuffer, size\_t \*eventIdArraySizeBytes,
CUpti\_EventID \*eventIdArray, size\_t \*numEventIdsRead)

Read the values for all the events in an event group.

#### **Parameters**

#### eventGroup

The event group

#### flags

Flags controlling the reading mode

#### eventValueBufferSizeBytes

The size of eventValueBuffer in bytes, and returns the number of bytes written to eventValueBuffer

#### eventValueBuffer

Returns the event values

#### eventIdArraySizeBytes

The size of eventIdArray in bytes, and returns the number of bytes written to eventIdArray

#### eventIdArray

Returns the IDs of the events in the same order as the values return in eventValueBuffer.

#### numEventIdsRead

Returns the number of event IDs returned in eventIdArray

#### Returns

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI ERROR HARDWARE
- CUPTI\_ERROR\_INVALID\_OPERATION
  - if eventGroup is disabled
- ► CUPTI ERROR INVALID PARAMETER

if eventGroup, eventValueBufferSizeBytes, eventValueBuffer, eventIdArraySizeBytes, eventIdArray or numEventIdsRead is NULL

► CUPTI\_ERROR\_PARAMETER\_SIZE\_NOT\_SUFFICIENT

if size of eventValueBuffer or eventIdArray is not sufficient

#### Description

Read the values for all the events in an event group. The event values are returned in the eventValueBuffer buffer. eventValueBufferSizeBytes indicates the size of eventValueBuffer. The buffer must be at least (sizeof(uint64) \* number of events in group) if CUPTI\_EVENT\_GROUP\_ATTR\_PROFILE\_ALL\_DOMAIN\_INSTANCES is not set on the group containing the events. The buffer must be at least (sizeof(uint64) \* number of domain instances \* number of events in group) if CUPTI\_EVENT\_GROUP\_ATTR\_PROFILE\_ALL\_DOMAIN\_INSTANCES is set on the group.

The data format returned in eventValueBuffer is:

- domain instance 0: event0 event1 ... eventN
- domain instance 1: event0 event1 ... eventN
- ▶ ..
- domain instance M: event0 event1 ... eventN

The event order in eventValueBuffer is returned in eventIdArray. The size of eventIdArray is specified in eventIdArraySizeBytes. The size should be at least (sizeof(CUpti\_EventID) \* number of events in group).

If any instance of any event counter overflows, the value returned for that event instance will be CUPTI\_EVENT\_OVERFLOW.

The only allowed value for flags is CUPTI\_EVENT\_READ\_FLAG\_NONE.

Reading events from a disabled event group is not allowed. After being read, an event's value is reset to zero.



Thread-safety: this function is thread safe but client must guard against simultaneous destruction or modification of eventGroup (for example, client must guard against simultaneous calls to cuptiEventGroupDestroy, cuptiEventGroupAddEvent, etc.), and must guard against simultaneous destruction of the context in which eventGroup was created (for example, client must guard against simultaneous calls to cudaDeviceReset, cuCtxDestroy, etc.). If cuptiEventGroupResetAllEvents is called simultaneously with this function, then returned event values are undefined.

CUptiResult cuptiEventGroupReadEvent (CUpti\_EventGroup eventGroup, CUpti\_ReadEventFlags flags, CUpti\_EventID event, size\_t \*eventValueBufferSizeBytes, uint64\_t \*eventValueBuffer)

Read the value for an event in an event group.

#### **Parameters**

#### eventGroup

The event group

#### flags

Flags controlling the reading mode

#### event

The event to read

#### eventValueBufferSizeBytes

The size of eventValueBuffer in bytes, and returns the number of bytes written to eventValueBuffer

#### eventValueBuffer

Returns the event value(s)

#### Returns

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_INVALID\_EVENT\_ID
- CUPTI ERROR HARDWARE
- CUPTI\_ERROR\_INVALID\_OPERATION

if eventGroup is disabled

► CUPTI\_ERROR\_INVALID\_PARAMETER

 $if\ event \ Group,\ event \ Value \ Buffer \ Size Bytes\ or\ event \ Value \ Buffer\ is\ NULL$ 

CUPTI ERROR PARAMETER SIZE NOT SUFFICIENT

if size of eventValueBuffer is not sufficient

#### Description

Read the value for an event in an event group. The event value is returned in the eventValueBuffer buffer. eventValueBufferSizeBytes indicates the size of the eventValueBuffer buffer. The buffer must be at least sizeof(uint64) if CUPTI\_EVENT\_GROUP\_ATTR\_PROFILE\_ALL\_DOMAIN\_INSTANCES is not set on the group containing the event. The buffer must be at least (sizeof(uint64) \* number of domain instances) if CUPTI\_EVENT\_GROUP\_ATTR\_PROFILE\_ALL\_DOMAIN\_INSTANCES is set on the group.

If any instance of an event counter overflows, the value returned for that event instance will be CUPTI\_EVENT\_OVERFLOW.

The only allowed value for flags is CUPTI\_EVENT\_READ\_FLAG\_NONE.

Reading an event from a disabled event group is not allowed. After being read, an event's value is reset to zero.



Thread-safety: this function is thread safe but client must guard against simultaneous destruction or modification of eventGroup (for example, client must guard against simultaneous calls to cuptiEventGroupDestroy, cuptiEventGroupAddEvent, etc.), and must guard against simultaneous destruction of the context in which eventGroup was created (for example, client must guard against simultaneous calls to cudaDeviceReset, cuCtxDestroy, etc.). If cuptiEventGroupResetAllEvents is called simultaneously with this function, then returned event values are undefined.

### CUptiResult cuptiEventGroupRemoveAllEvents (CUpti\_EventGroup eventGroup)

Remove all events from an event group.

#### **Parameters**

#### eventGroup

The event group

#### Returns

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_INVALID\_OPERATION
  - if eventGroup is enabled
- ► CUPTI\_ERROR\_INVALID\_PARAMETER

if eventGroup is NULL

#### Description

Remove all events from an event group. Events cannot be removed if the event group is enabled.



Thread-safety: this function is thread safe.

### CUptiResult cuptiEventGroupRemoveEvent (CUpti\_EventGroup eventGroup, CUpti\_EventID event)

Remove an event from an event group.

#### **Parameters**

#### eventGroup

The event group

#### event

The event to remove from the group

#### Returns

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_INVALID\_EVENT\_ID
- CUPTI\_ERROR\_INVALID\_OPERATION

if eventGroup is enabled

CUPTI\_ERROR\_INVALID\_PARAMETER

if eventGroup is NULL

#### Description

Remove event from the an event group. The event cannot be removed if the event group is enabled.



Thread-safety: this function is thread safe.

### CUptiResult cuptiEventGroupResetAllEvents (CUpti\_EventGroup eventGroup)

Zero all the event counts in an event group.

#### **Parameters**

#### eventGroup

The event group

#### Returns

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- ► CUPTI\_ERROR\_HARDWARE
- ► CUPTI\_ERROR\_INVALID\_PARAMETER
  if eventGroup is NULL

#### Description

Zero all the event counts in an event group.



Thread-safety: this function is thread safe but client must guard against simultaneous destruction or modification of eventGroup (for example, client must guard against simultaneous calls to cuptiEventGroupDestroy, cuptiEventGroupAddEvent, etc.), and must guard against simultaneous destruction of the context in which eventGroup was created (for example, client must guard against simultaneous calls to cudaDeviceReset, cuCtxDestroy, etc.).

### CUptiResult cuptiEventGroupSetAttribute (CUpti\_EventGroup eventGroup,

### CUpti\_EventGroupAttribute attrib, size\_t valueSize, void \*value)

Write an event group attribute.

#### **Parameters**

#### eventGroup

The event group

#### attrib

The attribute to write

#### valueSize

The size, in bytes, of the value

#### value

The attribute value to write

#### Returns

- CUPTI SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- ► CUPTI\_ERROR\_INVALID\_PARAMETER

if valueSize or value is NULL, or if attrib is not an event group attribute, or if attrib is not a writable attribute

► CUPTI\_ERROR\_PARAMETER\_SIZE\_NOT\_SUFFICIENT

Indicates that the value buffer is too small to hold the attribute value.

#### Description

Write an event group attribute.



Thread-safety: this function is thread safe.

### CUptiResult cuptiEventGroupSetDisable (CUpti\_EventGroupSet \*eventGroupSet)

Disable an event group set.

#### **Parameters**

#### eventGroupSet

The pointer to the event group set

#### **Returns**

- CUPTI\_SUCCESS
- CUPTI ERROR NOT INITIALIZED
- CUPTI\_ERROR\_HARDWARE
- ► CUPTI\_ERROR\_INVALID\_PARAMETER

if eventGroupSet is NULL

#### Description

Disable a set of event groups. Disabling a set of event groups stops collection of events contained in the groups.



- Thread-safety: this function is thread safe.
- If this call fails, some of the event groups in the set may be disabled and other event groups may remain enabled.

### CUptiResult cuptiEventGroupSetEnable (CUpti\_EventGroupSet \*eventGroupSet)

Enable an event group set.

#### **Parameters**

#### eventGroupSet

The pointer to the event group set

#### Returns

- CUPTI SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_HARDWARE
- CUPTI\_ERROR\_NOT\_READY

if eventGroup does not contain any events

CUPTI\_ERROR\_NOT\_COMPATIBLE

if eventGroup cannot be enabled due to other already enabled event groups

CUPTI\_ERROR\_INVALID\_PARAMETER

if eventGroupSet is NULL

CUPTI ERROR HARDWARE BUSY

if other client is profiling and hardware is busy

#### Description

Enable a set of event groups. Enabling a set of event groups zeros the value of all the events in all the groups and then starts collection of those events.



Thread-safety: this function is thread safe.

# CUptiResult cuptiEventGroupSetsCreate (CUcontext context, size\_t eventIdArraySizeBytes, CUpti\_EventID \*eventIdArray, CUpti\_EventGroupSets \*\*eventGroupPasses)

For a set of events, get the grouping that indicates the number of passes and the event groups necessary to collect the events.

#### **Parameters**

#### context

The context for event collection

#### eventIdArraySizeBytes

Size of eventIdArray in bytes

#### eventIdArray

Array of event IDs that need to be grouped

#### eventGroupPasses

Returns a CUpti\_EventGroupSets object that indicates the number of passes required to collect the events and the events to collect on each pass

#### Returns

- CUPTI\_SUCCESS
- CUPTI ERROR NOT INITIALIZED
- CUPTI\_ERROR\_INVALID\_CONTEXT
- CUPTI\_ERROR\_INVALID\_EVENT\_ID
- CUPTI\_ERROR\_INVALID\_PARAMETER

if eventIdArray or eventGroupPasses is NULL

#### Description

The number of events that can be collected simultaneously varies by device and by the type of the events. When events can be collected simultaneously, they may need to be grouped into multiple event groups because they are from different event domains. This function takes a set of events and determines how many passes are required to collect all those events, and which events can be collected simultaneously in each pass.

The CUpti\_EventGroupSets returned in eventGroupPasses indicates how many passes are required to collect the events with the numSets field. Within each event group set, the sets array indicates the event groups that should be collected on each pass.



Thread-safety: this function is thread safe, but client must guard against another thread simultaneously destroying context.

### CUptiResult cuptiEventGroupSetsDestroy (CUpti\_EventGroupSets \*eventGroupSets)

Destroy a CUpti\_EventGroupSets object.

#### **Parameters**

#### eventGroupSets

The object to destroy

#### **Returns**

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_INVALID\_OPERATION

if any of the event groups contained in the sets is enabled

CUPTI\_ERROR\_INVALID\_PARAMETER

if eventGroupSets is NULL

#### Description

Destroy a CUpti\_EventGroupSets object.



Thread-safety: this function is thread safe.

### CUptiResult cuptiGetNumEventDomains (uint32\_t \*numDomains)

Get the number of event domains available on any device.

#### **Parameters**

#### numDomains

Returns the number of domains

#### Returns

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_INVALID\_PARAMETER

if numDomains is NULL

#### Description

Returns the total number of event domains available on any CUDA-capable device.



Thread-safety: this function is thread safe.

# CUptiResult cuptiKernelReplaySubscribeUpdate (CUpti\_KernelReplayUpdateFunc updateFunc, void \*customData)

Subscribe to kernel replay updates.

#### **Parameters**

#### updateFunc

The update function pointer

#### customData

Pointer to any custom data

#### Returns

CUPTI SUCCESS

#### Description

When subscribed, the function pointer passed in will be called each time a kernel run is finished during kernel replay. Previously subscribed function pointer will be replaced. Pass in NULL as the function pointer unsubscribes the update.

### CUptiResult cuptiSetEventCollectionMode (CUcontext context, CUpti\_EventCollectionMode mode)

Set the event collection mode.

#### **Parameters**

#### context

The context

#### mode

The event collection mode

#### Returns

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI ERROR INVALID CONTEXT
- CUPTI\_ERROR\_INVALID\_OPERATION
  - if called when replay mode is enabled
- ► CUPTI\_ERROR\_NOT\_SUPPORTED

if mode is not supported on the device

#### Description

Set the event collection mode for a context. The mode controls the event collection behavior of all events in event groups created in the context. This API is invalid in kernel replay mode.



Thread-safety: this function is thread safe.

### #define CUPTI\_EVENT\_INVALID ((uint64\_t)0xFFFFFFFFFFFFFFEULL)

The value that indicates the event value is invalid.

### #define CUPTI\_EVENT\_OVERFLOW ((uint64\_t)0xFFFFFFFFFFFFFFULL)

The overflow value for a CUPTI event.

The CUPTI event value that indicates an overflow.

#### 2.6. CUPTI Metric API

Functions, types, and enums that implement the CUPTI Metric API.

#### union CUpti\_MetricValue

A metric value.

#### enum CUpti\_MetricAttribute

Metric attributes.

Metric attributes describe properties of a metric. These attributes can be read using cuptiMetricGetAttribute.

#### **Values**

#### CUPTI\_METRIC\_ATTR\_NAME = 0

Metric name. Value is a null terminated const c-string.

#### CUPTI\_METRIC\_ATTR\_SHORT\_DESCRIPTION = 1

Short description of metric. Value is a null terminated const c-string.

#### CUPTI\_METRIC\_ATTR\_LONG\_DESCRIPTION = 2

Long description of metric. Value is a null terminated const c-string.

#### CUPTI\_METRIC\_ATTR\_CATEGORY = 3

Category of the metric. Value is of type CUpti\_MetricCategory.

#### CUPTI\_METRIC\_ATTR\_VALUE\_KIND = 4

Value type of the metric. Value is of type CUpti\_MetricValueKind.

#### CUPTI\_METRIC\_ATTR\_EVALUATION\_MODE = 5

Metric evaluation mode. Value is of type CUpti MetricEvaluationMode.

CUPTI\_METRIC\_ATTR\_FORCE\_INT = 0x7fffffff

#### enum CUpti\_MetricCategory

A metric category.

Each metric is assigned to a category that represents the general type of the metric. A metric's category is accessed using cuptiMetricGetAttribute and the CUPTI\_METRIC\_ATTR\_CATEGORY attribute.

#### **Values**

#### CUPTI\_METRIC\_CATEGORY\_MEMORY = 0

A memory related metric.

#### CUPTI\_METRIC\_CATEGORY\_INSTRUCTION = 1

An instruction related metric.

CUPTI\_METRIC\_CATEGORY\_MULTIPROCESSOR = 2

A multiprocessor related metric.

CUPTI\_METRIC\_CATEGORY\_CACHE = 3

A cache related metric.

CUPTI\_METRIC\_CATEGORY\_TEXTURE = 4

A texture related metric.

CUPTI\_METRIC\_CATEGORY\_NVLINK = 5

A Nylink related metric.

CUPTI\_METRIC\_CATEGORY\_FORCE\_INT = 0x7fffffff

#### enum CUpti\_MetricEvaluationMode

A metric evaluation mode.

A metric can be evaluated per hardware instance to know the load balancing across instances of a domain or the metric can be evaluated in aggregate mode when the events involved in metric evaluation are from different event domains. It might be possible to evaluate some metrics in both modes for convenience. A metric's evaluation mode is accessed using CUpti\_MetricEvaluationMode and the CUPTI\_METRIC\_ATTR\_EVALUATION\_MODE attribute.

#### **Values**

#### CUPTI\_METRIC\_EVALUATION\_MODE\_PER\_INSTANCE = 1

If this bit is set, the metric can be profiled for each instance of the domain. The event values passed to cuptiMetricGetValue can contain values for one instance of the domain. And cuptiMetricGetValue can be called for each instance.

#### CUPTI\_METRIC\_EVALUATION\_MODE\_AGGREGATE = 1<<1

If this bit is set, the metric can be profiled over all instances. The event values passed to cuptiMetricGetValue can be aggregated values of events for all instances of the domain.

CUPTI\_METRIC\_EVALUATION\_MODE\_FORCE\_INT = 0x7fffffff

#### enum CUpti\_MetricPropertyDeviceClass

Device class.

Enumeration of device classes for metric property CUPTI\_METRIC\_PROPERTY\_DEVICE\_CLASS.

#### **Values**

CUPTI\_METRIC\_PROPERTY\_DEVICE\_CLASS\_TESLA = 0 CUPTI\_METRIC\_PROPERTY\_DEVICE\_CLASS\_QUADRO = 1 CUPTI\_METRIC\_PROPERTY\_DEVICE\_CLASS\_GEFORCE = 2 CUPTI\_METRIC\_PROPERTY\_DEVICE\_CLASS\_TEGRA = 3

#### enum CUpti\_MetricPropertyID

Metric device properties.

Metric device properties describe device properties which are needed for a metric. Some of these properties can be collected using cuDeviceGetAttribute.

#### **Values**

CUPTI\_METRIC\_PROPERTY\_WARPS\_PER\_MULTIPROCESSOR
CUPTI\_METRIC\_PROPERTY\_KERNEL\_GPU\_TIME
CUPTI\_METRIC\_PROPERTY\_CLOCK\_RATE
CUPTI\_METRIC\_PROPERTY\_FRAME\_BUFFER\_COUNT
CUPTI\_METRIC\_PROPERTY\_GLOBAL\_MEMORY\_BANDWIDTH
CUPTI\_METRIC\_PROPERTY\_PCIE\_LINK\_RATE
CUPTI\_METRIC\_PROPERTY\_PCIE\_LINK\_WIDTH
CUPTI\_METRIC\_PROPERTY\_PCIE\_GEN
CUPTI\_METRIC\_PROPERTY\_DEVICE\_CLASS

CUPTI\_METRIC\_PROPERTY\_MULTIPROCESSOR\_COUNT

CUPTI\_METRIC\_PROPERTY\_FLOP\_SP\_PER\_CYCLE CUPTI\_METRIC\_PROPERTY\_FLOP\_DP\_PER\_CYCLE

CURTI METRIC DEODERTY IN UNITE

CUPTI\_METRIC\_PROPERTY\_L2\_UNITS

CUPTI\_METRIC\_PROPERTY\_ECC\_ENABLED

CUPTI\_METRIC\_PROPERTY\_FLOP\_HP\_PER\_CYCLE

CUPTI\_METRIC\_PROPERTY\_GPU\_CPU\_NVLINK\_BANDWIDTH

#### enum CUpti\_MetricValueKind

Kinds of metric values.

Metric values can be one of several different kinds. Corresponding to each kind is a member of the CUpti\_MetricValue union. The metric value returned by cuptiMetricGetValue should be accessed using the appropriate member of that union based on its value kind.

#### **Values**

#### CUPTI\_METRIC\_VALUE\_KIND\_DOUBLE = 0

The metric value is a 64-bit double.

#### CUPTI\_METRIC\_VALUE\_KIND\_UINT64 = 1

The metric value is a 64-bit unsigned integer.

#### CUPTI\_METRIC\_VALUE\_KIND\_PERCENT = 2

The metric value is a percentage represented by a 64-bit double. For example, 57.5% is represented by the value 57.5.

CUPTI\_METRIC\_VALUE\_KIND\_THROUGHPUT = 3

The metric value is a throughput represented by a 64-bit integer. The unit for throughput values is bytes/second.

#### CUPTI\_METRIC\_VALUE\_KIND\_INT64 = 4

The metric value is a 64-bit signed integer.

#### CUPTI\_METRIC\_VALUE\_KIND\_UTILIZATION\_LEVEL = 5

The metric value is a utilization level, as represented by CUpti\_MetricValueUtilizationLevel.

CUPTI\_METRIC\_VALUE\_KIND\_FORCE\_INT = 0x7fffffff

#### enum CUpti\_MetricValueUtilizationLevel

Enumeration of utilization levels for metrics values of kind CUPTI\_METRIC\_VALUE\_KIND\_UTILIZATION\_LEVEL. Utilization values can vary from IDLE (0) to MAX (10) but the enumeration only provides specific names for a few values.

#### **Values**

```
CUPTI_METRIC_VALUE_UTILIZATION_IDLE = 0
CUPTI_METRIC_VALUE_UTILIZATION_LOW = 2
CUPTI_METRIC_VALUE_UTILIZATION_MID = 5
CUPTI_METRIC_VALUE_UTILIZATION_HIGH = 8
CUPTI_METRIC_VALUE_UTILIZATION_MAX = 10
CUPTI_METRIC_VALUE_UTILIZATION_FORCE_INT = 0x7fffffff
```

#### typedef uint32\_t CUpti\_MetricID

ID for a metric.

A metric provides a measure of some aspect of the device.

### CUptiResult cuptiDeviceEnumMetrics (CUdevice device, size\_t \*arraySizeBytes, CUpti\_MetricID \*metricArray)

Get the metrics for a device.

#### **Parameters**

#### device

The CUDA device

#### arraySizeBytes

The size of metricArray in bytes, and returns the number of bytes written to metricArray

#### metricArray

Returns the IDs of the metrics for the device

#### Returns

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_INVALID\_DEVICE
- CUPTI\_ERROR\_INVALID\_PARAMETER

if arraySizeBytes or metricArray are NULL

#### Description

Returns the metric IDs in metricArray for a device. The size of the metricArray buffer is given by \*arraySizeBytes. The size of the metricArray buffer must be at least numMetrics \* sizeof(CUpti\_MetricID) or else all metric IDs will not be returned. The value returned in \*arraySizeBytes contains the number of bytes returned in metricArray.

### CUptiResult cuptiDeviceGetNumMetrics (CUdevice device, uint32\_t \*numMetrics)

Get the number of metrics for a device.

#### **Parameters**

#### device

The CUDA device

#### numMetrics

Returns the number of metrics available for the device

#### Returns

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_INVALID\_DEVICE
- CUPTI\_ERROR\_INVALID\_PARAMETER

if numMetrics is NULL

#### Description

Returns the number of metrics available for a device.

### CUptiResult cuptiEnumMetrics (size\_t \*arraySizeBytes, CUpti\_MetricID \*metricArray)

Get all the metrics available on any device.

#### **Parameters**

#### arraySizeBytes

The size of metricArray in bytes, and returns the number of bytes written to metricArray

#### metricArray

Returns the IDs of the metrics

#### Returns

- CUPTI SUCCESS
- CUPTI\_ERROR\_INVALID\_PARAMETER

if arraySizeBytes or metricArray are NULL

#### Description

Returns the metric IDs in metricArray for all CUDA-capable devices. The size of the metricArray buffer is given by \*arraySizeBytes. The size of the metricArray buffer must be at least numMetrics \* sizeof(CUpti\_MetricID) or all metric IDs will not be returned. The value returned in \*arraySizeBytes contains the number of bytes returned in metricArray.

#### CUptiResult cuptiGetNumMetrics (uint32\_t \*numMetrics)

Get the total number of metrics available on any device.

#### **Parameters**

#### numMetrics

Returns the number of metrics

#### Returns

- CUPTI SUCCESS
- CUPTI\_ERROR\_INVALID\_PARAMETER

if numMetrics is NULL

#### Description

Returns the total number of metrics available on any CUDA-capable devices.

# CUptiResult cuptiMetricCreateEventGroupSets (CUcontext context, size\_t metricIdArraySizeBytes, CUpti\_MetricID \*metricIdArray, CUpti\_EventGroupSets \*\*eventGroupPasses)

For a set of metrics, get the grouping that indicates the number of passes and the event groups necessary to collect the events required for those metrics.

#### **Parameters**

#### context

The context for event collection

#### metricIdArraySizeBytes

Size of the metricIdArray in bytes

#### metricIdArray

Array of metric IDs

#### eventGroupPasses

Returns a CUpti\_EventGroupSets object that indicates the number of passes required to collect the events and the events to collect on each pass

#### **Returns**

- CUPTI SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_INVALID\_CONTEXT
- CUPTI\_ERROR\_INVALID\_METRIC\_ID
- CUPTI\_ERROR\_INVALID\_PARAMETER

if metricIdArray or eventGroupPasses is NULL

#### Description

For a set of metrics, get the grouping that indicates the number of passes and the event groups necessary to collect the events required for those metrics.

#### See also:

cuptiEventGroupSetsCreate for details on event group set creation.

# CUptiResult cuptiMetricEnumEvents (CUpti\_MetricID metric, size\_t \*eventIdArraySizeBytes, CUpti\_EventID \*eventIdArray)

Get the events required to calculating a metric.

#### **Parameters**

#### metric

ID of the metric

#### eventIdArraySizeBytes

The size of eventIdArray in bytes, and returns the number of bytes written to eventIdArray

#### eventIdArray

Returns the IDs of the events required to calculate metric

#### Returns

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_INVALID\_METRIC\_ID
- CUPTI ERROR INVALID PARAMETER

if eventIdArraySizeBytes or eventIdArray are NULL.

#### Description

Gets the event IDs in eventIdArray required to calculate a metric. The size of the eventIdArray buffer is given by \*eventIdArraySizeBytes and must be at least numEvents \* sizeof(CUpti\_EventID) or all events will not be returned. The value returned in \*eventIdArraySizeBytes contains the number of bytes returned in eventIdArray.

# CUptiResult cuptiMetricEnumProperties (CUpti\_MetricID metric, size\_t \*propldArraySizeBytes, CUpti\_MetricPropertyID \*propldArray)

Get the properties required to calculating a metric.

#### **Parameters**

#### metric

ID of the metric

#### propIdArraySizeBytes

The size of propIdArray in bytes, and returns the number of bytes written to propIdArray

#### propIdArray

Returns the IDs of the properties required to calculate metric

#### Returns

- CUPTI SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- ► CUPTI ERROR INVALID METRIC ID
- CUPTI ERROR INVALID PARAMETER

if propIdArraySizeBytes or propIdArray are NULL.

#### Description

Gets the property IDs in propldArray required to calculate a metric. The size of the propldArray buffer is given by \*propldArraySizeBytes and must be at least numProp \* sizeof(CUpti\_DeviceAttribute) or all properties will not be returned. The value returned in \*propldArraySizeBytes contains the number of bytes returned in propldArray.

# CUptiResult cuptiMetricGetAttribute (CUpti\_MetricID metric, CUpti\_MetricAttribute attrib, size\_t \*valueSize, void \*value)

Get a metric attribute.

#### **Parameters**

#### metric

ID of the metric

#### attrib

The metric attribute to read

#### valueSize

The size of the value buffer in bytes, and returns the number of bytes written to value

#### value

Returns the attribute's value

#### Returns

CUPTI\_SUCCESS

- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI ERROR INVALID METRIC ID
- CUPTI\_ERROR\_INVALID\_PARAMETER

if valueSize or value is NULL, or if attrib is not a metric attribute

CUPTI\_ERROR\_PARAMETER\_SIZE\_NOT\_SUFFICIENT

For non-c-string attribute values, indicates that the value buffer is too small to hold the attribute value.

#### Description

Returns a metric attribute in \*value. The size of the value buffer is given by \*valueSize. The value returned in \*valueSize contains the number of bytes returned in value.

If the attribute value is a c-string that is longer than \*valueSize, then only the first \*valueSize characters will be returned and there will be no terminating null byte.

# CUptiResult cuptiMetricGetIdFromName (CUdevice device, const char \*metricName, CUpti\_MetricID \*metric)

Find an metric by name.

#### **Parameters**

#### device

The CUDA device

#### metricName

The name of metric to find

#### metric

Returns the ID of the found metric or undefined if unable to find the metric

#### Returns

- CUPTI SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_INVALID\_DEVICE
- ► CUPTI\_ERROR\_INVALID\_METRIC\_NAME

if unable to find a metric with name metricName. In this case \*metric is undefined

CUPTI ERROR INVALID PARAMETER

if metricName or metric are NULL.

#### Description

Find a metric by name and return the metric ID in \*metric.

### CUptiResult cuptiMetricGetNumEvents (CUpti\_MetricID metric, uint32\_t \*numEvents)

Get number of events required to calculate a metric.

#### **Parameters**

#### metric

ID of the metric

#### numEvents

Returns the number of events required for the metric

#### Returns

- CUPTI\_SUCCESS
- CUPTI ERROR NOT INITIALIZED
- CUPTI\_ERROR\_INVALID\_METRIC\_ID
- CUPTI ERROR INVALID PARAMETER

if numEvents is NULL

#### Description

Returns the number of events in numEvents that are required to calculate a metric.

### CUptiResult cuptiMetricGetNumProperties (CUpti\_MetricID metric, uint32\_t \*numProp)

Get number of properties required to calculate a metric.

#### **Parameters**

#### metric

ID of the metric

#### numProp

Returns the number of properties required for the metric

#### Returns

CUPTI\_SUCCESS

- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_INVALID\_METRIC\_ID
- ► CUPTI\_ERROR\_INVALID\_PARAMETER if numProp is NULL

#### Description

Returns the number of properties in numProp that are required to calculate a metric.

# CUptiResult cuptiMetricGetRequiredEventGroupSets (CUcontext context, CUpti\_MetricID metric, CUpti\_EventGroupSets \*\*eventGroupSets)

For a metric get the groups of events that must be collected in the same pass.

#### **Parameters**

#### context

The context for event collection

#### metric

The metric ID

#### eventGroupSets

Returns a CUpti\_EventGroupSets object that indicates the events that must be collected in the same pass to ensure the metric is calculated correctly. Returns NULL if no grouping is required for metric

#### Returns

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_INVALID\_METRIC\_ID

#### Description

For a metric get the groups of events that must be collected in the same pass to ensure that the metric is calculated correctly. If the events are not collected as specified then the metric value may be inaccurate.

The function returns NULL if a metric does not have any required event group. In this case the events needed for the metric can be grouped in any manner for collection.

CUptiResult cuptiMetricGetValue (CUdevice device, CUpti\_MetricID metric, size\_t eventIdArraySizeBytes, CUpti\_EventID \*eventIdArray, size\_t eventValueArraySizeBytes, uint64\_t \*eventValueArray, uint64\_t timeDuration, CUpti\_MetricValue \*metricValue)

Calculate the value for a metric.

#### **Parameters**

#### device

The CUDA device that the metric is being calculated for

#### metric

The metric ID

#### eventIdArraySizeBytes

The size of eventIdArray in bytes

#### eventIdArray

The event IDs required to calculate metric

#### eventValueArraySizeBytes

The size of eventValueArray in bytes

#### eventValueArray

The normalized event values required to calculate metric. The values must be order to match the order of events in eventIdArray

#### timeDuration

The duration over which the events were collected, in ns

#### metricValue

Returns the value for the metric

#### Returns

- CUPTI\_SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_INVALID\_METRIC\_ID
- CUPTI\_ERROR\_INVALID\_OPERATION
- CUPTI\_ERROR\_PARAMETER\_SIZE\_NOT\_SUFFICIENT

if the eventIdArray does not contain all the events needed for metric

CUPTI\_ERROR\_INVALID\_EVENT\_VALUE

if any of the event values required for the metric is CUPTI EVENT OVERFLOW

CUPTI\_ERROR\_INVALID\_METRIC\_VALUE

if the computed metric value cannot be represented in the metric's value type. For example, if the metric value type is unsigned and the computed metric value is negative

CUPTI\_ERROR\_INVALID\_PARAMETER

if metricValue, eventIdArray or eventValueArray is NULL

#### Description

Use the events collected for a metric to calculate the metric value. Metric value evaluation depends on the evaluation mode CUpti\_MetricEvaluationMode that the metric supports. If a metric has evaluation mode as CUPTI\_METRIC\_EVALUATION\_MODE\_PER\_INSTANCE, then it assumes that the input event value is for one domain instance. If a metric has evaluation mode as CUPTI\_METRIC\_EVALUATION\_MODE\_AGGREGATE, it assumes that input event values are normalized to represent all domain instances on a device. For the most accurate metric collection, the events required for the metric should be collected for all profiled domain instances. For example, to collect all instances of an event, set the CUPTI\_EVENT\_GROUP\_ATTR\_PROFILE\_ALL\_DOMAIN\_INSTANCES attribute on the group containing the event to 1. The normalized value for the event is then: (sum event values \* totalInstanceCount) / instanceCount, where sum event values is the summation of the event values across all profiled domain instances, totalInstanceCount is obtained from querying CUPTI\_EVENT\_DOMAIN\_ATTR\_TOTAL\_INSTANCE\_COUNT and instanceCount is obtained from querying CUPTI\_EVENT\_GROUP\_ATTR\_INSTANCE\_COUNT (or CUPTI EVENT DOMAIN ATTR INSTANCE COUNT).

CUptiResult cuptiMetricGetValue2 (CUpti\_MetricID metric, size\_t eventIdArraySizeBytes, CUpti\_EventID \*eventIdArray, size\_t eventValueArraySizeBytes, uint64\_t \*eventValueArray, size\_t propIdArraySizeBytes, CUpti\_MetricPropertyID \*propIdArray, size\_t propValueArraySizeBytes, uint64\_t \*propValueArray, CUpti\_MetricValue \*metricValue)

Calculate the value for a metric.

#### **Parameters**

#### metric

The metric ID

#### eventIdArraySizeBytes

The size of eventIdArray in bytes

#### eventIdArray

The event IDs required to calculate metric

#### eventValueArraySizeBytes

The size of eventValueArray in bytes

#### eventValueArray

The normalized event values required to calculate metric. The values must be order to match the order of events in eventIdArray

#### propIdArraySizeBytes

The size of propIdArray in bytes

#### propIdArray

The metric property IDs required to calculate metric

#### propValueArraySizeBytes

The size of propValueArray in bytes

#### propValueArray

The metric property values required to calculate metric. The values must be order to match the order of metric properties in propldArray

#### metricValue

Returns the value for the metric

#### Returns

- CUPTI SUCCESS
- CUPTI\_ERROR\_NOT\_INITIALIZED
- CUPTI\_ERROR\_INVALID\_METRIC\_ID
- CUPTI ERROR INVALID OPERATION
- ► CUPTI\_ERROR\_PARAMETER\_SIZE\_NOT\_SUFFICIENT

if the eventIdArray does not contain all the events needed for metric

CUPTI\_ERROR\_INVALID\_EVENT\_VALUE

if any of the event values required for the metric is CUPTI\_EVENT\_OVERFLOW

CUPTI ERROR NOT COMPATIBLE

if the computed metric value cannot be represented in the metric's value type. For example, if the metric value type is unsigned and the computed metric value is negative

CUPTI\_ERROR\_INVALID\_PARAMETER

if metricValue, eventIdArray or eventValueArray is NULL

#### Description

Use the events and properties collected for a metric to calculate the metric value. Metric value evaluation depends on the evaluation mode CUpti MetricEvaluationMode that the metric supports. If a metric has evaluation mode as CUPTI\_METRIC\_EVALUATION\_MODE\_PER\_INSTANCE, then it assumes that the input event value is for one domain instance. If a metric has evaluation mode as CUPTI\_METRIC\_EVALUATION\_MODE\_AGGREGATE, it assumes that input event values are normalized to represent all domain instances on a device. For the most accurate metric collection, the events required for the metric should be collected for all profiled domain instances. For example, to collect all instances of an event, set the CUPTI\_EVENT\_GROUP\_ATTR\_PROFILE\_ALL\_DOMAIN\_INSTANCES attribute on the group containing the event to 1. The normalized value for the event is then: (sum event values \* totalInstanceCount) / instanceCount, where sum event values is the summation of the event values across all profiled domain instances, totalInstanceCount is obtained from querying CUPTI\_EVENT\_DOMAIN\_ATTR\_TOTAL\_INSTANCE\_COUNT and instanceCount is obtained from querying CUPTI\_EVENT\_GROUP\_ATTR\_INSTANCE\_COUNT (or CUPTI\_EVENT\_DOMAIN\_ATTR\_INSTANCE\_COUNT).

# Chapter 3. DATA STRUCTURES

Here are the data structures with brief descriptions:

#### CUpti\_Activity

The base activity record

#### CUpti\_ActivityAPI

The activity record for a driver or runtime API invocation

#### CUpti\_ActivityAutoBoostState

Device auto boost state structure

#### CUpti\_ActivityBranch

The activity record for source level result branch. (deprecated)

#### CUpti\_ActivityBranch2

The activity record for source level result branch

#### CUpti\_ActivityCdpKernel

The activity record for CDP (CUDA Dynamic Parallelism) kernel

#### CUpti\_ActivityContext

The activity record for a context

#### CUpti\_ActivityCudaEvent

The activity record for CUDA event

#### CUpti\_ActivityDevice

The activity record for a device. (deprecated)

#### CUpti\_ActivityDevice2

The activity record for a device. (CUDA 7.0 onwards)

#### CUpti\_ActivityDeviceAttribute

The activity record for a device attribute

#### CUpti\_ActivityEnvironment

The activity record for CUPTI environmental data

#### CUpti\_ActivityEvent

The activity record for a CUPTI event

#### CUpti\_ActivityEventInstance

The activity record for a CUPTI event with instance information

#### CUpti\_ActivityExternalCorrelation

The activity record for correlation with external records

#### **CUpti\_ActivityFunction**

The activity record for global/device functions

#### CUpti\_ActivityGlobalAccess

The activity record for source-level global access. (deprecated)

#### CUpti\_ActivityGlobalAccess2

The activity record for source-level global access. (deprecated in CUDA 9.0)

#### CUpti\_ActivityGlobalAccess3

The activity record for source-level global access

#### CUpti\_ActivityInstantaneousEvent

The activity record for an instantaneous CUPTI event

#### CUpti\_ActivityInstantaneousEventInstance

The activity record for an instantaneous CUPTI event with event domain instance information

#### CUpti\_ActivityInstantaneousMetric

The activity record for an instantaneous CUPTI metric

#### CUpti\_ActivityInstantaneousMetricInstance

The instantaneous activity record for a CUPTI metric with instance information

#### CUpti\_ActivityInstructionCorrelation

The activity record for source-level sass/source line-by-line correlation

#### CUpti\_ActivityInstructionExecution

The activity record for source-level instruction execution

#### CUpti\_ActivityKernel

The activity record for kernel. (deprecated)

#### CUpti\_ActivityKernel2

The activity record for kernel. (deprecated)

#### CUpti\_ActivityKernel3

The activity record for a kernel (CUDA 6.5(with sm\_52 support) onwards).

(deprecated in CUDA 9.0)

#### CUpti\_ActivityKernel4

The activity record for a kernel

#### CUpti\_ActivityMarker

The activity record providing a marker which is an instantaneous point in time. (deprecated in CUDA 8.0)

#### CUpti\_ActivityMarker2

The activity record providing a marker which is an instantaneous point in time

#### CUpti\_ActivityMarkerData

The activity record providing detailed information for a marker

#### CUpti\_ActivityMemcpy

The activity record for memory copies

#### CUpti\_ActivityMemcpy2

The activity record for peer-to-peer memory copies

#### CUpti\_ActivityMemory

The activity record for memory

#### CUpti\_ActivityMemset

The activity record for memset

#### CUpti\_ActivityMetric

The activity record for a CUPTI metric

#### CUpti\_ActivityMetricInstance

The activity record for a CUPTI metric with instance information

#### CUpti\_ActivityModule

The activity record for a CUDA module

#### CUpti\_ActivityName

The activity record providing a name

#### CUpti\_ActivityNvLink

NVLink information. (deprecated in CUDA 9.0)

#### CUpti\_ActivityNvLink2

**NVLink** information

#### CUpti\_ActivityObjectKindId

Identifiers for object kinds as specified by CUpti\_ActivityObjectKind

#### CUpti\_ActivityOpenAcc

The base activity record for OpenAcc records

#### CUpti\_ActivityOpenAccData

The activity record for OpenACC data

#### CUpti\_ActivityOpenAccLaunch

The activity record for OpenACC launch

#### CUpti\_ActivityOpenAccOther

The activity record for OpenACC other

#### CUpti\_ActivityOverhead

The activity record for CUPTI and driver overheads

#### CUpti\_ActivityPCSampling

The activity record for PC sampling. (deprecated in CUDA 8.0)

#### CUpti\_ActivityPCSampling2

The activity record for PC sampling. (deprecated in CUDA 9.0)

#### CUpti\_ActivityPCSampling3

The activity record for PC sampling

#### CUpti\_ActivityPCSamplingConfig

PC sampling configuration structure

#### CUpti\_ActivityPCSamplingRecordInfo

The activity record for record status for PC sampling

#### CUpti\_ActivityPreemption

The activity record for a preemption of a CDP kernel

#### CUpti\_ActivitySharedAccess

The activity record for source-level shared access

#### CUpti\_ActivitySourceLocator

The activity record for source locator

#### CUpti\_ActivityStream

The activity record for CUDA stream

#### CUpti\_ActivitySynchronization

The activity record for synchronization management

#### CUpti\_ActivityUnifiedMemoryCounter

The activity record for Unified Memory counters (deprecated in CUDA 7.0)

#### CUpti\_ActivityUnifiedMemoryCounter2

The activity record for Unified Memory counters (CUDA 7.0 and beyond)

#### CUpti\_ActivityUnifiedMemoryCounterConfig

Unified Memory counters configuration structure

#### CUpti\_CallbackData

Data passed into a runtime or driver API callback function

#### CUpti\_EventGroupSet

A set of event groups

#### CUpti\_EventGroupSets

A set of event group sets

#### CUpti\_MetricValue

A metric value

#### CUpti\_ModuleResourceData

Module data passed into a resource callback function

#### CUpti NvtxData

Data passed into a NVTX callback function

#### CUpti\_ResourceData

Data passed into a resource callback function

#### CUpti\_SynchronizeData

Data passed into a synchronize callback function

# 3.1. CUpti\_Activity Struct Reference

The base activity record.

The activity API uses a CUpti\_Activity as a generic representation for any activity. The 'kind' field is used to determine the specific activity kind, and from that the CUpti\_Activity object can be cast to the specific activity record type appropriate for that kind.

Note that all activity record types are padded and aligned to ensure that each member of the record is naturally aligned.

#### See also:

CUpti\_ActivityKind

# CUpti\_ActivityKind CUpti\_Activity::kind

The kind of this activity.

# 3.2. CUpti\_ActivityAPI Struct Reference

The activity record for a driver or runtime API invocation.

This activity record represents an invocation of a driver or runtime API (CUPTI\_ACTIVITY\_KIND\_DRIVER and CUPTI\_ACTIVITY\_KIND\_RUNTIME).

# CUpti\_CallbackId CUpti\_ActivityAPI::cbid

The ID of the driver or runtime function.

## uint32\_t CUpti\_ActivityAPI::correlationId

The correlation ID of the driver or runtime CUDA function. Each function invocation is assigned a unique correlation ID that is identical to the correlation ID in the memcpy, memset, or kernel activity record that is associated with this function.

## uint64\_t CUpti\_ActivityAPI::end

The end timestamp for the function, in ns. A value of 0 for both the start and end timestamps indicates that timestamp information could not be collected for the function.

# CUpti\_ActivityKind CUpti\_ActivityAPI::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_DRIVER or CUPTI\_ACTIVITY\_KIND\_RUNTIME.

#### uint32\_t CUpti\_ActivityAPI::processId

The ID of the process where the driver or runtime CUDA function is executing.

## uint32\_t CUpti\_ActivityAPI::returnValue

The return value for the function. For a CUDA driver function with will be a CUresult value, and for a CUDA runtime function this will be a cudaError\_t value.

## uint64\_t CUpti\_ActivityAPI::start

The start timestamp for the function, in ns. A value of 0 for both the start and end timestamps indicates that timestamp information could not be collected for the function.

#### uint32\_t CUpti\_ActivityAPI::threadId

The ID of the thread where the driver or runtime CUDA function is executing.

# 3.3. CUpti\_ActivityAutoBoostState Struct Reference

Device auto boost state structure.

This structure defines auto boost state for a device. See function /ref cuptiGetAutoBoostState

## uint32\_t CUpti\_ActivityAutoBoostState::enabled

Returned auto boost state. 1 is returned in case auto boost is enabled, 0 otherwise

## uint32\_t CUpti\_ActivityAutoBoostState::pid

Id of process that has set the current boost state. The value will be CUPTI\_AUTO\_BOOST\_INVALID\_CLIENT\_PID if the user does not have the permission to query process ids or there is an error in querying the process id.

# 3.4. CUpti\_ActivityBranch Struct Reference

The activity record for source level result branch. (deprecated).

This activity record the locations of the branches in the source (CUPTI\_ACTIVITY\_KIND\_BRANCH). Branch activities are now reported using the CUpti\_ActivityBranch2 activity record.

## uint32\_t CUpti\_ActivityBranch::correlationId

The correlation ID of the kernel to which this result is associated.

#### uint32\_t CUpti\_ActivityBranch::diverged

Number of times this branch diverged

## uint32\_t CUpti\_ActivityBranch::executed

The number of times this instruction was executed per warp. It will be incremented regardless of predicate or condition code.

# CUpti\_ActivityKind CUpti\_ActivityBranch::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_BRANCH.

## uint32\_t CUpti\_ActivityBranch::pcOffset

The pc offset for the branch.

## uint32\_t CUpti\_ActivityBranch::sourceLocatorId

The ID for source locator.

## uint64\_t CUpti\_ActivityBranch::threadsExecuted

This increments each time when this instruction is executed by number of threads that executed this instruction

# 3.5. CUpti\_ActivityBranch2 Struct Reference

The activity record for source level result branch.

This activity record the locations of the branches in the source (CUPTI\_ACTIVITY\_KIND\_BRANCH).

## uint32\_t CUpti\_ActivityBranch2::correlationId

The correlation ID of the kernel to which this result is associated.

## uint32\_t CUpti\_ActivityBranch2::diverged

Number of times this branch diverged

# uint32\_t CUpti\_ActivityBranch2::executed

The number of times this instruction was executed per warp. It will be incremented regardless of predicate or condition code.

# uint32\_t CUpti\_ActivityBranch2::functionId

Correlation ID with global/device function name

## CUpti\_ActivityKind CUpti\_ActivityBranch2::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_BRANCH.

# uint32\_t CUpti\_ActivityBranch2::pad

Undefined. Reserved for internal use.

## uint32\_t CUpti\_ActivityBranch2::pcOffset

The pc offset for the branch.

## uint32\_t CUpti\_ActivityBranch2::sourceLocatorId

The ID for source locator.

# uint64\_t CUpti\_ActivityBranch2::threadsExecuted

This increments each time when this instruction is executed by number of threads that executed this instruction

# 3.6. CUpti\_ActivityCdpKernel Struct Reference

The activity record for CDP (CUDA Dynamic Parallelism) kernel.

This activity record represents a CDP kernel execution.

#### int32\_t CUpti\_ActivityCdpKernel::blockX

The X-dimension block size for the kernel.

#### int32\_t CUpti\_ActivityCdpKernel::blockY

The Y-dimension block size for the kernel.

## int32\_t CUpti\_ActivityCdpKernel::blockZ

The Z-dimension grid size for the kernel.

#### uint64\_t CUpti\_ActivityCdpKernel::completed

The timestamp when kernel is marked as completed, in ns. A value of CUPTI\_TIMESTAMP\_UNKNOWN indicates that the completion time is unknown.

## uint32\_t CUpti\_ActivityCdpKernel::contextId

The ID of the context where the kernel is executing.

## uint32\_t CUpti\_ActivityCdpKernel::correlationId

The correlation ID of the kernel. Each kernel execution is assigned a unique correlation ID that is identical to the correlation ID in the driver API activity record that launched the kernel.

# uint32\_t CUpti\_ActivityCdpKernel::deviceId

The ID of the device where the kernel is executing.

## int32\_t

# CUpti\_ActivityCdpKernel::dynamicSharedMemory

The dynamic shared memory reserved for the kernel, in bytes.

## uint64\_t CUpti\_ActivityCdpKernel::end

The end timestamp for the kernel execution, in ns. A value of 0 for both the start and end timestamps indicates that timestamp information could not be collected for the kernel.

## uint8\_t CUpti\_ActivityCdpKernel::executed

The cache configuration used for the kernel. The value is one of the CUfunc\_cache enumeration values from cuda.h.

## int64\_t CUpti\_ActivityCdpKernel::gridId

The grid ID of the kernel. Each kernel execution is assigned a unique grid ID.

## int32\_t CUpti\_ActivityCdpKernel::gridX

The X-dimension grid size for the kernel.

## int32\_t CUpti\_ActivityCdpKernel::gridY

The Y-dimension grid size for the kernel.

## int32\_t CUpti\_ActivityCdpKernel::gridZ

The Z-dimension grid size for the kernel.

## CUpti\_ActivityKind CUpti\_ActivityCdpKernel::kind

The activity record kind, must be CUPTI ACTIVITY KIND CDP KERNEL

#### uint32\_t

# CUpti\_ActivityCdpKernel::localMemoryPerThread

The amount of local memory reserved for each thread, in bytes.

# uint32\_t CUpti\_ActivityCdpKernel::localMemoryTotal

The total amount of local memory reserved for the kernel, in bytes.

## const char \*CUpti\_ActivityCdpKernel::name

The name of the kernel. This name is shared across all activity records representing the same kernel, and so should not be modified.

## uint32\_t CUpti\_ActivityCdpKernel::parentBlockX

The X-dimension of the parent block.

## uint32\_t CUpti\_ActivityCdpKernel::parentBlockY

The Y-dimension of the parent block.

## uint32 t CUpti ActivityCdpKernel::parentBlockZ

The Z-dimension of the parent block.

# int64\_t CUpti\_ActivityCdpKernel::parentGridId

The grid ID of the parent kernel.

## uint64\_t CUpti\_ActivityCdpKernel::queued

The timestamp when kernel is queued up, in ns. A value of CUPTI\_TIMESTAMP\_UNKNOWN indicates that the queued time is unknown.

# uint16\_t CUpti\_ActivityCdpKernel::registersPerThread

The number of registers required for each thread executing the kernel.

#### uint8 t CUpti ActivityCdpKernel::requested

The cache configuration requested by the kernel. The value is one of the CUfunc\_cache enumeration values from cuda.h.

## uint8\_t CUpti\_ActivityCdpKernel::sharedMemoryConfig

The shared memory configuration used for the kernel. The value is one of the CUshared configuration values from cuda.h.

## uint64\_t CUpti\_ActivityCdpKernel::start

The start timestamp for the kernel execution, in ns. A value of 0 for both the start and end timestamps indicates that timestamp information could not be collected for the kernel.

# int32\_t CUpti\_ActivityCdpKernel::staticSharedMemory

The static shared memory allocated for the kernel, in bytes.

## uint32\_t CUpti\_ActivityCdpKernel::streamId

The ID of the stream where the kernel is executing.

## uint64\_t CUpti\_ActivityCdpKernel::submitted

The timestamp when kernel is submitted to the gpu, in ns. A value of CUPTI TIMESTAMP UNKNOWN indicates that the submission time is unknown.

# 3.7. CUpti\_ActivityContext Struct Reference

The activity record for a context.

This activity record represents information about a context (CUPTI\_ACTIVITY\_KIND\_CONTEXT).

#### uint16\_t CUpti\_ActivityContext::computeApiKind

The compute API kind.

See also:

CUpti\_ActivityComputeApiKind

#### uint32\_t CUpti\_ActivityContext::contextId

The context ID.

#### uint32\_t CUpti\_ActivityContext::deviceId

The device ID.

# CUpti\_ActivityKind CUpti\_ActivityContext::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_CONTEXT.

#### uint16\_t CUpti\_ActivityContext::nullStreamId

The ID for the NULL stream in this context

# 3.8. CUpti\_ActivityCudaEvent Struct Reference

The activity record for CUDA event.

This activity is used to track recorded events. (CUPTI\_ACTIVITY\_KIND\_CUDA\_EVENT).

## uint32\_t CUpti\_ActivityCudaEvent::contextId

The ID of the context where the event was recorded.

#### uint32\_t CUpti\_ActivityCudaEvent::correlationId

The correlation ID of the API to which this result is associated.

#### uint32\_t CUpti\_ActivityCudaEvent::eventId

A unique event ID to identify the event record.

## CUpti\_ActivityKind CUpti\_ActivityCudaEvent::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_CUDA\_EVENT.

#### uint32\_t CUpti\_ActivityCudaEvent::pad

Undefined. Reserved for internal use.

#### uint32\_t CUpti\_ActivityCudaEvent::streamId

The compute stream where the event was recorded.

# 3.9. CUpti\_ActivityDevice Struct Reference

The activity record for a device. (deprecated).

This activity record represents information about a GPU device (CUPTI\_ACTIVITY\_KIND\_DEVICE). Device activity is now reported using the CUpti\_ActivityDevice2 activity record.

uint32\_t CUpti\_ActivityDevice::computeCapabilityMajor

Compute capability for the device, major number.

uint32\_t CUpti\_ActivityDevice::computeCapabilityMinor

Compute capability for the device, minor number.

uint32\_t CUpti\_ActivityDevice::constantMemorySize

The amount of constant memory on the device, in bytes.

uint32\_t CUpti\_ActivityDevice::coreClockRate

The core clock rate of the device, in kHz.

CUpti\_ActivityFlag CUpti\_ActivityDevice::flags

The flags associated with the device.

See also:

CUpti ActivityFlag

uint64\_t CUpti\_ActivityDevice::globalMemoryBandwidth

The global memory bandwidth available on the device, in kBytes/sec.

uint64\_t CUpti\_ActivityDevice::globalMemorySize

The amount of global memory on the device, in bytes.

uint32\_t CUpti\_ActivityDevice::id

The device ID.

# CUpti\_ActivityKind CUpti\_ActivityDevice::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_DEVICE.

## uint32\_t CUpti\_ActivityDevice::l2CacheSize

The size of the L2 cache on the device, in bytes.

## uint32\_t CUpti\_ActivityDevice::maxBlockDimX

Maximum allowed X dimension for a block.

## uint32\_t CUpti\_ActivityDevice::maxBlockDimY

Maximum allowed Y dimension for a block.

#### uint32\_t CUpti\_ActivityDevice::maxBlockDimZ

Maximum allowed Z dimension for a block.

#### uint32\_t

## CUpti\_ActivityDevice::maxBlocksPerMultiprocessor

Maximum number of blocks that can be present on a multiprocessor at any given time.

#### uint32\_t CUpti\_ActivityDevice::maxGridDimX

Maximum allowed X dimension for a grid.

#### uint32\_t CUpti\_ActivityDevice::maxGridDimY

Maximum allowed Y dimension for a grid.

#### uint32\_t CUpti\_ActivityDevice::maxGridDimZ

Maximum allowed Z dimension for a grid.

#### uint32\_t CUpti\_ActivityDevice::maxIPC

The maximum "instructions per cycle" possible on each device multiprocessor.

## uint32\_t CUpti\_ActivityDevice::maxRegistersPerBlock

Maximum number of registers that can be allocated to a block.

#### uint32\_t

# CUpti\_ActivityDevice::maxSharedMemoryPerBlock

Maximum amount of shared memory that can be assigned to a block, in bytes.

# uint32\_t CUpti\_ActivityDevice::maxThreadsPerBlock

Maximum number of threads allowed in a block.

## uint32\_t

## CUpti\_ActivityDevice::maxWarpsPerMultiprocessor

Maximum number of warps that can be present on a multiprocessor at any given time.

# const char \*CUpti\_ActivityDevice::name

The device name. This name is shared across all activity records representing instances of the device, and so should not be modified.

## uint32\_t CUpti\_ActivityDevice::numMemcpyEngines

Number of memory copy engines on the device.

#### uint32\_t CUpti\_ActivityDevice::numMultiprocessors

Number of multiprocessors on the device.

#### uint32\_t CUpti\_ActivityDevice::numThreadsPerWarp

The number of threads per warp on the device.

# 3.10. CUpti\_ActivityDevice2 Struct Reference

The activity record for a device. (CUDA 7.0 onwards).

This activity record represents information about a GPU device (CUPTI\_ACTIVITY\_KIND\_DEVICE).

#### uint32\_t

#### CUpti ActivityDevice2::computeCapabilityMajor

Compute capability for the device, major number.

#### uint32\_t

# CUpti\_ActivityDevice2::computeCapabilityMinor

Compute capability for the device, minor number.

# uint32\_t CUpti\_ActivityDevice2::constantMemorySize

The amount of constant memory on the device, in bytes.

## uint32\_t CUpti\_ActivityDevice2::coreClockRate

The core clock rate of the device, in kHz.

## uint32\_t CUpti\_ActivityDevice2::eccEnabled

ECC enabled flag for device

# CUpti\_ActivityFlag CUpti\_ActivityDevice2::flags

The flags associated with the device.

See also:

CUpti\_ActivityFlag

#### uint64\_t

# CUpti\_ActivityDevice2::globalMemoryBandwidth

The global memory bandwidth available on the device, in kBytes/sec.

#### uint64 t CUpti ActivityDevice2::globalMemorySize

The amount of global memory on the device, in bytes.

uint32\_t CUpti\_ActivityDevice2::id

The device ID.

# CUpti\_ActivityKind CUpti\_ActivityDevice2::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_DEVICE.

## uint32\_t CUpti\_ActivityDevice2::l2CacheSize

The size of the L2 cache on the device, in bytes.

uint32\_t CUpti\_ActivityDevice2::maxBlockDimX

Maximum allowed X dimension for a block.

uint32\_t CUpti\_ActivityDevice2::maxBlockDimY

Maximum allowed Y dimension for a block.

uint32\_t CUpti\_ActivityDevice2::maxBlockDimZ

Maximum allowed Z dimension for a block.

uint32\_t

CUpti\_ActivityDevice2::maxBlocksPerMultiprocessor

Maximum number of blocks that can be present on a multiprocessor at any given time.

uint32\_t CUpti\_ActivityDevice2::maxGridDimX

Maximum allowed X dimension for a grid.

uint32\_t CUpti\_ActivityDevice2::maxGridDimY

Maximum allowed Y dimension for a grid.

uint32\_t CUpti\_ActivityDevice2::maxGridDimZ

Maximum allowed Z dimension for a grid.

uint32\_t CUpti\_ActivityDevice2::maxIPC

The maximum "instructions per cycle" possible on each device multiprocessor.

uint32\_t CUpti\_ActivityDevice2::maxRegistersPerBlock

Maximum number of registers that can be allocated to a block.

uint32\_t

CUpti\_ActivityDevice2::maxRegistersPerMultiprocessor

Maximum number of 32-bit registers available per multiprocessor.

#### uint32\_t

# CUpti\_ActivityDevice2::maxSharedMemoryPerBlock

Maximum amount of shared memory that can be assigned to a block, in bytes.

#### uint32 t

# CUpti\_ActivityDevice2::maxSharedMemoryPerMultiprocessor

Maximum amount of shared memory available per multiprocessor, in bytes.

## uint32\_t CUpti\_ActivityDevice2::maxThreadsPerBlock

Maximum number of threads allowed in a block.

## uint32 t

# CUpti\_ActivityDevice2::maxWarpsPerMultiprocessor

Maximum number of warps that can be present on a multiprocessor at any given time.

# const char \*CUpti\_ActivityDevice2::name

The device name. This name is shared across all activity records representing instances of the device, and so should not be modified.

## uint32\_t CUpti\_ActivityDevice2::numMemcpyEngines

Number of memory copy engines on the device.

#### uint32\_t CUpti\_ActivityDevice2::numMultiprocessors

Number of multiprocessors on the device.

## uint32\_t CUpti\_ActivityDevice2::numThreadsPerWarp

The number of threads per warp on the device.

#### uint32\_t CUpti\_ActivityDevice2::pad

Undefined. Reserved for internal use.

## CUuuid CUpti\_ActivityDevice2::uuid

The device UUID. This value is the globally unique immutable alphanumeric identifier of the device.

# 3.11. CUpti\_ActivityDeviceAttribute Struct Reference

The activity record for a device attribute.

This activity record represents information about a GPU device: either a CUpti\_DeviceAttribute or CUdevice\_attribute value (CUPTI\_ACTIVITY\_KIND\_DEVICE\_ATTRIBUTE).

# CUpti\_ActivityDeviceAttribute::@10 CUpti\_ActivityDeviceAttribute::attribute

The attribute, either a CUpti\_DeviceAttribute or CUdevice\_attribute. Flag CUPTI\_ACTIVITY\_FLAG\_DEVICE\_ATTRIBUTE\_CUDEVICE is used to indicate what kind of attribute this is. If CUPTI\_ACTIVITY\_FLAG\_DEVICE\_ATTRIBUTE\_CUDEVICE is 1 then CUdevice\_attribute field is value, otherwise CUpti\_DeviceAttribute field is valid.

## uint32\_t CUpti\_ActivityDeviceAttribute::deviceId

The ID of the device that this attribute applies to.

# CUpti\_ActivityFlag CUpti\_ActivityDeviceAttribute::flags

The flags associated with the device.

See also:

CUpti\_ActivityFlag

#### CUpti ActivityKind CUpti ActivityDeviceAttribute::kind

The activity record kind, must be CUPTI ACTIVITY KIND DEVICE ATTRIBUTE.

# CUpti\_ActivityDeviceAttribute::@11 CUpti\_ActivityDeviceAttribute::value

The value for the attribute. See CUpti\_DeviceAttribute and CUdevice\_attribute for the type of the value for a given attribute.

# 3.12. CUpti\_ActivityEnvironment Struct Reference

The activity record for CUPTI environmental data.

This activity record provides CUPTI environmental data, include power, clocks, and thermals. This information is sampled at various rates and returned in this activity record. The consumer of the record needs to check the environmentKind field to figure out what kind of environmental record this is.

# CUpti\_EnvironmentClocksThrottleReason CUpti\_ActivityEnvironment::clocksThrottleReasons

The clocks throttle reasons.

CUpti\_ActivityEnvironment::@12::@16 CUpti\_ActivityEnvironment::cooling

Data returned for CUPTI\_ACTIVITY\_ENVIRONMENT\_COOLING environment kind.

uint32\_t CUpti\_ActivityEnvironment::deviceId

The ID of the device

CUpti\_ActivityEnvironmentKind CUpti\_ActivityEnvironment::environmentKind

The kind of data reported in this record.

uint32\_t CUpti\_ActivityEnvironment::fanSpeed

The fan speed as percentage of maximum.

uint32\_t CUpti\_ActivityEnvironment::gpuTemperature

The GPU temperature in degrees C.

## CUpti\_ActivityKind CUpti\_ActivityEnvironment::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_ENVIRONMENT.

## uint32\_t CUpti\_ActivityEnvironment::memoryClock

The memory frequency in MHz

#### uint32\_t CUpti\_ActivityEnvironment::pcieLinkGen

The PCIe link generation.

#### uint32\_t CUpti\_ActivityEnvironment::pcieLinkWidth

The PCIe link width.

# CUpti\_ActivityEnvironment::@12::@15

CUpti\_ActivityEnvironment::power

Data returned for CUPTI\_ACTIVITY\_ENVIRONMENT\_POWER environment kind.

#### uint32\_t CUpti\_ActivityEnvironment::power

The power in milliwatts consumed by GPU and associated circuitry.

#### uint32\_t CUpti\_ActivityEnvironment::powerLimit

The power in milliwatts that will trigger power management algorithm.

## uint32\_t CUpti\_ActivityEnvironment::smClock

The SM frequency in MHz

#### CUpti\_ActivityEnvironment::@12::@13

CUpti\_ActivityEnvironment::speed

Data returned for CUPTI\_ACTIVITY\_ENVIRONMENT\_SPEED environment kind.

# CUpti\_ActivityEnvironment::@12::@14

CUpti\_ActivityEnvironment::temperature

Data returned for CUPTI\_ACTIVITY\_ENVIRONMENT\_TEMPERATURE environment kind.

## uint64\_t CUpti\_ActivityEnvironment::timestamp

The timestamp when this sample was retrieved, in ns. A value of 0 indicates that timestamp information could not be collected for the marker.

# 3.13. CUpti\_ActivityEvent Struct Reference

The activity record for a CUPTI event.

This activity record represents a CUPTI event value (CUPTI\_ACTIVITY\_KIND\_EVENT). This activity record kind is not produced by the activity API but is included for completeness and ease-of-use. Profile frameworks built on top of CUPTI that collect event data may choose to use this type to store the collected event data.

#### uint32\_t CUpti\_ActivityEvent::correlationId

The correlation ID of the event. Use of this ID is user-defined, but typically this ID value will equal the correlation ID of the kernel for which the event was gathered.

## CUpti\_EventDomainID CUpti\_ActivityEvent::domain

The event domain ID.

## CUpti\_EventID CUpti\_ActivityEvent::id

The event ID.

# CUpti\_ActivityKind CUpti\_ActivityEvent::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_EVENT.

#### uint64\_t CUpti\_ActivityEvent::value

The event value.

# 3.14. CUpti\_ActivityEventInstance Struct Reference

The activity record for a CUPTI event with instance information.

This activity record represents the a CUPTI event value for a specific event domain instance (CUPTI\_ACTIVITY\_KIND\_EVENT\_INSTANCE). This activity record kind is not produced by the activity API but is included for completeness and ease-of-use.

Profile frameworks built on top of CUPTI that collect event data may choose to use this type to store the collected event data. This activity record should be used when event domain instance information needs to be associated with the event.

## uint32\_t CUpti\_ActivityEventInstance::correlationId

The correlation ID of the event. Use of this ID is user-defined, but typically this ID value will equal the correlation ID of the kernel for which the event was gathered.

# CUpti\_EventDomainID CUpti\_ActivityEventInstance::domain

The event domain ID.

# CUpti\_EventID CUpti\_ActivityEventInstance::id

The event ID.

# uint32\_t CUpti\_ActivityEventInstance::instance

The event domain instance.

# CUpti\_ActivityKind CUpti\_ActivityEventInstance::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_EVENT\_INSTANCE.

# uint32\_t CUpti\_ActivityEventInstance::pad

Undefined. Reserved for internal use.

## uint64\_t CUpti\_ActivityEventInstance::value

The event value.

# 3.15. CUpti\_ActivityExternalCorrelation Struct Reference

The activity record for correlation with external records.

This activity record correlates native CUDA records (e.g. CUDA Driver API, kernels, memcpys, ...) with records from external APIs such as OpenACC. (CUPTI ACTIVITY KIND EXTERNAL CORRELATION).

See also:

#### CUpti\_ActivityKind

#### uint32\_t

# CUpti\_ActivityExternalCorrelation::correlationId

The correlation ID of the associated CUDA driver or runtime API record.

# uint64\_t CUpti\_ActivityExternalCorrelation::externalId

The correlation ID of the associated non-CUDA API record. The exact field in the associated external record depends on that record's activity kind (

#### See also:

externalKind).

# CUpti\_ExternalCorrelationKind CUpti\_ActivityExternalCorrelation::externalKind

The kind of external API this record correlated to.

# CUpti\_ActivityKind CUpti\_ActivityExternalCorrelation::kind

The kind of this activity.

## uint32\_t CUpti\_ActivityExternalCorrelation::reserved

Undefined. Reserved for internal use.

# 3.16. CUpti\_ActivityFunction Struct Reference

The activity record for global/device functions.

This activity records function name and corresponding module information. (CUPTI\_ACTIVITY\_KIND\_FUNCTION).

#### uint32\_t CUpti\_ActivityFunction::contextId

The ID of the context where the function is launched.

#### uint32\_t CUpti\_ActivityFunction::functionIndex

The function's unique symbol index in the module.

## uint32\_t CUpti\_ActivityFunction::id

ID to uniquely identify the record

# CUpti\_ActivityKind CUpti\_ActivityFunction::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_FUNCTION.

## uint32\_t CUpti\_ActivityFunction::moduleId

The module ID in which this global/device function is present.

## const char \*CUpti\_ActivityFunction::name

The name of the function. This name is shared across all activity records representing the same kernel, and so should not be modified.

# 3.17. CUpti\_ActivityGlobalAccess Struct Reference

The activity record for source-level global access. (deprecated).

This activity records the locations of the global accesses in the source (CUPTI\_ACTIVITY\_KIND\_GLOBAL\_ACCESS). Global access activities are now reported using the CUpti\_ActivityGlobalAccess3 activity record.

## uint32\_t CUpti\_ActivityGlobalAccess::correlationId

The correlation ID of the kernel to which this result is associated.

## uint32\_t CUpti\_ActivityGlobalAccess::executed

The number of times this instruction was executed per warp. It will be incremented when at least one of thread among warp is active with predicate and condition code evaluating to true.

#### CUpti\_ActivityFlag CUpti\_ActivityGlobalAccess::flags

The properties of this global access.

#### CUpti\_ActivityKind CUpti\_ActivityGlobalAccess::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_GLOBAL\_ACCESS.

#### uint64\_t CUpti\_ActivityGlobalAccess::l2\_transactions

The total number of 32 bytes transactions to L2 cache generated by this access

## uint32\_t CUpti\_ActivityGlobalAccess::pcOffset

The pc offset for the access.

## uint32\_t CUpti\_ActivityGlobalAccess::sourceLocatorId

The ID for source locator.

## uint64\_t CUpti\_ActivityGlobalAccess::threadsExecuted

This increments each time when this instruction is executed by number of threads that executed this instruction with predicate and condition code evaluating to true.

# 3.18. CUpti\_ActivityGlobalAccess2 Struct Reference

The activity record for source-level global access. (deprecated in CUDA 9.0).

This activity records the locations of the global accesses in the source (CUPTI\_ACTIVITY\_KIND\_GLOBAL\_ACCESS). Global access activities are now reported using the CUpti\_ActivityGlobalAccess3 activity record.

#### uint32\_t CUpti\_ActivityGlobalAccess2::correlationId

The correlation ID of the kernel to which this result is associated.

## uint32\_t CUpti\_ActivityGlobalAccess2::executed

The number of times this instruction was executed per warp. It will be incremented when at least one of thread among warp is active with predicate and condition code evaluating to true.

# CUpti\_ActivityFlag CUpti\_ActivityGlobalAccess2::flags

The properties of this global access.

## uint32\_t CUpti\_ActivityGlobalAccess2::functionId

Correlation ID with global/device function name

# CUpti\_ActivityKind CUpti\_ActivityGlobalAccess2::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_GLOBAL\_ACCESS.

# uint64\_t CUpti\_ActivityGlobalAccess2::l2\_transactions

The total number of 32 bytes transactions to L2 cache generated by this access

## uint32\_t CUpti\_ActivityGlobalAccess2::pad

Undefined. Reserved for internal use.

## uint32\_t CUpti\_ActivityGlobalAccess2::pcOffset

The pc offset for the access.

#### uint32\_t CUpti\_ActivityGlobalAccess2::sourceLocatorId

The ID for source locator.

#### uint64\_t

# CUpti\_ActivityGlobalAccess2::theoreticalL2Transactions

The minimum number of L2 transactions possible based on the access pattern.

#### uint64\_t CUpti\_ActivityGlobalAccess2::threadsExecuted

This increments each time when this instruction is executed by number of threads that executed this instruction with predicate and condition code evaluating to true.

# 3.19. CUpti\_ActivityGlobalAccess3 Struct Reference

The activity record for source-level global access.

This activity records the locations of the global accesses in the source (CUPTI\_ACTIVITY\_KIND\_GLOBAL\_ACCESS).

#### uint32 t CUpti ActivityGlobalAccess3::correlationId

The correlation ID of the kernel to which this result is associated.

#### uint32\_t CUpti\_ActivityGlobalAccess3::executed

The number of times this instruction was executed per warp. It will be incremented when at least one of thread among warp is active with predicate and condition code evaluating to true.

# CUpti\_ActivityFlag CUpti\_ActivityGlobalAccess3::flags

The properties of this global access.

#### uint32\_t CUpti\_ActivityGlobalAccess3::functionId

Correlation ID with global/device function name

#### CUpti\_ActivityKind CUpti\_ActivityGlobalAccess3::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_GLOBAL\_ACCESS.

#### uint64\_t CUpti\_ActivityGlobalAccess3::l2\_transactions

The total number of 32 bytes transactions to L2 cache generated by this access

#### uint64\_t CUpti\_ActivityGlobalAccess3::pcOffset

The pc offset for the access.

#### uint32\_t CUpti\_ActivityGlobalAccess3::sourceLocatorId

The ID for source locator.

#### uint64\_t

## CUpti\_ActivityGlobalAccess3::theoreticalL2Transactions

The minimum number of L2 transactions possible based on the access pattern.

## uint64\_t CUpti\_ActivityGlobalAccess3::threadsExecuted

This increments each time when this instruction is executed by number of threads that executed this instruction with predicate and condition code evaluating to true.

# 3.20. CUpti\_ActivityInstantaneousEvent Struct Reference

The activity record for an instantaneous CUPTI event.

This activity record represents a CUPTI event value (CUPTI\_ACTIVITY\_KIND\_EVENT) sampled at a particular instant. This activity record kind is not produced by the activity API but is included for completeness and ease-of-use. Profiler frameworks built on top of CUPTI that collect event data at a particular time may choose to use this type to store the collected event data.

uint32\_t CUpti\_ActivityInstantaneousEvent::deviceId

The device id

CUpti\_EventID CUpti\_ActivityInstantaneousEvent::id

The event ID.

CUpti\_ActivityKind CUpti\_ActivityInstantaneousEvent::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_INSTANTANEOUS\_EVENT.

uint32\_t CUpti\_ActivityInstantaneousEvent::reserved

Undefined. reserved for internal use

uint64\_t CUpti\_ActivityInstantaneousEvent::timestamp

The timestamp at which event is sampled

uint64\_t CUpti\_ActivityInstantaneousEvent::value

The event value.

# 3.21. CUpti\_ActivityInstantaneousEventInstance Struct Reference

The activity record for an instantaneous CUPTI event with event domain instance information.

This activity record represents the a CUPTI event value for a specific event domain instance (CUPTI\_ACTIVITY\_KIND\_EVENT\_INSTANCE) sampled at a particular instant. This activity record kind is not produced by the activity API but is included for completeness and ease-of-use. Profiler frameworks built on top of CUPTI that collect event data may choose to use this type to store the collected event data. This activity record should be used when event domain instance information needs to be associated with the event.

uint32\_t

CUpti\_ActivityInstantaneousEventInstance::deviceId

The device id

CUpti\_EventID

CUpti\_ActivityInstantaneousEventInstance::id

The event ID.

uint8\_t

CUpti\_ActivityInstantaneousEventInstance::instance

The event domain instance

CUpti\_ActivityKind CUpti\_ActivityInstantaneousEventInstance::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_INSTANTANEOUS\_EVENT\_INSTANCE.

uint8\_t CUpti\_ActivityInstantaneousEventInstance::pad

Undefined. reserved for internal use

#### uint64\_t

# CUpti\_ActivityInstantaneousEventInstance::timestamp

The timestamp at which event is sampled

#### uint64\_t

CUpti\_ActivityInstantaneousEventInstance::value

The event value.

# 3.22. CUpti\_ActivityInstantaneousMetric Struct Reference

The activity record for an instantaneous CUPTI metric.

This activity record represents the collection of a CUPTI metric value (CUPTI\_ACTIVITY\_KIND\_METRIC) at a particular instance. This activity record kind is not produced by the activity API but is included for completeness and ease-of-use. Profiler frameworks built on top of CUPTI that collect metric data may choose to use this type to store the collected metric data.

## uint32\_t CUpti\_ActivityInstantaneousMetric::deviceId

The device id

## uint8\_t CUpti\_ActivityInstantaneousMetric::flags

The properties of this metric.

See also:

CUpti\_ActivityFlag

# CUpti\_MetricID CUpti\_ActivityInstantaneousMetric::id

The metric ID.

# CUpti\_ActivityKind CUpti\_ActivityInstantaneousMetric::kind

The activity record kind, must be CUPTI ACTIVITY KIND INSTANTANEOUS METRIC.

## uint8\_t CUpti\_ActivityInstantaneousMetric::pad

Undefined. reserved for internal use

# uint64\_t CUpti\_ActivityInstantaneousMetric::timestamp

The timestamp at which metric is sampled

# CUpti\_ActivityInstantaneousMetric::value

The metric value.

# 3.23. CUpti\_ActivityInstantaneousMetricInstance Struct Reference

The instantaneous activity record for a CUPTI metric with instance information.

This activity record represents a CUPTI metric value for a specific metric domain instance (CUPTI\_ACTIVITY\_KIND\_METRIC\_INSTANCE) sampled at a particular time. This activity record kind is not produced by the activity API but is included for completeness and ease-of-use. Profiler frameworks built on top of CUPTI that collect metric data may choose to use this type to store the collected metric data. This activity record should be used when metric domain instance information needs to be associated with the metric.

## uint32\_t

# CUpti\_ActivityInstantaneousMetricInstance::deviceId

The device id

#### uint8\_t

#### CUpti\_ActivityInstantaneousMetricInstance::flags

The properties of this metric.

See also:

CUpti\_ActivityFlag

# CUpti\_MetricID CUpti\_ActivityInstantaneousMetricInstance::id

The metric ID.

#### uint8\_t

# CUpti\_ActivityInstantaneousMetricInstance::instance

The metric domain instance

# CUpti\_ActivityKind CUpti\_ActivityInstantaneousMetricInstance::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_INSTANTANEOUS\_METRIC\_INSTANCE.

# uint8\_t CUpti\_ActivityInstantaneousMetricInstance::pad

Undefined. reserved for internal use

## uint64\_t

## CUpti\_ActivityInstantaneousMetricInstance::timestamp

The timestamp at which metric is sampled

# CUpti\_ActivityInstantaneousMetricInstance::value

The metric value.

# 3.24. CUpti\_ActivityInstructionCorrelation Struct Reference

The activity record for source-level sass/source line-by-line correlation.

This activity records source level sass/source correlation information. (CUPTI\_ACTIVITY\_KIND\_INSTRUCTION\_CORRELATION).

# CUpti\_ActivityFlag CUpti\_ActivityInstructionCorrelation::flags

The properties of this instruction.

#### uint32 t

CUpti\_ActivityInstructionCorrelation::functionId

Correlation ID with global/device function name

# CUpti\_ActivityKind CUpti\_ActivityInstructionCorrelation::kind

The activity record kind, must be CUPTI ACTIVITY KIND INSTRUCTION CORRELATION.

# uint32\_t CUpti\_ActivityInstructionCorrelation::pad

Undefined. Reserved for internal use.

# uint32\_t CUpti\_ActivityInstructionCorrelation::pcOffset

The pc offset for the instruction.

## uint32\_t

## CUpti\_ActivityInstructionCorrelation::sourceLocatorId

The ID for source locator.

# 3.25. CUpti\_ActivityInstructionExecution Struct Reference

The activity record for source-level instruction execution.

This activity records result for source level instruction execution. (CUPTI\_ACTIVITY\_KIND\_INSTRUCTION\_EXECUTION).

#### uint32\_t

## CUpti\_ActivityInstructionExecution::correlationId

The correlation ID of the kernel to which this result is associated.

#### uint32\_t CUpti\_ActivityInstructionExecution::executed

The number of times this instruction was executed per warp. It will be incremented regardless of predicate or condition code.

# CUpti\_ActivityFlag CUpti\_ActivityInstructionExecution::flags

The properties of this instruction execution.

#### uint32\_t CUpti\_ActivityInstructionExecution::functionId

Correlation ID with global/device function name

# CUpti\_ActivityKind CUpti\_ActivityInstructionExecution::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_INSTRUCTION\_EXECUTION.

#### uint64 t

# CUpti\_ActivityInstructionExecution::notPredOffThreadsExecuted

This increments each time when this instruction is executed by number of threads that executed this instruction with predicate and condition code evaluating to true.

#### uint32\_t CUpti\_ActivityInstructionExecution::pad

Undefined. Reserved for internal use.

#### uint32\_t CUpti\_ActivityInstructionExecution::pcOffset

The pc offset for the instruction.

#### uint32\_t

## CUpti\_ActivityInstructionExecution::sourceLocatorId

The ID for source locator.

#### uint64 t

#### CUpti\_ActivityInstructionExecution::threadsExecuted

This increments each time when this instruction is executed by number of threads that executed this instruction, regardless of predicate or condition code.

# 3.26. CUpti\_ActivityKernel Struct Reference

The activity record for kernel. (deprecated).

This activity record represents a kernel execution (CUPTI\_ACTIVITY\_KIND\_KERNEL and CUPTI\_ACTIVITY\_KIND\_CONCURRENT\_KERNEL) but is no longer generated by CUPTI. Kernel activities are now reported using the CUpti\_ActivityKernel4 activity record.

#### int32\_t CUpti\_ActivityKernel::blockX

The X-dimension block size for the kernel.

#### int32\_t CUpti\_ActivityKernel::blockY

The Y-dimension block size for the kernel.

## int32\_t CUpti\_ActivityKernel::blockZ

The Z-dimension grid size for the kernel.

# uint8\_t CUpti\_ActivityKernel::cacheConfigExecuted

The cache configuration used for the kernel. The value is one of the CUfunc\_cache enumeration values from cuda.h.

#### uint8\_t CUpti\_ActivityKernel::cacheConfigRequested

The cache configuration requested by the kernel. The value is one of the CUfunc\_cache enumeration values from cuda.h.

#### uint32\_t CUpti\_ActivityKernel::contextId

The ID of the context where the kernel is executing.

#### uint32\_t CUpti\_ActivityKernel::correlationId

The correlation ID of the kernel. Each kernel execution is assigned a unique correlation ID that is identical to the correlation ID in the driver API activity record that launched the kernel.

#### uint32\_t CUpti\_ActivityKernel::deviceId

The ID of the device where the kernel is executing.

# int32\_t CUpti\_ActivityKernel::dynamicSharedMemory

The dynamic shared memory reserved for the kernel, in bytes.

#### uint64\_t CUpti\_ActivityKernel::end

The end timestamp for the kernel execution, in ns. A value of 0 for both the start and end timestamps indicates that timestamp information could not be collected for the kernel.

#### int32\_t CUpti\_ActivityKernel::gridX

The X-dimension grid size for the kernel.

#### int32\_t CUpti\_ActivityKernel::gridY

The Y-dimension grid size for the kernel.

#### int32\_t CUpti\_ActivityKernel::gridZ

The Z-dimension grid size for the kernel.

## CUpti\_ActivityKind CUpti\_ActivityKernel::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_KERNEL or CUPTI\_ACTIVITY\_KIND\_CONCURRENT\_KERNEL.

#### uint32\_t CUpti\_ActivityKernel::localMemoryPerThread

The amount of local memory reserved for each thread, in bytes.

#### uint32\_t CUpti\_ActivityKernel::localMemoryTotal

The total amount of local memory reserved for the kernel, in bytes.

#### const char \*CUpti\_ActivityKernel::name

The name of the kernel. This name is shared across all activity records representing the same kernel, and so should not be modified.

#### uint32\_t CUpti\_ActivityKernel::pad

Undefined. Reserved for internal use.

## uint16\_t CUpti\_ActivityKernel::registersPerThread

The number of registers required for each thread executing the kernel.

#### void \*CUpti\_ActivityKernel::reserved0

Undefined. Reserved for internal use.

#### uint32\_t CUpti\_ActivityKernel::runtimeCorrelationId

The runtime correlation ID of the kernel. Each kernel execution is assigned a unique runtime correlation ID that is identical to the correlation ID in the runtime API activity record that launched the kernel.

#### uint64\_t CUpti\_ActivityKernel::start

The start timestamp for the kernel execution, in ns. A value of 0 for both the start and end timestamps indicates that timestamp information could not be collected for the kernel.

# int32\_t CUpti\_ActivityKernel::staticSharedMemory

The static shared memory allocated for the kernel, in bytes.

#### uint32\_t CUpti\_ActivityKernel::streamId

The ID of the stream where the kernel is executing.

# 3.27. CUpti\_ActivityKernel2 Struct Reference

The activity record for kernel. (deprecated).

This activity record represents a kernel execution (CUPTI\_ACTIVITY\_KIND\_KERNEL and CUPTI\_ACTIVITY\_KIND\_CONCURRENT\_KERNEL) but is no longer generated by CUPTI. Kernel activities are now reported using the CUpti\_ActivityKernel4 activity record.

#### int32\_t CUpti\_ActivityKernel2::blockX

The X-dimension block size for the kernel.

#### int32\_t CUpti\_ActivityKernel2::blockY

The Y-dimension block size for the kernel.

## int32\_t CUpti\_ActivityKernel2::blockZ

The Z-dimension grid size for the kernel.

#### uint64\_t CUpti\_ActivityKernel2::completed

The completed timestamp for the kernel execution, in ns. It represents the completion of all it's child kernels and the kernel itself. A value of CUPTI\_TIMESTAMP\_UNKNOWN indicates that the completion time is unknown.

## uint32\_t CUpti\_ActivityKernel2::contextId

The ID of the context where the kernel is executing.

#### uint32\_t CUpti\_ActivityKernel2::correlationId

The correlation ID of the kernel. Each kernel execution is assigned a unique correlation ID that is identical to the correlation ID in the driver or runtime API activity record that launched the kernel.

#### uint32\_t CUpti\_ActivityKernel2::deviceId

The ID of the device where the kernel is executing.

#### int32\_t CUpti\_ActivityKernel2::dynamicSharedMemory

The dynamic shared memory reserved for the kernel, in bytes.

#### uint64\_t CUpti\_ActivityKernel2::end

The end timestamp for the kernel execution, in ns. A value of 0 for both the start and end timestamps indicates that timestamp information could not be collected for the kernel.

#### uint8\_t CUpti\_ActivityKernel2::executed

The cache configuration used for the kernel. The value is one of the CUfunc\_cache enumeration values from cuda.h.

#### int64\_t CUpti\_ActivityKernel2::gridId

The grid ID of the kernel. Each kernel is assigned a unique grid ID at runtime.

#### int32\_t CUpti\_ActivityKernel2::gridX

The X-dimension grid size for the kernel.

#### int32\_t CUpti\_ActivityKernel2::gridY

The Y-dimension grid size for the kernel.

#### int32\_t CUpti\_ActivityKernel2::gridZ

The Z-dimension grid size for the kernel.

#### CUpti\_ActivityKind CUpti\_ActivityKernel2::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_KERNEL or CUPTI\_ACTIVITY\_KIND\_CONCURRENT\_KERNEL.

#### uint32\_t CUpti\_ActivityKernel2::localMemoryPerThread

The amount of local memory reserved for each thread, in bytes.

#### uint32\_t CUpti\_ActivityKernel2::localMemoryTotal

The total amount of local memory reserved for the kernel, in bytes.

#### const char \*CUpti\_ActivityKernel2::name

The name of the kernel. This name is shared across all activity records representing the same kernel, and so should not be modified.

#### uint16\_t CUpti\_ActivityKernel2::registersPerThread

The number of registers required for each thread executing the kernel.

#### uint8\_t CUpti\_ActivityKernel2::requested

The cache configuration requested by the kernel. The value is one of the CUfunc\_cache enumeration values from cuda.h.

## void \*CUpti\_ActivityKernel2::reserved0

Undefined. Reserved for internal use.

#### uint8\_t CUpti\_ActivityKernel2::sharedMemoryConfig

The shared memory configuration used for the kernel. The value is one of the CUshared configuration values from cuda.h.

## uint64\_t CUpti\_ActivityKernel2::start

The start timestamp for the kernel execution, in ns. A value of 0 for both the start and end timestamps indicates that timestamp information could not be collected for the kernel.

#### int32\_t CUpti\_ActivityKernel2::staticSharedMemory

The static shared memory allocated for the kernel, in bytes.

#### uint32\_t CUpti\_ActivityKernel2::streamId

The ID of the stream where the kernel is executing.

# 3.28. CUpti\_ActivityKernel3 Struct Reference

The activity record for a kernel (CUDA 6.5(with sm\_52 support) onwards). (deprecated in CUDA 9.0).

This activity record represents a kernel execution (CUPTI\_ACTIVITY\_KIND\_KERNEL and CUPTI\_ACTIVITY\_KIND\_CONCURRENT\_KERNEL). Kernel activities are now reported using the CUpti\_ActivityKernel4 activity record.

#### int32\_t CUpti\_ActivityKernel3::blockX

The X-dimension block size for the kernel.

#### int32\_t CUpti\_ActivityKernel3::blockY

The Y-dimension block size for the kernel.

#### int32\_t CUpti\_ActivityKernel3::blockZ

The Z-dimension grid size for the kernel.

#### uint64\_t CUpti\_ActivityKernel3::completed

The completed timestamp for the kernel execution, in ns. It represents the completion of all it's child kernels and the kernel itself. A value of CUPTI\_TIMESTAMP\_UNKNOWN indicates that the completion time is unknown.

#### uint32\_t CUpti\_ActivityKernel3::contextId

The ID of the context where the kernel is executing.

# uint32\_t CUpti\_ActivityKernel3::correlationId

The correlation ID of the kernel. Each kernel execution is assigned a unique correlation ID that is identical to the correlation ID in the driver or runtime API activity record that launched the kernel.

#### uint32\_t CUpti\_ActivityKernel3::deviceId

The ID of the device where the kernel is executing.

#### int32\_t CUpti\_ActivityKernel3::dynamicSharedMemory

The dynamic shared memory reserved for the kernel, in bytes.

#### uint64\_t CUpti\_ActivityKernel3::end

The end timestamp for the kernel execution, in ns. A value of 0 for both the start and end timestamps indicates that timestamp information could not be collected for the kernel.

#### uint8\_t CUpti\_ActivityKernel3::executed

The cache configuration used for the kernel. The value is one of the CUfunc\_cache enumeration values from cuda.h.

#### int64\_t CUpti\_ActivityKernel3::gridId

The grid ID of the kernel. Each kernel is assigned a unique grid ID at runtime.

#### int32\_t CUpti\_ActivityKernel3::gridX

The X-dimension grid size for the kernel.

#### int32\_t CUpti\_ActivityKernel3::gridY

The Y-dimension grid size for the kernel.

#### int32\_t CUpti\_ActivityKernel3::gridZ

The Z-dimension grid size for the kernel.

#### CUpti\_ActivityKind CUpti\_ActivityKernel3::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_KERNEL or CUPTI\_ACTIVITY\_KIND\_CONCURRENT\_KERNEL.

## uint32\_t CUpti\_ActivityKernel3::localMemoryPerThread

The amount of local memory reserved for each thread, in bytes.

#### uint32\_t CUpti\_ActivityKernel3::localMemoryTotal

The total amount of local memory reserved for the kernel, in bytes.

#### const char \*CUpti\_ActivityKernel3::name

The name of the kernel. This name is shared across all activity records representing the same kernel, and so should not be modified.

# CUpti\_ActivityPartitionedGlobalCacheConfig CUpti\_ActivityKernel3::partitionedGlobalCacheExecuted

The partitioned global caching executed for the kernel. Partitioned global caching is required to enable caching on certain chips, such as devices with compute capability 5.2. Partitioned global caching can be automatically disabled if the occupancy requirement of the launch cannot support caching.

# CUpti\_ActivityPartitionedGlobalCacheConfig CUpti\_ActivityKernel3::partitionedGlobalCacheRequested

The partitioned global caching requested for the kernel. Partitioned global caching is required to enable caching on certain chips, such as devices with compute capability 5.2.

## uint16\_t CUpti\_ActivityKernel3::registersPerThread

The number of registers required for each thread executing the kernel.

#### uint8\_t CUpti\_ActivityKernel3::requested

The cache configuration requested by the kernel. The value is one of the CUfunc\_cache enumeration values from cuda.h.

#### void \*CUpti\_ActivityKernel3::reserved0

Undefined. Reserved for internal use.

#### uint8\_t CUpti\_ActivityKernel3::sharedMemoryConfig

The shared memory configuration used for the kernel. The value is one of the CUshared configuration values from cuda.h.

#### uint64\_t CUpti\_ActivityKernel3::start

The start timestamp for the kernel execution, in ns. A value of 0 for both the start and end timestamps indicates that timestamp information could not be collected for the kernel.

## int32\_t CUpti\_ActivityKernel3::staticSharedMemory

The static shared memory allocated for the kernel, in bytes.

#### uint32\_t CUpti\_ActivityKernel3::streamId

The ID of the stream where the kernel is executing.

# 3.29. CUpti\_ActivityKernel4 Struct Reference

The activity record for a kernel.

This activity record represents a kernel execution (CUPTI\_ACTIVITY\_KIND\_KERNEL and CUPTI\_ACTIVITY\_KIND\_CONCURRENT\_KERNEL).

#### int32\_t CUpti\_ActivityKernel4::blockX

The X-dimension block size for the kernel.

#### int32\_t CUpti\_ActivityKernel4::blockY

The Y-dimension block size for the kernel.

# int32\_t CUpti\_ActivityKernel4::blockZ

The Z-dimension grid size for the kernel.

# CUpti\_ActivityKernel4::@6 CUpti\_ActivityKernel4::cacheConfig

For devices with compute capability 7.0+ cacheConfig values are not updated in case field isSharedMemoryCarveoutRequested is set

#### uint64\_t CUpti\_ActivityKernel4::completed

The completed timestamp for the kernel execution, in ns. It represents the completion of all it's child kernels and the kernel itself. A value of CUPTI\_TIMESTAMP\_UNKNOWN indicates that the completion time is unknown.

#### uint32\_t CUpti\_ActivityKernel4::contextId

The ID of the context where the kernel is executing.

#### uint32\_t CUpti\_ActivityKernel4::correlationId

The correlation ID of the kernel. Each kernel execution is assigned a unique correlation ID that is identical to the correlation ID in the driver or runtime API activity record that launched the kernel.

## uint32\_t CUpti\_ActivityKernel4::deviceId

The ID of the device where the kernel is executing.

## int32\_t CUpti\_ActivityKernel4::dynamicSharedMemory

The dynamic shared memory reserved for the kernel, in bytes.

#### uint64\_t CUpti\_ActivityKernel4::end

The end timestamp for the kernel execution, in ns. A value of 0 for both the start and end timestamps indicates that timestamp information could not be collected for the kernel.

# uint8\_t CUpti\_ActivityKernel4::executed

The cache configuration used for the kernel. The value is one of the CUfunc\_cache enumeration values from cuda.h.

# int64\_t CUpti\_ActivityKernel4::gridId

The grid ID of the kernel. Each kernel is assigned a unique grid ID at runtime.

#### int32\_t CUpti\_ActivityKernel4::gridX

The X-dimension grid size for the kernel.

#### int32\_t CUpti\_ActivityKernel4::gridY

The Y-dimension grid size for the kernel.

## int32\_t CUpti\_ActivityKernel4::gridZ

The Z-dimension grid size for the kernel.

#### uint8\_t

# CUpti\_ActivityKernel4::isSharedMemoryCarveoutRequested

This indicates if

CU\_FUNC\_ATTRIBUTE\_PREFERRED\_SHARED\_MEMORY\_CARVEOUT was updated for the kernel launch

## CUpti\_ActivityKind CUpti\_ActivityKernel4::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_KERNEL or CUPTI\_ACTIVITY\_KIND\_CONCURRENT\_KERNEL.

#### uint8\_t CUpti\_ActivityKernel4::launchType

The indicates if the kernel was executed via a regular launch or via a single/multi device cooperative launch.

#### See also:

CUpti\_ActivityLaunchType

#### uint32\_t CUpti\_ActivityKernel4::localMemoryPerThread

The amount of local memory reserved for each thread, in bytes.

#### uint32\_t CUpti\_ActivityKernel4::localMemoryTotal

The total amount of local memory reserved for the kernel, in bytes.

#### const char \*CUpti\_ActivityKernel4::name

The name of the kernel. This name is shared across all activity records representing the same kernel, and so should not be modified.

#### uint8\_t CUpti\_ActivityKernel4::padding

Undefined. Reserved for internal use.

# CUpti\_ActivityPartitionedGlobalCacheConfig CUpti\_ActivityKernel4::partitionedGlobalCacheExecuted

The partitioned global caching executed for the kernel. Partitioned global caching is required to enable caching on certain chips, such as devices with compute capability 5.2.

Partitioned global caching can be automatically disabled if the occupancy requirement of the launch cannot support caching.

# CUpti\_ActivityPartitionedGlobalCacheConfig CUpti\_ActivityKernel4::partitionedGlobalCacheRequested

The partitioned global caching requested for the kernel. Partitioned global caching is required to enable caching on certain chips, such as devices with compute capability 5.2.

#### uint64\_t CUpti\_ActivityKernel4::queued

The timestamp when the kernel is queued up in the command buffer, in ns. A value of CUPTI\_TIMESTAMP\_UNKNOWN indicates that the queued time could not be collected for the kernel. This timestamp is not collected by default. Use API cuptiActivityEnableLatencyTimestamps() to enable collection.

Command buffer is a buffer written by CUDA driver to send commands like kernel launch, memory copy etc to the GPU. All launches of CUDA kernels are asynchrnous with respect to the host, the host requests the launch by writing commands into the command buffer, then returns without checking the GPU's progress.

#### uint16\_t CUpti\_ActivityKernel4::registersPerThread

The number of registers required for each thread executing the kernel.

#### uint8\_t CUpti\_ActivityKernel4::requested

The cache configuration requested by the kernel. The value is one of the CUfunc\_cache enumeration values from cuda.h.

#### void \*CUpti\_ActivityKernel4::reserved0

Undefined. Reserved for internal use.

#### uint8\_t

#### CUpti\_ActivityKernel4::sharedMemoryCarveoutRequested

Shared memory carveout value requested for the function in percentage of the total resource. The value will be updated only if field isSharedMemoryCarveoutRequested is set.

## uint8\_t CUpti\_ActivityKernel4::sharedMemoryConfig

The shared memory configuration used for the kernel. The value is one of the CUshared configuration values from cuda.h.

#### uint64\_t CUpti\_ActivityKernel4::start

The start timestamp for the kernel execution, in ns. A value of 0 for both the start and end timestamps indicates that timestamp information could not be collected for the kernel.

#### int32\_t CUpti\_ActivityKernel4::staticSharedMemory

The static shared memory allocated for the kernel, in bytes.

#### uint32\_t CUpti\_ActivityKernel4::streamId

The ID of the stream where the kernel is executing.

## uint64\_t CUpti\_ActivityKernel4::submitted

The timestamp when the command buffer containing the kernel launch is submitted to the GPU, in ns. A value of CUPTI\_TIMESTAMP\_UNKNOWN indicates that the submitted time could not be collected for the kernel. This timestamp is not collected by default. Use API cuptiActivityEnableLatencyTimestamps() to enable collection.

# 3.30. CUpti\_ActivityMarker Struct Reference

The activity record providing a marker which is an instantaneous point in time. (deprecated in CUDA 8.0).

The marker is specified with a descriptive name and unique id (CUPTI\_ACTIVITY\_KIND\_MARKER). Marker activity is now reported using the CUpti\_ActivityMarker2 activity record.

#### CUpti\_ActivityFlag CUpti\_ActivityMarker::flags

The flags associated with the marker.

See also:

CUpti\_ActivityFlag

# uint32\_t CUpti\_ActivityMarker::id

The marker ID.

#### CUpti\_ActivityKind CUpti\_ActivityMarker::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_MARKER.

#### const char \*CUpti\_ActivityMarker::name

The marker name for an instantaneous or start marker. This will be NULL for an end marker.

#### CUpti\_ActivityMarker::objectId

The identifier for the activity object associated with this marker. 'objectKind' indicates which ID is valid for this record.

# CUpti\_ActivityObjectKind CUpti\_ActivityMarker::objectKind

The kind of activity object associated with this marker.

#### uint64\_t CUpti\_ActivityMarker::timestamp

The timestamp for the marker, in ns. A value of 0 indicates that timestamp information could not be collected for the marker.

# 3.31. CUpti\_ActivityMarker2 Struct Reference

The activity record providing a marker which is an instantaneous point in time.

The marker is specified with a descriptive name and unique id (CUPTI\_ACTIVITY\_KIND\_MARKER).

#### const char \*CUpti\_ActivityMarker2::domain

The name of the domain to which this marker belongs to. This will be NULL for default domain.

# CUpti\_ActivityFlag CUpti\_ActivityMarker2::flags

The flags associated with the marker.

#### See also:

CUpti\_ActivityFlag

## uint32\_t CUpti\_ActivityMarker2::id

The marker ID.

## CUpti\_ActivityKind CUpti\_ActivityMarker2::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_MARKER.

#### const char \*CUpti\_ActivityMarker2::name

The marker name for an instantaneous or start marker. This will be NULL for an end marker.

## CUpti\_ActivityMarker2::objectId

The identifier for the activity object associated with this marker. 'objectKind' indicates which ID is valid for this record.

# CUpti\_ActivityObjectKind CUpti\_ActivityMarker2::objectKind

The kind of activity object associated with this marker.

#### uint32\_t CUpti\_ActivityMarker2::pad

Undefined. Reserved for internal use.

# uint64\_t CUpti\_ActivityMarker2::timestamp

The timestamp for the marker, in ns. A value of 0 indicates that timestamp information could not be collected for the marker.

# 3.32. CUpti\_ActivityMarkerData Struct Reference

The activity record providing detailed information for a marker.

The marker data contains color, payload, and category. (CUPTI\_ACTIVITY\_KIND\_MARKER\_DATA).

#### uint32\_t CUpti\_ActivityMarkerData::category

The category for the marker.

#### uint32\_t CUpti\_ActivityMarkerData::color

The color for the marker.

#### CUpti\_ActivityFlag CUpti\_ActivityMarkerData::flags

The flags associated with the marker.

See also:

CUpti\_ActivityFlag

#### uint32\_t CUpti\_ActivityMarkerData::id

The marker ID.

#### CUpti\_ActivityKind CUpti\_ActivityMarkerData::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_MARKER\_DATA.

## CUpti\_ActivityMarkerData::payload

The payload value.

# CUpti\_MetricValueKind CUpti\_ActivityMarkerData::payloadKind

Defines the payload format for the value associated with the marker.

# 3.33. CUpti\_ActivityMemcpy Struct Reference

The activity record for memory copies.

This activity record represents a memory copy (CUPTI\_ACTIVITY\_KIND\_MEMCPY).

#### uint64\_t CUpti\_ActivityMemcpy::bytes

The number of bytes transferred by the memory copy.

#### uint32\_t CUpti\_ActivityMemcpy::contextId

The ID of the context where the memory copy is occurring.

#### uint8\_t CUpti\_ActivityMemcpy::copyKind

The kind of the memory copy, stored as a byte to reduce record size.

#### See also:

CUpti\_ActivityMemcpyKind

#### uint32\_t CUpti\_ActivityMemcpy::correlationId

The correlation ID of the memory copy. Each memory copy is assigned a unique correlation ID that is identical to the correlation ID in the driver API activity record that launched the memory copy.

#### uint32\_t CUpti\_ActivityMemcpy::deviceId

The ID of the device where the memory copy is occurring.

#### uint8\_t CUpti\_ActivityMemcpy::dstKind

The destination memory kind read by the memory copy, stored as a byte to reduce record size.

#### See also:

CUpti\_ActivityMemoryKind

#### uint64\_t CUpti\_ActivityMemcpy::end

The end timestamp for the memory copy, in ns. A value of 0 for both the start and end timestamps indicates that timestamp information could not be collected for the memory copy.

# uint8\_t CUpti\_ActivityMemcpy::flags

The flags associated with the memory copy.

#### See also:

CUpti\_ActivityFlag

#### CUpti\_ActivityKind CUpti\_ActivityMemcpy::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_MEMCPY.

#### void \*CUpti\_ActivityMemcpy::reserved0

Undefined. Reserved for internal use.

#### uint32\_t CUpti\_ActivityMemcpy::runtimeCorrelationId

The runtime correlation ID of the memory copy. Each memory copy is assigned a unique runtime correlation ID that is identical to the correlation ID in the runtime API activity record that launched the memory copy.

#### uint8\_t CUpti\_ActivityMemcpy::srcKind

The source memory kind read by the memory copy, stored as a byte to reduce record size.

#### See also:

CUpti\_ActivityMemoryKind

#### uint64\_t CUpti\_ActivityMemcpy::start

The start timestamp for the memory copy, in ns. A value of 0 for both the start and end timestamps indicates that timestamp information could not be collected for the memory copy.

#### uint32\_t CUpti\_ActivityMemcpy::streamId

The ID of the stream where the memory copy is occurring.

# 3.34. CUpti\_ActivityMemcpy2 Struct Reference

The activity record for peer-to-peer memory copies.

This activity record represents a peer-to-peer memory copy (CUPTI\_ACTIVITY\_KIND\_MEMCPY2).

#### uint64\_t CUpti\_ActivityMemcpy2::bytes

The number of bytes transferred by the memory copy.

#### uint32\_t CUpti\_ActivityMemcpy2::contextId

The ID of the context where the memory copy is occurring.

#### uint8\_t CUpti\_ActivityMemcpy2::copyKind

The kind of the memory copy, stored as a byte to reduce record size.

#### See also:

CUpti\_ActivityMemcpyKind

#### uint32\_t CUpti\_ActivityMemcpy2::correlationId

The correlation ID of the memory copy. Each memory copy is assigned a unique correlation ID that is identical to the correlation ID in the driver and runtime API activity record that launched the memory copy.

## uint32\_t CUpti\_ActivityMemcpy2::deviceId

The ID of the device where the memory copy is occurring.

#### uint32\_t CUpti\_ActivityMemcpy2::dstContextId

The ID of the context owning the memory being copied to.

## uint32\_t CUpti\_ActivityMemcpy2::dstDeviceId

The ID of the device where memory is being copied to.

#### uint8\_t CUpti\_ActivityMemcpy2::dstKind

The destination memory kind read by the memory copy, stored as a byte to reduce record size.

#### See also:

CUpti\_ActivityMemoryKind

# uint64\_t CUpti\_ActivityMemcpy2::end

The end timestamp for the memory copy, in ns. A value of 0 for both the start and end timestamps indicates that timestamp information could not be collected for the memory copy.

#### uint8\_t CUpti\_ActivityMemcpy2::flags

The flags associated with the memory copy.

#### See also:

CUpti\_ActivityFlag

#### CUpti\_ActivityKind CUpti\_ActivityMemcpy2::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_MEMCPY2.

#### uint32\_t CUpti\_ActivityMemcpy2::pad

Undefined. Reserved for internal use.

#### void \*CUpti\_ActivityMemcpy2::reserved0

Undefined. Reserved for internal use.

#### uint32\_t CUpti\_ActivityMemcpy2::srcContextId

The ID of the context owning the memory being copied from.

#### uint32\_t CUpti\_ActivityMemcpy2::srcDeviceId

The ID of the device where memory is being copied from.

#### uint8\_t CUpti\_ActivityMemcpy2::srcKind

The source memory kind read by the memory copy, stored as a byte to reduce record size.

#### See also:

CUpti\_ActivityMemoryKind

#### uint64\_t CUpti\_ActivityMemcpy2::start

The start timestamp for the memory copy, in ns. A value of 0 for both the start and end timestamps indicates that timestamp information could not be collected for the memory copy.

#### uint32\_t CUpti\_ActivityMemcpy2::streamId

The ID of the stream where the memory copy is occurring.

# 3.35. CUpti\_ActivityMemory Struct Reference

The activity record for memory.

This activity record represents a memory allocation and free operation (CUPTI\_ACTIVITY\_KIND\_MEMORY).

#### uint64\_t CUpti\_ActivityMemory::address

The virtual address of the allocation

#### uint64\_t CUpti\_ActivityMemory::allocPC

The program counter of the allocation of memory

#### uint64\_t CUpti\_ActivityMemory::bytes

The number of bytes of memory allocated.

#### uint32\_t CUpti\_ActivityMemory::contextId

The ID of the context

#### uint32\_t CUpti\_ActivityMemory::deviceId

The ID of the device where the memory allocation is taking place.

#### uint64\_t CUpti\_ActivityMemory::end

The end timestamp for the memory operation, i.e. the time when memory was freed, in ns. This will be 0 if memory is not freed in the application

## uint64\_t CUpti\_ActivityMemory::freePC

The program counter of the freeing of memory. This will be 0 if memory is not freed in the application

## CUpti\_ActivityKind CUpti\_ActivityMemory::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_MEMORY

# CUpti\_ActivityMemoryKind CUpti\_ActivityMemory::memoryKind

The memory kind requested by the user

## const char \*CUpti\_ActivityMemory::name

Variable name. This name is shared across all activity records representing the same symbol, and so should not be modified.

#### uint32\_t CUpti\_ActivityMemory::processId

The ID of the process to which this record belongs to.

#### uint64\_t CUpti\_ActivityMemory::start

The start timestamp for the memory operation, i.e. the time when memory was allocated, in ns.

# 3.36. CUpti\_ActivityMemset Struct Reference

The activity record for memset.

This activity record represents a memory set operation (CUPTI\_ACTIVITY\_KIND\_MEMSET).

#### uint64\_t CUpti\_ActivityMemset::bytes

The number of bytes being set by the memory set.

## uint32\_t CUpti\_ActivityMemset::contextId

The ID of the context where the memory set is occurring.

#### uint32\_t CUpti\_ActivityMemset::correlationId

The correlation ID of the memory set. Each memory set is assigned a unique correlation ID that is identical to the correlation ID in the driver API activity record that launched the memory set.

#### uint32\_t CUpti\_ActivityMemset::deviceId

The ID of the device where the memory set is occurring.

#### uint64\_t CUpti\_ActivityMemset::end

The end timestamp for the memory set, in ns. A value of 0 for both the start and end timestamps indicates that timestamp information could not be collected for the memory set.

#### uint16\_t CUpti\_ActivityMemset::flags

The flags associated with the memset.

#### See also:

CUpti\_ActivityFlag

## CUpti\_ActivityKind CUpti\_ActivityMemset::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_MEMSET.

#### uint16\_t CUpti\_ActivityMemset::memoryKind

The memory kind of the memory set

See also:

CUpti\_ActivityMemoryKind

#### void \*CUpti\_ActivityMemset::reserved0

Undefined. Reserved for internal use.

#### uint64\_t CUpti\_ActivityMemset::start

The start timestamp for the memory set, in ns. A value of 0 for both the start and end timestamps indicates that timestamp information could not be collected for the memory set.

#### uint32\_t CUpti\_ActivityMemset::streamId

The ID of the stream where the memory set is occurring.

#### uint32\_t CUpti\_ActivityMemset::value

The value being assigned to memory by the memory set.

# 3.37. CUpti\_ActivityMetric Struct Reference

The activity record for a CUPTI metric.

This activity record represents the collection of a CUPTI metric value (CUPTI\_ACTIVITY\_KIND\_METRIC). This activity record kind is not produced by the activity API but is included for completeness and ease-of-use. Profile frameworks built on top of CUPTI that collect metric data may choose to use this type to store the collected metric data.

## uint32\_t CUpti\_ActivityMetric::correlationId

The correlation ID of the metric. Use of this ID is user-defined, but typically this ID value will equal the correlation ID of the kernel for which the metric was gathered.

#### uint8\_t CUpti\_ActivityMetric::flags

The properties of this metric.

See also:

CUpti\_ActivityFlag

## CUpti\_MetricID CUpti\_ActivityMetric::id

The metric ID.

## CUpti\_ActivityKind CUpti\_ActivityMetric::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_METRIC.

#### uint8\_t CUpti\_ActivityMetric::pad

Undefined. Reserved for internal use.

#### CUpti\_ActivityMetric::value

The metric value.

# 3.38. CUpti\_ActivityMetricInstance Struct Reference

The activity record for a CUPTI metric with instance information.

This activity record represents a CUPTI metric value for a specific metric domain instance (CUPTI\_ACTIVITY\_KIND\_METRIC\_INSTANCE). This activity record kind is not produced by the activity API but is included for completeness and ease-of-use. Profile frameworks built on top of CUPTI that collect metric data may choose to use this type to store the collected metric data. This activity record should be used when metric domain instance information needs to be associated with the metric.

#### uint32\_t CUpti\_ActivityMetricInstance::correlationId

The correlation ID of the metric. Use of this ID is user-defined, but typically this ID value will equal the correlation ID of the kernel for which the metric was gathered.

#### uint8\_t CUpti\_ActivityMetricInstance::flags

The properties of this metric.

#### See also:

CUpti\_ActivityFlag

#### CUpti\_MetricID CUpti\_ActivityMetricInstance::id

The metric ID.

#### uint32\_t CUpti\_ActivityMetricInstance::instance

The metric domain instance.

## CUpti\_ActivityKind CUpti\_ActivityMetricInstance::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_METRIC\_INSTANCE.

## uint8\_t CUpti\_ActivityMetricInstance::pad

Undefined. Reserved for internal use.

#### CUpti\_ActivityMetricInstance::value

The metric value.

# 3.39. CUpti\_ActivityModule Struct Reference

The activity record for a CUDA module.

This activity record represents a CUDA module (CUPTI\_ACTIVITY\_KIND\_MODULE). This activity record kind is not produced by the activity API but is included for completeness and ease-of-use. Profile frameworks built on top of CUPTI that collect module data from the module callback may choose to use this type to store the collected module data.

#### uint32\_t CUpti\_ActivityModule::contextId

The ID of the context where the module is loaded.

#### const void \*CUpti\_ActivityModule::cubin

The pointer to cubin.

#### uint32\_t CUpti\_ActivityModule::cubinSize

The cubin size.

#### uint32\_t CUpti\_ActivityModule::id

The module ID.

## CUpti\_ActivityKind CUpti\_ActivityModule::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_MODULE.

#### uint32\_t CUpti\_ActivityModule::pad

Undefined. Reserved for internal use.

# 3.40. CUpti\_ActivityName Struct Reference

The activity record providing a name.

This activity record provides a name for a device, context, thread, etc. (CUPTI\_ACTIVITY\_KIND\_NAME).

## CUpti\_ActivityKind CUpti\_ActivityName::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_NAME.

#### const char \*CUpti\_ActivityName::name

The name.

#### CUpti\_ActivityName::objectId

The identifier for the activity object. 'objectKind' indicates which ID is valid for this record.

# CUpti\_ActivityObjectKind CUpti\_ActivityName::objectKind

The kind of activity object being named.

# 3.41. CUpti\_ActivityNvLink Struct Reference

NVLink information. (deprecated in CUDA 9.0).

This structure gives capabilities of each logical NVLink connection between two devices, gpu<->gpu or gpu<->CPU which can be used to understand the topology. NVLink information are now reported using the CUpti\_ActivityNvLink2 activity record.

#### uint64\_t CUpti\_ActivityNvLink::bandwidth

Banwidth of NVLink in kbytes/sec

#### uint32\_t CUpti\_ActivityNvLink::domainId

Domain ID of NPU. On Linux, this can be queried using lspci.

#### uint32\_t CUpti\_ActivityNvLink::flag

Flag gives capabilities of the link

See also:

CUpti\_LinkFlag

## CUpti\_ActivityNvLink::@17 CUpti\_ActivityNvLink::idDev0

If typeDev0 is CUPTI\_DEV\_TYPE\_GPU, UUID for device 0. CUpti\_ActivityDevice2. If typeDev0 is CUPTI\_DEV\_TYPE\_NPU, struct npu for NPU.

## CUpti\_ActivityNvLink::@18 CUpti\_ActivityNvLink::idDev1

If typeDev1 is CUPTI\_DEV\_TYPE\_GPU, UUID for device 1. CUpti\_ActivityDevice2. If typeDev1 is CUPTI\_DEV\_TYPE\_NPU, struct npu for NPU.

#### uint32\_t CUpti\_ActivityNvLink::index

Index of the NPU. First index will always be zero.

#### CUpti\_ActivityKind CUpti\_ActivityNvLink::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_NVLINK.

#### uint32\_t CUpti\_ActivityNvLink::nvlinkVersion

NVLink version.

#### uint32\_t CUpti\_ActivityNvLink::physicalNvLinkCount

Number of physical NVLinks present between two devices.

#### int8\_t CUpti\_ActivityNvLink::portDev0

Port numbers for maximum 4 NVLinks connected to device 0. If typeDev0 is CUPTI\_DEV\_TYPE\_NPU, ignore this field. In case of invalid/unknown port number, this field will be set to value CUPTI\_NVLINK\_INVALID\_PORT. This will be used to correlate the metric values to individual physical link and attribute traffic to the logical NVLink in the topology.

## int8\_t CUpti\_ActivityNvLink::portDev1

Port numbers for maximum 4 NVLinks connected to device 1. If typeDev1 is CUPTI\_DEV\_TYPE\_NPU, ignore this field. In case of invalid/unknown port number, this field will be set to value CUPTI\_NVLINK\_INVALID\_PORT. This will be used to correlate the metric values to individual physical link and attribute traffic to the logical NVLink in the topology.

## CUpti\_DevType CUpti\_ActivityNvLink::typeDev0

Type of device 0 CUpti\_DevType

## CUpti\_DevType CUpti\_ActivityNvLink::typeDev1

Type of device 1 CUpti\_DevType

# 3.42. CUpti\_ActivityNvLink2 Struct Reference

NVLink information.

This structure gives capabilities of each logical NVLink connection between two devices, gpu<->gpu or gpu<->CPU which can be used to understand the topology.

#### uint64\_t CUpti\_ActivityNvLink2::bandwidth

Banwidth of NVLink in kbytes/sec

#### uint32\_t CUpti\_ActivityNvLink2::domainId

Domain ID of NPU. On Linux, this can be queried using lspci.

#### uint32\_t CUpti\_ActivityNvLink2::flag

Flag gives capabilities of the link

See also:

#### CUpti\_LinkFlag

# CUpti\_ActivityNvLink2::@21 CUpti\_ActivityNvLink2::idDev0

If typeDev0 is CUPTI\_DEV\_TYPE\_GPU, UUID for device 0. CUpti\_ActivityDevice2. If typeDev0 is CUPTI\_DEV\_TYPE\_NPU, struct npu for NPU.

# CUpti\_ActivityNvLink2::@22 CUpti\_ActivityNvLink2::idDev1

If typeDev1 is CUPTI\_DEV\_TYPE\_GPU, UUID for device 1. CUpti\_ActivityDevice2. If typeDev1 is CUPTI\_DEV\_TYPE\_NPU, struct npu for NPU.

#### uint32\_t CUpti\_ActivityNvLink2::index

Index of the NPU. First index will always be zero.

## CUpti\_ActivityKind CUpti\_ActivityNvLink2::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_NVLINK.

#### uint32\_t CUpti\_ActivityNvLink2::nvlinkVersion

NvLink version.

#### uint32\_t CUpti\_ActivityNvLink2::physicalNvLinkCount

Number of physical NVLinks present between two devices.

#### int8\_t CUpti\_ActivityNvLink2::portDev0

Port numbers for maximum 16 NVLinks connected to device 0. If typeDev0 is CUPTI\_DEV\_TYPE\_NPU, ignore this field. In case of invalid/unknown port number, this field will be set to value CUPTI\_NVLINK\_INVALID\_PORT. This will be used to correlate the metric values to individual physical link and attribute traffic to the logical NVLink in the topology.

#### int8\_t CUpti\_ActivityNvLink2::portDev1

Port numbers for maximum 16 NVLinks connected to device 1. If typeDev1 is CUPTI\_DEV\_TYPE\_NPU, ignore this field. In case of invalid/unknown port number, this field will be set to value CUPTI\_NVLINK\_INVALID\_PORT. This will be used to

correlate the metric values to individual physical link and attribute traffic to the logical NVLink in the topology.

#### CUpti\_DevType CUpti\_ActivityNvLink2::typeDev0

Type of device 0 CUpti\_DevType

#### CUpti\_DevType CUpti\_ActivityNvLink2::typeDev1

Type of device 1 CUpti\_DevType

# 3.43. CUpti\_ActivityObjectKindId Union Reference

Identifiers for object kinds as specified by CUpti\_ActivityObjectKind.

#### See also:

CUpti\_ActivityObjectKind

# CUpti\_ActivityObjectKindId::@1 CUpti\_ActivityObjectKindId::dcs

A device object requires that we identify the device ID. A context object requires that we identify both the device and context ID. A stream object requires that we identify device, context, and stream ID.

# CUpti\_ActivityObjectKindId::@0 CUpti\_ActivityObjectKindId::pt

A process object requires that we identify the process ID. A thread object requires that we identify both the process and thread ID.

# 3.44. CUpti\_ActivityOpenAcc Struct Reference

The base activity record for OpenAcc records.

The OpenACC activity API part uses a CUpti\_ActivityOpenAcc as a generic representation for any OpenACC activity. The 'kind' field is used to determine the specific activity kind, and from that the CUpti\_ActivityOpenAcc object can be cast to the specific OpenACC activity record type appropriate for that kind.

Note that all OpenACC activity record types are padded and aligned to ensure that each member of the record is naturally aligned.

#### See also:

#### CUpti\_ActivityKind

## uint32\_t CUpti\_ActivityOpenAcc::cuContextId

CUDA context id Valid only if deviceType is acc\_device\_nvidia.

#### uint32\_t CUpti\_ActivityOpenAcc::cuDeviceId

CUDA device id Valid only if deviceType is acc\_device\_nvidia.

#### uint32\_t CUpti\_ActivityOpenAcc::cuProcessId

The ID of the process where the OpenACC activity is executing.

# uint32\_t CUpti\_ActivityOpenAcc::cuStreamId

CUDA stream id Valid only if deviceType is acc\_device\_nvidia.

#### uint32\_t CUpti\_ActivityOpenAcc::cuThreadId

The ID of the thread where the OpenACC activity is executing.

#### uint64\_t CUpti\_ActivityOpenAcc::end

CUPTI end timestamp

# CUpti\_OpenAccEventKind CUpti\_ActivityOpenAcc::eventKind

CUPTI OpenACC event kind (

See also:

CUpti\_OpenAccEventKind)

#### uint32\_t CUpti\_ActivityOpenAcc::externalId

The OpenACC correlation ID. Valid only if deviceType is acc\_device\_nvidia. If not 0, it uniquely identifies this record. It is identical to the externalId in the preceding external correlation record of type CUPTI\_EXTERNAL\_CORRELATION\_KIND\_OPENACC.

#### CUpti\_ActivityKind CUpti\_ActivityOpenAcc::kind

The kind of this activity.

# CUpti\_OpenAccConstructKind CUpti\_ActivityOpenAcc::parentConstruct

CUPTI OpenACC parent construct kind (

See also:

CUpti OpenAccConstructKind)

Note that for applications using PGI OpenACC runtime < 16.1, this will always be CUPTI\_OPENACC\_CONSTRUCT\_KIND\_UNKNOWN.

uint64\_t CUpti\_ActivityOpenAcc::start

CUPTI start timestamp

uint32\_t CUpti\_ActivityOpenAcc::threadId

ThreadId

# 3.45. CUpti\_ActivityOpenAccData Struct Reference

The activity record for OpenACC data.

(CUPTI\_ACTIVITY\_KIND\_OPENACC\_DATA).

uint64\_t CUpti\_ActivityOpenAccData::bytes

Number of bytes

uint32\_t CUpti\_ActivityOpenAccData::cuContextId

CUDA context id Valid only if deviceType is acc\_device\_nvidia.

uint32\_t CUpti\_ActivityOpenAccData::cuDeviceId

CUDA device id Valid only if deviceType is acc\_device\_nvidia.

uint32\_t CUpti\_ActivityOpenAccData::cuProcessId

The ID of the process where the OpenACC activity is executing.

#### uint32\_t CUpti\_ActivityOpenAccData::cuStreamId

CUDA stream id Valid only if deviceType is acc\_device\_nvidia.

#### uint32\_t CUpti\_ActivityOpenAccData::cuThreadId

The ID of the thread where the OpenACC activity is executing.

#### uint64\_t CUpti\_ActivityOpenAccData::devicePtr

Device pointer if available

#### uint64\_t CUpti\_ActivityOpenAccData::end

CUPTI end timestamp

# CUpti\_OpenAccEventKind CUpti\_ActivityOpenAccData::eventKind

CUPTI OpenACC event kind (

See also:

CUpti\_OpenAccEventKind)

#### uint32\_t CUpti\_ActivityOpenAccData::externalId

The OpenACC correlation ID. Valid only if deviceType is acc\_device\_nvidia. If not 0, it uniquely identifies this record. It is identical to the externalId in the preceding external correlation record of type CUPTI\_EXTERNAL\_CORRELATION\_KIND\_OPENACC.

# uint64\_t CUpti\_ActivityOpenAccData::hostPtr

Host pointer if available

# CUpti\_ActivityKind CUpti\_ActivityOpenAccData::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_OPENACC\_DATA.

# uint32\_t CUpti\_ActivityOpenAccData::pad1

Undefined. Reserved for internal use.

uint64\_t CUpti\_ActivityOpenAccData::start

**CUPTI** start timestamp

uint32\_t CUpti\_ActivityOpenAccData::threadId

ThreadId

# 3.46. CUpti\_ActivityOpenAccLaunch Struct Reference

The activity record for OpenACC launch.

(CUPTI\_ACTIVITY\_KIND\_OPENACC\_LAUNCH).

uint32\_t CUpti\_ActivityOpenAccLaunch::cuContextId

CUDA context id Valid only if deviceType is acc\_device\_nvidia.

uint32\_t CUpti\_ActivityOpenAccLaunch::cuDeviceId

CUDA device id Valid only if deviceType is acc\_device\_nvidia.

uint32\_t CUpti\_ActivityOpenAccLaunch::cuProcessId

The ID of the process where the OpenACC activity is executing.

uint32\_t CUpti\_ActivityOpenAccLaunch::cuStreamId

CUDA stream id Valid only if deviceType is acc\_device\_nvidia.

uint32\_t CUpti\_ActivityOpenAccLaunch::cuThreadId

The ID of the thread where the OpenACC activity is executing.

uint64\_t CUpti\_ActivityOpenAccLaunch::end

CUPTI end timestamp

CUpti\_OpenAccEventKind

CUpti\_ActivityOpenAccLaunch::eventKind

CUPTI OpenACC event kind (

#### See also:

CUpti\_OpenAccEventKind)

#### uint32\_t CUpti\_ActivityOpenAccLaunch::externalId

The OpenACC correlation ID. Valid only if deviceType is acc\_device\_nvidia. If not 0, it uniquely identifies this record. It is identical to the externalId in the preceding external correlation record of type CUPTI\_EXTERNAL\_CORRELATION\_KIND\_OPENACC.

# CUpti\_ActivityKind CUpti\_ActivityOpenAccLaunch::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_OPENACC\_LAUNCH.

#### uint64\_t CUpti\_ActivityOpenAccLaunch::numGangs

The number of gangs created for this kernel launch

#### uint64\_t CUpti\_ActivityOpenAccLaunch::numWorkers

The number of workers created for this kernel launch

#### uint32\_t CUpti\_ActivityOpenAccLaunch::pad1

Undefined. Reserved for internal use.

#### uint64\_t CUpti\_ActivityOpenAccLaunch::start

**CUPTI** start timestamp

#### uint32\_t CUpti\_ActivityOpenAccLaunch::threadId

ThreadId

#### uint64\_t CUpti\_ActivityOpenAccLaunch::vectorLength

The number of vector lanes created for this kernel launch

# 3.47. CUpti\_ActivityOpenAccOther Struct Reference

The activity record for OpenACC other.

(CUPTI\_ACTIVITY\_KIND\_OPENACC\_OTHER).

### uint32\_t CUpti\_ActivityOpenAccOther::cuContextId

CUDA context id Valid only if deviceType is acc\_device\_nvidia.

### uint32\_t CUpti\_ActivityOpenAccOther::cuDeviceId

CUDA device id Valid only if deviceType is acc\_device\_nvidia.

### uint32\_t CUpti\_ActivityOpenAccOther::cuProcessId

The ID of the process where the OpenACC activity is executing.

### uint32\_t CUpti\_ActivityOpenAccOther::cuStreamId

CUDA stream id Valid only if deviceType is acc\_device\_nvidia.

### uint32\_t CUpti\_ActivityOpenAccOther::cuThreadId

The ID of the thread where the OpenACC activity is executing.

### uint64\_t CUpti\_ActivityOpenAccOther::end

CUPTI end timestamp

## CUpti\_OpenAccEventKind CUpti\_ActivityOpenAccOther::eventKind

CUPTI OpenACC event kind (

See also:

CUpti\_OpenAccEventKind)

### uint32\_t CUpti\_ActivityOpenAccOther::externalId

The OpenACC correlation ID. Valid only if deviceType is acc\_device\_nvidia. If not 0, it uniquely identifies this record. It is identical to the externalId in the preceding external correlation record of type CUPTI\_EXTERNAL\_CORRELATION\_KIND\_OPENACC.

### CUpti\_ActivityKind CUpti\_ActivityOpenAccOther::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_OPENACC\_OTHER.

### uint64\_t CUpti\_ActivityOpenAccOther::start

**CUPTI** start timestamp

## uint32\_t CUpti\_ActivityOpenAccOther::threadId

ThreadId

## 3.48. CUpti\_ActivityOverhead Struct Reference

The activity record for CUPTI and driver overheads.

This activity record provides CUPTI and driver overhead information (CUPTI\_ACTIVITY\_OVERHEAD).

### uint64\_t CUpti\_ActivityOverhead::end

The end timestamp for the overhead, in ns. A value of 0 for both the start and end timestamps indicates that timestamp information could not be collected for the overhead.

### CUpti\_ActivityKind CUpti\_ActivityOverhead::kind

The activity record kind, must be CUPTI\_ACTIVITY\_OVERHEAD.

### CUpti\_ActivityOverhead::objectId

The identifier for the activity object. 'objectKind' indicates which ID is valid for this record.

## CUpti\_ActivityObjectKind CUpti\_ActivityOverhead::objectKind

The kind of activity object that the overhead is associated with.

## CUpti\_ActivityOverheadKind CUpti\_ActivityOverhead::overheadKind

The kind of overhead, CUPTI, DRIVER, COMPILER etc.

### uint64\_t CUpti\_ActivityOverhead::start

The start timestamp for the overhead, in ns. A value of 0 for both the start and end timestamps indicates that timestamp information could not be collected for the overhead.

## 3.49. CUpti\_ActivityPCSampling Struct Reference

The activity record for PC sampling. (deprecated in CUDA 8.0).

This activity records information obtained by sampling PC (CUPTI\_ACTIVITY\_KIND\_PC\_SAMPLING). PC sampling activities are now reported using the CUpti\_ActivityPCSampling2 activity record.

### uint32\_t CUpti\_ActivityPCSampling::correlationId

The correlation ID of the kernel to which this result is associated.

### CUpti\_ActivityFlag CUpti\_ActivityPCSampling::flags

The properties of this instruction.

### uint32\_t CUpti\_ActivityPCSampling::functionId

Correlation ID with global/device function name

### CUpti\_ActivityKind CUpti\_ActivityPCSampling::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_PC\_SAMPLING.

### uint32\_t CUpti\_ActivityPCSampling::pcOffset

The pc offset for the instruction.

### uint32\_t CUpti\_ActivityPCSampling::samples

Number of times the PC was sampled with the stallReason in the record. The same PC can be sampled with different stall reasons.

### uint32\_t CUpti\_ActivityPCSampling::sourceLocatorId

The ID for source locator.

## CUpti\_ActivityPCSamplingStallReason CUpti\_ActivityPCSampling::stallReason

Current stall reason. Includes one of the reasons from CUpti\_ActivityPCSamplingStallReason

## 3.50. CUpti\_ActivityPCSampling2 Struct Reference

The activity record for PC sampling. (deprecated in CUDA 9.0).

This activity records information obtained by sampling PC (CUPTI\_ACTIVITY\_KIND\_PC\_SAMPLING). PC sampling activities are now reported using the CUpti\_ActivityPCSampling3 activity record.

### uint32\_t CUpti\_ActivityPCSampling2::correlationId

The correlation ID of the kernel to which this result is associated.

### CUpti\_ActivityFlag CUpti\_ActivityPCSampling2::flags

The properties of this instruction.

### uint32\_t CUpti\_ActivityPCSampling2::functionId

Correlation ID with global/device function name

### CUpti\_ActivityKind CUpti\_ActivityPCSampling2::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_PC\_SAMPLING.

### uint32\_t CUpti\_ActivityPCSampling2::latencySamples

Number of times the PC was sampled with the stallReason in the record. These samples indicate that no instruction was issued in that cycle from the warp scheduler from where the warp was sampled. Field is valid for devices with compute capability 6.0 and higher

### uint32\_t CUpti\_ActivityPCSampling2::pcOffset

The pc offset for the instruction.

### uint32\_t CUpti\_ActivityPCSampling2::samples

Number of times the PC was sampled with the stallReason in the record. The same PC can be sampled with different stall reasons. The count includes latencySamples.

### uint32\_t CUpti\_ActivityPCSampling2::sourceLocatorId

The ID for source locator.

## CUpti\_ActivityPCSamplingStallReason CUpti\_ActivityPCSampling2::stallReason

Current stall reason. Includes one of the reasons from CUpti\_ActivityPCSamplingStallReason

## 3.51. CUpti\_ActivityPCSampling3 Struct Reference

The activity record for PC sampling.

This activity records information obtained by sampling PC (CUPTI\_ACTIVITY\_KIND\_PC\_SAMPLING).

### uint32\_t CUpti\_ActivityPCSampling3::correlationId

The correlation ID of the kernel to which this result is associated.

### CUpti\_ActivityFlag CUpti\_ActivityPCSampling3::flags

The properties of this instruction.

### uint32\_t CUpti\_ActivityPCSampling3::functionId

Correlation ID with global/device function name

### CUpti\_ActivityKind CUpti\_ActivityPCSampling3::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_PC\_SAMPLING.

### uint32\_t CUpti\_ActivityPCSampling3::latencySamples

Number of times the PC was sampled with the stallReason in the record. These samples indicate that no instruction was issued in that cycle from the warp scheduler from where the warp was sampled. Field is valid for devices with compute capability 6.0 and higher

### uint64\_t CUpti\_ActivityPCSampling3::pcOffset

The pc offset for the instruction.

### uint32\_t CUpti\_ActivityPCSampling3::samples

Number of times the PC was sampled with the stallReason in the record. The same PC can be sampled with different stall reasons. The count includes latencySamples.

### uint32\_t CUpti\_ActivityPCSampling3::sourceLocatorId

The ID for source locator.

## CUpti\_ActivityPCSamplingStallReason CUpti\_ActivityPCSampling3::stallReason

Current stall reason. Includes one of the reasons from CUpti\_ActivityPCSamplingStallReason

## 3.52. CUpti\_ActivityPCSamplingConfig Struct Reference

PC sampling configuration structure.

This structure defines the pc sampling configuration.

See function /ref cuptiActivityConfigurePCSampling

## CUpti\_ActivityPCSamplingPeriod CUpti\_ActivityPCSamplingConfig::samplingPeriod

There are 5 level provided for sampling period. The level internally maps to a period in terms of cycles. Same level can map to different number of cycles on different gpus. No of cycles will be chosen to minimize information loss. The period chosen will be given by samplingPeriodInCycles in /ref CUpti\_ActivityPCSamplingRecordInfo for each kernel instance.

## uint32\_t CUpti\_ActivityPCSamplingConfig::samplingPeriod2

This will override the period set by samplingPeriod. Value 0 in samplingPeriod2 will be considered as samplingPeriod2 should not be used and samplingPeriod should be used. Valid values for samplingPeriod2 are between 5 to 31 both inclusive. This will set the sampling period to (2^samplingPeriod2) cycles.

### uint32\_t CUpti\_ActivityPCSamplingConfig::size

Size of configuration structure. CUPTI client should set the size of the structure. It will be used in CUPTI to check what fields are available in the structure. Used to preserve backward compatibility.

## 3.53. CUpti\_ActivityPCSamplingRecordInfo Struct Reference

The activity record for record status for PC sampling.

This activity records information obtained by sampling PC (CUPTI\_ACTIVITY\_KIND\_PC\_SAMPLING\_RECORD\_INFO).

### uint32 t

### CUpti\_ActivityPCSamplingRecordInfo::correlationId

The correlation ID of the kernel to which this result is associated.

### uint64\_t

### CUpti\_ActivityPCSamplingRecordInfo::droppedSamples

Number of samples that were dropped by hardware due to backpressure/overflow.

## CUpti\_ActivityKind CUpti\_ActivityPCSamplingRecordInfo::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_PC\_SAMPLING\_RECORD\_INFO.

### uint64 t

## CUpti\_ActivityPCSamplingRecordInfo::samplingPeriodInCycles

Sampling period in terms of number of cycles.

### uint64\_t

### CUpti\_ActivityPCSamplingRecordInfo::totalSamples

Number of times the PC was sampled for this kernel instance including all dropped samples.

## 3.54. CUpti\_ActivityPreemption Struct Reference

The activity record for a preemption of a CDP kernel.

This activity record represents a preemption of a CDP kernel.

uint32\_t CUpti\_ActivityPreemption::blockX

The X-dimension of the block that is preempted

uint32\_t CUpti\_ActivityPreemption::blockY

The Y-dimension of the block that is preempted

uint32\_t CUpti\_ActivityPreemption::blockZ

The Z-dimension of the block that is preempted

int64\_t CUpti\_ActivityPreemption::gridId

The grid-id of the block that is preempted

CUpti\_ActivityKind CUpti\_ActivityPreemption::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_PREEMPTION

uint32\_t CUpti\_ActivityPreemption::pad

Undefined. Reserved for internal use.

CUpti\_ActivityPreemptionKind CUpti\_ActivityPreemption::preemptionKind

kind of the preemption

uint64\_t CUpti\_ActivityPreemption::timestamp

The timestamp of the preemption, in ns. A value of 0 indicates that timestamp information could not be collected for the preemption.

## 3.55. CUpti\_ActivitySharedAccess Struct Reference

The activity record for source-level shared access.

This activity records the locations of the shared accesses in the source (CUPTI\_ACTIVITY\_KIND\_SHARED\_ACCESS).

### uint32\_t CUpti\_ActivitySharedAccess::correlationId

The correlation ID of the kernel to which this result is associated.

### uint32\_t CUpti\_ActivitySharedAccess::executed

The number of times this instruction was executed per warp. It will be incremented when at least one of thread among warp is active with predicate and condition code evaluating to true.

### CUpti\_ActivityFlag CUpti\_ActivitySharedAccess::flags

The properties of this shared access.

### uint32\_t CUpti\_ActivitySharedAccess::functionId

Correlation ID with global/device function name

### CUpti\_ActivityKind CUpti\_ActivitySharedAccess::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_SHARED\_ACCESS.

### uint32\_t CUpti\_ActivitySharedAccess::pad

Undefined. Reserved for internal use.

### uint32\_t CUpti\_ActivitySharedAccess::pcOffset

The pc offset for the access.

### uint64\_t

### CUpti\_ActivitySharedAccess::sharedTransactions

The total number of shared memory transactions generated by this access

### uint32\_t CUpti\_ActivitySharedAccess::sourceLocatorId

The ID for source locator.

### uint64\_t

### CUpti\_ActivitySharedAccess::theoreticalSharedTransactions

The minimum number of shared memory transactions possible based on the access pattern.

### uint64\_t CUpti\_ActivitySharedAccess::threadsExecuted

This increments each time when this instruction is executed by number of threads that executed this instruction with predicate and condition code evaluating to true.

## 3.56. CUpti\_ActivitySourceLocator Struct Reference

The activity record for source locator.

This activity record represents a source locator (CUPTI\_ACTIVITY\_KIND\_SOURCE\_LOCATOR).

### const char \*CUpti\_ActivitySourceLocator::fileName

The path for the file.

### uint32\_t CUpti\_ActivitySourceLocator::id

The ID for the source path, will be used in all the source level results.

### CUpti\_ActivityKind CUpti\_ActivitySourceLocator::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_SOURCE\_LOCATOR.

### uint32\_t CUpti\_ActivitySourceLocator::lineNumber

The line number in the source.

## 3.57. CUpti\_ActivityStream Struct Reference

The activity record for CUDA stream.

This activity is used to track created streams. (CUPTI\_ACTIVITY\_KIND\_STREAM).

### uint32\_t CUpti\_ActivityStream::contextId

The ID of the context where the stream was created.

### CUpti\_ActivityStreamFlag CUpti\_ActivityStream::flag

Flags associated with the stream.

### CUpti\_ActivityKind CUpti\_ActivityStream::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_STREAM.

### uint32\_t CUpti\_ActivityStream::pad

Undefined. Reserved for internal use.

### uint32\_t CUpti\_ActivityStream::priority

The clamped priority for the stream.

### uint32\_t CUpti\_ActivityStream::streamId

A unique stream ID to identify the stream.

## 3.58. CUpti\_ActivitySynchronization Struct Reference

The activity record for synchronization management.

This activity is used to track various CUDA synchronization APIs. (CUPTI\_ACTIVITY\_KIND\_SYNCHRONIZATION).

### uint32\_t CUpti\_ActivitySynchronization::contextId

The ID of the context for which the synchronization API is called. In case of context synchronization API it is the context id for which the API is called. In case of stream/event synchronization it is the ID of the context where the stream/event was created.

### uint32\_t CUpti\_ActivitySynchronization::correlationId

The correlation ID of the API to which this result is associated.

### uint32\_t CUpti\_ActivitySynchronization::cudaEventId

The event ID for which the synchronization API is called. A CUPTI\_SYNCHRONIZATION\_INVALID\_VALUE value indicate the field is not applicable for this record. Not valid for cuCtxSynchronize, cuStreamSynchronize.

### uint64\_t CUpti\_ActivitySynchronization::end

The end timestamp for the function, in ns. A value of 0 for both the start and end timestamps indicates that timestamp information could not be collected for the function.

### CUpti\_ActivityKind CUpti\_ActivitySynchronization::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_SYNCHRONIZATION.

### uint64\_t CUpti\_ActivitySynchronization::start

The start timestamp for the function, in ns. A value of 0 for both the start and end timestamps indicates that timestamp information could not be collected for the function.

## uint32\_t CUpti\_ActivitySynchronization::streamId

The compute stream for which the synchronization API is called. A CUPTI\_SYNCHRONIZATION\_INVALID\_VALUE value indicate the field is not applicable for this record. Not valid for cuCtxSynchronize, cuEventSynchronize.

## CUpti\_ActivitySynchronizationType CUpti\_ActivitySynchronization::type

The type of record.

## 3.59. CUpti\_ActivityUnifiedMemoryCounter Struct Reference

The activity record for Unified Memory counters (deprecated in CUDA 7.0).

This activity record represents a Unified Memory counter (CUPTI\_ACTIVITY\_KIND\_UNIFIED\_MEMORY\_COUNTER).

## CUpti\_ActivityUnifiedMemoryCounterKind CUpti\_ActivityUnifiedMemoryCounter::counterKind

The Unified Memory counter kind. See /ref CUpti\_ActivityUnifiedMemoryCounterKind

### uint32\_t CUpti\_ActivityUnifiedMemoryCounter::deviceId

The ID of the device involved in the memory transfer operation. It is not relevant if the scope of the counter is global (all devices).

## CUpti\_ActivityKind CUpti\_ActivityUnifiedMemoryCounter::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_UNIFIED\_MEMORY\_COUNTER

### uint32\_t CUpti\_ActivityUnifiedMemoryCounter::pad

Undefined. Reserved for internal use.

## uint32\_t CUpti ActivityUnifiedMemoryCounter::processId

The ID of the process to which this record belongs to. In case of global scope, processId is undefined.

## CUpti\_ActivityUnifiedMemoryCounterScope CUpti\_ActivityUnifiedMemoryCounter::scope

Scope of the Unified Memory counter. See /ref CUpti\_ActivityUnifiedMemoryCounterScope

## uint64\_t CUpti\_ActivityUnifiedMemoryCounter::timestamp

The timestamp when this sample was retrieved, in ns. A value of 0 indicates that timestamp information could not be collected

### uint64\_t CUpti\_ActivityUnifiedMemoryCounter::value

Value of the counter

## 3.60. CUpti\_ActivityUnifiedMemoryCounter2 Struct Reference

The activity record for Unified Memory counters (CUDA 7.0 and beyond).

This activity record represents a Unified Memory counter (CUPTI ACTIVITY KIND UNIFIED MEMORY COUNTER).

### uint64\_t CUpti\_ActivityUnifiedMemoryCounter2::address

This is the virtual base address of the page/s being transferred. For cpu and gpu faults, the virtual address for the page that faulted.

## CUpti\_ActivityUnifiedMemoryCounterKind CUpti\_ActivityUnifiedMemoryCounter2::counterKind

The Unified Memory counter kind

### uint32\_t CUpti\_ActivityUnifiedMemoryCounter2::dstld

The ID of the destination CPU/device involved in the memory transfer or remote map operation. Ignore this field if counterKind is CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_GPU\_PAGE\_FAULT or CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_CPU\_PAGE\_FAULT\_COUNT or CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_THRASHING or CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_THROTTLING

### uint64\_t CUpti\_ActivityUnifiedMemoryCounter2::end

The end timestamp of the counter, in ns. Ignore this field if counterKind is CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_CPU\_PAGE\_FAULT\_COUNT or CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_THRASHING or CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_REMOTE\_MAP. For counterKind

CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_BYTES\_TRANSFER\_HTOD and

CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_BYTES\_TRANSFER\_DTOH, timestamp is captured when activity finishes on GPU. For counterKind CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_GPU\_PAGE\_FAULT, timestamp is captured when CUDA driver queues the replay of faulting memory accesses on the GPU For counterKind CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_THROTTLING, timestamp is captured when throttling operation was finished by CUDA driver

### uint32\_t CUpti\_ActivityUnifiedMemoryCounter2::flags

The flags associated with this record. See enums

CUpti\_ActivityUnifiedMemoryAccessType if counterKind is

CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_GPU\_PAGE\_FAULT

and CUpti\_ActivityUnifiedMemoryMigrationCause if counterKind is

CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_BYTES\_TRANSFER\_HTOD

or

CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_BYTES\_TRANSFER\_HTOD and CUpti\_ActivityUnifiedMemoryRemoteMapCause if counterKind is CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_REMOTE\_MAP and CUpti\_ActivityFlag if counterKind is CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_THRASHING or CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_THROTTLING

## CUpti\_ActivityKind CUpti\_ActivityUnifiedMemoryCounter2::kind

The activity record kind, must be CUPTI\_ACTIVITY\_KIND\_UNIFIED\_MEMORY\_COUNTER

### uint32\_t CUpti\_ActivityUnifiedMemoryCounter2::pad

Undefined. Reserved for internal use.

## uint32\_t CUpti\_ActivityUnifiedMemoryCounter2::processId

The ID of the process to which this record belongs to.

### uint32\_t CUpti\_ActivityUnifiedMemoryCounter2::srcld

The ID of the source CPU/device involved in the memory transfer, page fault, thrashing, throttling or remote map operation. For counterKind CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_THRASHING, it is a bitwise ORing of the device IDs fighting for the memory region. Ignore this field if counterKind is CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_CPU\_PAGE\_FAULT\_COUNT

### uint64\_t CUpti\_ActivityUnifiedMemoryCounter2::start

The start timestamp of the counter, in ns. For counterKind CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_BYTES\_TRANSFER\_HTOD

and

CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_BYTES\_TRANSFER\_DTOH, timestamp is captured when activity starts on GPU. For counterKind CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_GPU\_PAGE\_FAULT and CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_CPU\_PAGE\_FAULT\_COUNT, timestamp is captured when CUDA driver started processing the fault. For counterKind CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_THRASHING, timestamp is captured when CUDA driver detected thrashing of memory region. For counterKind CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_THROTTLING, timestamp is captured when throttling opeeration was started by CUDA driver. For counterKind CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_REMOTE\_MAP, timestamp is captured when CUDA driver has pushed all required operations to the processor specified by dstId.

## uint32\_t CUpti\_ActivityUnifiedMemoryCounter2::streamId

The ID of the stream causing the transfer. This value of this field is invalid.

### uint64\_t CUpti\_ActivityUnifiedMemoryCounter2::value

Value of the counter For counterKind

CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_BYTES\_TRANSFER\_HTOD,

CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_BYTES\_TRANSFER\_DTOH,

CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_THREASHING and

CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_REMOTE\_MAP,

it is the size of the memory region in bytes. For counterKind

CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_GPU\_PAGE\_FAULT,

it is the number of page fault groups for the same page. For counterKind

CUPTI\_ACTIVITY\_UNIFIED\_MEMORY\_COUNTER\_KIND\_CPU\_PAGE\_FAULT\_COUNT,

it is the program counter for the instruction that caused fault.

## 3.61. CUpti\_ActivityUnifiedMemoryCounterConfig Struct Reference

Unified Memory counters configuration structure.

This structure controls the enable/disable of the various Unified Memory counters consisting of scope, kind and other parameters. See function /ref cuptiActivityConfigureUnifiedMemoryCounter

### uint32\_t

### CUpti\_ActivityUnifiedMemoryCounterConfig::deviceId

Device id of the traget device. This is relevant only for single device scopes. (deprecated in CUDA 7.0)

## uint32\_t

### CUpti\_ActivityUnifiedMemoryCounterConfig::enable

Control to enable/disable the counter. To enable the counter set it to non-zero value while disable is indicated by zero.

## CUpti\_ActivityUnifiedMemoryCounterKind CUpti\_ActivityUnifiedMemoryCounterConfig::kind

Unified Memory counter Counter kind

## CUpti\_ActivityUnifiedMemoryCounterScope CUpti\_ActivityUnifiedMemoryCounterConfig::scope

Unified Memory counter Counter scope. (deprecated in CUDA 7.0)

## 3.62. CUpti\_CallbackData Struct Reference

Data passed into a runtime or driver API callback function.

Data passed into a runtime or driver API callback function as the cbdata argument to CUpti\_CallbackFunc. The cbdata will be this type for domain equal to CUPTI\_CB\_DOMAIN\_DRIVER\_API or CUPTI\_CB\_DOMAIN\_RUNTIME\_API. The callback data is valid only within the invocation of the callback function that is passed the data. If you need to retain some data for use outside of the callback, you must make a copy of that data. For example, if you make a shallow copy of CUpti\_CallbackData within a callback, you cannot dereference functionParams outside of that callback to access the function parameters. functionName is an exception: the string pointed to by functionName is a global constant and so may be accessed outside of the callback.

### CUpti\_ApiCallbackSite CUpti\_CallbackData::callbackSite

Point in the runtime or driver function from where the callback was issued.

### CUcontext CUpti\_CallbackData::context

Driver context current to the thread, or null if no context is current. This value can change from the entry to exit callback of a runtime API function if the runtime initializes a context.

### uint32\_t CUpti\_CallbackData::contextUid

Unique ID for the CUDA context associated with the thread. The UIDs are assigned sequentially as contexts are created and are unique within a process.

### uint64\_t \*CUpti\_CallbackData::correlationData

Pointer to data shared between the entry and exit callbacks of a given runtime or drive API function invocation. This field can be used to pass 64-bit values from the entry callback to the corresponding exit callback.

## uint32\_t CUpti\_CallbackData::correlationId

The activity record correlation ID for this callback. For a driver domain callback (i.e. domain CUPTI\_CB\_DOMAIN\_DRIVER\_API) this ID will equal the correlation ID in the CUpti\_ActivityAPI record corresponding to the CUDA driver function call. For a runtime domain callback (i.e. domain CUPTI\_CB\_DOMAIN\_RUNTIME\_API) this ID will equal the correlation ID in the CUpti\_ActivityAPI record corresponding to the CUDA runtime function call. Within the callback, this ID can be recorded to correlate user data with the activity record. This field is new in 4.1.

### const char \*CUpti\_CallbackData::functionName

Name of the runtime or driver API function which issued the callback. This string is a global constant and so may be accessed outside of the callback.

### const void \*CUpti\_CallbackData::functionParams

Pointer to the arguments passed to the runtime or driver API call. See generated\_cuda\_runtime\_api\_meta.h and generated\_cuda\_meta.h for structure definitions for the parameters for each runtime and driver API function.

### void \*CUpti\_CallbackData::functionReturnValue

Pointer to the return value of the runtime or driver API call. This field is only valid within the exit::CUPTI\_API\_EXIT callback. For a runtime API functionReturnValue points to a cudaError\_t. For a driver API functionReturnValue points to a CUresult.

### const char \*CUpti\_CallbackData::symbolName

Name of the symbol operated on by the runtime or driver API function which issued the callback. This entry is valid only for driver and runtime launch callbacks, where it returns the name of the kernel.

### 3.63. CUpti\_EventGroupSet Struct Reference

A set of event groups.

A set of event groups. When returned by cuptiEventGroupSetsCreate and cuptiMetricCreateEventGroupSets a set indicates that event groups that can be enabled at the same time (i.e. all the events in the set can be collected simultaneously).

### CUpti\_EventGroup \*CUpti\_EventGroupSet::eventGroups

An array of numEventGroups event groups.

### uint32\_t CUpti\_EventGroupSet::numEventGroups

The number of event groups in the set.

## 3.64. CUpti\_EventGroupSets Struct Reference

A set of event group sets.

A set of event group sets. When returned by cuptiEventGroupSetsCreate and cuptiMetricCreateEventGroupSets a CUpti\_EventGroupSets indicates the number of passes required to collect all the events, and the event groups that should be collected during each pass.

### uint32\_t CUpti\_EventGroupSets::numSets

Number of event group sets.

### CUpti\_EventGroupSet \*CUpti\_EventGroupSets::sets

An array of numSets event group sets.

### 3.65. CUpti\_MetricValue Union Reference

A metric value.

Metric values can be one of several different kinds. Corresponding to each kind is a member of the CUpti\_MetricValue union. The metric value returned by cuptiMetricGetValue should be accessed using the appropriate member of that union based on its value kind.

## 3.66. CUpti\_ModuleResourceData Struct Reference

Module data passed into a resource callback function.

CUDA module data passed into a resource callback function as the <code>cbdata</code> argument to <code>CUpti\_CallbackFunc</code>. The <code>cbdata</code> will be this type for <code>domain</code> equal to <code>CUPTI\_CB\_DOMAIN\_RESOURCE</code>. The module data is valid only within the invocation of the callback function that is passed the data. If you need to retain some data for use outside of the callback, you must make a copy of that data.

### size\_t CUpti\_ModuleResourceData::cubinSize

The size of the cubin.

### uint32\_t CUpti\_ModuleResourceData::moduleId

Identifier to associate with the CUDA module.

### const char \*CUpti\_ModuleResourceData::pCubin

Pointer to the associated cubin.

## 3.67. CUpti\_NvtxData Struct Reference

Data passed into a NVTX callback function.

Data passed into a NVTX callback function as the cbdata argument to CUpti\_CallbackFunc. The cbdata will be this type for domain equal to CUPTI\_CB\_DOMAIN\_NVTX. Unless otherwise notes, the callback data is valid only within the invocation of the callback function that is passed the data. If you need to retain some data for use outside of the callback, you must make a copy of that data.

### const char \*CUpti\_NvtxData::functionName

Name of the NVTX API function which issued the callback. This string is a global constant and so may be accessed outside of the callback.

### const void \*CUpti\_NvtxData::functionParams

Pointer to the arguments passed to the NVTX API call. See generated\_nvtx\_meta.h for structure definitions for the parameters for each NVTX API function.

### 3.68. CUpti\_ResourceData Struct Reference

Data passed into a resource callback function.

Data passed into a resource callback function as the cbdata argument to CUpti\_CallbackFunc. The cbdata will be this type for domain equal to CUPTI\_CB\_DOMAIN\_RESOURCE. The callback data is valid only within the invocation of the callback function that is passed the data. If you need to retain some data for use outside of the callback, you must make a copy of that data.

### CUcontext CUpti\_ResourceData::context

For CUPTI\_CBID\_RESOURCE\_CONTEXT\_CREATED and CUPTI\_CBID\_RESOURCE\_CONTEXT\_DESTROY\_STARTING, the context being created or destroyed. For CUPTI\_CBID\_RESOURCE\_STREAM\_CREATED and CUPTI\_CBID\_RESOURCE\_STREAM\_DESTROY\_STARTING, the context containing the stream being created or destroyed.

### void \*CUpti\_ResourceData::resourceDescriptor

Reserved for future use.

### CUstream CUpti\_ResourceData::stream

For CUPTI\_CBID\_RESOURCE\_STREAM\_CREATED and CUPTI\_CBID\_RESOURCE\_STREAM\_DESTROY\_STARTING, the stream being created or destroyed.

## 3.69. CUpti\_SynchronizeData Struct Reference

Data passed into a synchronize callback function.

Data passed into a synchronize callback function as the cbdata argument to CUpti\_CallbackFunc. The cbdata will be this type for domain equal to

CUPTI\_CB\_DOMAIN\_SYNCHRONIZE. The callback data is valid only within the invocation of the callback function that is passed the data. If you need to retain some data for use outside of the callback, you must make a copy of that data.

## CUcontext CUpti\_SynchronizeData::context

The context of the stream being synchronized.

## CUstream CUpti\_SynchronizeData::stream

The stream being synchronized.

# Chapter 4. DATA FIELDS

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  CUpti_ActivityInstantaneousEvent
  CUpti_ActivityInstantaneousMetric
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  CUpti_ActivityNvLink2
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  CUpti_ActivityNvLink2
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  CUpti_ActivityNvLink2
  CUpti_ActivityNvLink
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  CUpti_ActivityInstantaneousMetricInstance
  CUpti_ActivityMetricInstance
  CUpti_ActivityEventInstance
  CUpti_ActivityInstantaneousEventInstance
isSharedMemoryCarveoutRequested
  CUpti_ActivityKernel4
```

### K

#### kind

CUpti\_ActivityUnifiedMemoryCounterConfig

CUpti\_ActivityInstantaneousMetricInstance

CUpti\_ActivityInstantaneousMetric

 $CUpti\_ActivityInstantaneousEventInstance\\$ 

CUpti\_ActivityInstantaneousEvent

CUpti\_ActivityNvLink2

CUpti\_ActivityNvLink

CUpti\_ActivityExternalCorrelation

CUpti\_ActivityOpenAccOther

CUpti\_ActivityOpenAccLaunch

CUpti\_ActivityOpenAccData

CUpti ActivityOpenAcc

CUpti\_ActivityInstructionCorrelation

CUpti\_ActivitySynchronization

CUpti\_ActivityStream

CUpti\_ActivityCudaEvent

CUpti\_ActivitySharedAccess

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CUpti\_ActivityUnifiedMemoryCounter2

CUpti ActivityUnifiedMemoryCounter

CUpti\_ActivityPCSamplingRecordInfo

CUpti\_ActivityPCSampling3

CUpti\_ActivityPCSampling2

CUpti\_ActivityPCSampling

CUpti\_ActivityInstructionExecution

CUpti\_ActivityEnvironment

CUpti\_ActivityOverhead

CUpti\_ActivityMarkerData

CUpti\_ActivityMarker2

CUpti ActivityMarker

CUpti\_ActivityName

CUpti\_ActivityContext

CUpti\_ActivityDeviceAttribute

CUpti\_ActivityDevice2

CUpti\_ActivityDevice

CUpti\_ActivityBranch2

CUpti\_ActivityBranch

CUpti\_ActivityGlobalAccess3

CUpti\_ActivityGlobalAccess2

CUpti\_ActivityGlobalAccess

 $CUpti\_ActivitySourceLocator\\$ 

CUpti\_ActivityMetricInstance

CUpti\_ActivityMetric

CUpti\_ActivityEventInstance

CUpti\_ActivityEvent

CUpti\_ActivityAPI

CUpti\_ActivityPreemption

CUpti\_ActivityCdpKernel

CUpti\_ActivityKernel4

CUpti\_ActivityKernel3

CUpti\_ActivityKernel2

CUpti\_ActivityKernel

CUpti\_ActivityMemory

CUpti\_ActivityMemset

CUpti\_ActivityMemcpy2

CUpti\_ActivityMemcpy

CUpti\_Activity

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### 12\_transactions

CUpti\_ActivityGlobalAccess

CUpti\_ActivityGlobalAccess2

CUpti\_ActivityGlobalAccess3

### 12CacheSize

CUpti\_ActivityDevice

CUpti\_ActivityDevice2

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CUpti\_ActivityPCSampling2

CUpti\_ActivityPCSampling3

### launchType

CUpti\_ActivityKernel4

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CUpti ActivitySourceLocator

### localMemoryPerThread

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CUpti\_ActivityKernel2

CUpti\_ActivityKernel3

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CUpti\_ActivityKernel2

CUpti\_ActivityKernel4

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### CUpti\_ActivityCdpKernel CUpti\_ActivityKernel3

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#### maxBlockDimX

CUpti\_ActivityDevice

CUpti\_ActivityDevice2

### maxBlockDimY

CUpti\_ActivityDevice2

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### maxBlockDimZ

CUpti\_ActivityDevice

CUpti\_ActivityDevice2

### maxBlocksPerMultiprocessor

CUpti\_ActivityDevice2

CUpti\_ActivityDevice

### maxGridDimX

CUpti\_ActivityDevice

CUpti\_ActivityDevice2

### maxGridDimY

CUpti\_ActivityDevice

CUpti\_ActivityDevice2

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CUpti\_ActivityDevice

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CUpti\_ActivityDevice2

CUpti\_ActivityDevice

### maxRegistersPerBlock

CUpti\_ActivityDevice

CUpti\_ActivityDevice2

### maxRegistersPerMultiprocessor

CUpti\_ActivityDevice2

### maxSharedMemoryPerBlock

CUpti\_ActivityDevice

CUpti\_ActivityDevice2

### max Shared Memory Per Multiprocessor

CUpti\_ActivityDevice2

### maxThreadsPerBlock

CUpti\_ActivityDevice

CUpti\_ActivityDevice2

### maxWarpsPerMultiprocessor

CUpti\_ActivityDevice2

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  CUpti_ActivityEnvironment
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  CUpti_ActivityMemset
  CUpti_ActivityMemory
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  CUpti_ActivityFunction
  CUpti_ModuleResourceData
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  CUpti_ActivityMemory
  CUpti_ActivityKernel
  CUpti_ActivityKernel3
  CUpti_ActivityDevice2
  CUpti_ActivityName
  CUpti_ActivityKernel4
  CUpti_ActivityMarker
  CUpti_ActivityMarker2
  CUpti_ActivityKernel2
  CUpti_ActivityCdpKernel
  CUpti_ActivityFunction
  CUpti_ActivityDevice
not PredOff Threads Executed \\
  CUpti_ActivityInstructionExecution
nullStreamId
  CUpti_ActivityContext
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  CUpti_EventGroupSet
numGangs
  CUpti_ActivityOpenAccLaunch
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  CUpti_ActivityDevice
  CUpti_ActivityDevice2
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  CUpti_ActivityDevice2
  CUpti_ActivityDevice
numSets
  CUpti_EventGroupSets
numThreadsPerWarp
  CUpti_ActivityDevice
  CUpti_ActivityDevice2
```

# numWorkers CUpti\_ActivityOpenAccLaunch nvlinkVersion CUpti\_ActivityNvLink2 CUpti\_ActivityNvLink 0 objectId CUpti\_ActivityName CUpti\_ActivityMarker CUpti\_ActivityOverhead CUpti\_ActivityMarker2 objectKind CUpti\_ActivityMarker CUpti\_ActivityName CUpti\_ActivityOverhead CUpti\_ActivityMarker2 overheadKind CUpti\_ActivityOverhead Ρ pad CUpti\_ActivityMemcpy2 CUpti\_ActivityKernel CUpti\_ActivityEventInstance CUpti ActivityBranch2 CUpti\_ActivityCudaEvent CUpti\_ActivityStream CUpti\_ActivityDevice2 CUpti\_ActivityInstructionCorrelation CUpti\_ActivityInstantaneousEventInstance CUpti\_ActivityMetric CUpti\_ActivityMarker2 CUpti\_ActivityInstantaneousMetric CUpti\_ActivityInstantaneousMetricInstance CUpti\_ActivityInstructionExecution CUpti\_ActivityPreemption CUpti\_ActivityMetricInstance CUpti\_ActivityUnifiedMemoryCounter

 $CUpti\_ActivityUnifiedMemoryCounter2\\$ 

CUpti\_ActivityGlobalAccess2

CUpti\_ActivitySharedAccess

CUpti\_ActivityModule

# pad1 CUpti\_ActivityOpenAccData CUpti\_ActivityOpenAccLaunch padding CUpti\_ActivityKernel4 parentBlockX CUpti\_ActivityCdpKernel parentBlockY CUpti\_ActivityCdpKernel parentBlockZ CUpti\_ActivityCdpKernel parentConstruct CUpti\_ActivityOpenAcc parentGridId CUpti\_ActivityCdpKernel partitionedGlobalCacheExecuted CUpti\_ActivityKernel3 CUpti\_ActivityKernel4 partitionedGlobalCacheRequested CUpti\_ActivityKernel3 CUpti\_ActivityKernel4 payload CUpti\_ActivityMarkerData payloadKind CUpti\_ActivityMarkerData pcieLinkGen CUpti\_ActivityEnvironment pcieLinkWidth CUpti\_ActivityEnvironment pcOffset CUpti\_ActivityGlobalAccess CUpti\_ActivityGlobalAccess3 CUpti ActivityBranch CUpti\_ActivityBranch2 CUpti\_ActivityPCSampling CUpti\_ActivityPCSampling2 CUpti\_ActivityPCSampling3 CUpti\_ActivitySharedAccess CUpti\_ActivityInstructionCorrelation CUpti\_ActivityInstructionExecution CUpti\_ActivityGlobalAccess2 pCubin CUpti\_ModuleResourceData

```
physicalNvLinkCount
  CUpti_ActivityNvLink2
  CUpti_ActivityNvLink
pid
  CUpti\_ActivityAutoBoostState\\
portDev0
  CUpti_ActivityNvLink2
  CUpti_ActivityNvLink
portDev1
  CUpti_ActivityNvLink
  CUpti_ActivityNvLink2
power
  CUpti_ActivityEnvironment
powerLimit
  CUpti_ActivityEnvironment
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  CUpti_ActivityPreemption
priority
  CUpti_ActivityStream
processId
  CUpti_ActivityMemory
  CUpti_ActivityAPI
  CUpti_ActivityUnifiedMemoryCounter
  CUpti_ActivityUnifiedMemoryCounter2
pt
  CUpti_ActivityObjectKindId
Q
queued
  CUpti_ActivityKernel4
  CUpti_ActivityCdpKernel
R
registersPerThread
  CUpti_ActivityKernel
  CUpti_ActivityKernel2
  CUpti_ActivityKernel4
  CUpti_ActivityCdpKernel
  CUpti_ActivityKernel3
requested
  CUpti_ActivityKernel4
  CUpti_ActivityCdpKernel
  CUpti_ActivityKernel2
```

```
CUpti_ActivityKernel3
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  CUpti_ActivityExternalCorrelation
  CUpti_ActivityInstantaneousEvent
reserved0
  CUpti_ActivityMemcpy2
  CUpti_ActivityKernel2
  CUpti_ActivityKernel3
  CUpti_ActivityKernel4
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  CUpti_ActivityMemset
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  CUpti ResourceData
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  CUpti_ActivityPCSampling3
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  CUpti_ActivityPCSamplingConfig
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  CUpti_ActivityPCSamplingRecordInfo
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  CUpti ActivityUnifiedMemoryCounter
  CUpti_ActivityUnifiedMemoryCounterConfig
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  CUpti_EventGroupSets
shared Memory Carve out Requested\\
  CUpti_ActivityKernel4
sharedMemoryConfig
  CUpti_ActivityKernel2
  CUpti_ActivityKernel3
  CUpti_ActivityKernel4
  CUpti_ActivityCdpKernel
```

#### sharedTransactions

CUpti\_ActivitySharedAccess

#### size

CUpti\_ActivityPCSamplingConfig

# smClock

CUpti\_ActivityEnvironment

#### sourceLocatorId

CUpti\_ActivityGlobalAccess

CUpti\_ActivityGlobalAccess2

CUpti\_ActivityGlobalAccess3

CUpti\_ActivityBranch

CUpti\_ActivityBranch2

CUpti\_ActivityInstructionExecution

CUpti\_ActivityPCSampling

CUpti\_ActivityPCSampling2

CUpti\_ActivityPCSampling3

CUpti\_ActivitySharedAccess

CUpti\_ActivityInstructionCorrelation

#### speed

CUpti\_ActivityEnvironment

# srcContextId

CUpti\_ActivityMemcpy2

#### srcDeviceId

CUpti\_ActivityMemcpy2

#### srcId

CUpti\_ActivityUnifiedMemoryCounter2

#### srcKind

CUpti\_ActivityMemcpy

CUpti\_ActivityMemcpy2

#### stallReason

CUpti\_ActivityPCSampling

CUpti\_ActivityPCSampling2

CUpti ActivityPCSampling3

#### start

CUpti\_ActivityKernel2

CUpti ActivityMemcpy

CUpti\_ActivityMemcpy2

CUpti\_ActivityMemset

CUpti\_ActivityMemory

CUpti\_ActivityKernel

CUpti\_ActivityKernel3

CUpti\_ActivityKernel4

CUpti\_ActivityCdpKernel

```
CUpti_ActivityAPI
  CUpti_ActivityOverhead
  CUpti_ActivityUnifiedMemoryCounter2
  CUpti_ActivitySynchronization
  CUpti_ActivityOpenAccLaunch
  CUpti_ActivityOpenAccData
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  CUpti_ActivityOpenAcc
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  CUpti_ActivityKernel3
  CUpti_ActivityKernel
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  CUpti_SynchronizeData
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  CUpti_ActivityKernel2
  CUpti_ActivityKernel
  CUpti_ActivityMemcpy2
  CUpti_ActivityCudaEvent
  CUpti_ActivityCdpKernel
  CUpti_ActivitySynchronization
  CUpti_ActivityKernel3
  CUpti_ActivityStream
  CUpti_ActivityUnifiedMemoryCounter2
  CUpti ActivityKernel4
submitted
  CUpti_ActivityCdpKernel
  CUpti_ActivityKernel4
symbolName
  CUpti_CallbackData
Т
temperature
  CUpti_ActivityEnvironment
theoreticalL2Transactions
  CUpti_ActivityGlobalAccess2
  CUpti_ActivityGlobalAccess3
```

#### theoreticalSharedTransactions

CUpti\_ActivitySharedAccess

#### threadId

CUpti\_ActivityOpenAccLaunch

CUpti\_ActivityOpenAccOther

CUpti\_ActivityAPI

CUpti\_ActivityOpenAcc

CUpti\_ActivityOpenAccData

#### threadsExecuted

CUpti\_ActivitySharedAccess

CUpti\_ActivityGlobalAccess

CUpti\_ActivityGlobalAccess2

CUpti\_ActivityGlobalAccess3

CUpti\_ActivityBranch

CUpti\_ActivityBranch2

CUpti\_ActivityInstructionExecution

#### timestamp

CUpti\_ActivityUnifiedMemoryCounter

CUpti\_ActivityMarker2

CUpti\_ActivityPreemption

CUpti\_ActivityMarker

CUpti\_ActivityInstantaneousEvent

CUpti\_ActivityEnvironment

CUpti\_ActivityInstantaneousEventInstance

CUpti\_ActivityInstantaneousMetric

 $CUpti\_ActivityInstantaneousMetricInstance\\$ 

# totalSamples

CUpti\_ActivityPCSamplingRecordInfo

#### type

CUpti\_ActivitySynchronization

#### typeDev0

CUpti\_ActivityNvLink

CUpti ActivityNvLink2

#### typeDev1

CUpti\_ActivityNvLink

CUpti\_ActivityNvLink2

# U

# uuid

CUpti\_ActivityDevice2

# ٧

#### value

CUpti\_ActivityMemset

CUpti\_ActivityEvent

CUpti\_ActivityInstantaneousMetricInstance

 $CUpti\_ActivityInstantaneousMetric\\$ 

CUpti\_ActivityInstantaneousEventInstance

 $CUpti\_ActivityInstantaneousEvent\\$ 

CUpti\_ActivityMetric

 $CUpti\_ActivityUnifiedMemoryCounter\\$ 

CUpti\_ActivityDeviceAttribute

CUpti\_ActivityMetricInstance

 $CUpti\_ActivityUnifiedMemoryCounter2$ 

 $CUpti\_ActivityEventInstance$ 

# vectorLength

CUpti\_ActivityOpenAccLaunch

# Chapter 5. LIMITATIONS

The following are known issues with the current release.

- ► The Continuous event collection mode

  CUPTI\_EVENT\_COLLECTION\_MODE\_CONTINUOUS is supported only on Tesla
  devices.
- Profiling results might be inconsistent when auto boost is enabled. Profiler tries to disable auto boost by default. But it might fail to do so in some conditions and profiling will continue and results will be inconsistent. API cuptiGetAutoBoostState() can be used to query the auto boost state of the device. This API returns error CUPTI\_ERROR\_NOT\_SUPPORTED on devices that don't support auto boost. Note that auto boost is supported only on certain Tesla devices with compute capability 3.0 and higher.
- CUPTI doesn't populate the activity structures which are deprecated, instead the newer version of the activity structure is filled with the information.
- While collecting events in continuous mode, event reporting may be delayed i.e. event values may be returned by a later call to readEvent(s) API and the event values for the last readEvent(s) API may get lost.
- When profiling events, it is possible that the domain instance that gets profiled gives event value 0 due to absence of workload on the domain instance since CUPTI profiles one instance of the domain by default. To profile all instances of the domain, user can set event group attribute CUPTI\_EVENT\_GROUP\_ATTR\_PROFILE\_ALL\_DOMAIN\_INSTANCES through API cuptiEventGroupSetAttribute().

# Chapter 6. CHANGELOG

# **CUPTI changes in CUDA 8.0**

List of changes done as part of the CUDA Toolkit 8.0 release.

- Sampling of the program counter (PC) is enhanced to point out the true latency issues, it indicates if the stall reasons for warps are actually causing stalls in the issue pipeline. Field latencySamples of new activity record CUpti\_ActivityPCSampling2 provides true latency samples. This field is valid for devices with compute capability 6.0 and higher. See section PC Sampling for more details.
- ▶ Support for NVLink topology information such as the pair of devices connected via NVLink, peak bandwidth, memory access permissions etc is provided through new activity record CUpti\_ActivityNvLink. NVLink performance metrics for data transmitted/received, transmit/receive throughput and respective header overhead for each physical link. See section NVLink for more details.
- CUPTI now supports profiling of OpenACC applications. OpenACC profiling information is provided in the form of new activity records CUpti\_ActivityOpenAccData, CUpti\_ActivityOpenAccLaunch and CUpti\_ActivityOpenAccOther. This aids in correlating OpenACC constructs on the CPU with the corresponding activity taking place on the GPU, and mapping it back to the source code. New API cuptiOpenACCInitialize is used to initialize profiling for supported OpenACC runtimes. See section OpenACC for more details.
- ▶ Unified memory profiling now provides GPU page fault events on devices with compute capability 6.0 and 64 bit Linux platforms.

  Enum CUpti\_ActivityUnifiedMemoryAccessType lists memory access types for GPU page fault events and enum CUpti\_ActivityUnifiedMemoryMigrationCause lists migration causes for data transfer events.
- Unified Memory profiling support is extended to Mac platform.
- Support for 16-bit floating point (FP16) data format profiling. New metrics inst\_fp\_16, flop\_count\_hp\_add, flop\_count\_hp\_mul, flop\_count\_hp\_fma,

- flop\_count\_hp, flop\_hp\_efficiency, half\_precision\_fu\_utilization are supported. Peak FP16 flops per cycle for device can be queried using the enum CUPTI\_DEVICE\_ATTR\_FLOP\_HP\_PER\_CYCLE added to CUpti DeviceAttribute.
- Added new activity kinds CUPTI\_ACTIVITY\_KIND\_SYNCHRONIZATION,
  CUPTI\_ACTIVITY\_KIND\_STREAM and CUPTI\_ACTIVITY\_KIND\_CUDA\_EVENT,
  to support the tracing of CUDA synchronization constructs such as context,
  stream and CUDA event synchronization. Synchronization details are provided
  in the form of new activity record CUpti\_ActivitySynchronization. Enum
  CUpti\_ActivitySynchronizationType lists different types of CUDA
  synchronization constructs.
- ► APIs cuptiSetThreadIdType()/cuptiGetThreadIdType() to set/get the mechanism used to fetch the thread-id used in CUPTI records. Enum CUpti ActivityThreadIdType lists all supported mechanisms.
- ▶ Added API cuptiComputeCapabilitySupported() to check the support for a specific compute capability by the CUPTI.
- Added support to establish correlation between an external API (such as OpenACC, OpenMP) and CUPTI API activity records.

  APIs cuptiActivityPushExternalCorrelationId() and cuptiActivityPopExternalCorrelationId() should be used to push and pop external correlation ids for the calling thread. Generated records of type CUpti\_ActivityExternalCorrelation contain both external and CUPTI assigned correlation ids.
- Added containers to store the information of events and metrics in the form of activity records CUpti\_ActivityInstantaneousEvent,

  CUpti\_ActivityInstantaneousEventInstance,

  CUpti\_ActivityInstantaneousMetric and

  CUpti\_ActivityInstantaneousMetricInstance. These activity records are not produced by the CUPTI, these are included for completeness and ease-of-use.

  Profilers built on top of CUPTI that sample events may choose to use these records to store the collected event data.
- ► Support for domains and annotation of synchronization objects added in NVTX v2. New activity record CUpti\_ActivityMarker2 and enums to indicate various stages of synchronization object i.e. CUPTI\_ACTIVITY\_FLAG\_MARKER\_SYNC\_ACQUIRE, CUPTI\_ACTIVITY\_FLAG\_MARKER\_SYNC\_ACQUIRE\_SUCCESS, CUPTI\_ACTIVITY\_FLAG\_MARKER\_SYNC\_ACQUIRE\_FAILED and CUPTI\_ACTIVITY\_FLAG\_MARKER\_SYNC\_RELEASE are added.
- ► Unused field runtimeCorrelationId of the activity record CUpti\_ActivityMemset is broken into two fields flags and memoryKind to indicate the asynchronous behaviour and the kind of the memory used for the memset operation. It is supported by the new

- flag CUPTI\_ACTIVITY\_FLAG\_MEMSET\_ASYNC added in the enum CUpti ActivityFlag.
- ► Added flag CUPTI\_ACTIVITY\_MEMORY\_KIND\_MANAGED in the enum CUpti\_ActivityMemoryKind to indicate managed memory.
- ▶ API cuptiGetStreamId has been deprecated. A new API cuptiGetStreamIdEx is introduced to provide the stream id based on the legacy or per-thread default stream flag.

#### **CUPTI changes in CUDA 7.5**

List of changes done as part of the CUDA Toolkit 7.5 release.

- ▶ Device-wide sampling of the program counter (PC) is enabled by default. This was a preview feature in the CUDA Toolkit 7.0 release and it was not enabled by default.
- Ability to collect all events and metrics accurately in presence of multiple contexts on the GPU is extended for devices with compute capability 5.x.
- ▶ API cuptiGetLastError is introduced to return the last error that has been produced by any of the CUPTI API calls or the callbacks in the same host thread.
- Unified memory profiling is supported with MPS (Multi-Process Service)
- Callback is provided to collect replay information after every kernel run during kernel replay. See API cuptiKernelReplaySubscribeUpdate and callback type CUpti KernelReplayUpdateFunc.
- ▶ Added new attributes in enum CUpti\_DeviceAttribute to query maximum shared memory size for different cache preferences for a device function.

#### **CUPTI changes in CUDA 7.0**

List of changes done as part of the CUDA Toolkit 7.0 release.

- ► CUPTI supports device-wide sampling of the program counter (PC). Program counters along with the stall reasons from all active warps are sampled at a fixed frequency in the round robin order. Activity record CUpti\_ActivityPCSampling enabled using activity kind CUPTI\_ACTIVITY\_KIND\_PC\_SAMPLING outputs stall reason along with PC and other related information.

  Enum CUpti\_ActivityPCSamplingStallReason lists all the stall reasons. Sampling period is configurable and can be tuned using API cuptiActivityConfigurePCSampling. This feature is available on devices with compute capability 5.2.
- Added new activity record CUpti\_ActivityInstructionCorrelation which can be used to dump source locator records for all the PCs of the function.
- ▶ All events and metrics for devices with compute capability 3.x and 5.0 can be collected accurately in presence of multiple contexts on the GPU. In previous releases only some events and metrics could be collected accurately when multiple contexts were executing on the GPU.

- ▶ Unified memory profiling is enhanced by providing fine grain data transfers to and from the GPU, coupled with more accurate timestamps with each transfer. This information is provided through new activity record CUpti\_ActivityUnifiedMemoryCounter2, deprecating old record CUpti\_ActivityUnifiedMemoryCounter.
- ▶ MPS tracing and profiling support is extended on multi-gpu setups.
- Activity record CUpti\_ActivityDevice for device information has been deprecated and replaced by new activity record CUpti\_ActivityDevice2. New record adds device UUID which can be used to uniquely identify the device across profiler runs.
- Activity record CUpti\_ActivityKernel2 for kernel execution has been deprecated and replaced by new activity record CUpti\_ActivityKernel3. New record gives information about Global Partitioned Cache Configuration requested and executed. Partitioned global caching has an impact on occupancy calculation. If it is ON, then a CTA can only use a half SM, and thus a half of the registers available per SM. The new fields apply for devices with compute capability 5.2 and higher. Note that this change was done in CUDA 6.5 release with support for compute capability 5.2.

# **CUPTI** changes in CUDA 6.5

List of changes done as part of the CUDA Toolkit 6.5 release.

- ► Instruction classification is done for source-correlated Instruction Execution activity CUpti\_ActivityInstructionExecution. See CUpti ActivityInstructionClass for instruction classes.
- ► Two new device attributes are added to the activity CUpti\_DeviceAttribute:
  - CUPTI\_DEVICE\_ATTR\_FLOP\_SP\_PER\_CYCLE gives peak single precision flop per cycle for the GPU.
  - ► CUPTI\_DEVICE\_ATTR\_FLOP\_DP\_PER\_CYCLE gives peak double precision flop per cycle for the GPU.
- Two new metric properties are added:
  - ► CUPTI\_METRIC\_PROPERTY\_FLOP\_SP\_PER\_CYCLE gives peak single precision flop per cycle for the GPU.
  - ► CUPTI\_METRIC\_PROPERTY\_FLOP\_DP\_PER\_CYCLE gives peak double precision flop per cycle for the GPU.
- Activity record CUpti\_ActivityGlobalAccess for source level global access information has been deprecated and replaced by new activity record CUpti\_ActivityGlobalAccess2. New record additionally gives information needed to map SASS assembly instructions to CUDA C source code. And it also provides ideal L2 transactions count based on the access pattern.
- Activity record CUpti\_ActivityBranch for source level branch information has been deprecated and replaced by new activity record CUpti ActivityBranch2.

- New record additionally gives information needed to map SASS assembly instructions to CUDA C source code.
- ▶ Sample sass\_source\_map is added to demonstrate the mapping of SASS assembly instructions to CUDA C source code.
- ► Default event collection mode is changed to Kernel (CUPTI\_EVENT\_COLLECTION\_MODE\_KERNEL) from Continuous (CUPTI\_EVENT\_COLLECTION\_MODE\_CONTINUOUS). Also Continuous mode is now supported only on Tesla devices.
- Profiling results might be inconsistent when auto boost is enabled. Profiler tries to disable auto boost by default, it might fail to do so in some conditions, but profiling will continue. A new API cuptiGetAutoBoostState is added to query the auto boost state of the device. This API returns error CUPTI\_ERROR\_NOT\_SUPPORTED on devices that don't support auto boost. Note that auto boost is supported only on certain Tesla devices from the Kepler+ family.
- Activity record CUpti\_ActivityKernel2 for kernel execution has been deprecated and replaced by new activity record CUpti\_ActivityKernel3. New record additionally gives information about Global Partitioned Cache Configuration requested and executed. The new fields apply for devices with 5.2 Compute Capability.

# CUPTI changes in CUDA 6.0

List of changes done as part of the CUDA Toolkit 6.0 release.

- ► Two new CUPTI activity kinds have been introduced to enable two new types of source-correlated data collection. The Instruction Execution kind collects SASS-level instruction execution counts, divergence data, and predication data. The Shared Access kind collects source correlated data indication inefficient shared memory accesses.
- CUPTI now provides support for CUDA applications using Unified Memory. A new activity record reports Unified Memory activity such as transfers to and from a GPU and the number of Unified Memory related page faults.
- ► CUPTI now recognized and reports the special MPS context that is used by CUDA applications running on a system with MPS enabled.
- has been updated to introduce a new field into the structure in a backwards compatible manner. The 32-bit computeApiKind field was replaced with two 16 bit fields, computeApiKind and defaultStreamId. Because all valid computeApiKind values fit within 16 bits, and because all supported CUDA platforms are little-endian, persisted context record data read with the new structure will have the correct value for computeApiKind and have a value of zero for defaultStreamId. The CUPTI client is responsible for versioning the persisted context data to recognize when the defaultStreamId field is valid.

- To ensure that metric values are calculated as accurately as possible, a new metric API is introduced. Function cuptiMetricGetRequiredEventGroupSets can be used to get the groups of events that should be collected at the same time.
- Execution overheads introduced by CUPTI have been dramatically decreased.
- ► The new activity buffer API introduced in CUDA Toolkit 5.5 is now required. The legacy cuptiActivityEnqueueBuffer and cuptiActivityDequeueBuffer functions have been removed.

#### **CUPTI changes in CUDA 5.5**

List of changes done as part of CUDA Toolkit 5.5 release.

- ▶ Applications that use CUDA Dynamic Parallelism can now be profiled using CUPTI. Device-side kernel launches are reported using a new activity kind.
- Device attributes such as power usage, clocks, thermals, etc. are now reported via a new activity kind.
- A new activity buffer API uses callbacks to request and return buffers of activity records. The existing cuptiActivityEnqueueBuffer and cuptiActivityDequeueBuffer functions are still supported but are deprecated and will be removed in a future release.
- ► The Event API supports kernel replay so that any number of events can be collected during a single run of the application.
- A new metric API cuptiMetricGetValue2 allows metric values to be calculated for any device, even if that device is not available on the system.
- CUDA peer-to-peer memory copies are reported explicitly via the activity API. In previous releases these memory copies were only partially reported.

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