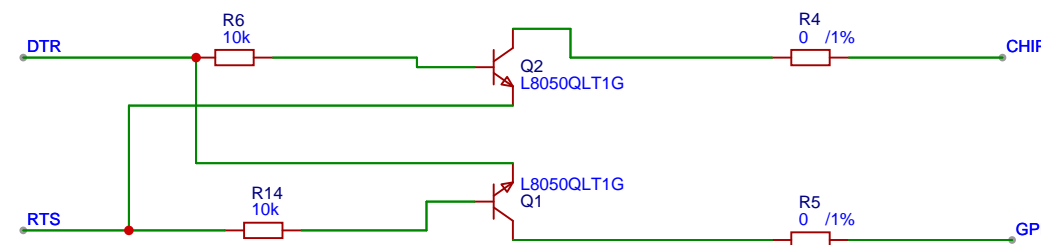
[illegible]

## PROGRAM SWITCHES

The diagram shows two switches, SW2 and SW3, used for selecting between different program memory configurations. SW2 is connected to VCC\_3V3 and CHIP\_PU, with a 10k resistor R10. SW3 is connected to GPIO0. Both switches have pull-down capacitors C20 and C22 (100nF/50V/10%).