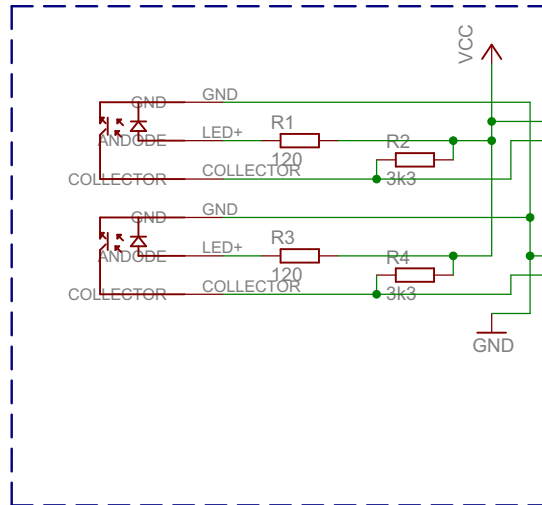
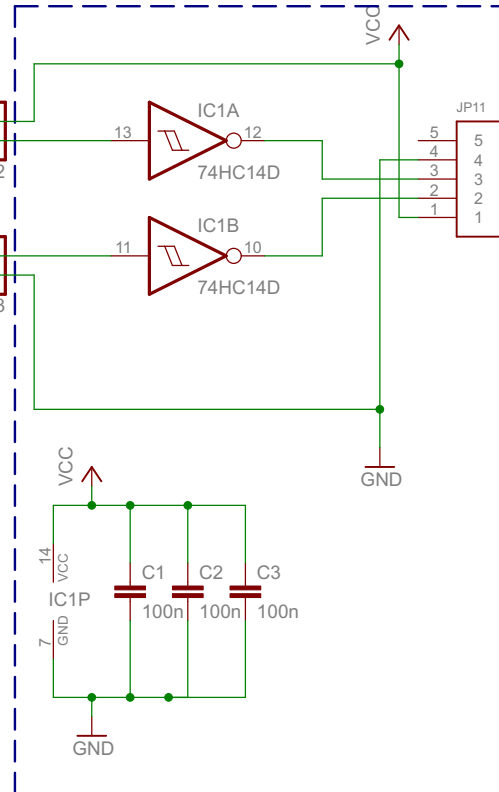


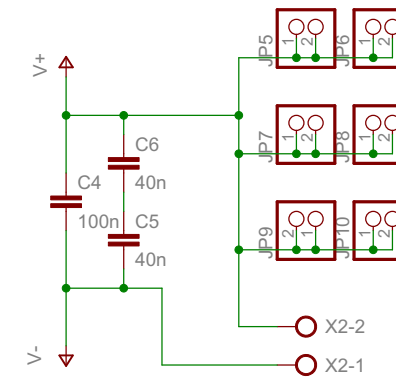
untere Platine



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VCC
SIGNAL1
SIGNAL2
GND



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