Pinbelegung

des ATMEGA16 Mainboard

PORT A

0 ***Taste S1***		4	
1 ***Taste S2***		5	
2 ***Taste S3***		6	
3		7	

PORT B

0 (T0)		4 (SS)	RFM12 Chip Select
1 (T1)		5 (MOSI)	SPI-BUS
2 (INT2) ***LED 1***		6 (MISO)	SPI-BUS
3 (OC0) ***LED 2***	USB Chip Select[1]	7 (SCK)	SPI-BUS

PORT C

0 (SCL)		4 (TDO)	
1 (SDA)		5 (TDI)	
2 (TCK)		6 (TOSC1)	
3 (TMS)		7 (TOSC2)	

PORT D

0 (RXD)	UART RX	4 (OC1B)	PWM für Motor (B) rechts
1 (TXT)	UART TX	5 (OCIA)	PWM für Motor (A) links
2 (INT0)		6 (ICP1)	
3 (INT1)		7 (OC2)	

[1] Nur auf dem Avr Board V2.2

Alternative Funktionen

PORT A

0	ADC0 (ADC input channel 0)	4	ADC4 (ADC input channel 4)
1	ADC1 (ADC input channel 1)	5	ADC5 (ADC input channel 5)
2	ADC2 (ADC input channel 2)	6	ADC6 (ADC input channel 6)
3	ADC3 (ADC input channel 3)	7	ADC7 (ADC input channel 7)

PORT B

0	T0 (Timer/Counter0 External Counter Input) XCK (USART External Clock Input/Output)	4	SS (SPI Slave Select Input)
1	T1 (Timer/Counter1 External Counter Input)	5	MOSI (SPI Bus Master Output/Slave Input)
2	AIN0 (Analog Comparator Positive Input) INT2 (External Interrupt 2 Input)	6	MISO (SPI Bus Master Input/Slave Output)
3	AIN1 (Analog Comparator Negative Input) OC0 (Timer/Counter9 Output Compare Match Output	7	SCK (SPI Bus Serial Clock)

PORT C

0	SCL (Two-wire Serial Bus Clock Line)	4	TDO (JTAG Test Data Out)
1	SDA (Two-wire Serial Bus Data Input/Output Line)	5	TDI (JTAG Test Data In)
2	TCK (JTAG Test Clock)	6	TOSC1 (Timer Oscillator Pin 1)
3	TMS (JTAG Test Mode Select)	7	TOSC2 (Timer Oscillator Pin 2)

PORT D

0	RXD (USART Input Pin)	4	OC1B (Timer/Counter Compare B Match Output)
1	TXD (USART Output Pin)	5	OC1A (Timer/Counter Compare A Match Output)
2	INT0 (External Interrupt 0 Input)	6	ICP1 (Timer/Counter1 Input Capture Pin)
3	INT1 (External Interrupt 1 Input)	7	OC2 (Timer/Counter2 Output Compare Match Output)