





Grid Vision T&D Sdn.Bhd.

Factory Acceptance Test Record DF1725IED RTU

Project Name	
Contract No.	

Approval				
	Name	Company	Date	Signature
Prepared by:	Muhammad Nor Farhan	Grid Vision T&D	17 th December 2024	
Reviewed by:	Lim Yee Chin	Grid Vision T&D	17 th December 2024	
Verified by:	Ir. Ts. Ramlah Binti Awang	Tenaga Nasional Berhad		

	
<input checked="" type="checkbox"/> NO FURTHER COMMENTS <input type="checkbox"/> REVISE AND RESUBMIT <input type="checkbox"/> INFORMATION ONLY	
By : 	Date : 19/12/24
This endorsement shall not : i. Be construed as acceptance of Variation and/or Contractor's Claim, and/or ii. Relieve the Contractor from any Contractual obligations or responsibilities	

FORM REF	1725IED-FAT-TR		
FORM DESCRIPTION	RTU FAT RECORD	FORM VERSION	02
PROJECT DETAILS	RTU SERIAL NO:	PRODUCT DETAILS	DF1725IED RTU
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FAT – TEST RECORD REF:	TABLE OF CONTENT	RECORD SHEET NO.	1 of 1

Change Log

No.	Date	Rev No.	Remark
1	22/12/2022	00	<ul style="list-style-type: none"> Initial Release
2	24/2/2024	00	<ul style="list-style-type: none"> Revise parameter setting for AI, IEC101.
3	16/5/2024	01	<ul style="list-style-type: none"> Separate Test Procedure and Test Record
4	10/9/2024	01	<ul style="list-style-type: none"> Update firmware version and date for MCU-1-A and MCU-4-A. Change Force to 0 setting under AI Card Module.
5	17/12/2024	02	<ul style="list-style-type: none"> Add Ecolink LED in accessories list. Add another firmware check under Processor Functional Test. Update IEC104 Channel Redundancy Test Procedure. Combine Physical and Functional result in 1 page. Add checkbox and signature box in FAT flowchart. Add tester's competency verification. Update IOA for I/O Fail. Add COM-6-A Module. Add Appendix for I/O Test Setup.



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FORM REF	1725IED-FAT-TR-TS		
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1.0 Overall Pre-FAT/FAT Result

RTU Serial No.	
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PASS ☐

FAIL ☐

REMARKS:



Tested By:
(Signature/Official Stamp)

Witnessed By:
(Signature/Official Stamp)

Tester's
Name:
Date:

TNB QAI's
Name:
Date: