

FORM REF	1725IED-FAT-TR-PIP		
FORM DESCRIPTION	RTU FAT RECORD – INSPECTION	FORM VERSION	02
PROJECT DETAILS	RTU SERIAL NO:	PRODUCT DETAILS	DF1725IED RTU
	CONTRACT NO:	TEST RECORD SHEET	19 of 39
FAT – TEST RECORD REF:	INSPECTION – PROCESSOR	RECORD SHEET NO.	of

## 1.0 Physical and Quality Inspection

Processor No.	Part Number	Subrack No.	Slot No.	Serial Number

No.	Quality Inspection	OK	NO
1	The module is free from defect e.g. scratches, deformities, corrosion, broken.	<input type="checkbox"/>	<input type="checkbox"/>
2	The jumper is set as per approved drawing.	<input type="checkbox"/>	<input type="checkbox"/>

## 2.0 Functional Test - Processor

No.	Inspection	OK	NO
1	The PWR LED is permanently lights up.	<input type="checkbox"/>	<input type="checkbox"/>
2	The RUN LED is blinking normally.	<input type="checkbox"/>	<input type="checkbox"/>
3	Processor can be connected to maintenance tools.	<input type="checkbox"/>	<input type="checkbox"/>
4	Firmware Version is <b>4.11.024 [24 Aug 2024]</b> for MCU-1-A or <b>4.02.008 [24 Nov 2023]</b> for MCU-4-A.	<input type="checkbox"/>	<input type="checkbox"/>



Tested By:  
(Signature/Official Stamp)

Witnessed By:  
(Signature/Official Stamp)

\_\_\_\_\_  
Tester's  
Name:  
Date:

\_\_\_\_\_  
TNB QAI's  
Name:  
Date: