

THIS DOCUMENT AND THE DATA DISCLOSED HEREIN OR HEREWITH IS THE PROPERTY OF ALTium LIMITED AND MAY BE FREELY DISTRIBUTED IN WHOLE. NO RIGHTS ARE RESERVED OR EXPRESS OR IMPLIED WARANTEE GIVEN.

														DWG TIT: =DOC_NO_ASSY_DWG		REV: .ite	D									
REV STATUS OF SHEETS	REV													REVISIONS												
	SHEET													ZONE	REV	DESCRIPTION				DATE	APPROVED					

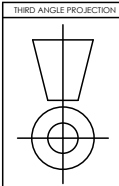
Layer Stack Legend


Material	Layer	Thickness	Dielectric Material	Type	Gerber
	Top Overlay			Legend	GTO
	Surface Material	0.010mm	SM-001	Solder Mask	GTS
PbSn	Top Surface Finish	0.020mm		Surface Finish	
Copper	TOP	0.015mm		Signal	GTL
Prepreg		0.064mm	FR-4	Dielectric	
Copper	L2_GND	0.035mm		Signal	G1
Core		0.075mm	FR-4	Dielectric	
Copper	L3_IN1	0.035mm		Signal	G2
Prepreg		0.064mm	FR-4	Dielectric	
CF-004	L4_IN2	0.035mm		Signal	G3
Core		0.580mm	Core-009	Dielectric	
CF-004	L5_PWR2	0.035mm		Signal	G4
Prepreg		0.064mm	PP-006	Dielectric	
Copper	L6_PWR1	0.035mm		Signal	G5
Core		0.075mm	FR4	Dielectric	
Copper	L7_GND_2	0.035mm		Signal	G6
Prepreg		0.064mm	FR-4	Dielectric	
Copper	BOTTOM	0.015mm		Signal	GBL
PbSn	Bottom Surface Finish	0.020mm		Surface Finish	
Surface Material	Bottom Solder	0.025mm	SM-001	Solder Mask	GBS
	Bottom Overlay			Legend	GBO

Total thickness: 1.299mm

Drill Table

Symbol	Count	Hole Size	Plated	Hole Tolerance
⊗	6	0.200mm	Plated	
□	1035	0.203mm	Plated	
▣	8	0.500mm	Plated	
⊗	8	0.550mm	Plated	
○	2	0.950mm	Non-Plated	
☆	2	1.067mm	Plated	
▽	4	3.200mm	Plated	
	1065 Total			

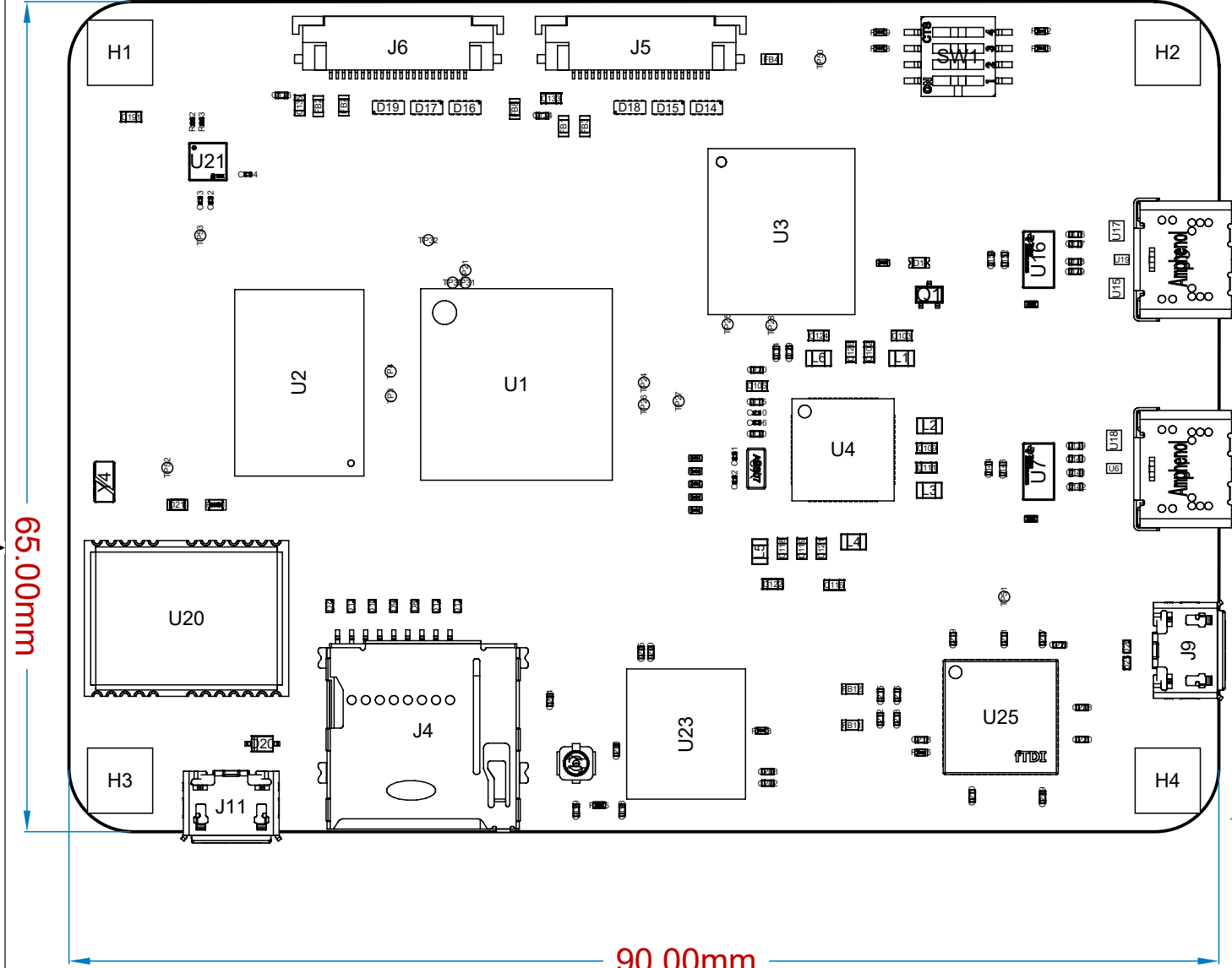


PART NO: =PCB_PART_NUMBER					
APPROVALS		DATE			
ENGINEER: =PCB_ENGINEER		=PCB_ENGINEER			
DESIGNER: =PCB_DESIGNER		=PCB_DESIGNER			
CHECKER: =PCB_CHECKER		=PCB_CHECKER			
Reference Documents		DESIGN ITEM: .Item		DESIGN ITEM REVISION: .ItemRevision	
BOM DOC: =DOC_NO_BOM		TITLE: =PCB_TITLE_1 =PCB_TITLE_2			
ASSY DOC: =DOC_NO_FAB_DWG					
SCH DOC: =DOC_NO_SCH_DWG		SIZE: B	CAGE CODE: =CAGE_CO	DWG NO:	REV:
PCB DOC: =PCB_DWG_NO		SCALE:	FILE NAME: Dashcam CPU_V2.0E.PCBDwf	SHEET: 1	OF 5

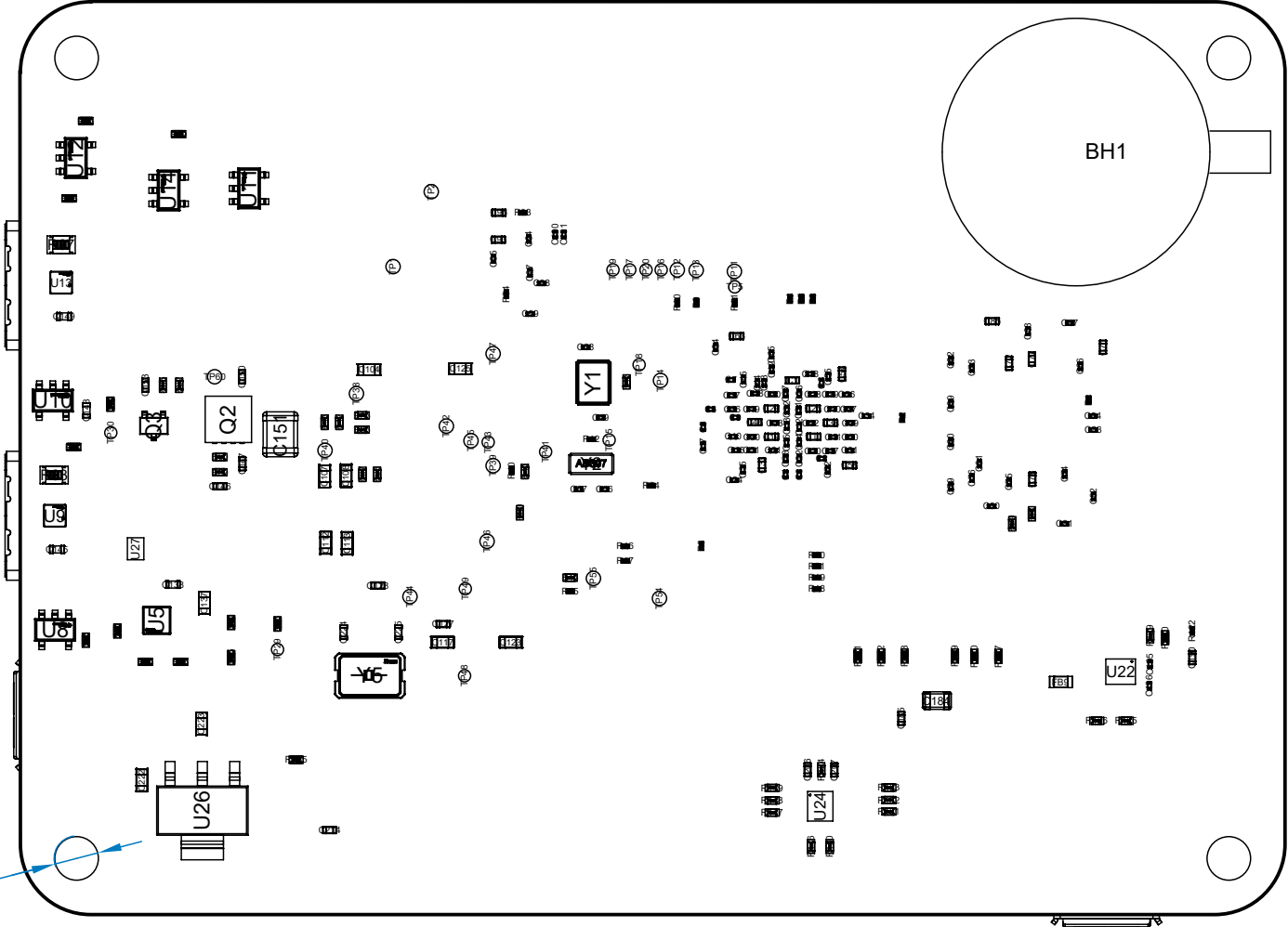
THIS DOCUMENT AND THE DATA DISCLOSED HEREIN OR HEREWITH IS THE PROPERTY OF ALTIUM LIMITED AND MAY BE FREELY DISTRIBUTED IN WHOLE. NO RIGHTS ARE RESERVED OR EXPRESS OR IMPLIED WARANTEE GIVEN.

REV STATUS OF SHEETS	REV										REVISIONS				
	SHEET										ZONE	REV	DESCRIPTION	DATE	APPROVED

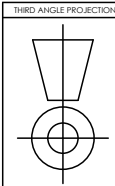
View from Top side (Scale 2:1)



View from Bottom side (Scale 2:1)



Ø3.20mm



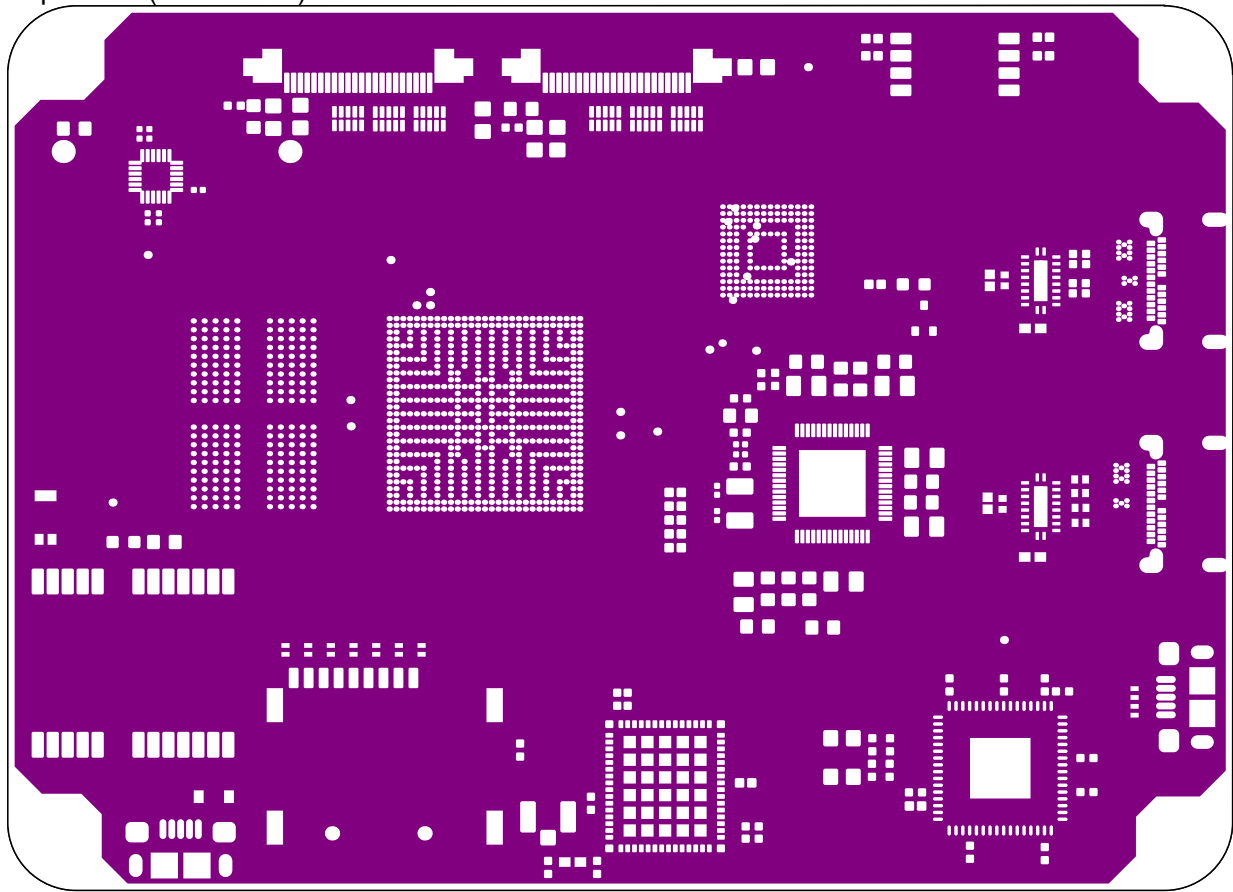
NEXT ASSY	USED ON
APPLICATION	

PART NO: =PCB_PART_NUMBER		APPROVALS		DATE		ADDRESS 1	
ENGINEER: =PCB_ENGINEER		=PCB_ENGINEER				ADDRESS 2	
DESIGNER: =PCB_DESIGNER		=PCB_DESIGNER				ADDRESS 3	
CHECKER: =PCB_CHECKER		=PCB_CHECKER				ADDRESS 4	
Reference Documents				DESIGN ITEM: .Item		DESIGN ITEM REVISION: .ItemRevision	
BOM DOC: =DOC_NO_BOM		TITLE: =PCB_TITLE_1					
ASSY DOC: =DOC_NO_FAB_DWG		=PCB_TITLE_2					
SCH DOC: =DOC_NO_SCH_DWG		SIZE: CAGE CODE: DWG NO: REV:					
PCB DOC: =PCB_DWG_NO		B =CAGE_CO					
SCALE:		FILE NAME: Dashcam CPU_V2.0E.PCBdwf		SHEET: 2		OF 5	

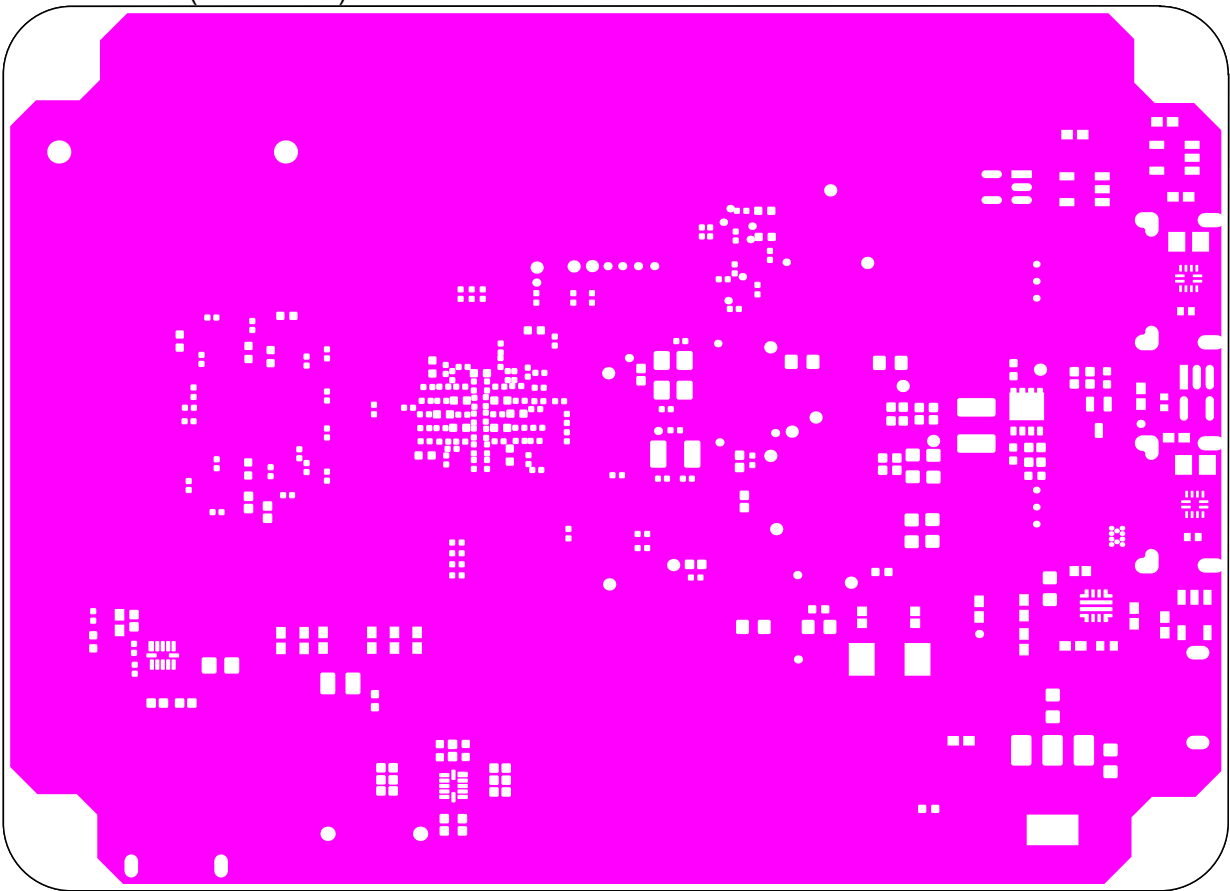
THIS DOCUMENT AND THE DATA DISCLOSED HEREIN OR HEREWITH IS THE PROPERTY OF ALTIUM LIMITED AND MAY BE FREELY DISTRIBUTED IN WHOLE. NO RIGHTS ARE RESERVED OR EXPRESS OR IMPLIED WARANTEE GIVEN.

[illegible]

Top solder (Scale 1.8:1)



Bottom Solder (Scale 1.8:1)

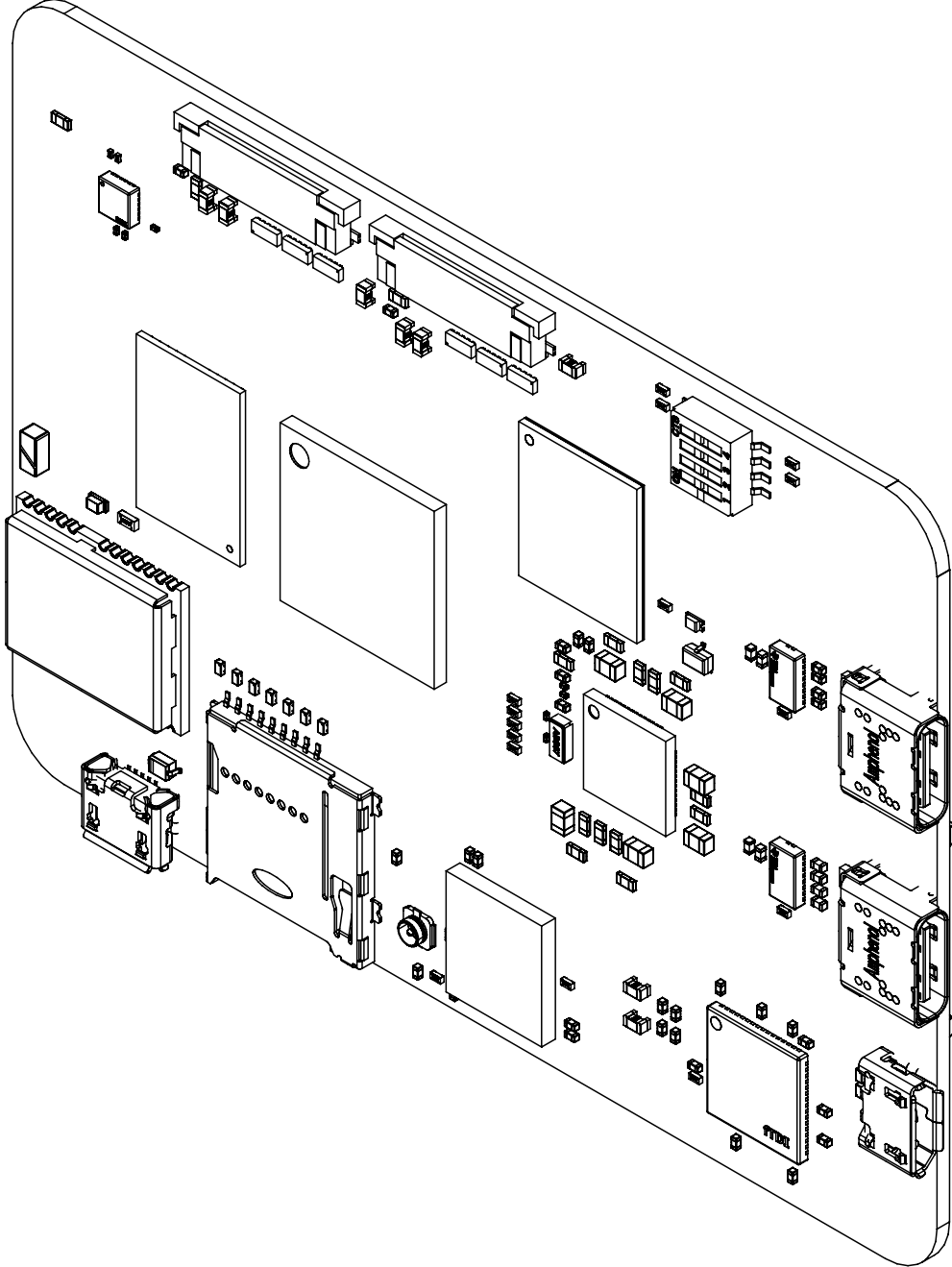


		PART NO.: =PCB_PART_NUMBER		<div> <div>ADDRESS 1</div> <div>ADDRESS 2</div> <div>ADDRESS 3</div> <div>ADDRESS 4</div> </div>	
		APPROVALS	DATE		
<div>THIRD ANGLE PROJECTION</div>		ENGINEER: =PCB_ENGINEER	=PCB_ENGINEER	<div>Altium™</div>	
		DESIGNER: =PCB_DESIGNER	=PCB_DESIGNER		
		CHECKER: =PCB_CHECKER	=PCB_CHECKER		
		Reference Documents			
		BOM DOC: =DOC_NO_BOM			
<div>NEXT ASSY</div> <div>USED ON</div>		ASSY DOC: =DOC_NO_FAB_DWG	DESIGN ITEM: .Item		DESIGN ITEM REVISION: .ItemRevision
		SCH DOC: =DOC_NO_SCH_DWG	TITLE: =PCB_TITLE_1		
		PCB DOC: =PCB_DWG_NO	=PCB_TITLE_2		
APPLICATION		SIZE: B	CAGE CODE: =CAGE_CO	DWG NO:	REV:
		SCALE:	FILE NAME: Dashcam CPU_V2.0E.PCB.Dwf	SHEET: 4	OF 5

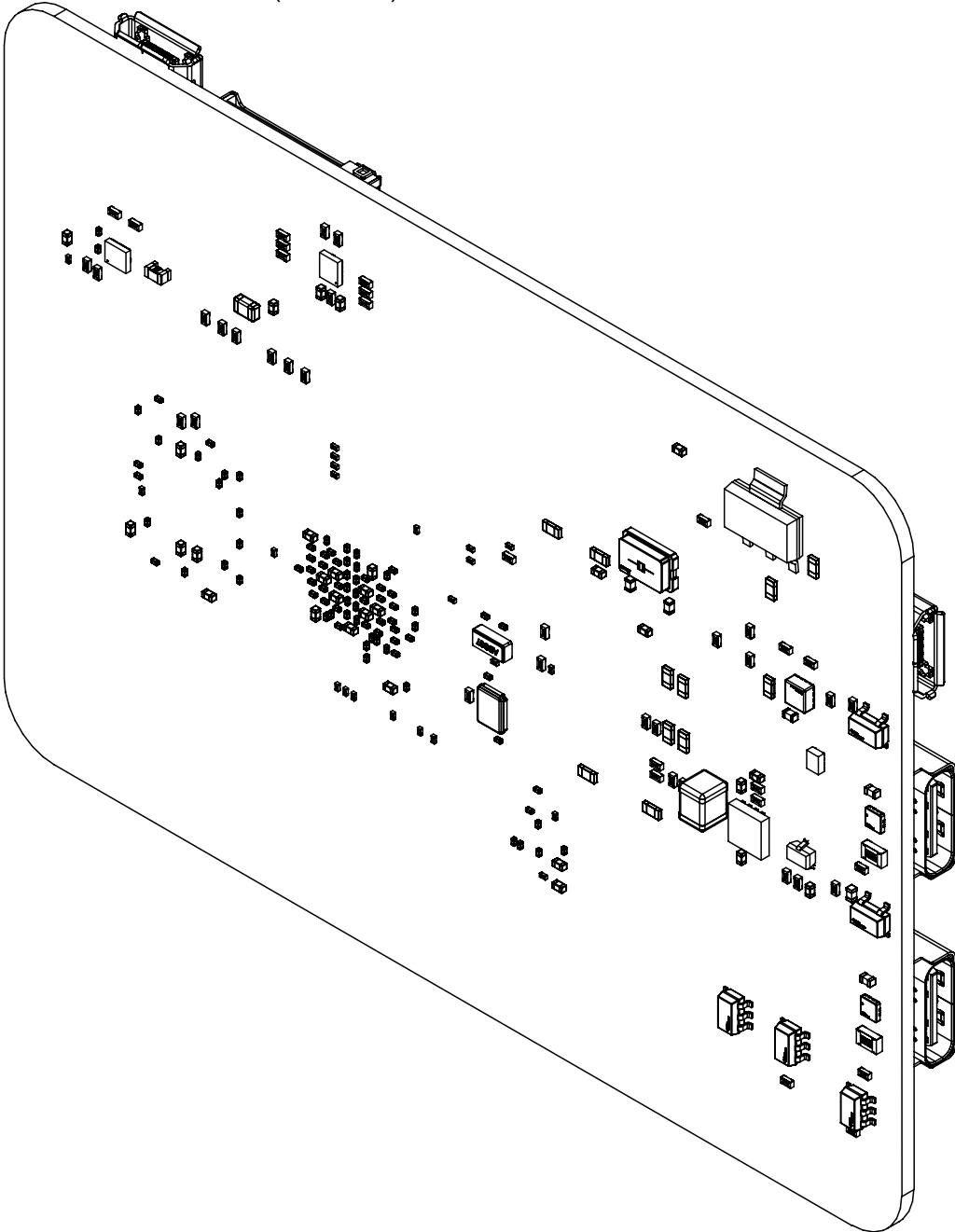
THIS DOCUMENT AND THE DATA DISCLOSED HEREIN OR HEREWITH IS THE PROPERTY OF ALTIUM LIMITED AND MAY BE FREELY DISTRIBUTED IN WHOLE. NO RIGHTS ARE RESERVED OR EXPRESS OR IMPLIED WARANTEE GIVEN.

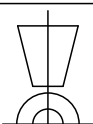
REV STATUS OF SHEETS	REV											REVISIONS				
	SHEET											ZONE	REV	DESCRIPTION	DATE	APPROVED

View from Top side (Scale 2:1)



View from Bottom side (Scale 2:1)



<div>THIRD ANGLE PROJECTION</div> 		PART NO: =PCB_PART_NUMBER		<div>Altium™</div>		ADDRESS 1		
		APPROVALS				ADDRESS 2		
		ENGINEER: =PCB_ENGINEER	=PCB_ENGINEER			ADDRESS 3		
		DESIGNER: =PCB_DESIGNER	=PCB_DESIGNER			ADDRESS 4		
		CHECKER: =PCB_CHECKER	=PCB_CHECKER	DESIGN ITEM: .Item		DESIGN ITEM REVISION: .ItemRevision		
		Reference Documents		TITLE: =PCB_TITLE_1				
		BOM DOC: =DOC_NO_BOM						
		ASSY DOC: =DOC_NO_FAB_DWG						
		SCH DOC: =DOC_NO_SCH_DWG		SIZE: B		CAGE CODE: =CAGE_CO	DWG NO:	REV:
NEXT ASSY		USED ON	PCB DOC: =PCB_DWG_NO		SCALE:		FILE NAME: Dashcam CPU_V2.0E.PCBdwf	SHEET: 5 OF 5
APPLICATION								