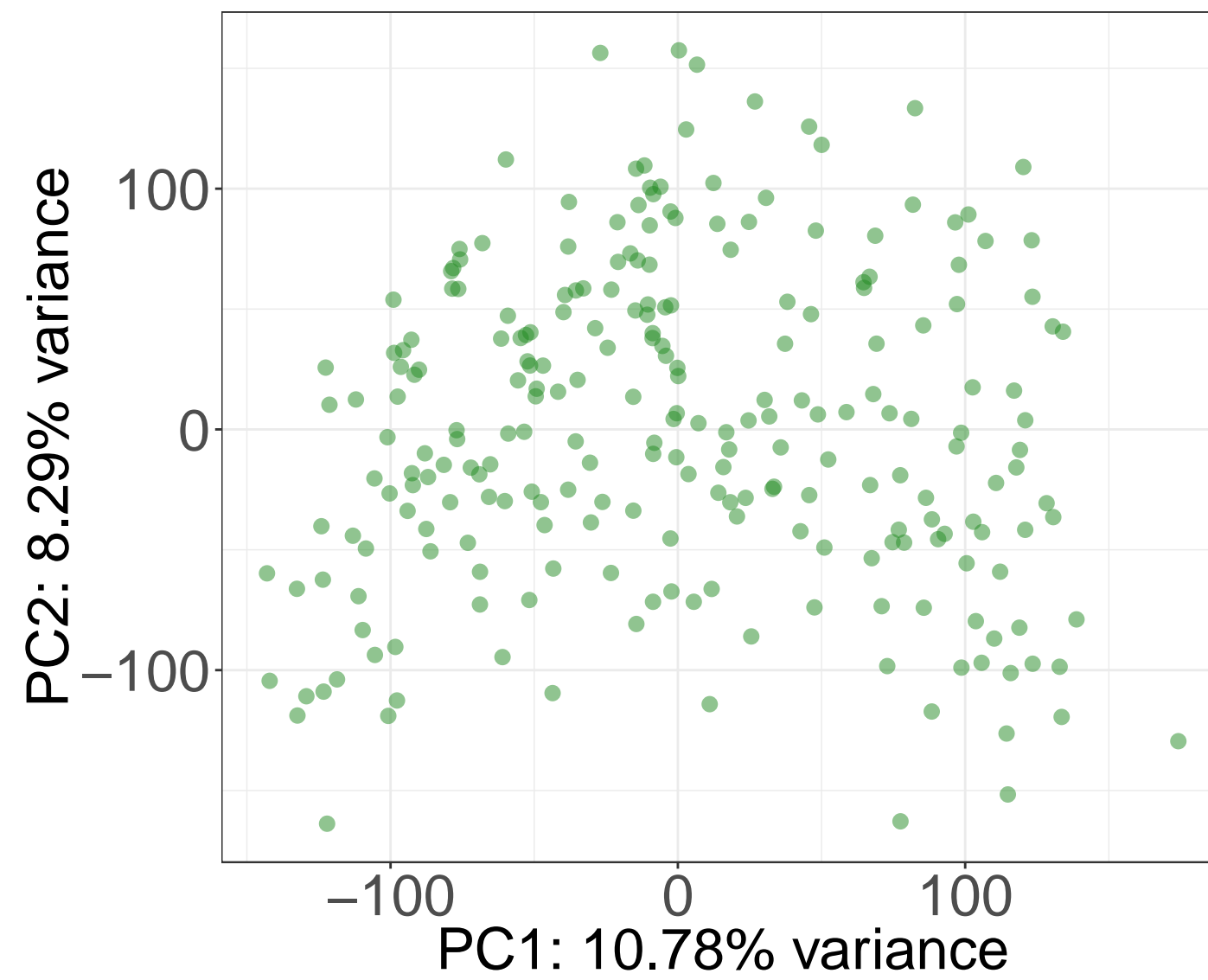
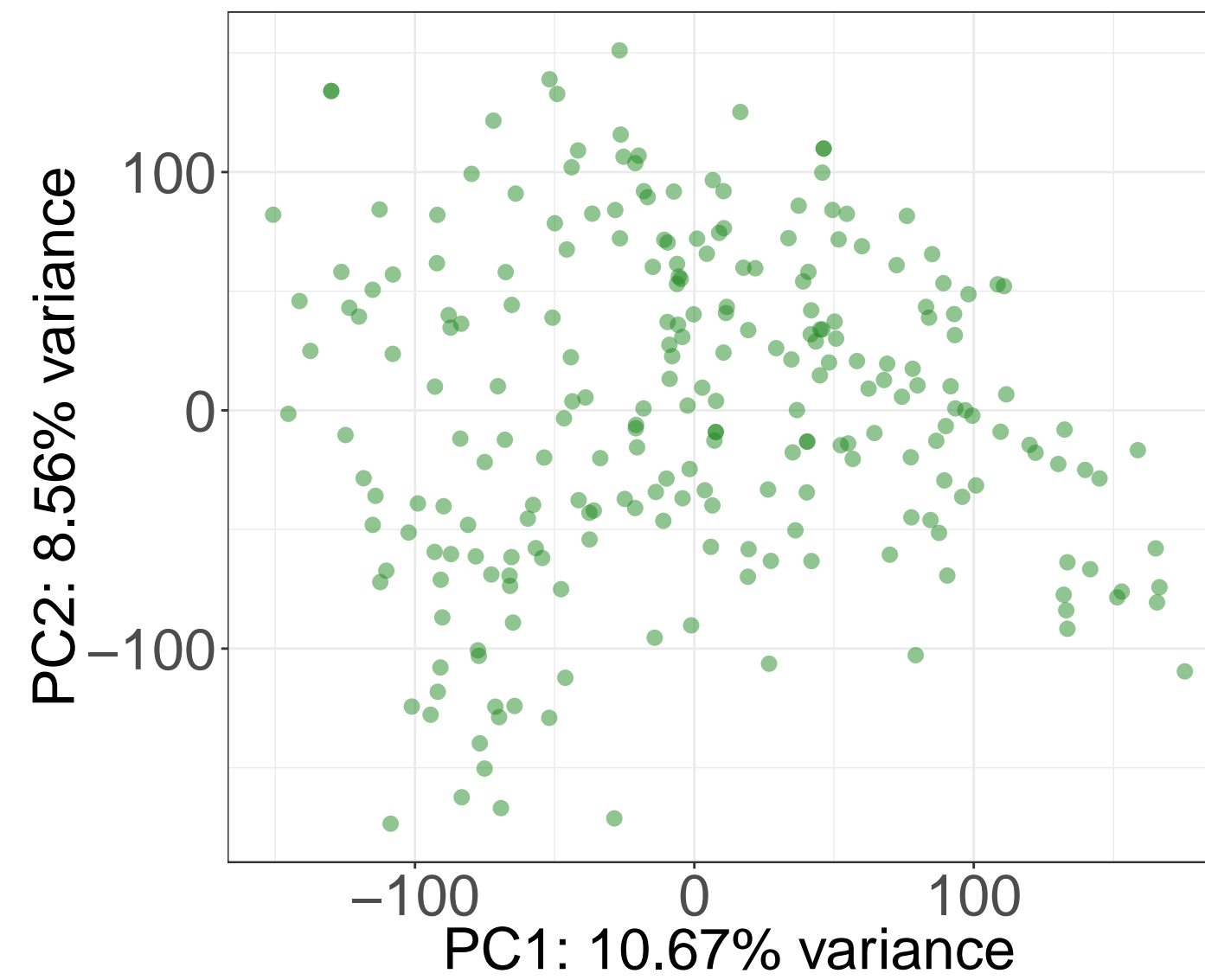
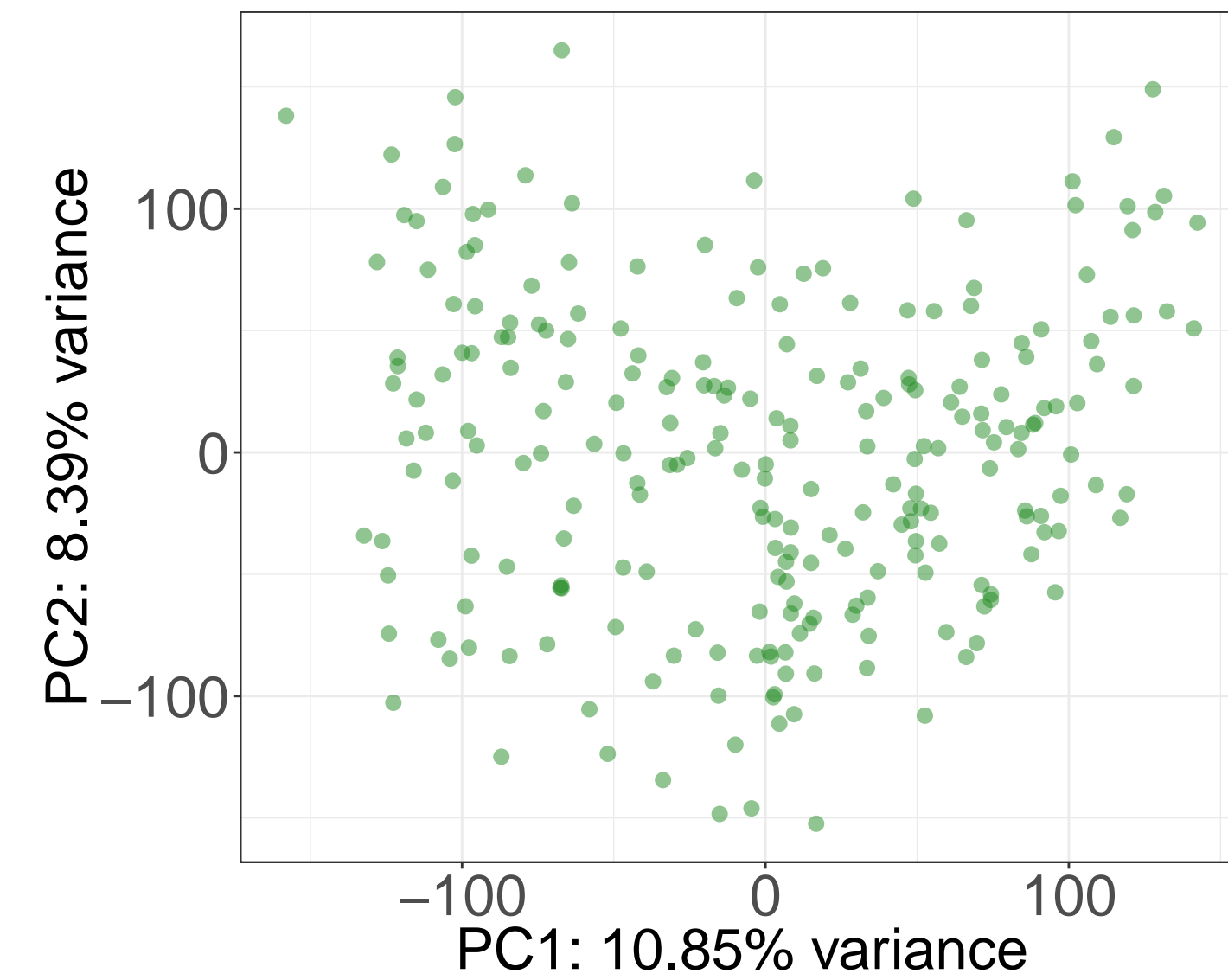
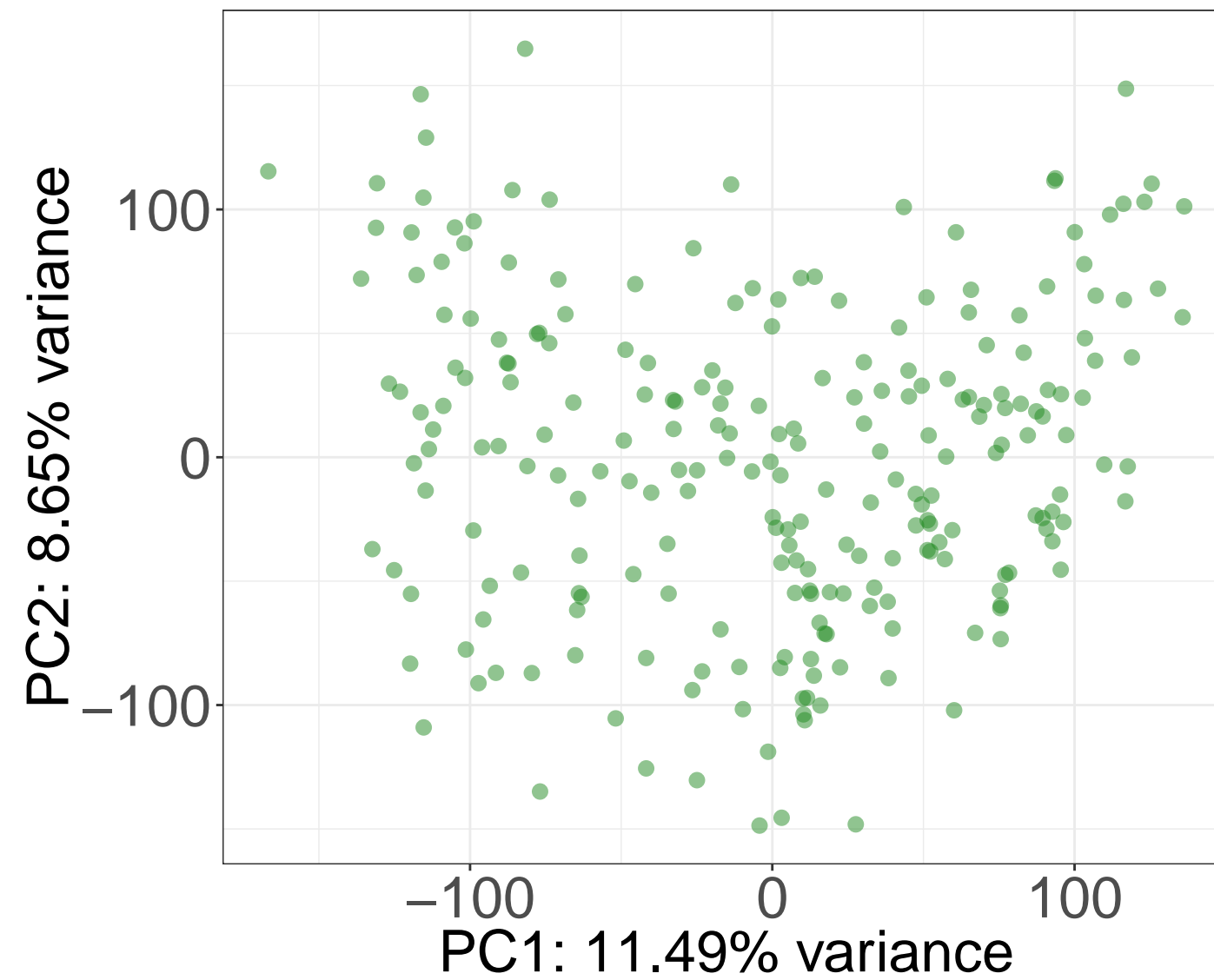
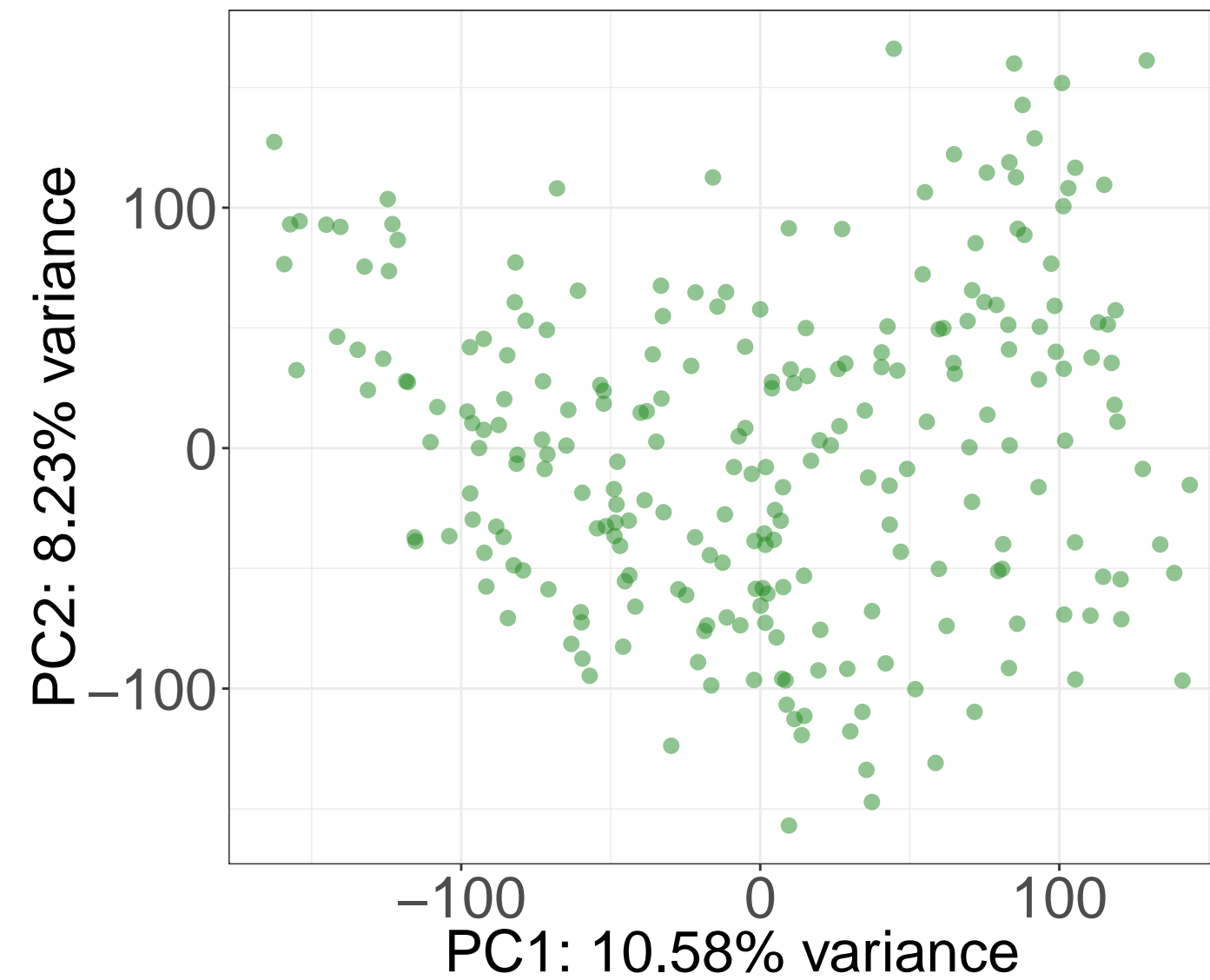
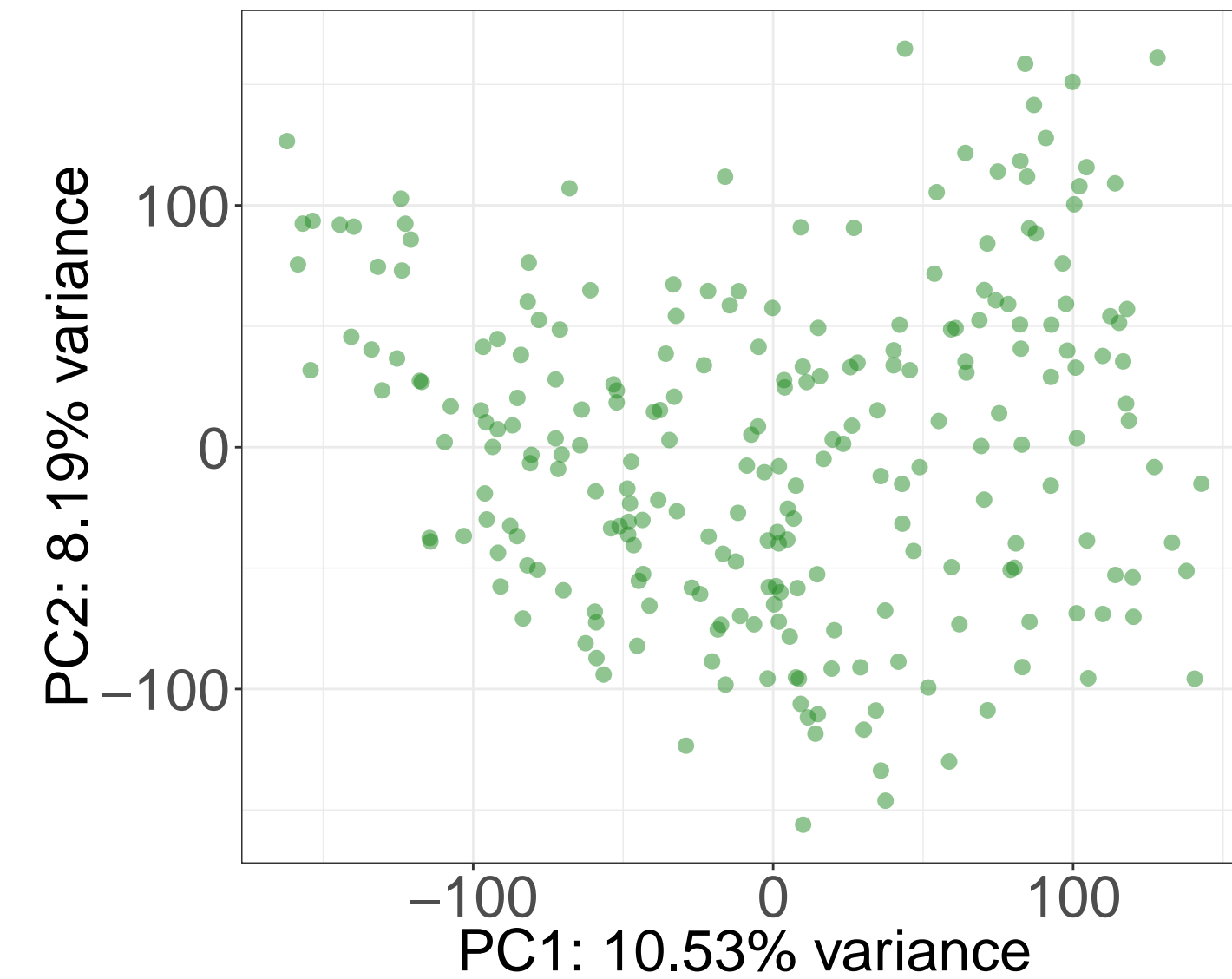
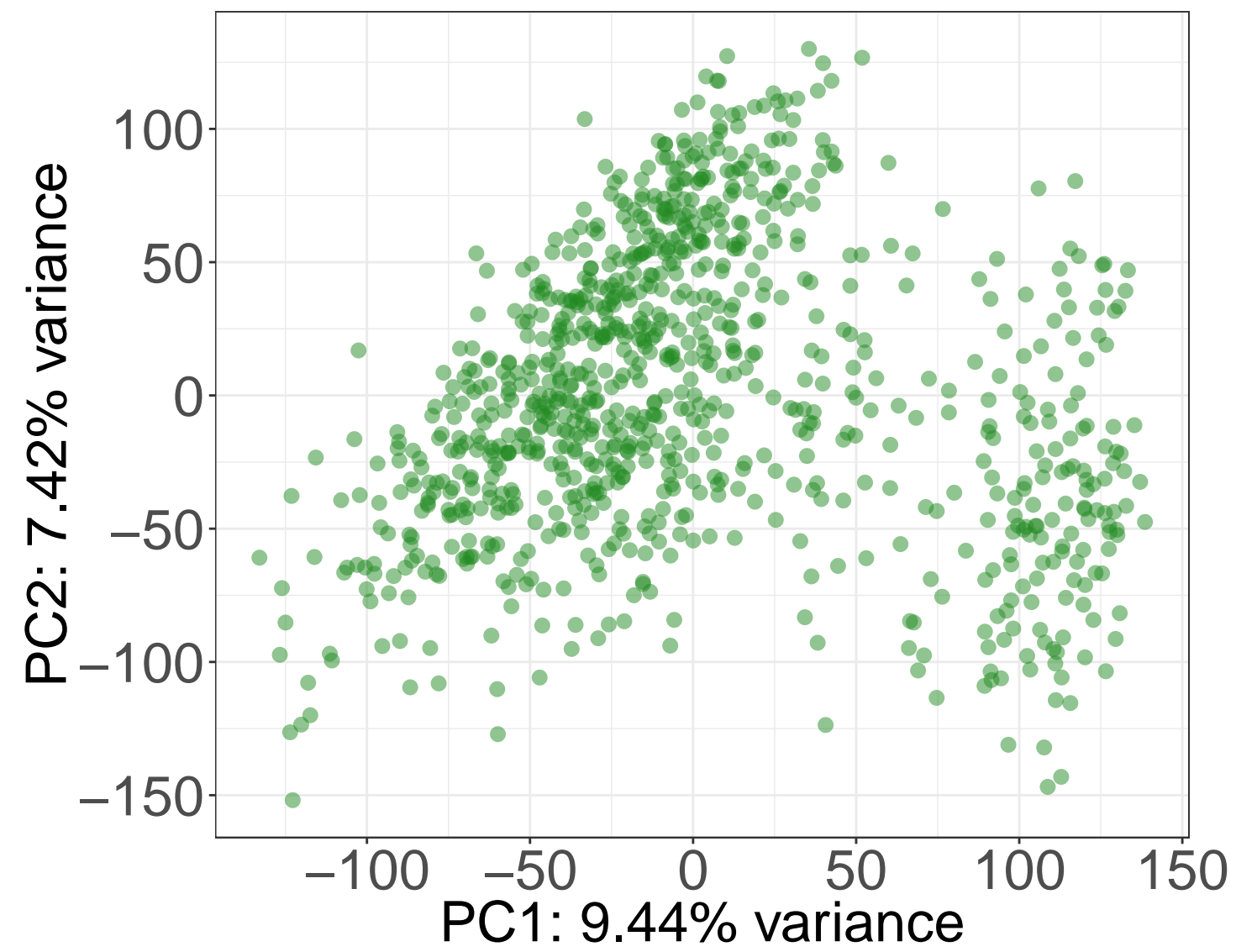
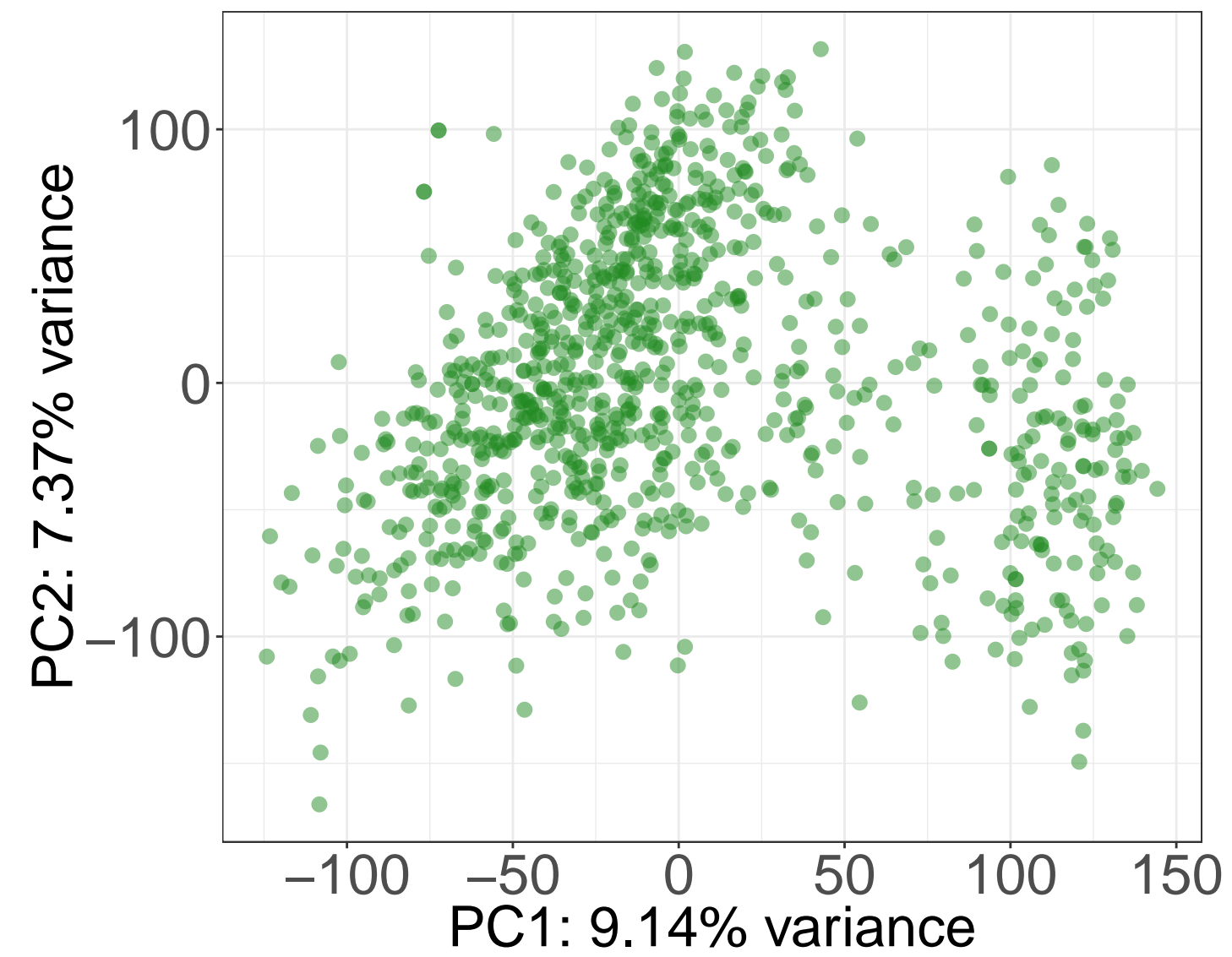
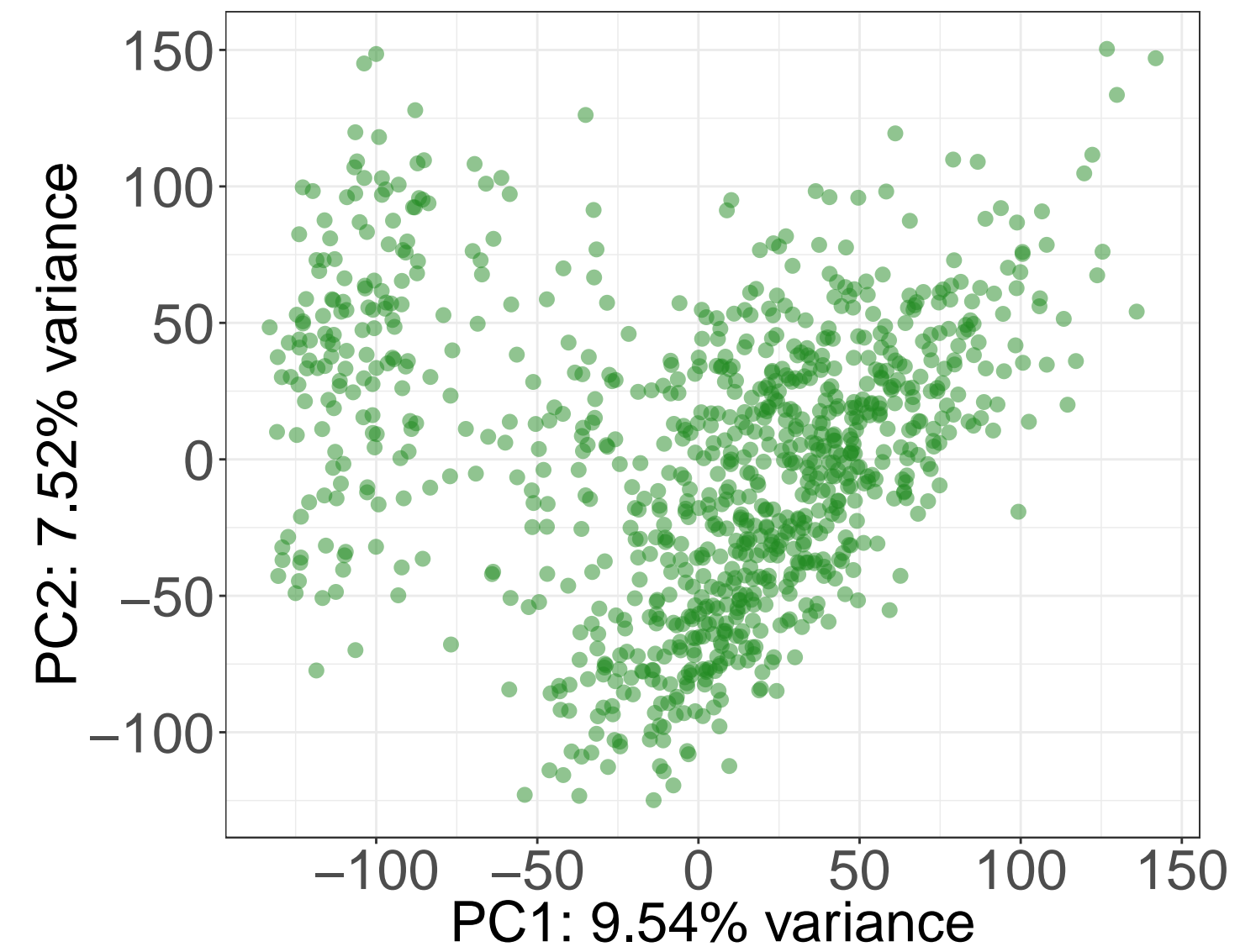
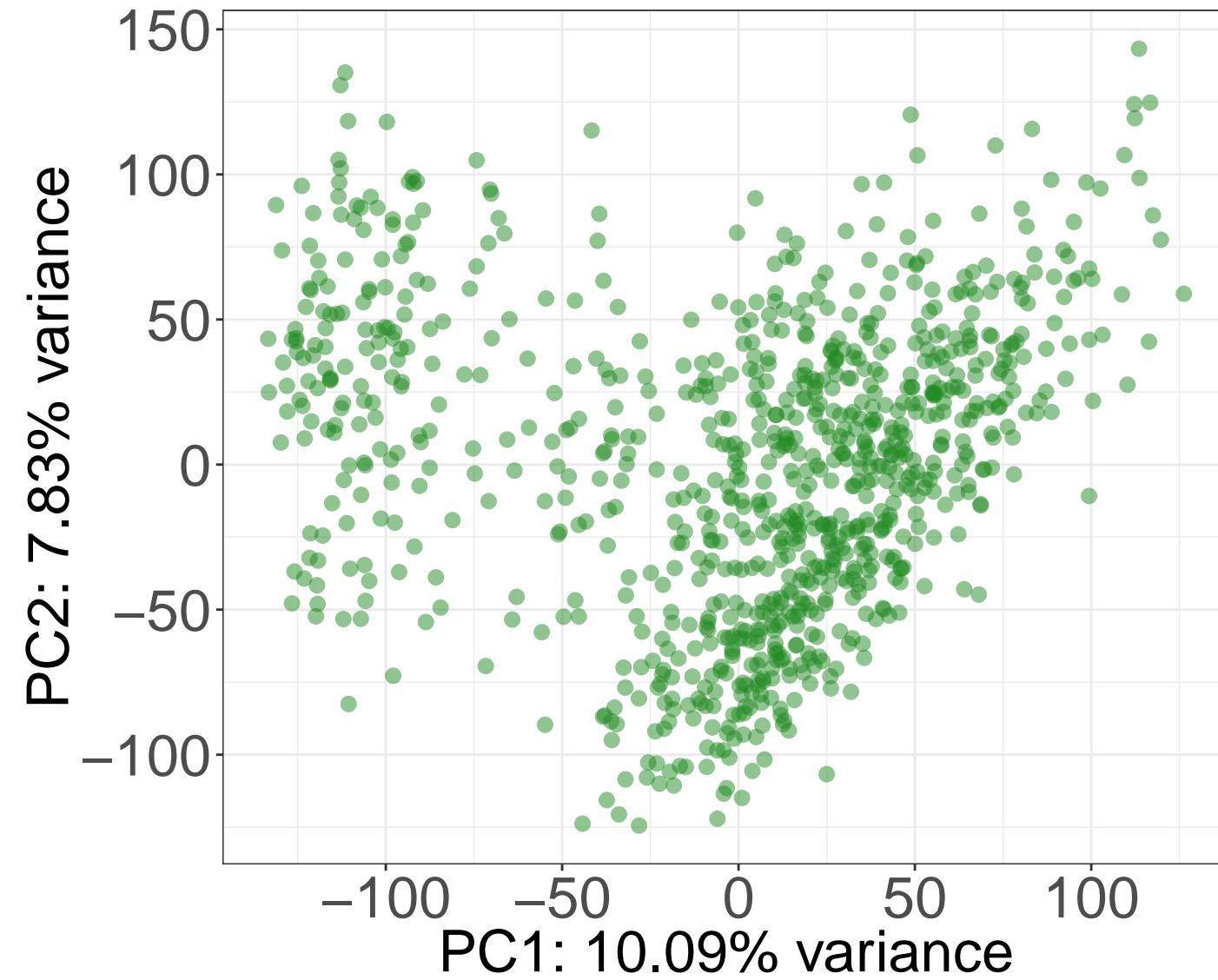
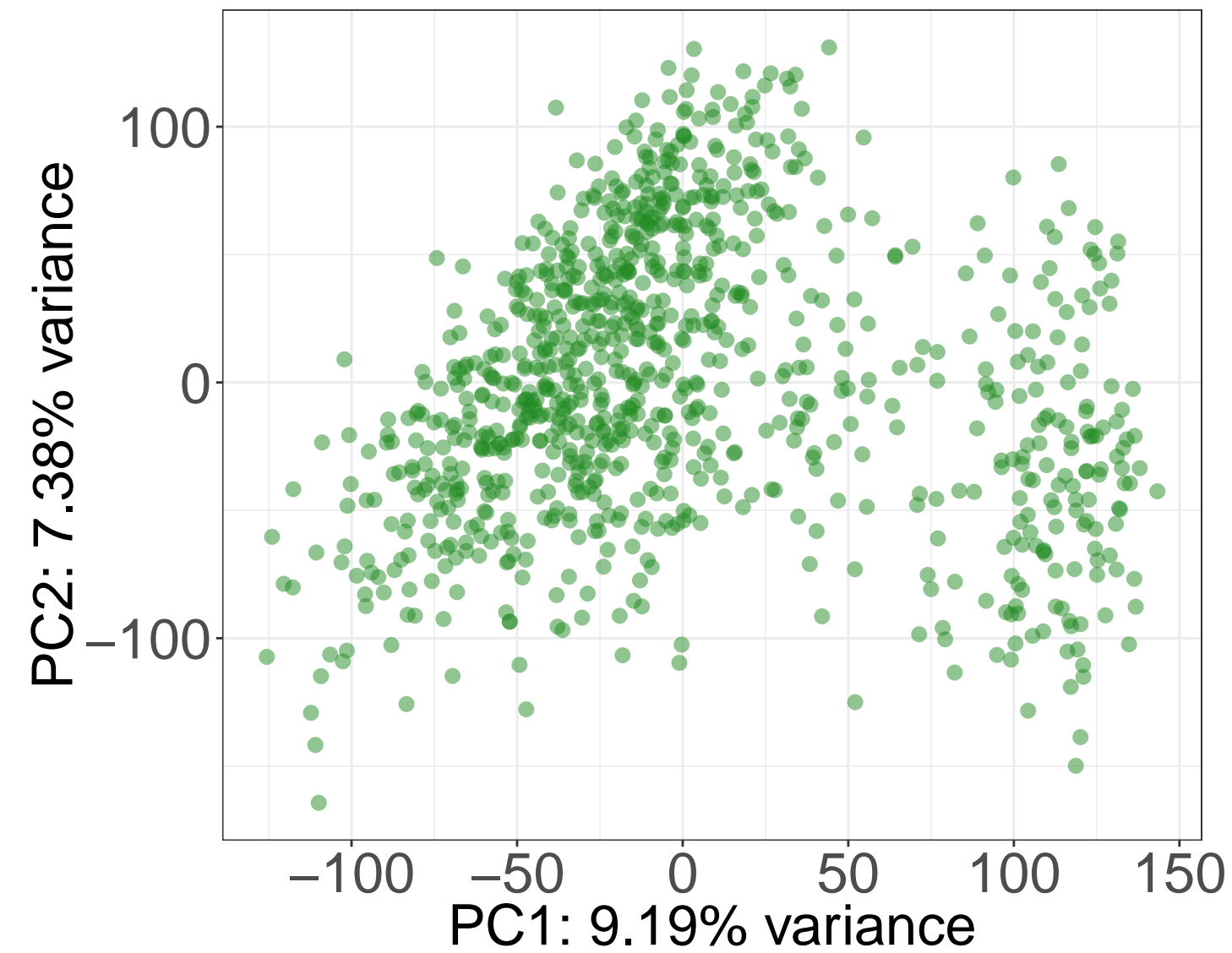
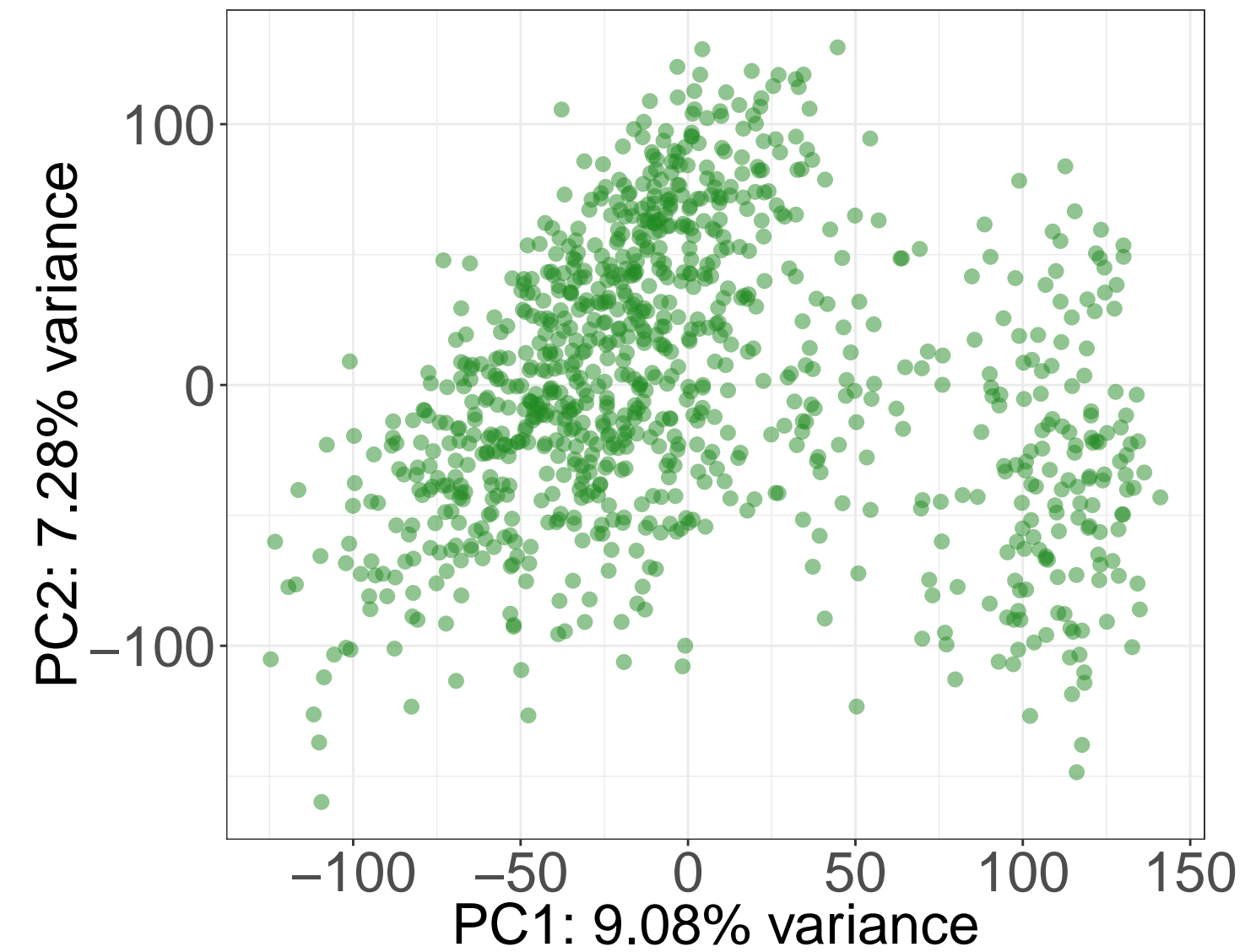
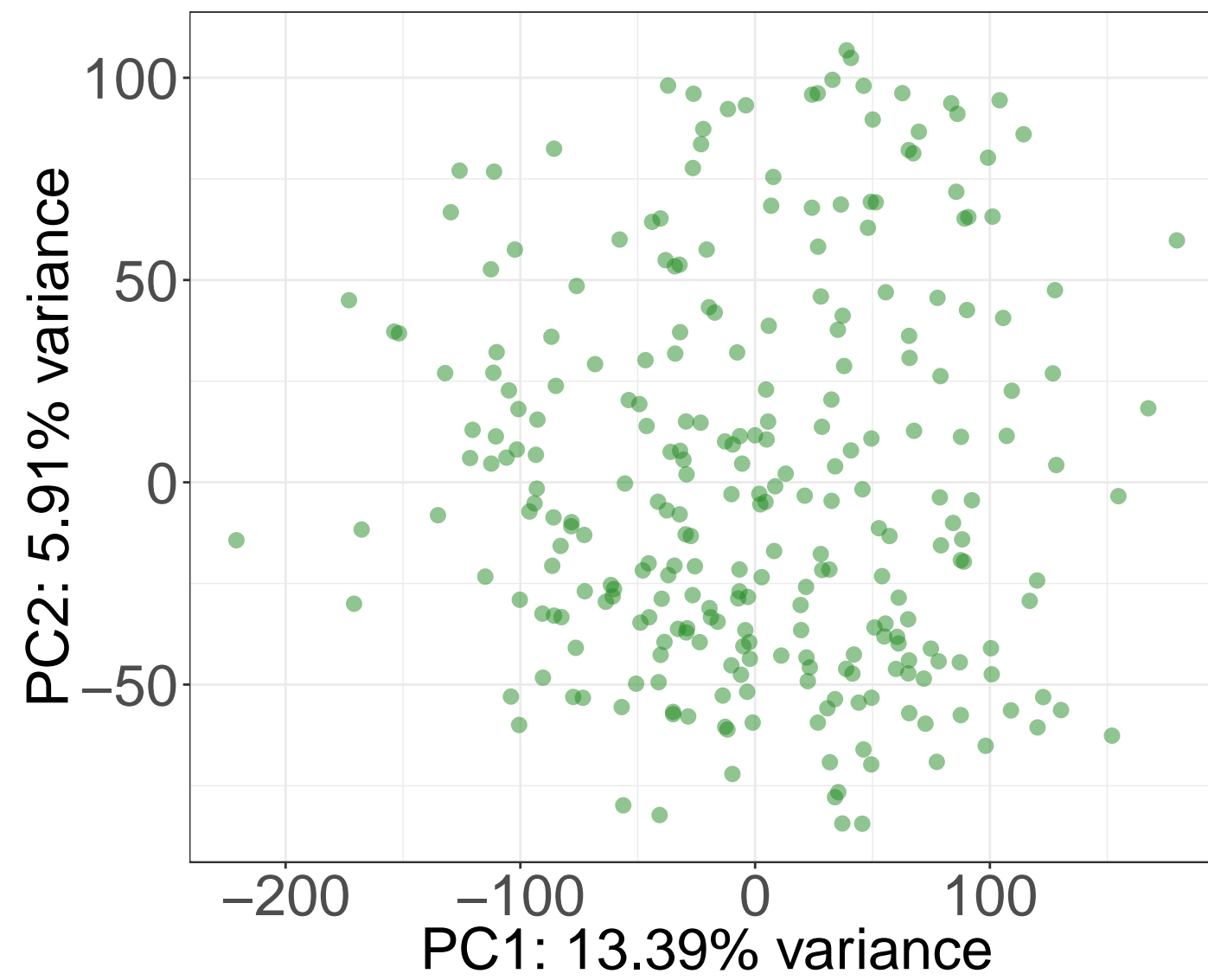
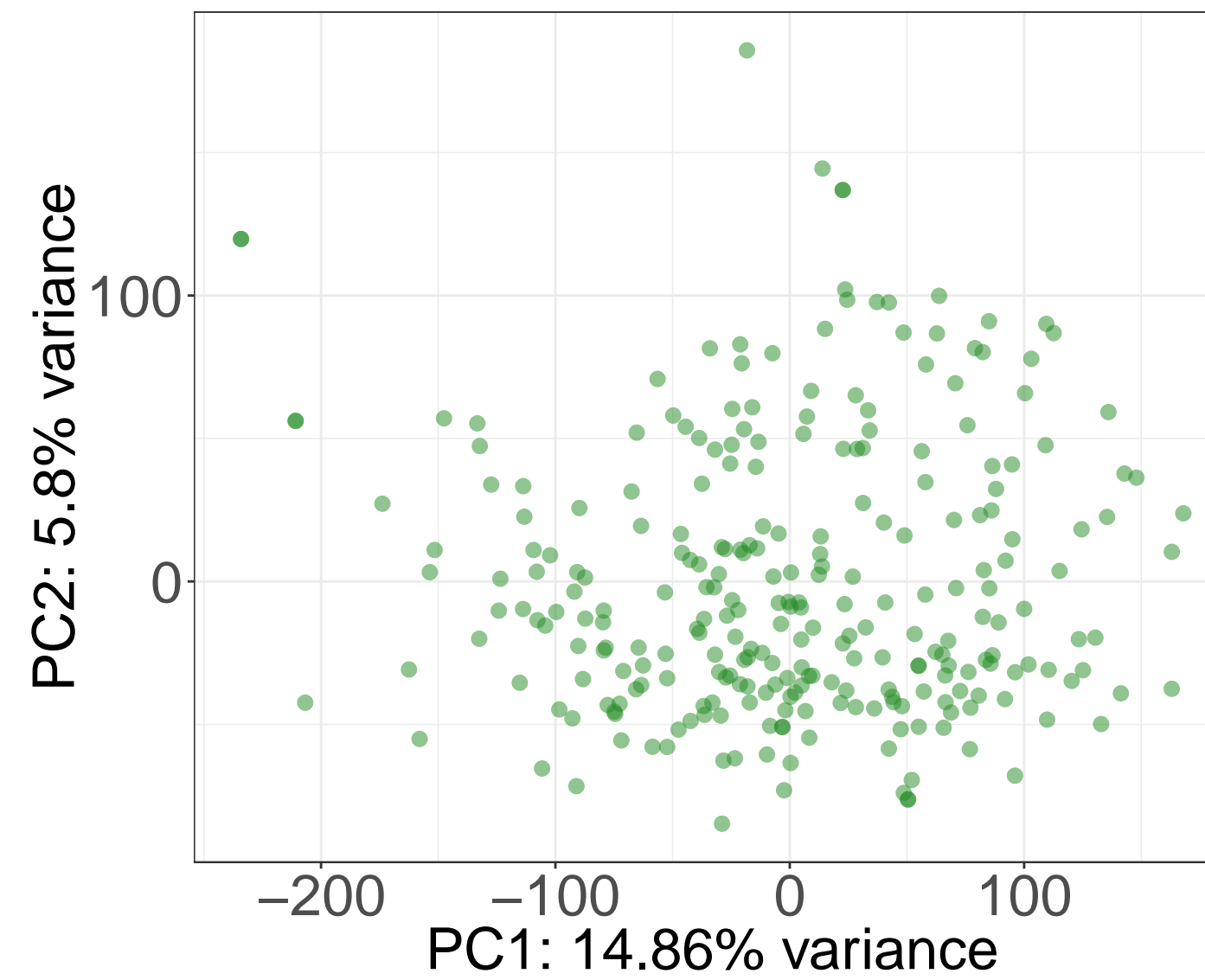
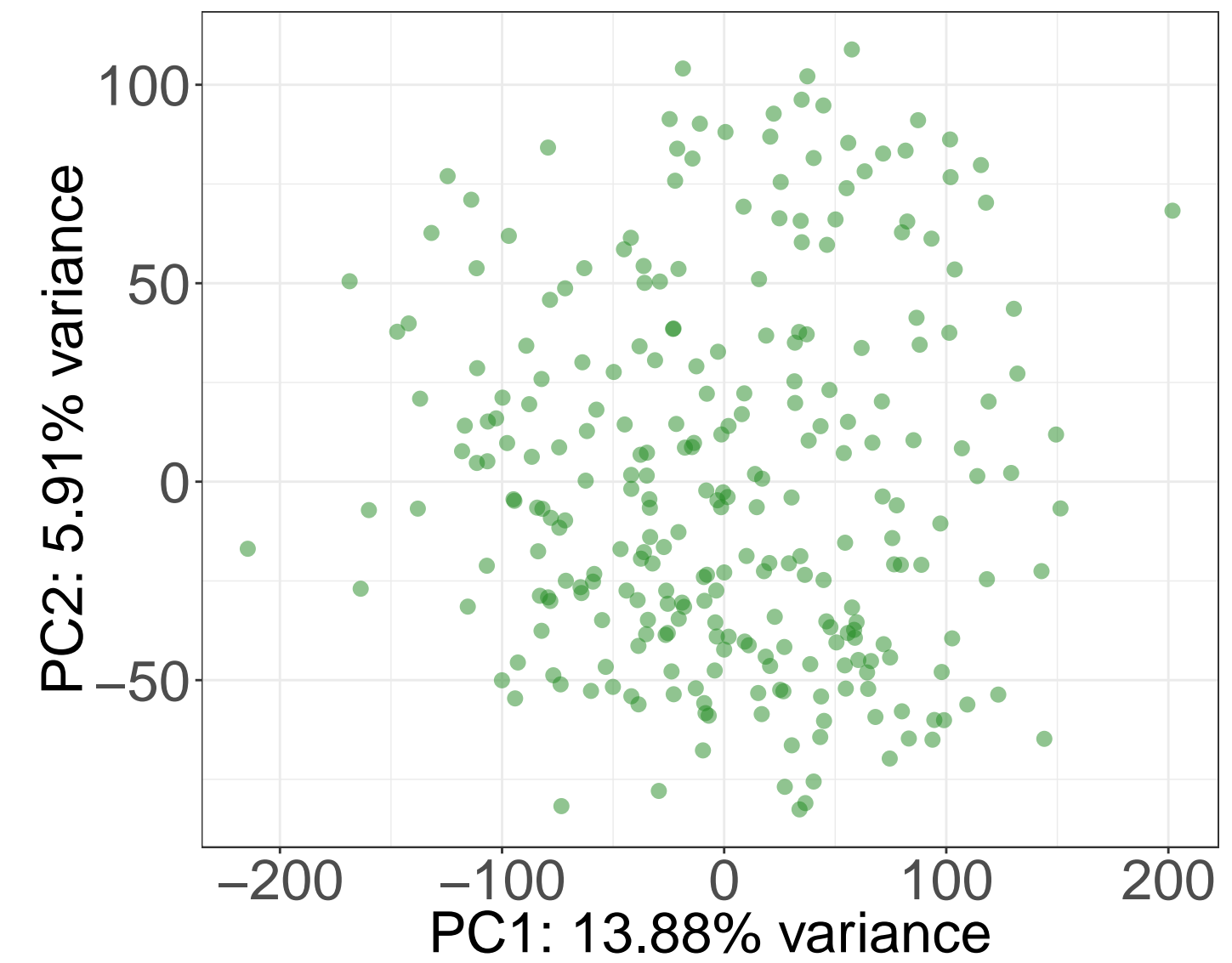
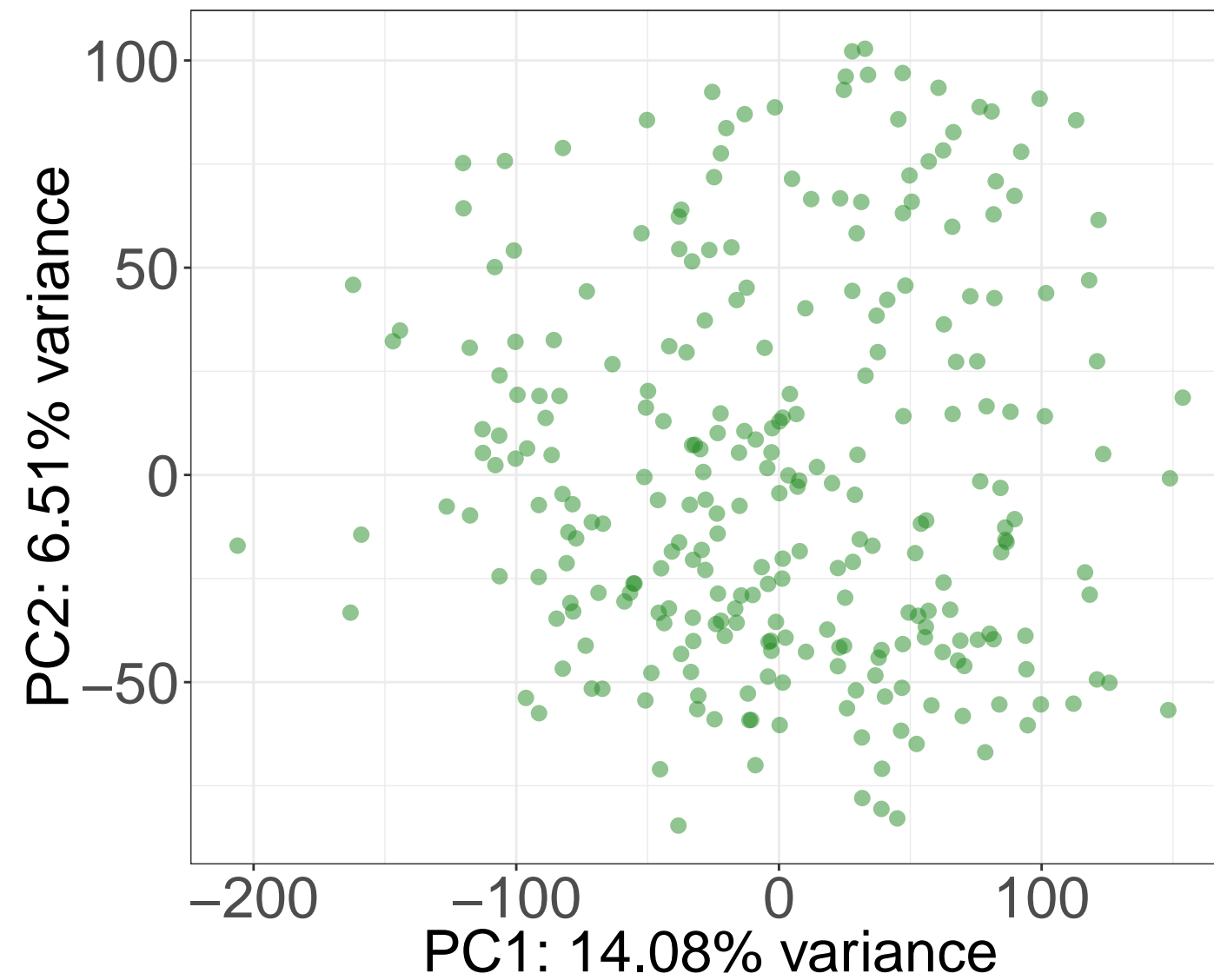
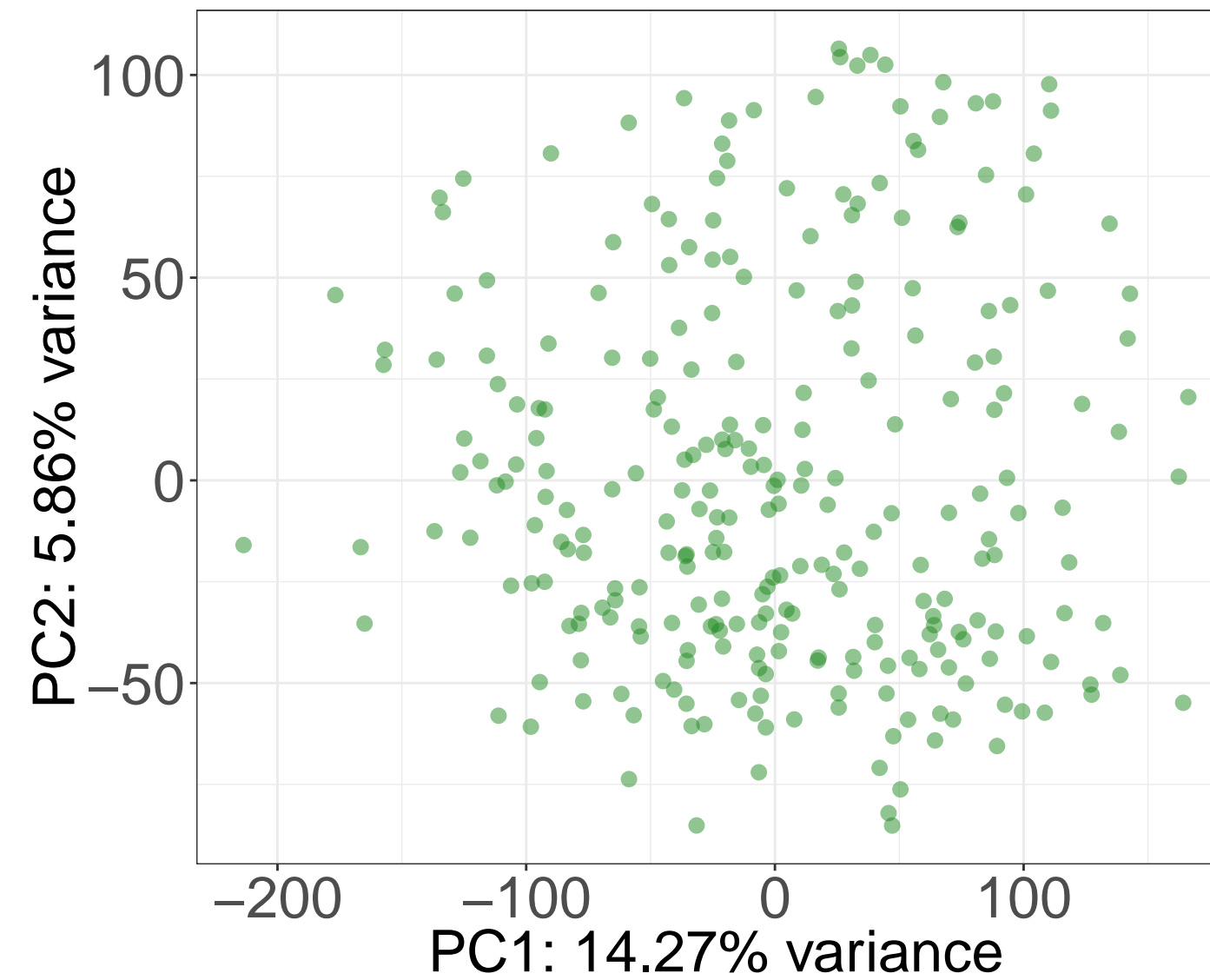
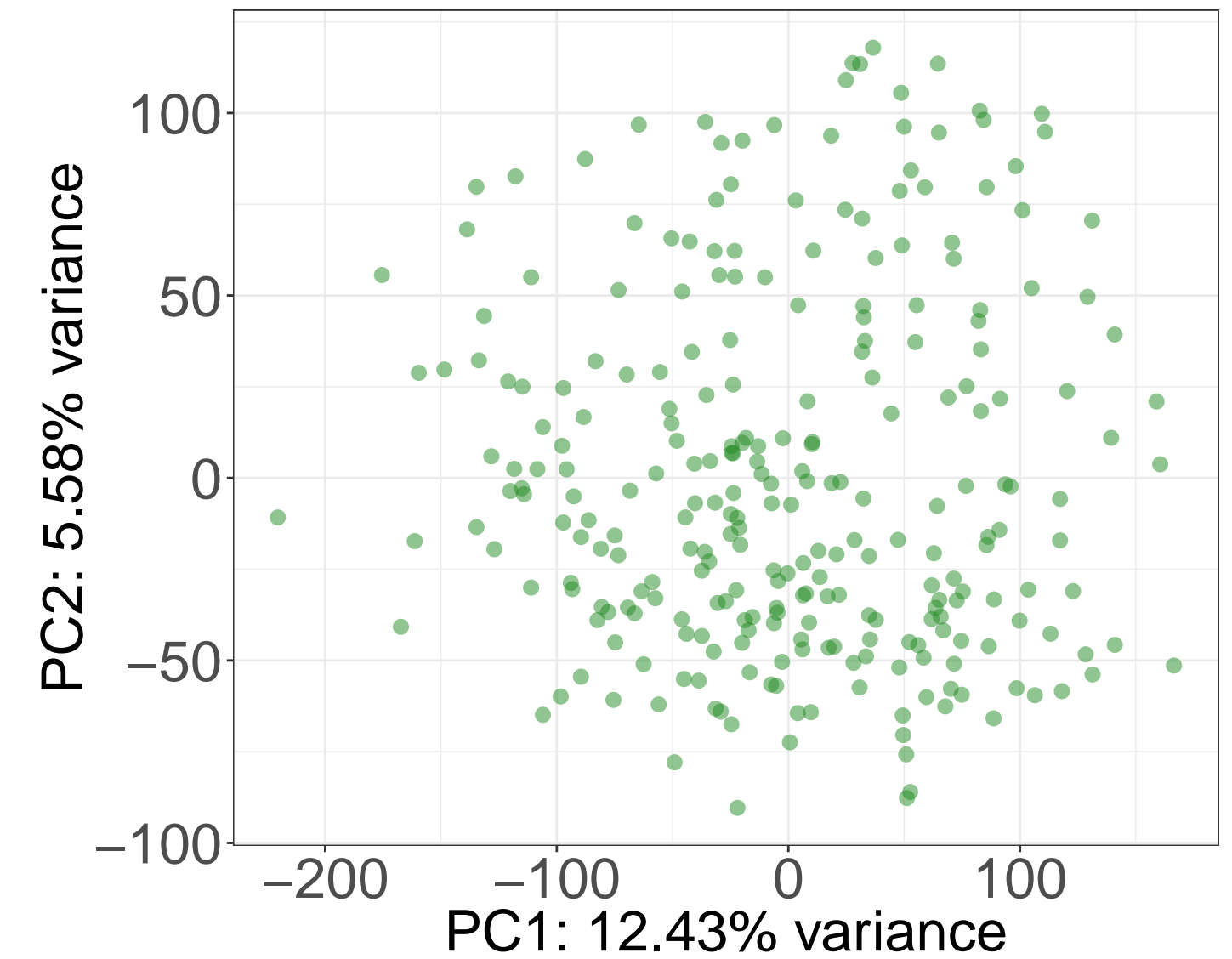
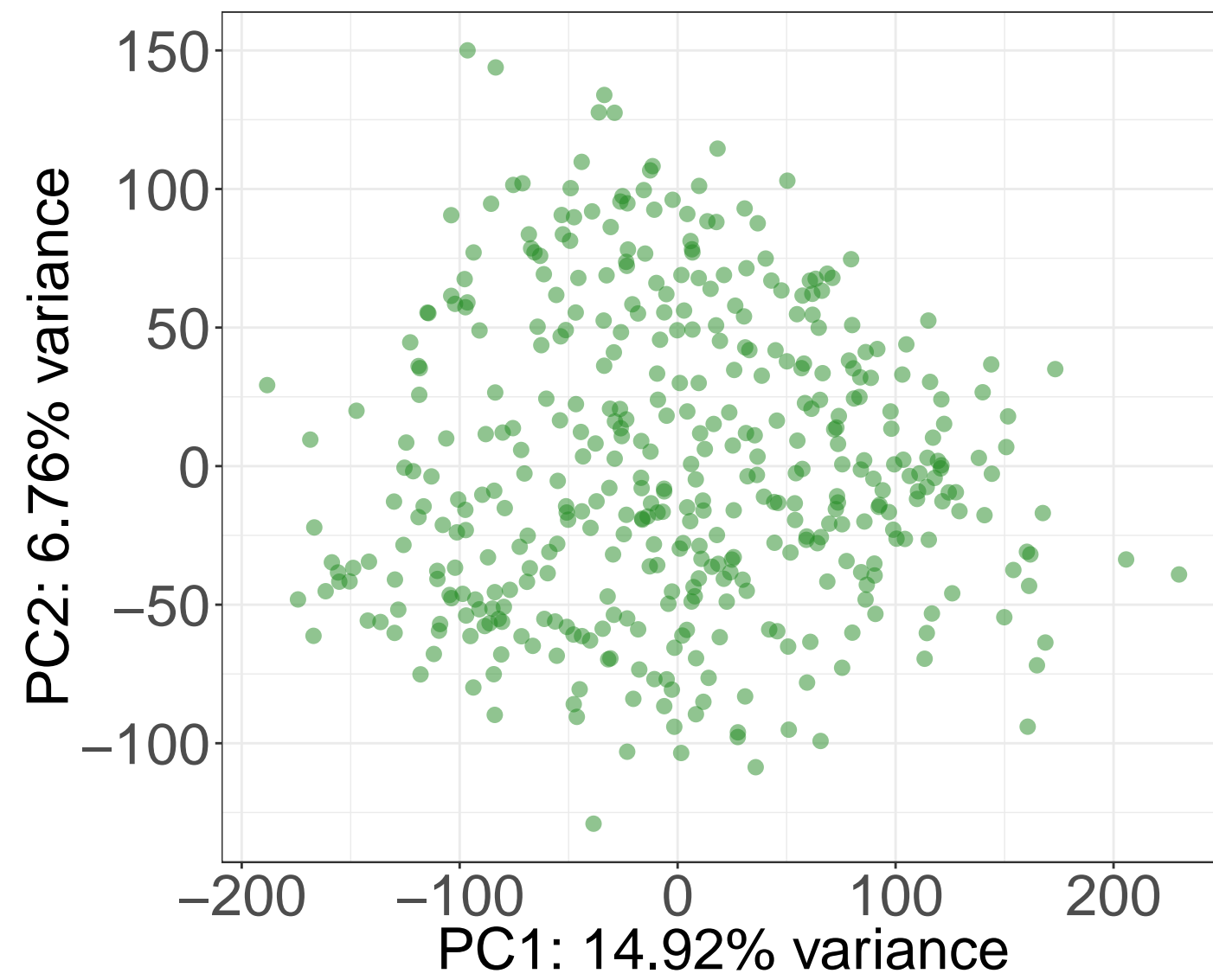
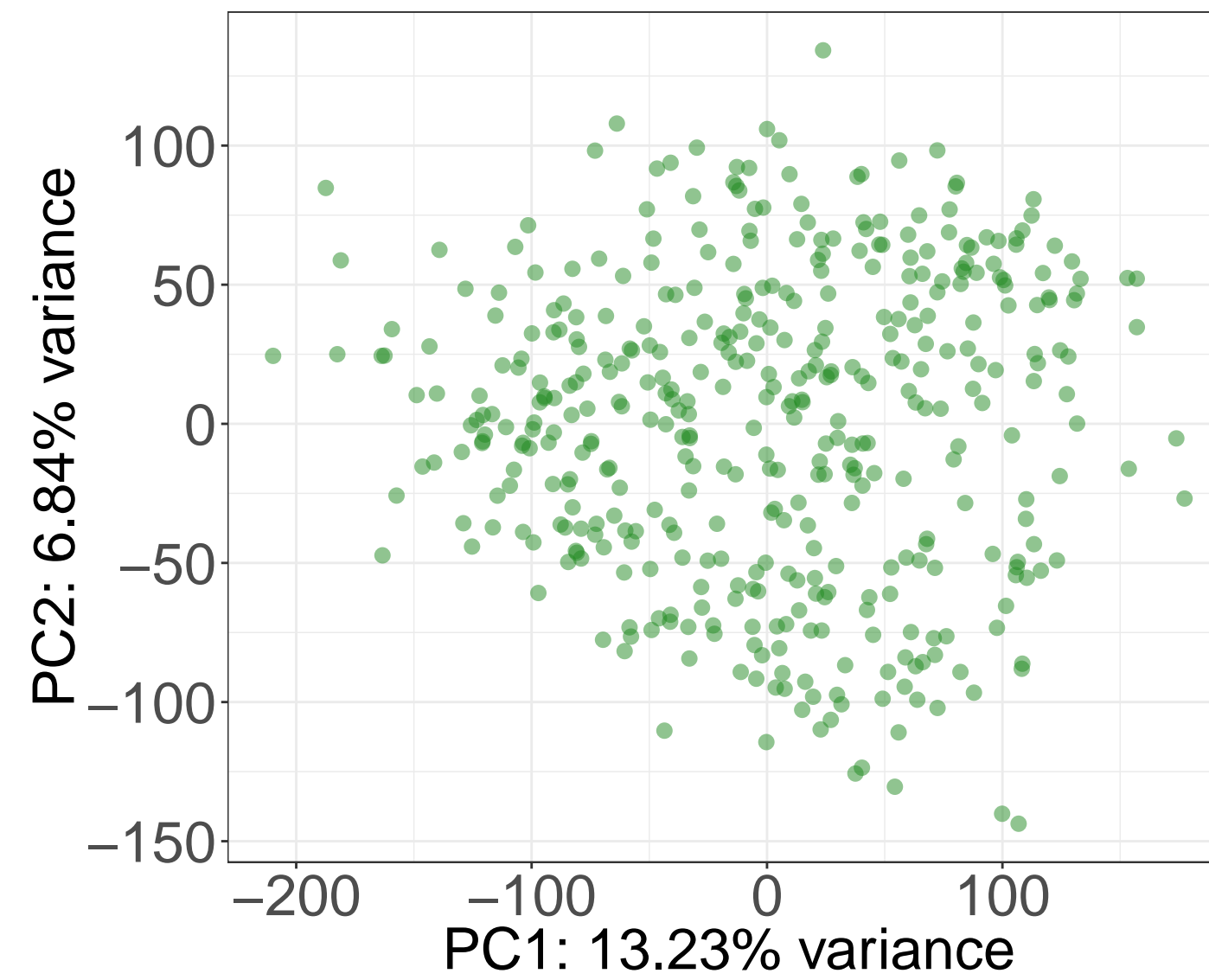
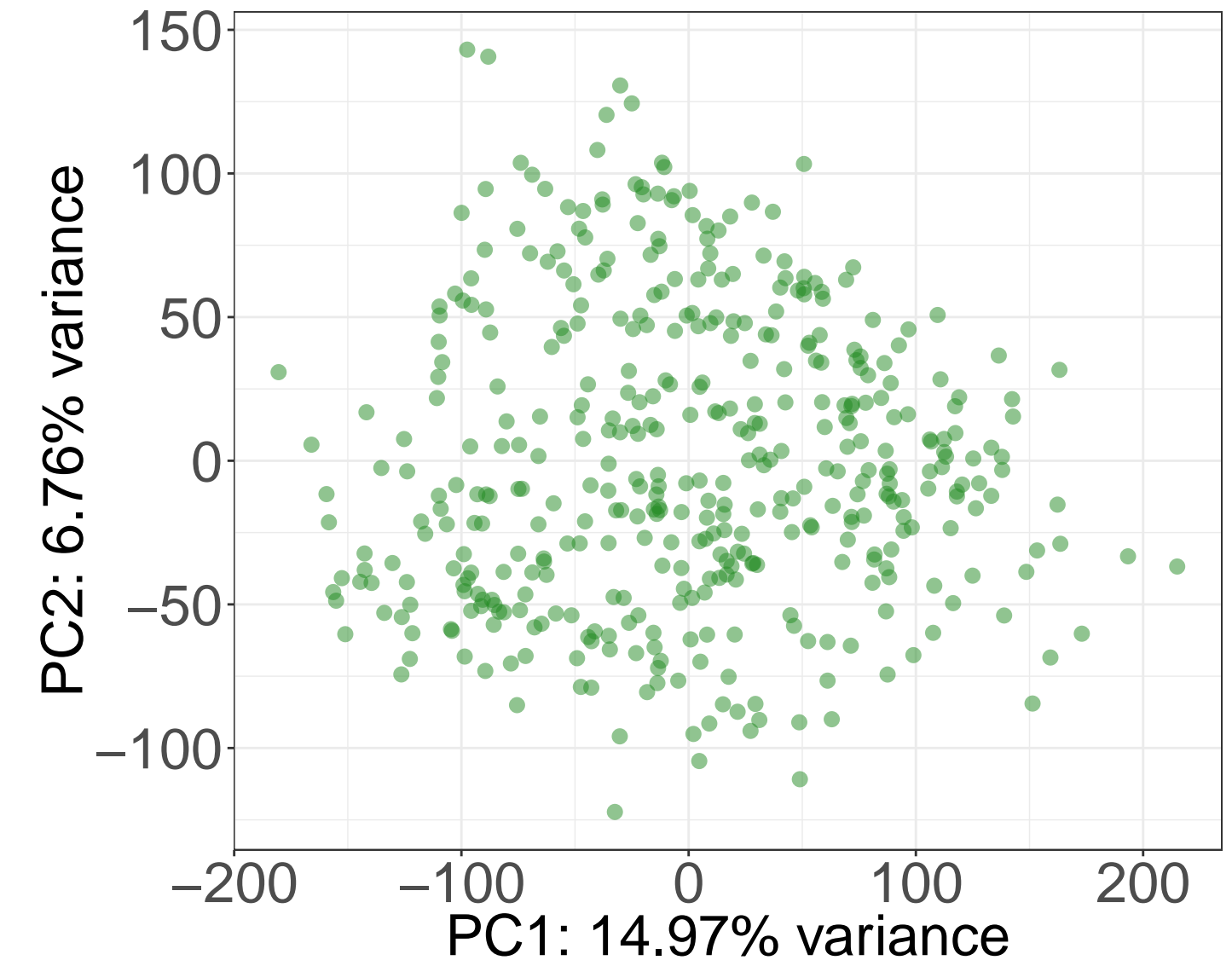
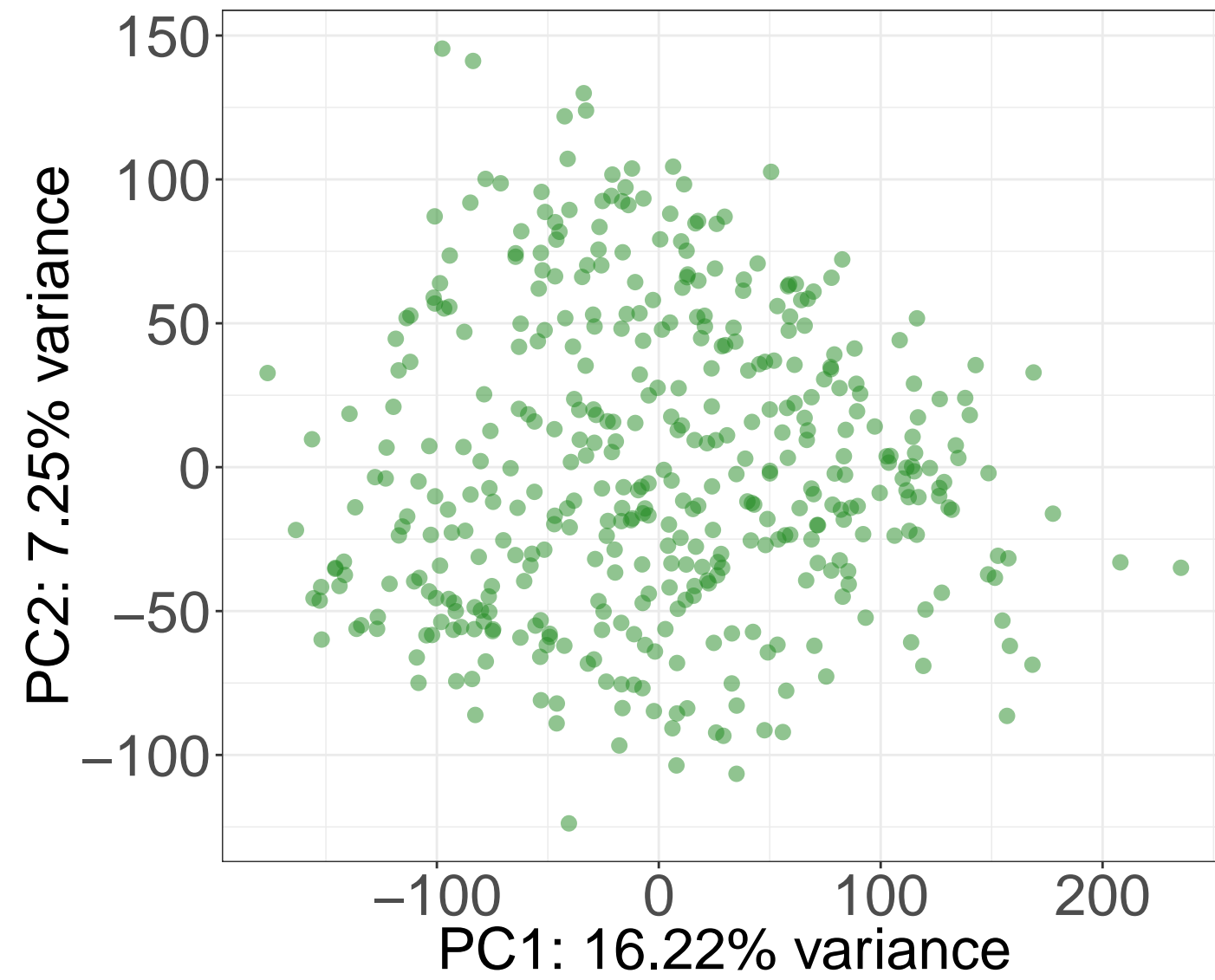
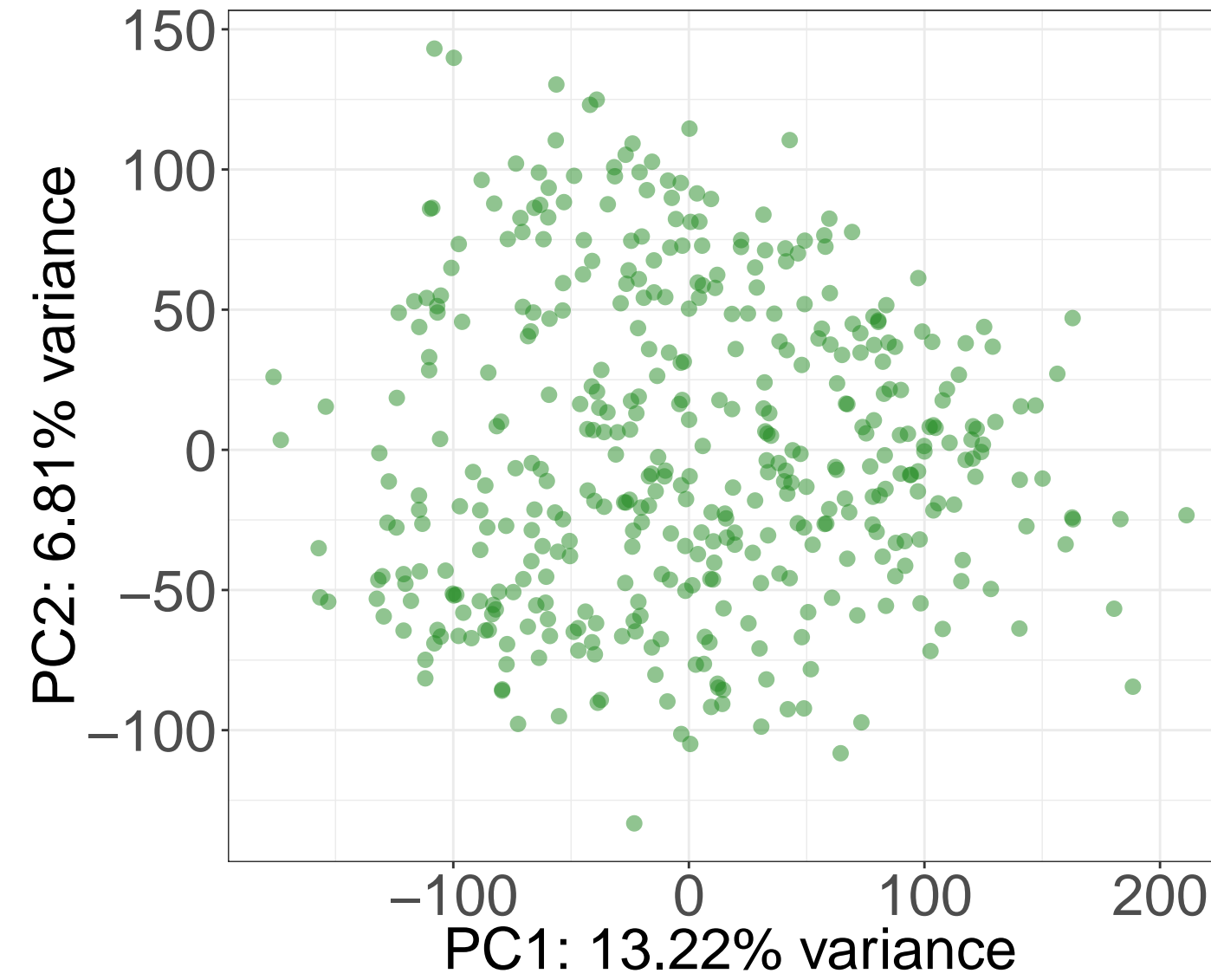
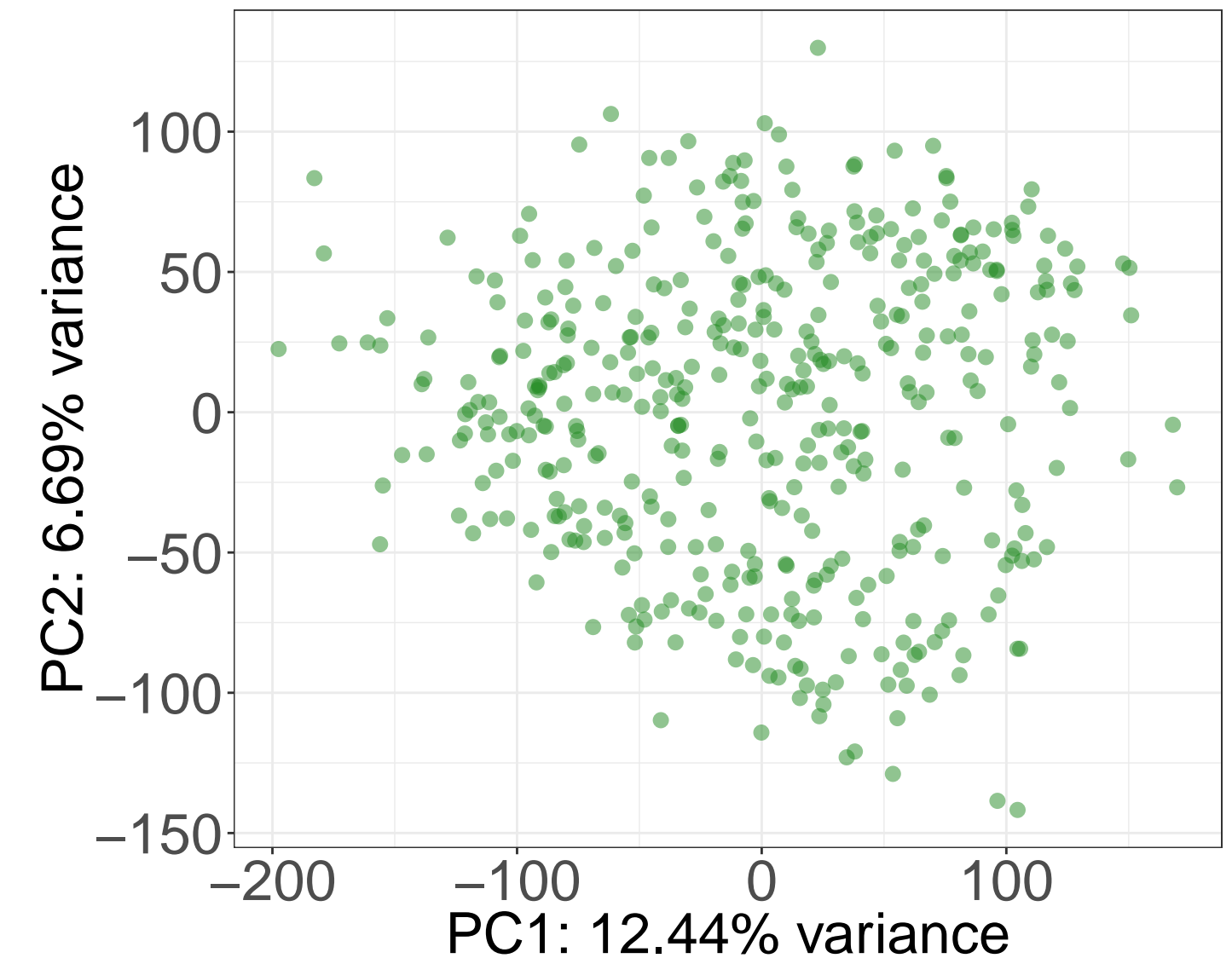
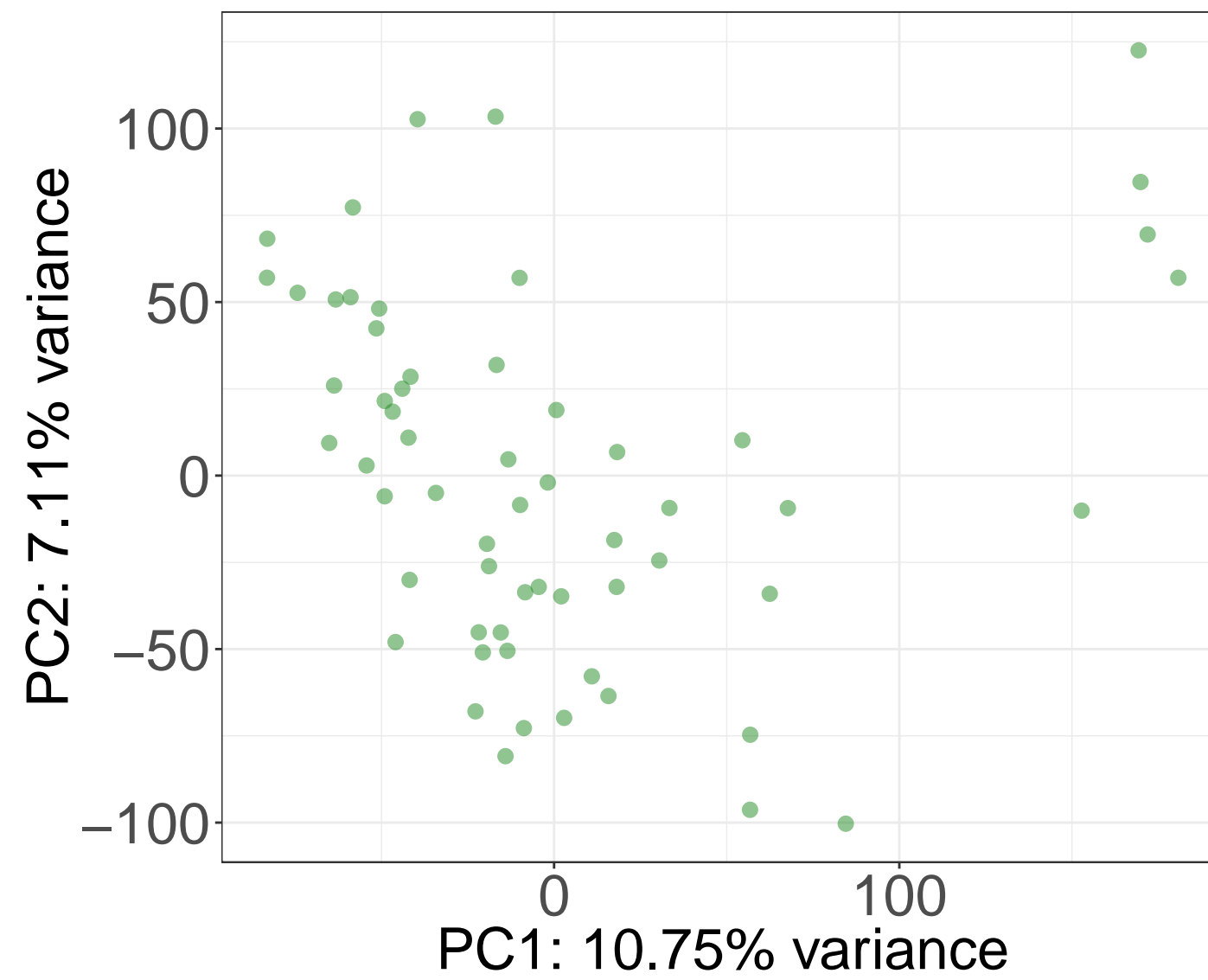
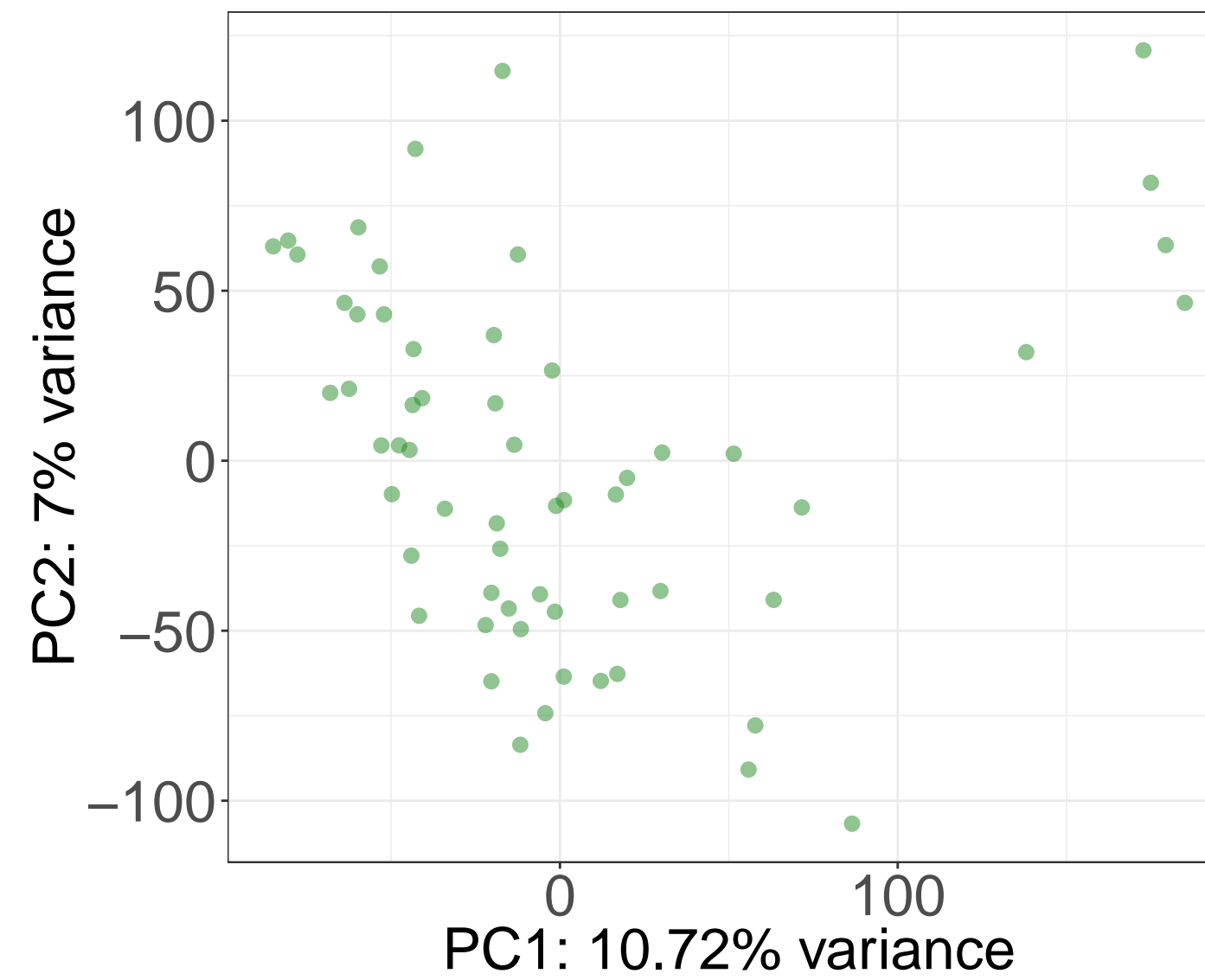
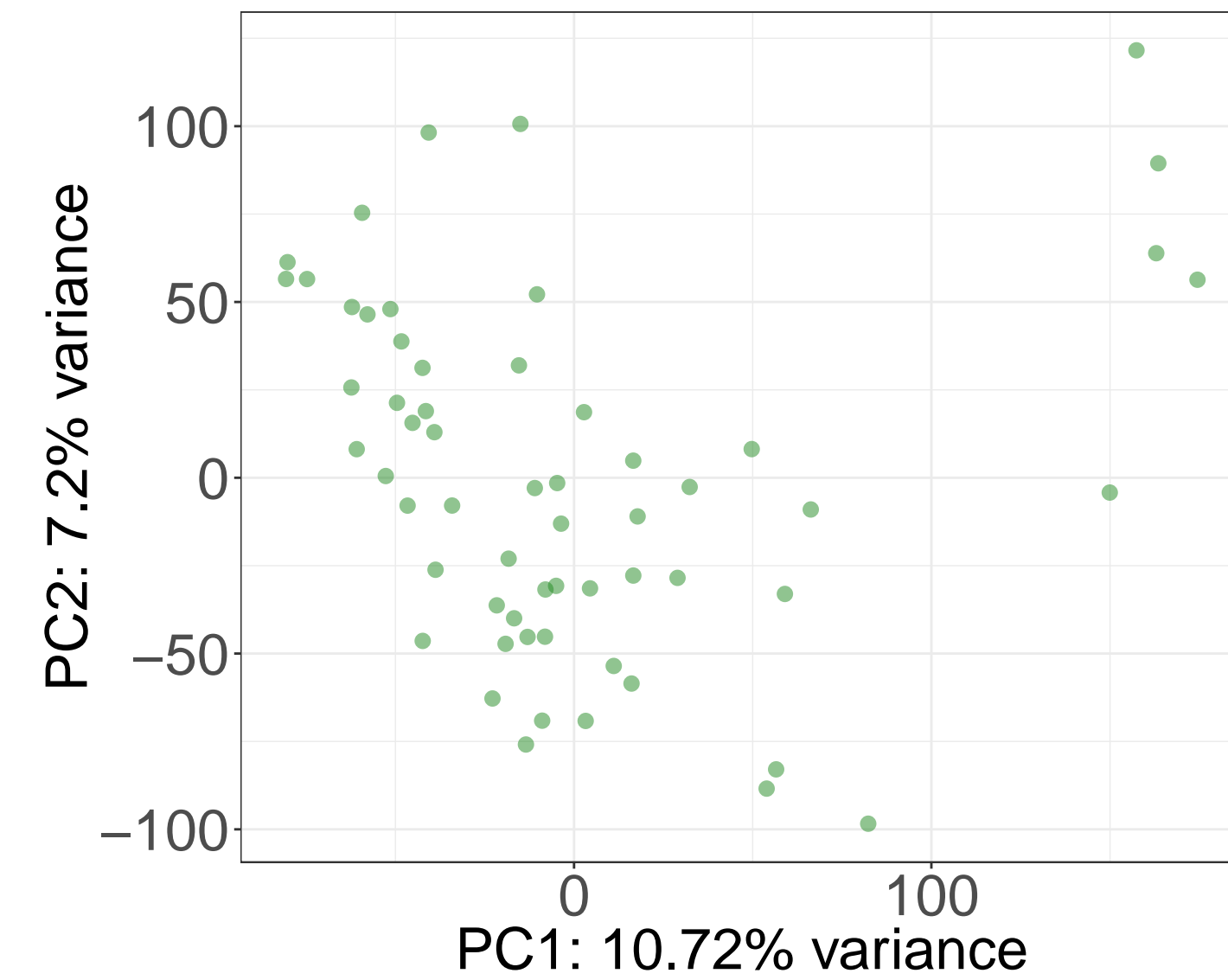
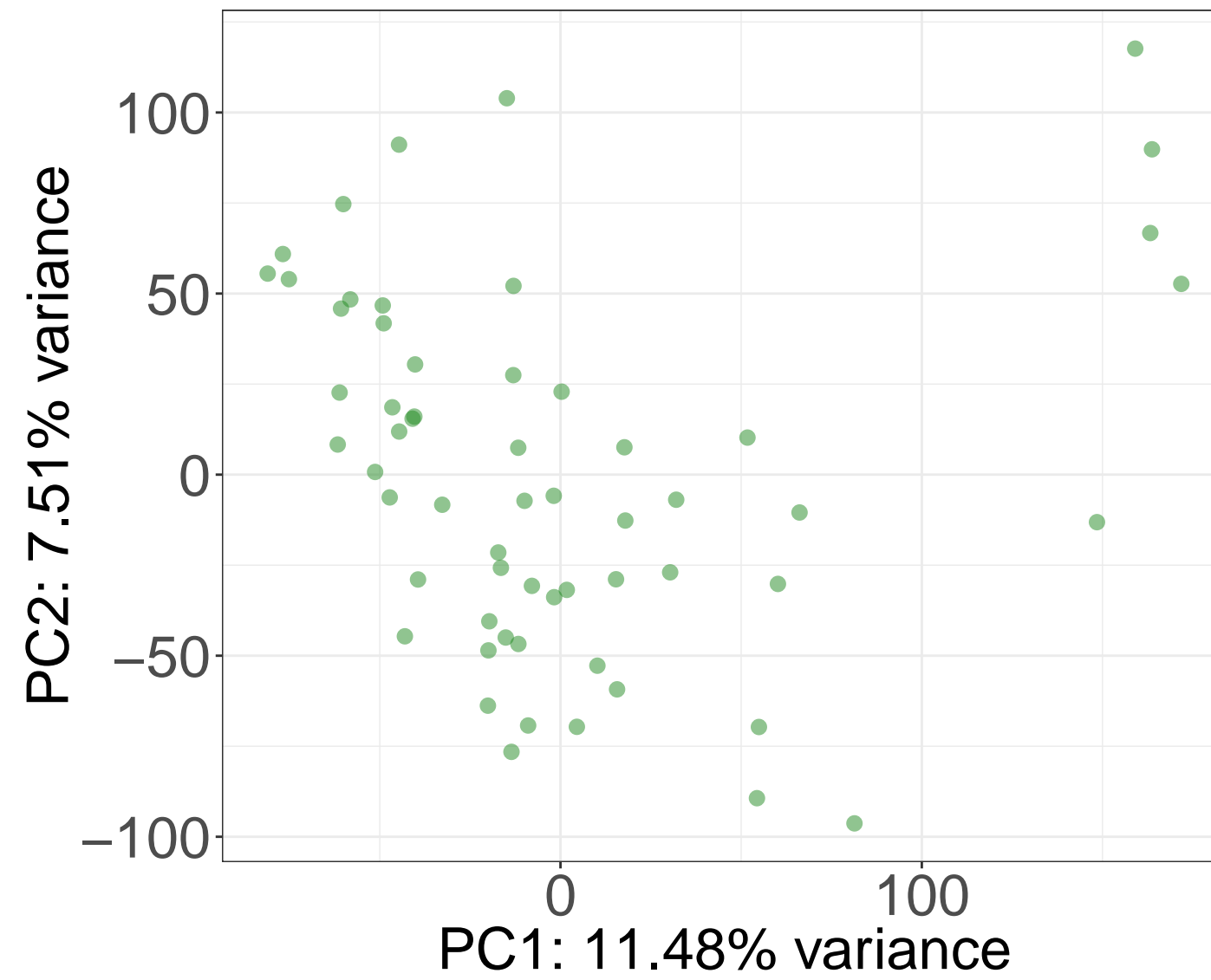
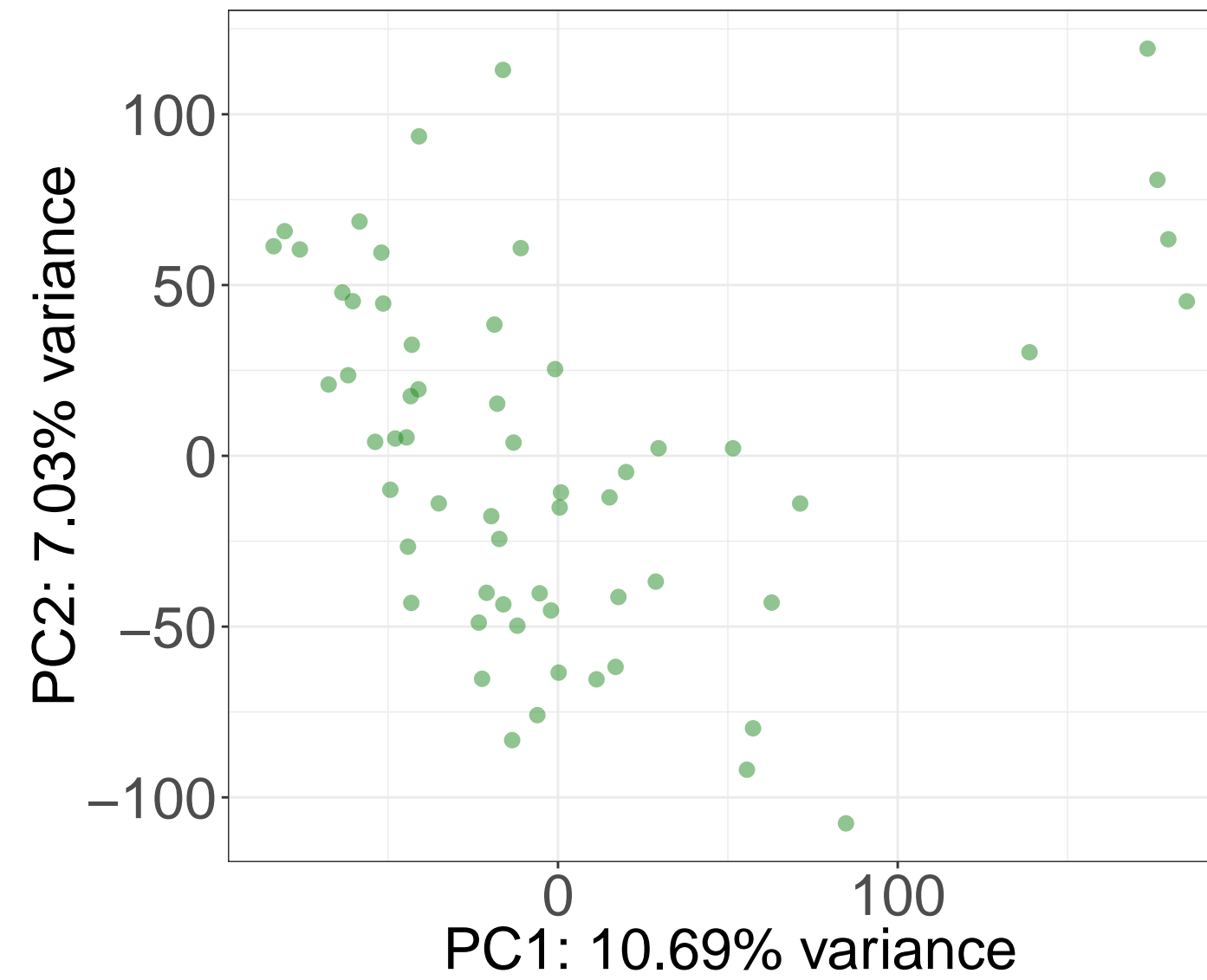
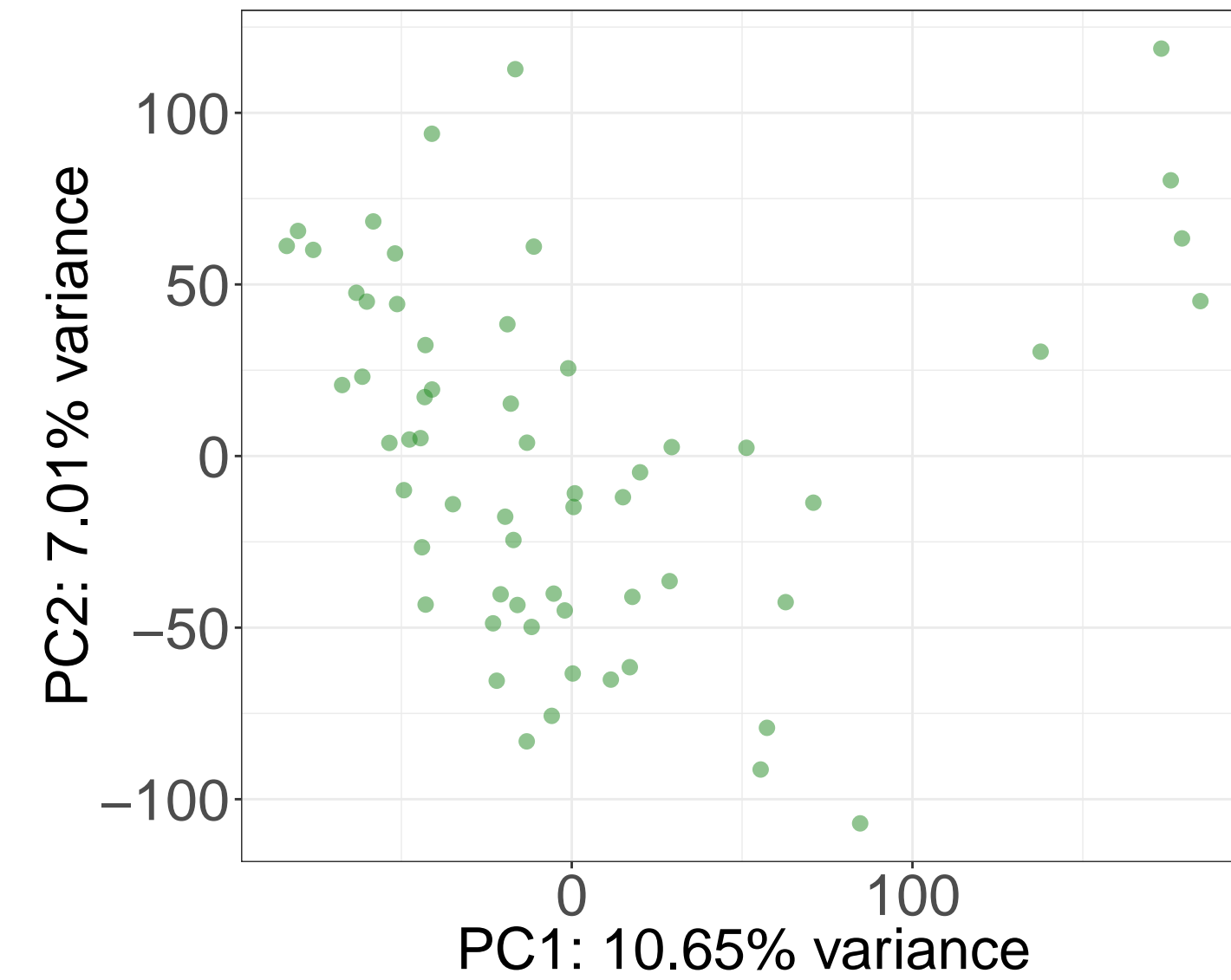


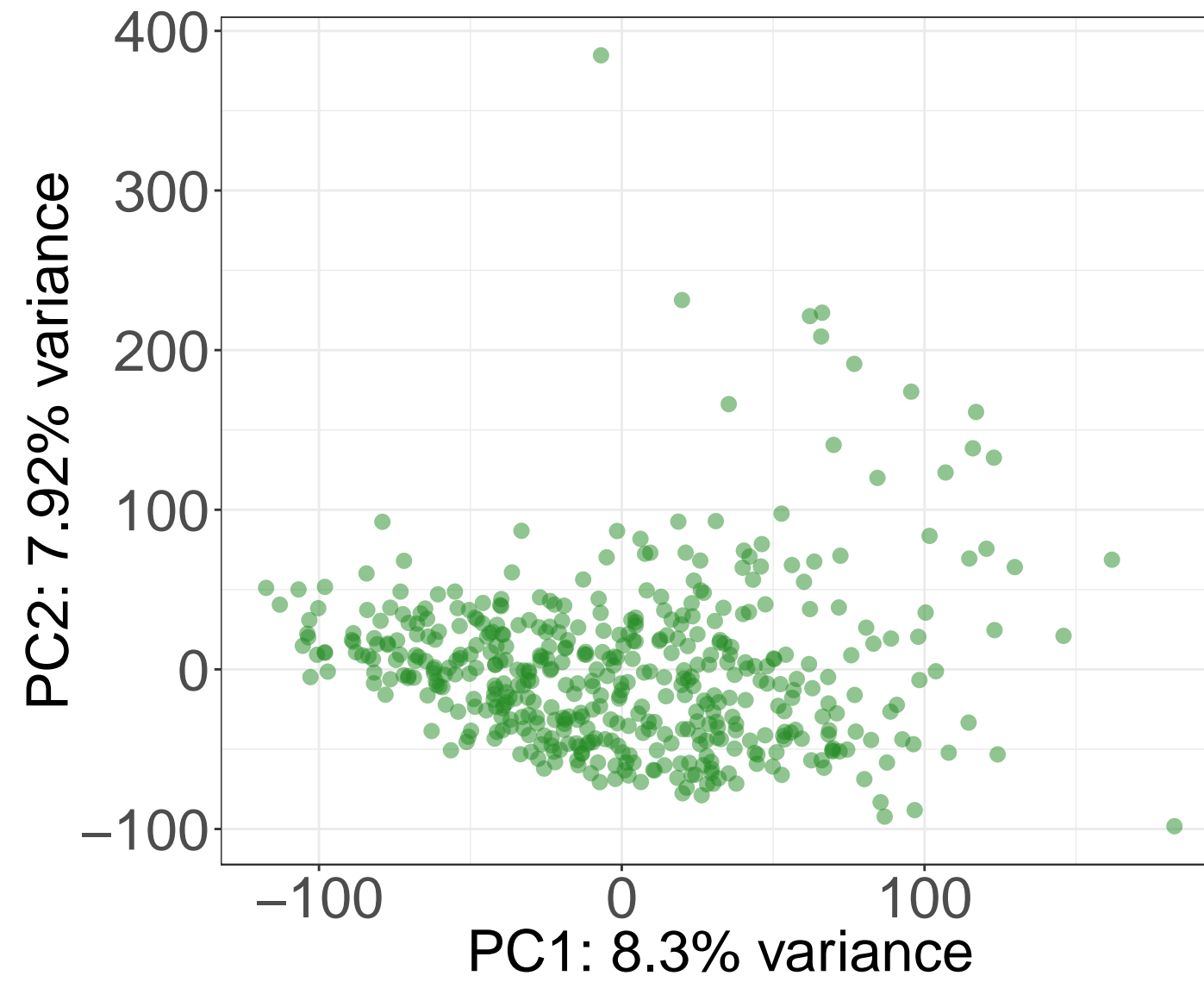
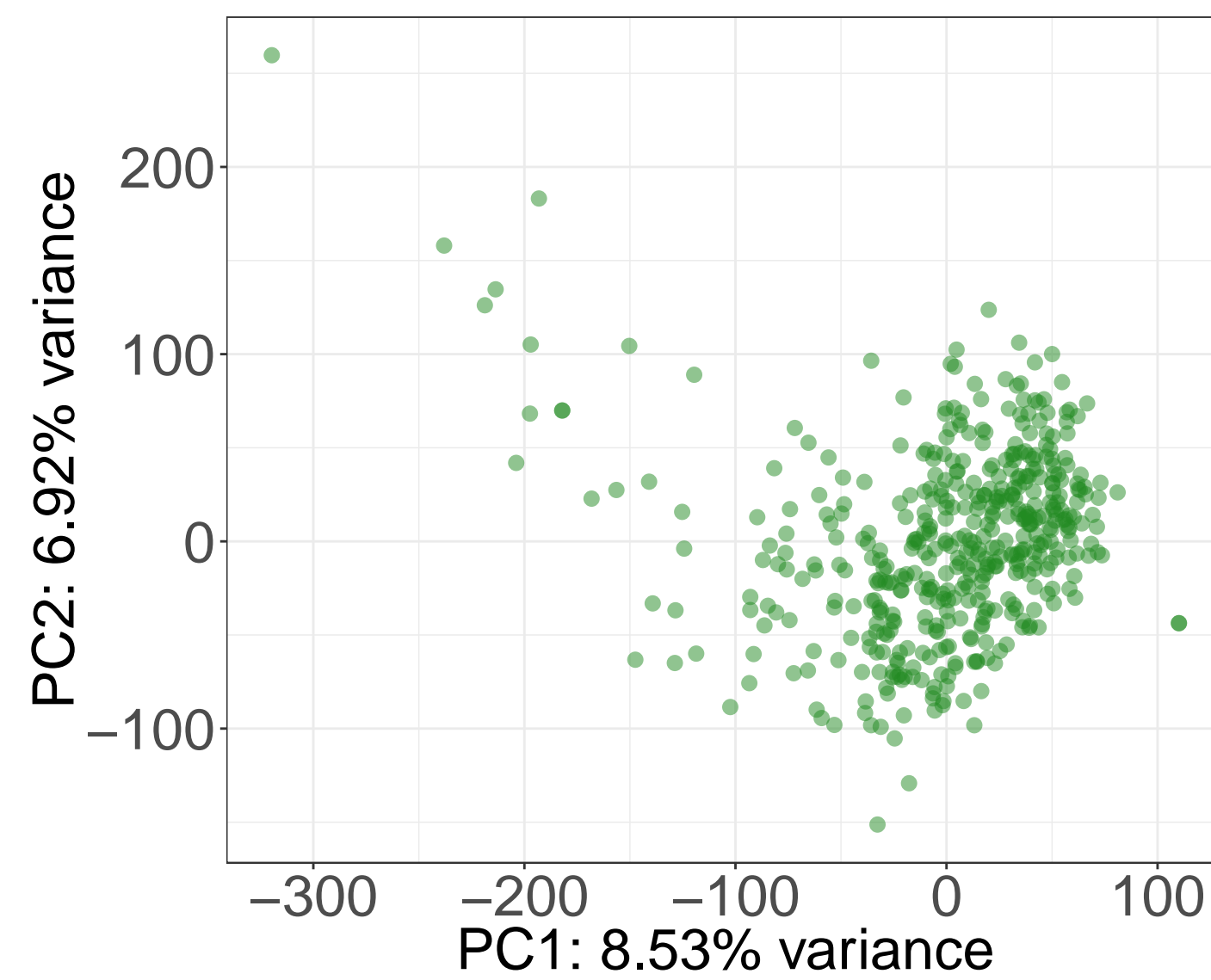
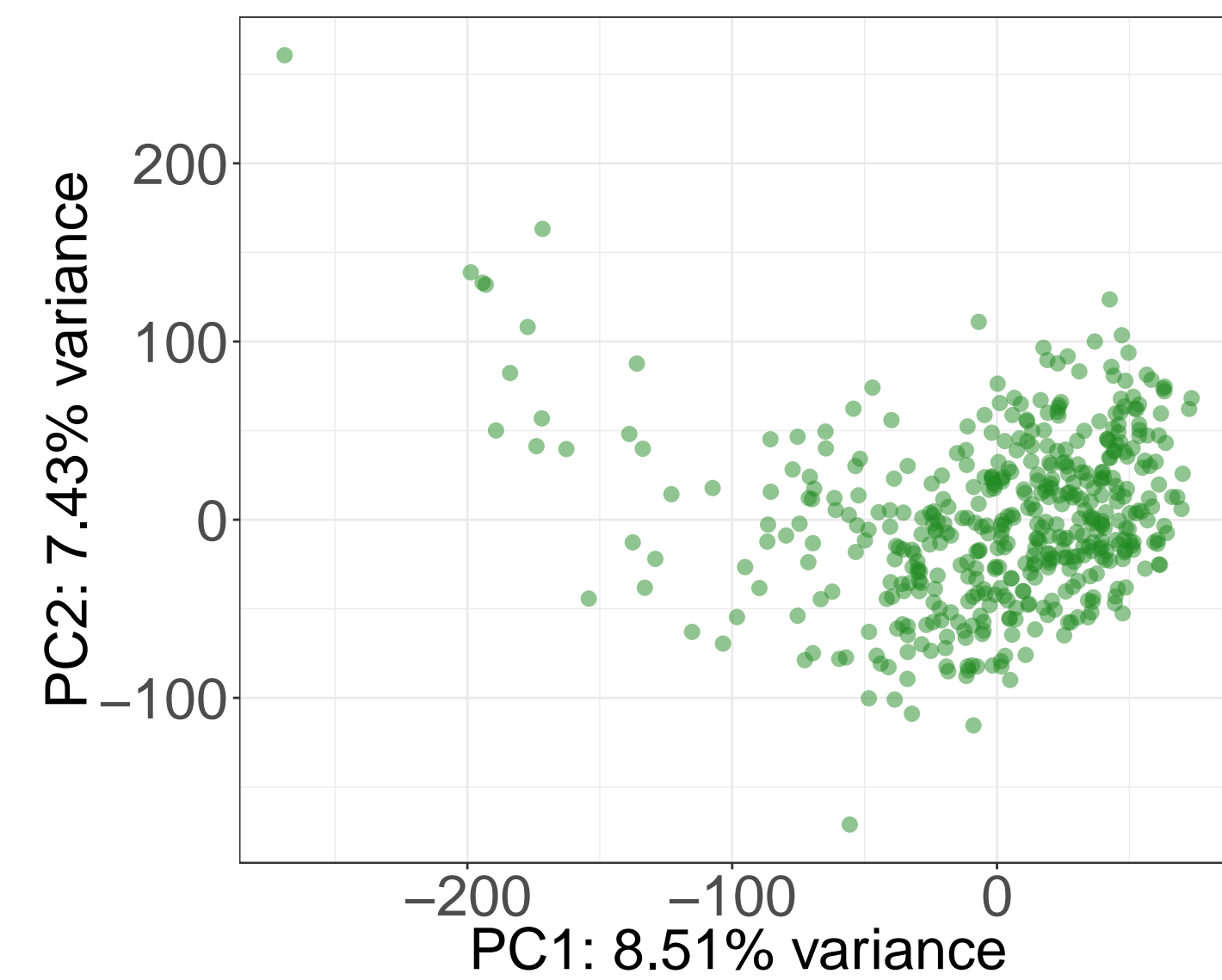
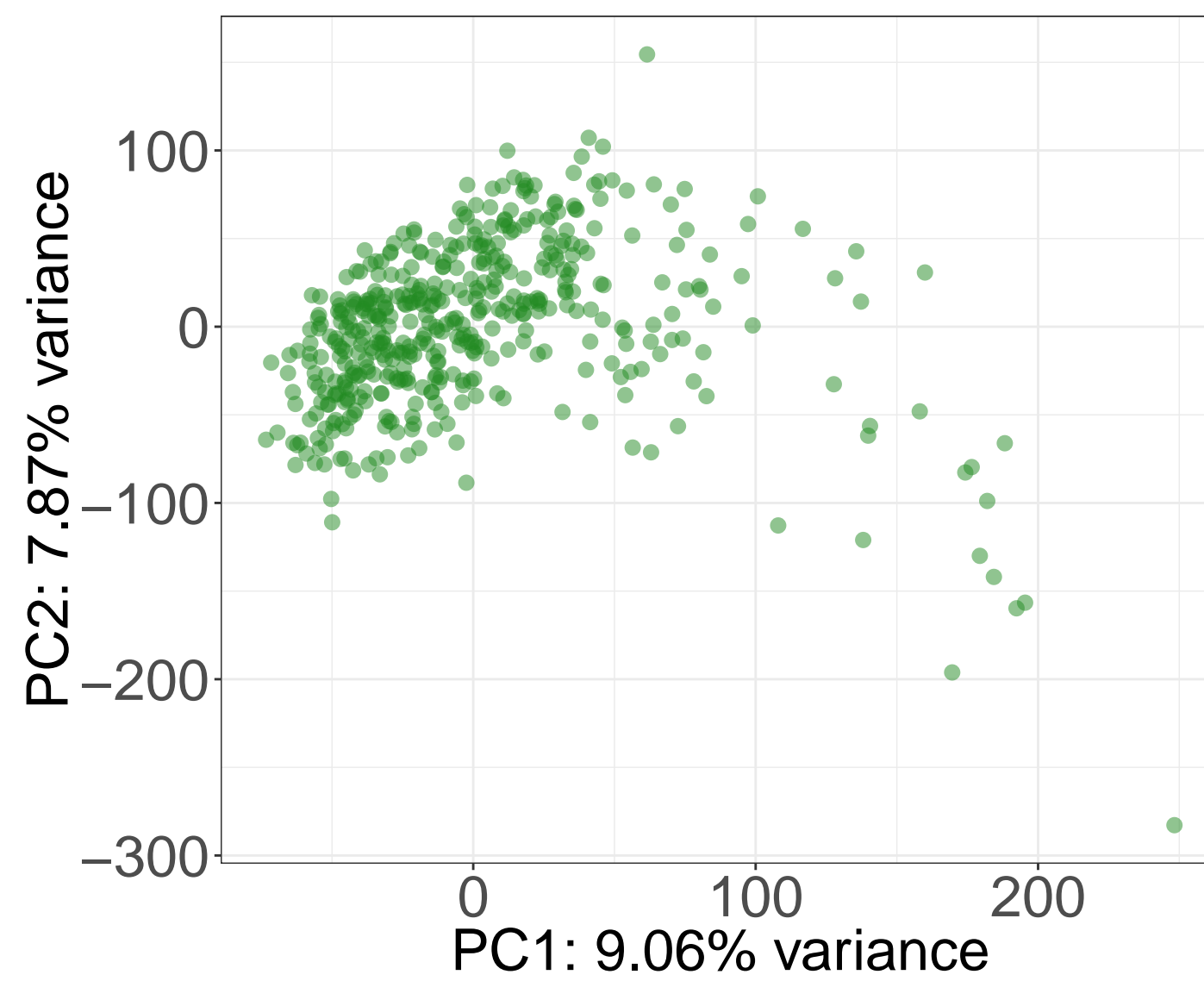
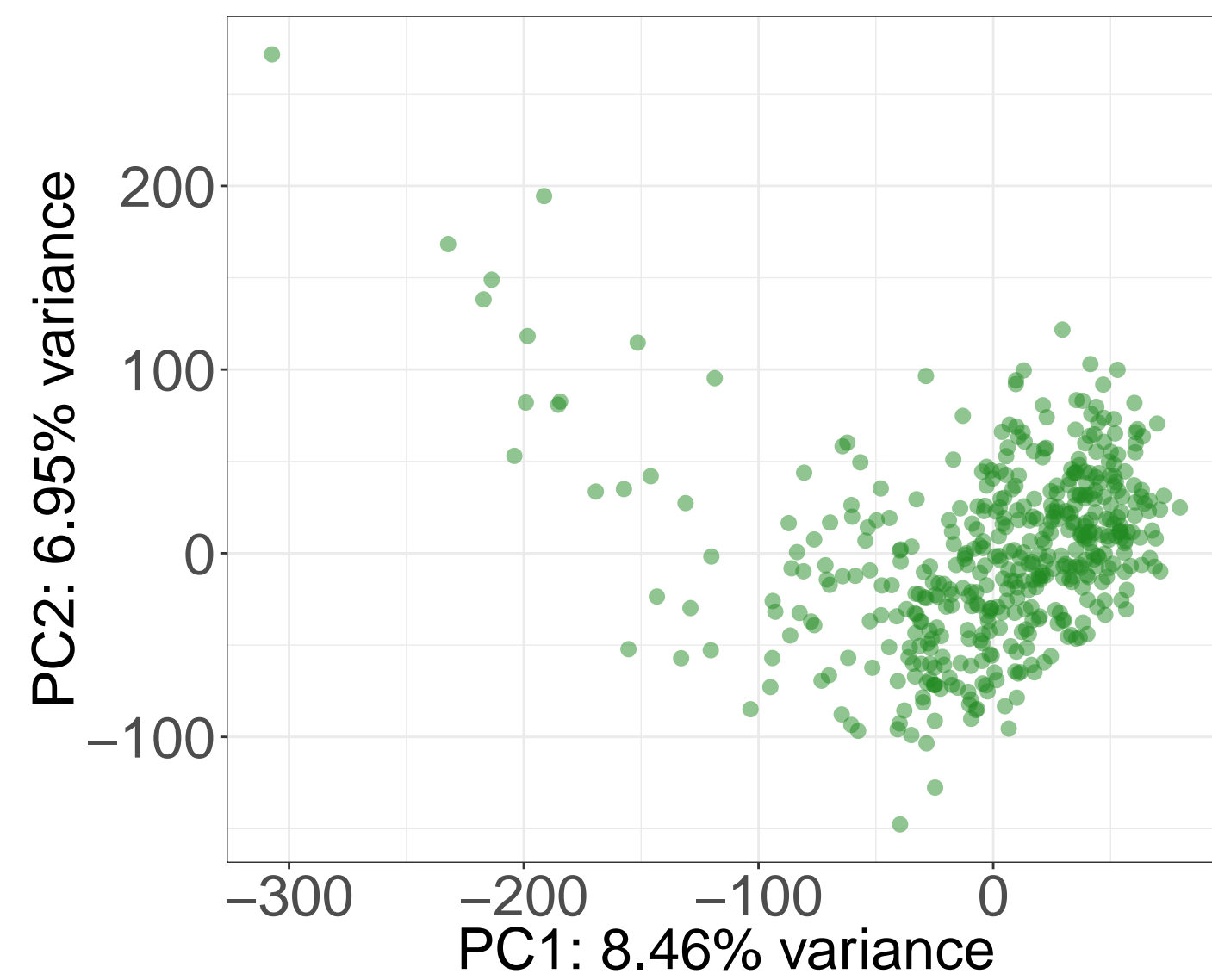
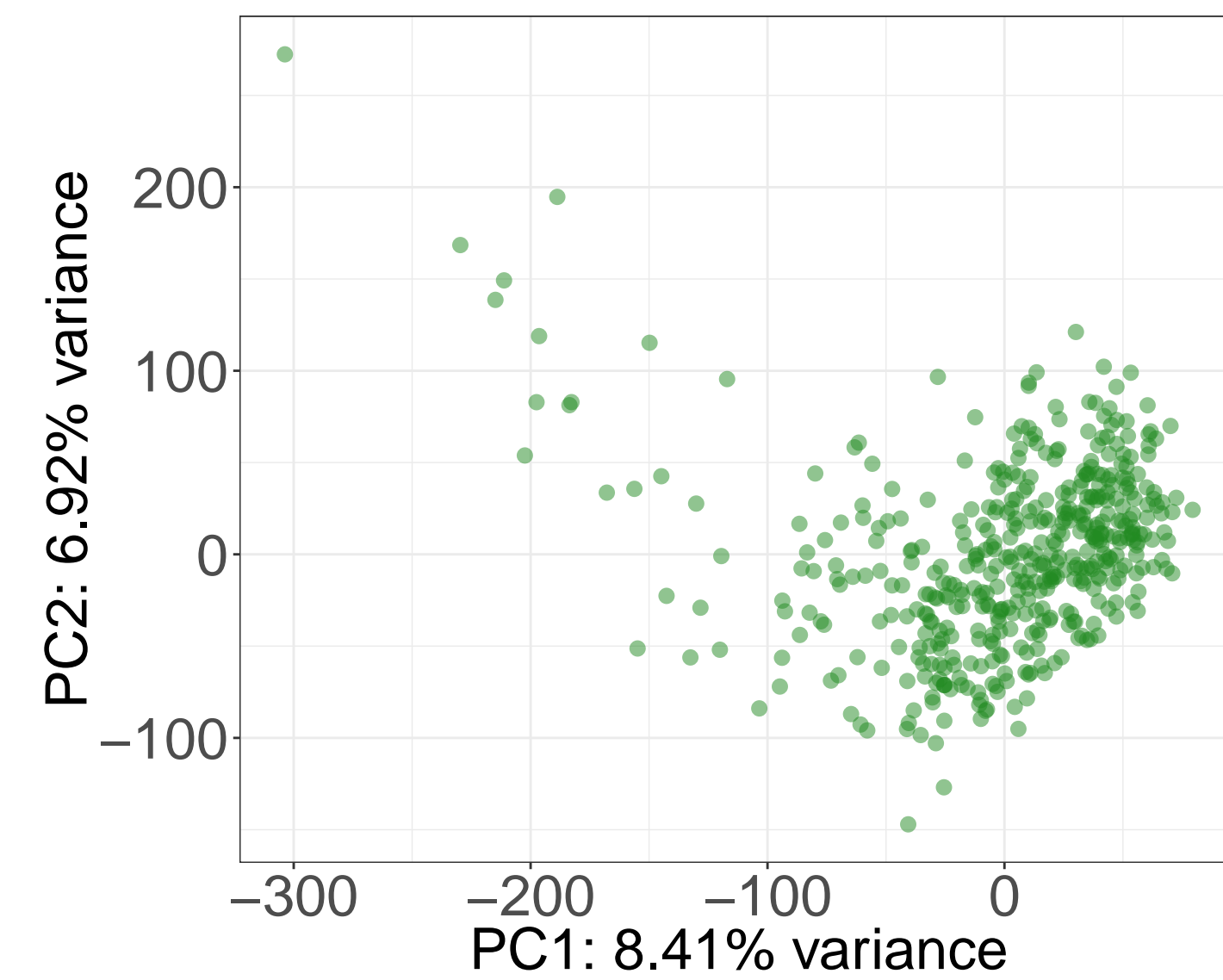
GDC**Xena/Toil****Piccolo****Recount2****MSKCC****MSKCC Batch**

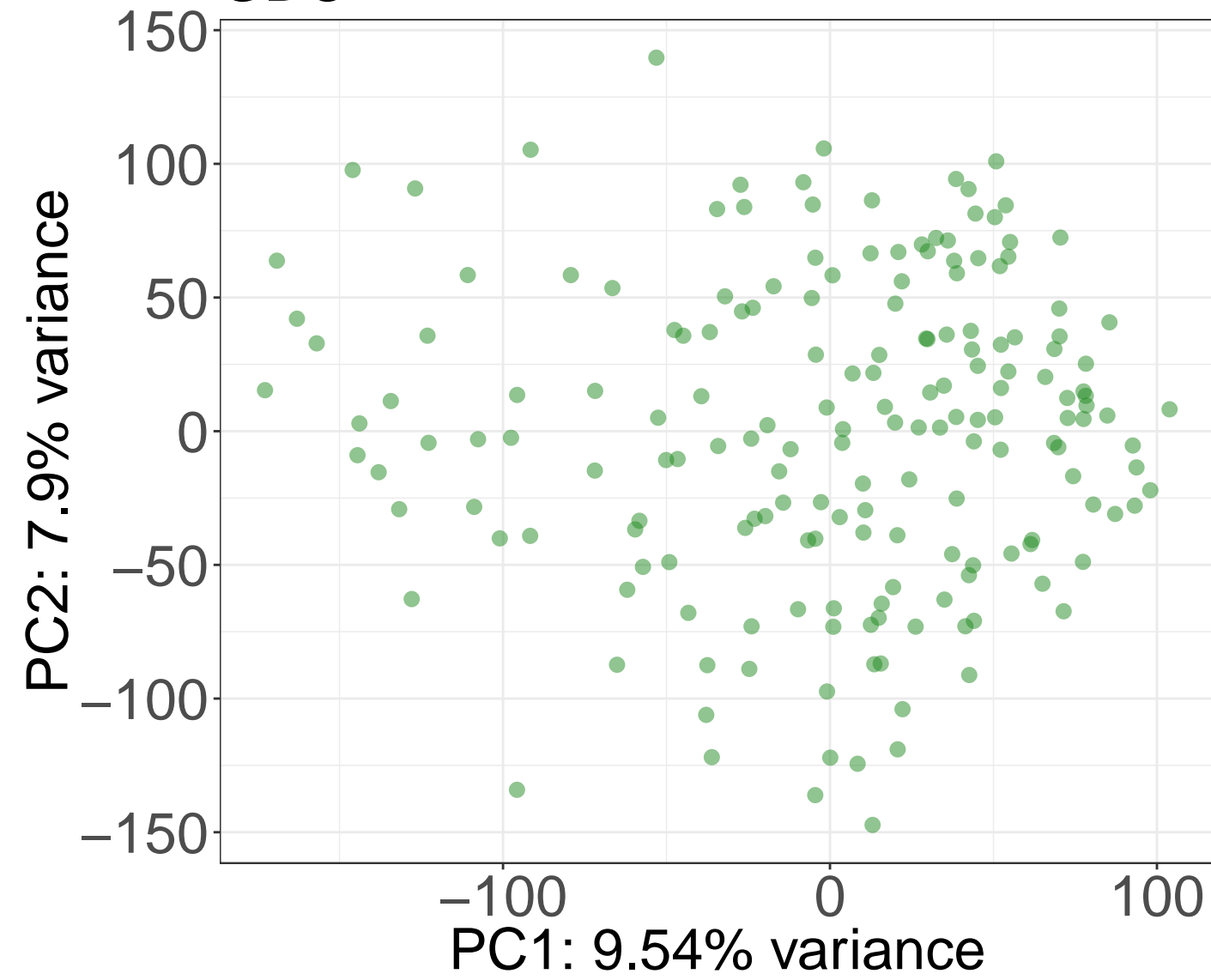
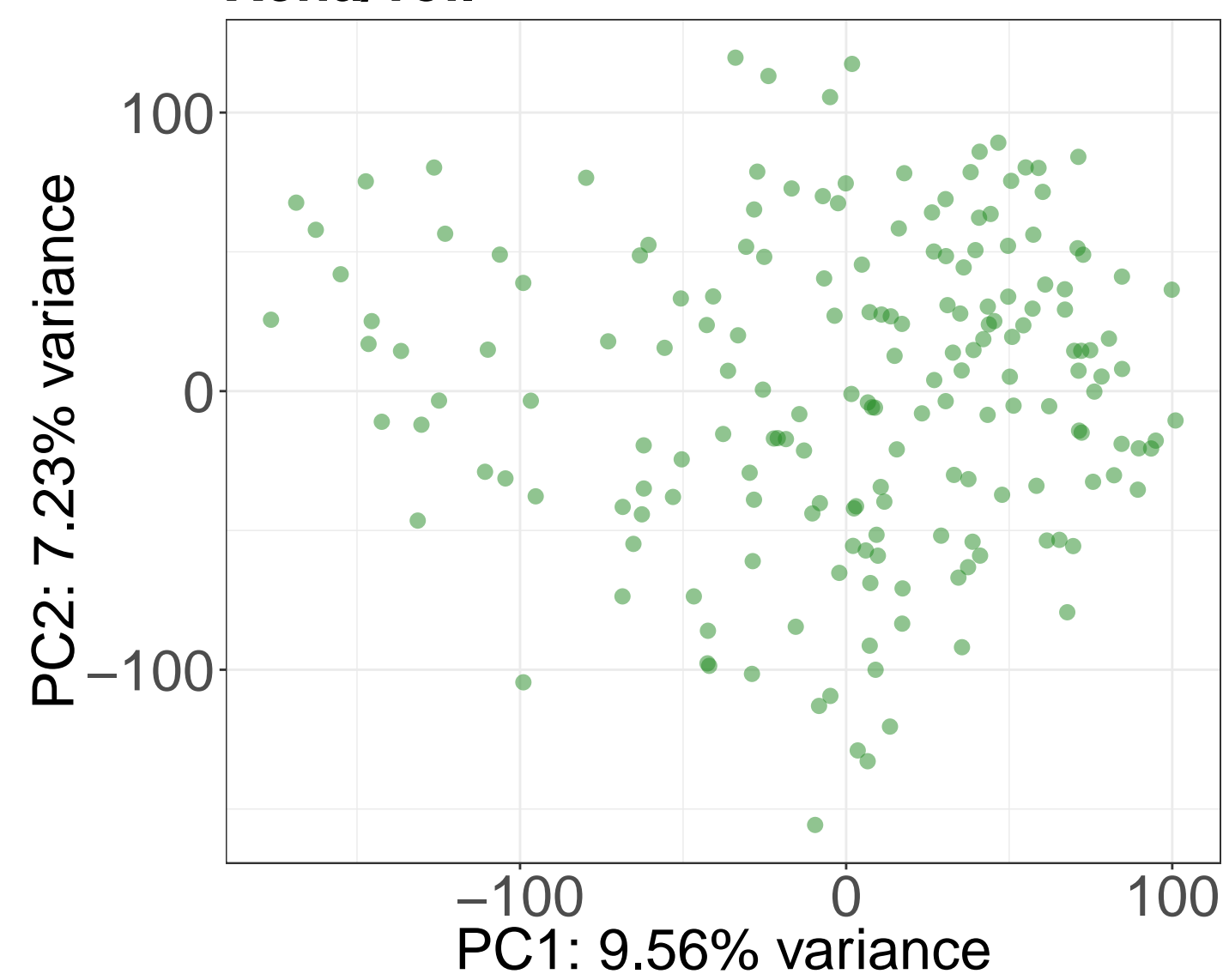
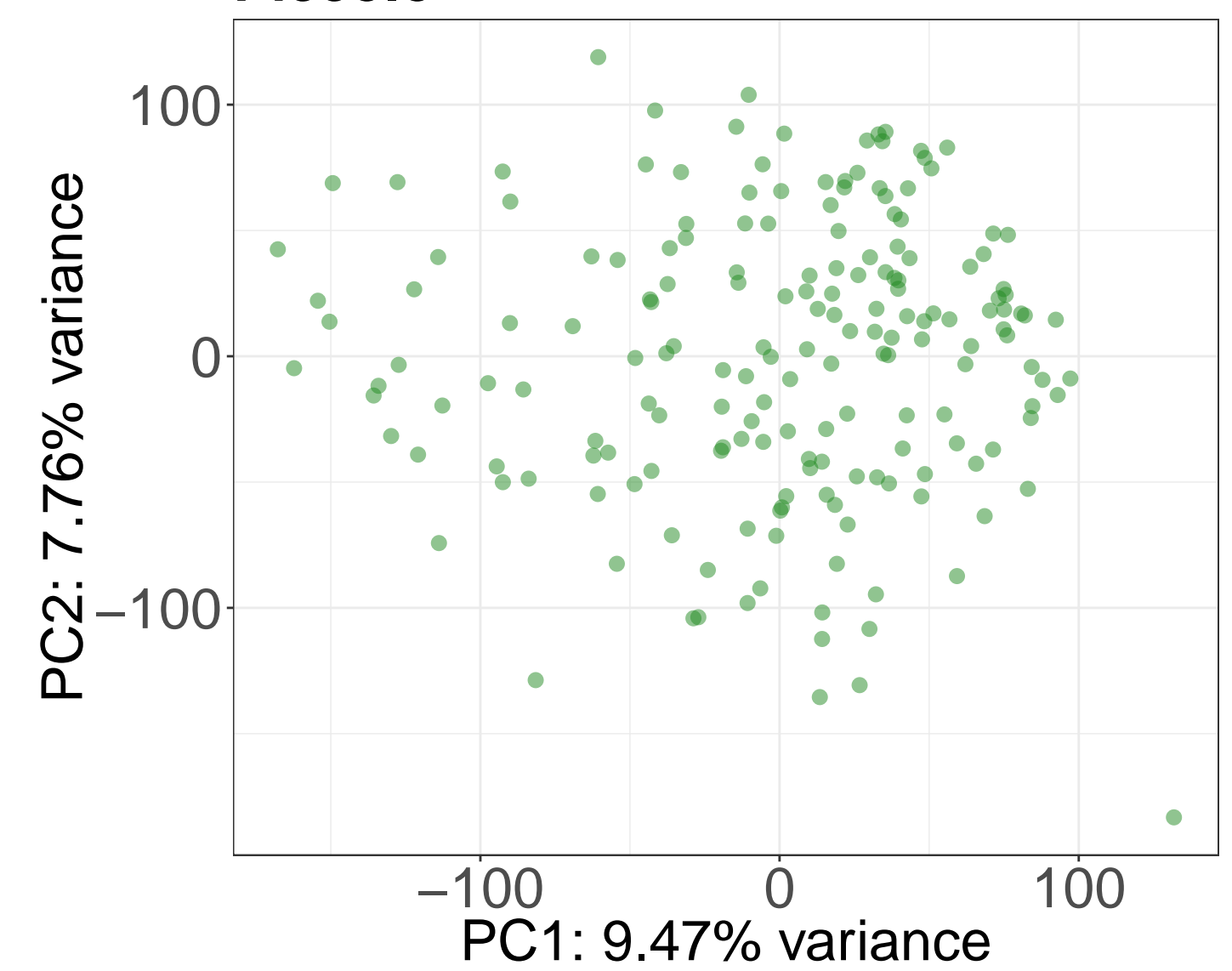
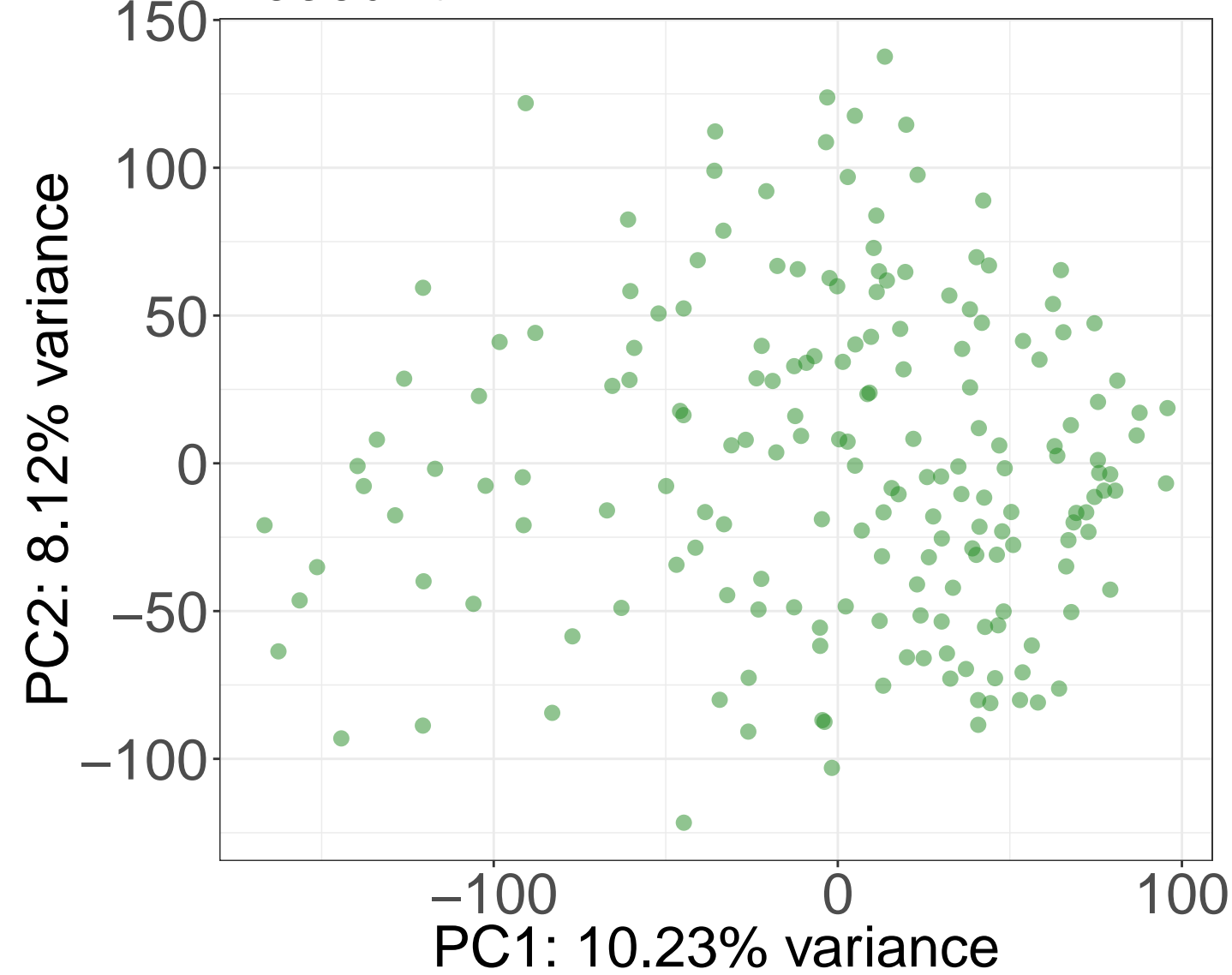
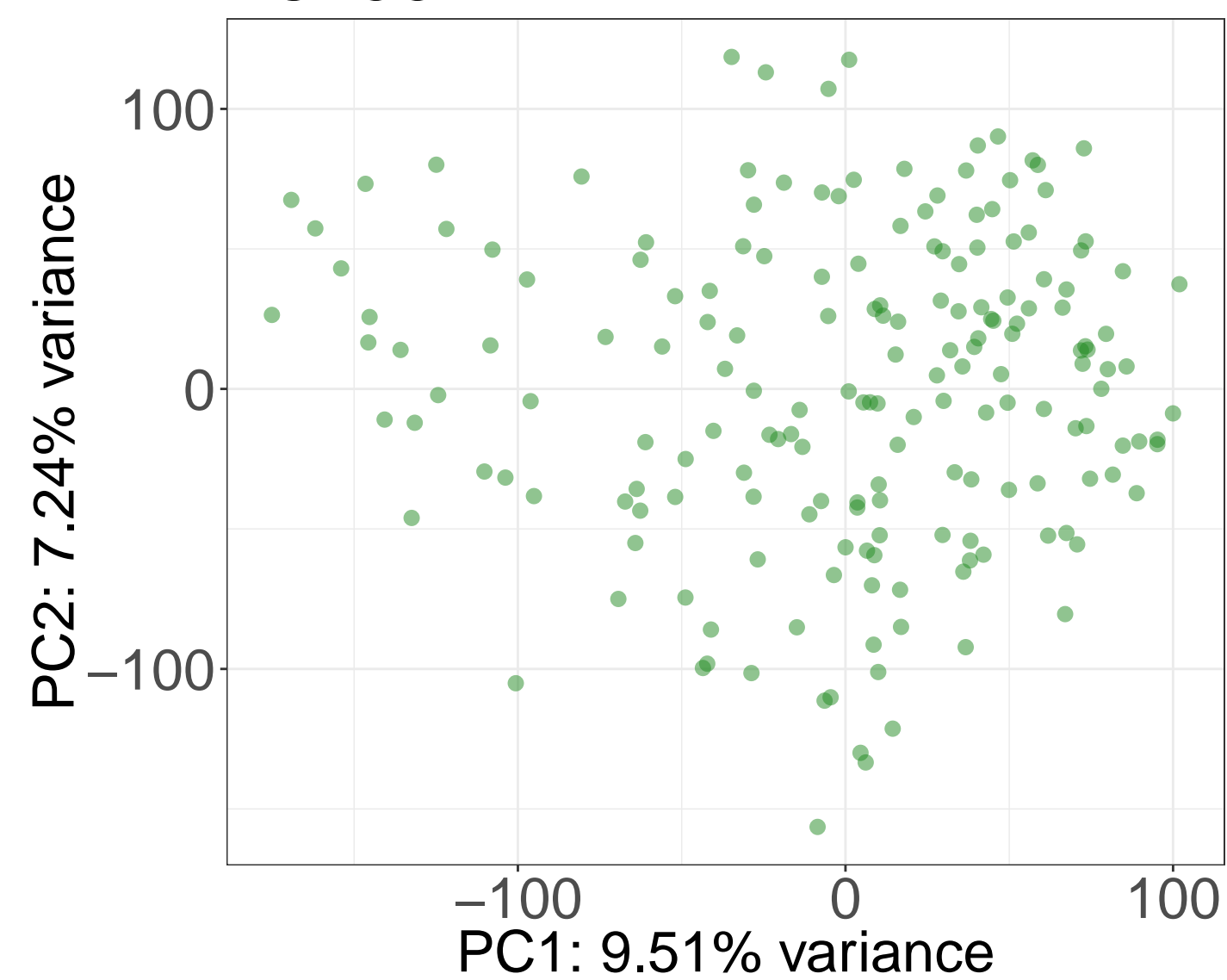
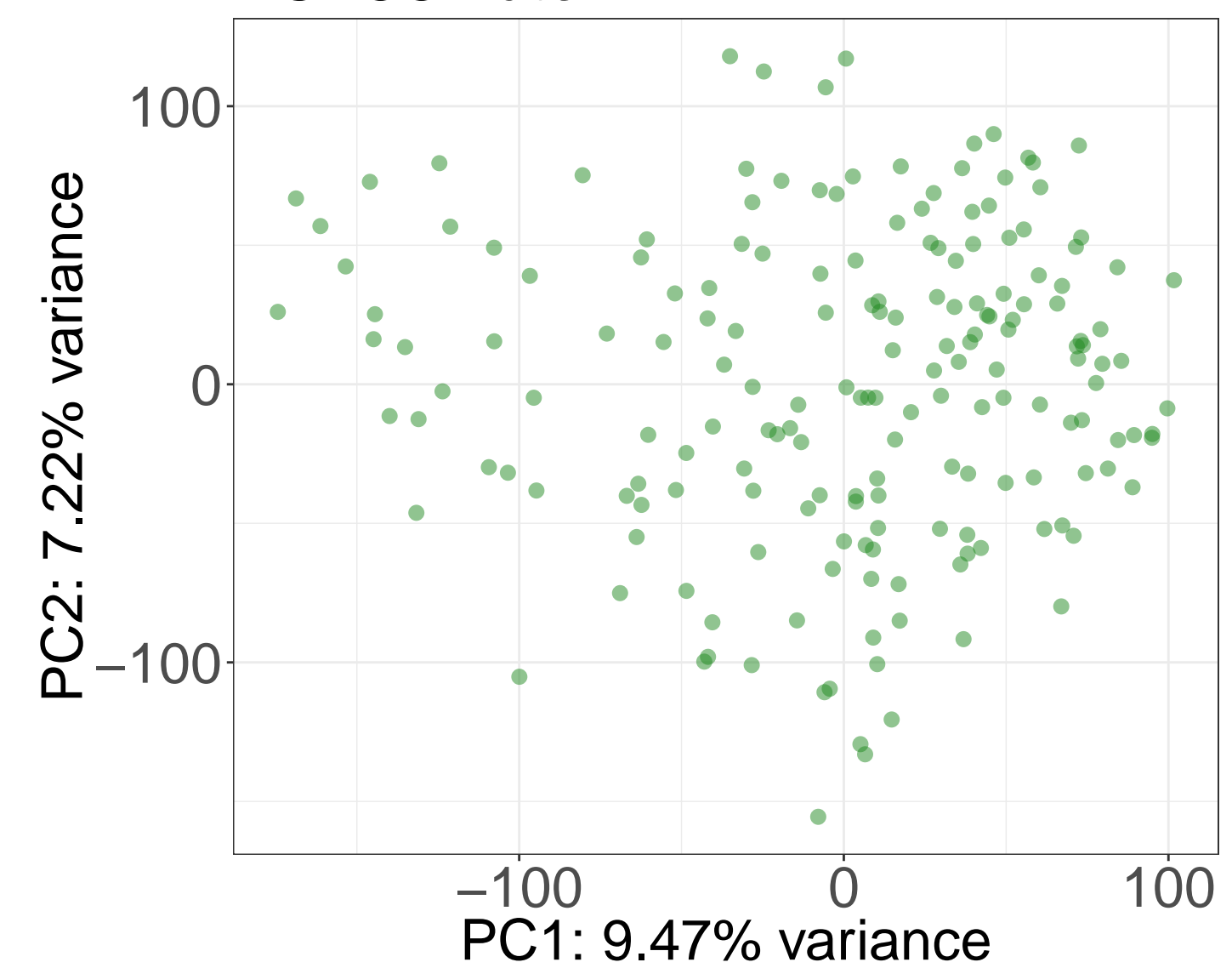
GDC**Xena/Toil****Piccolo****Recount2****MSKCC****MSKCC Batch**

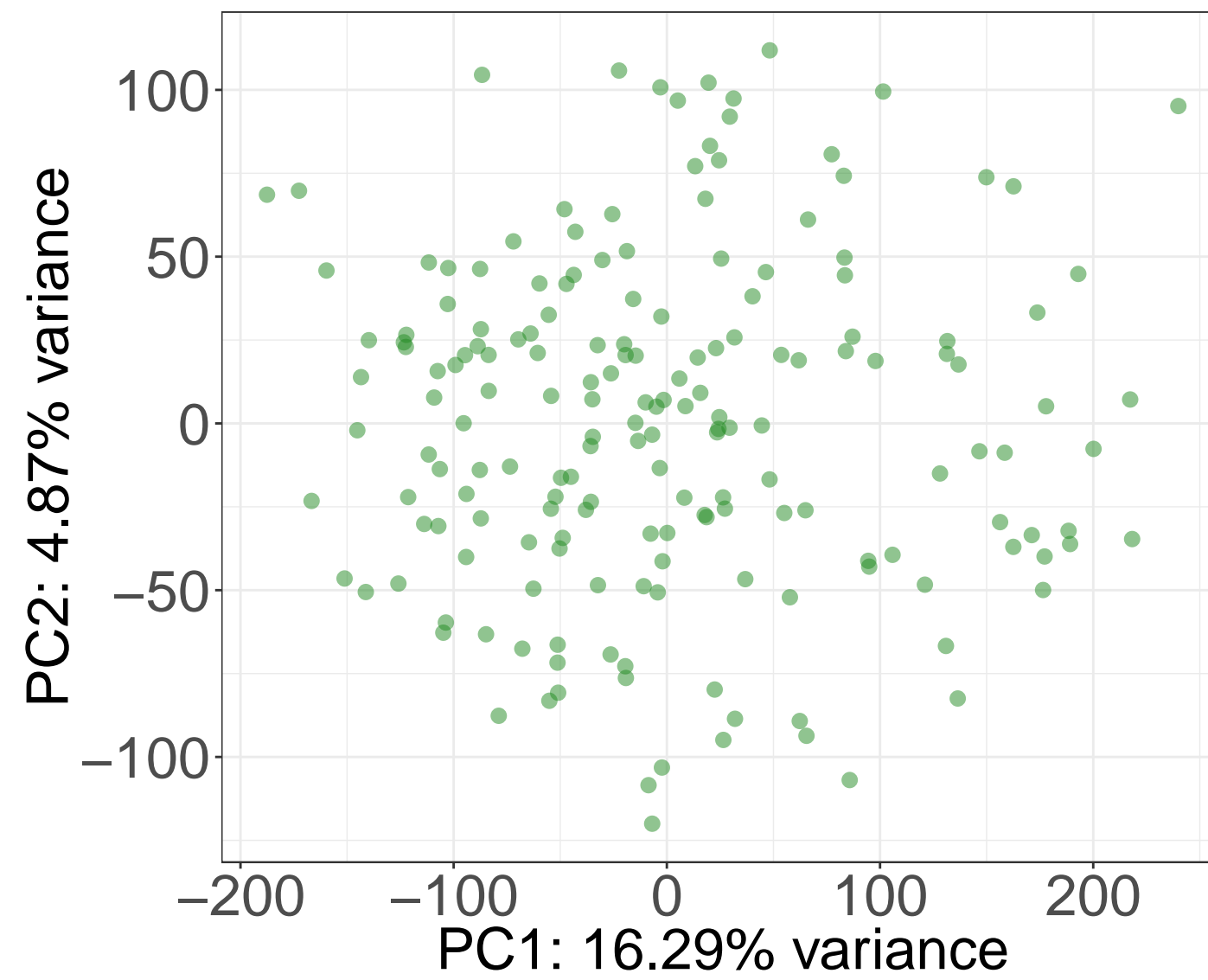
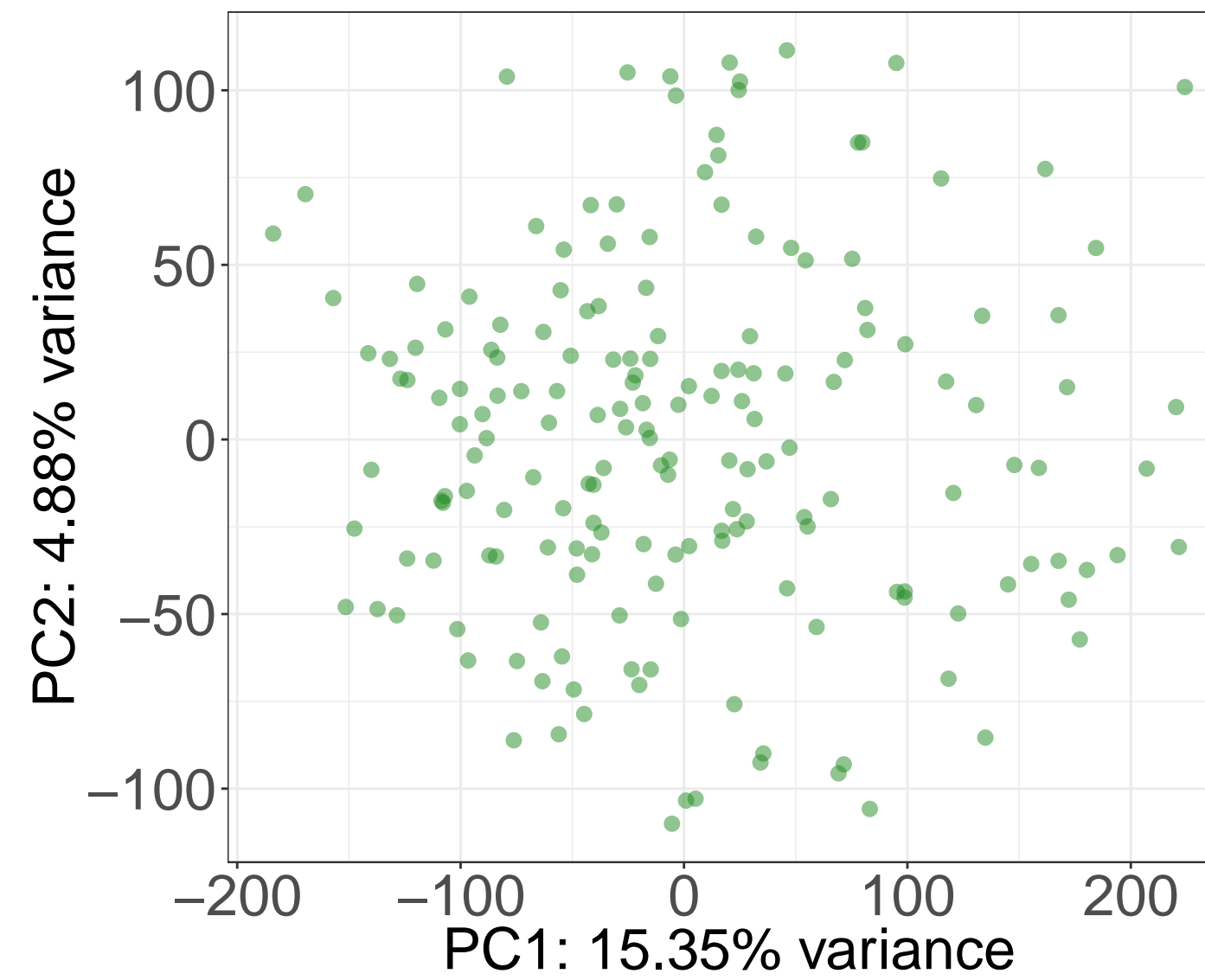
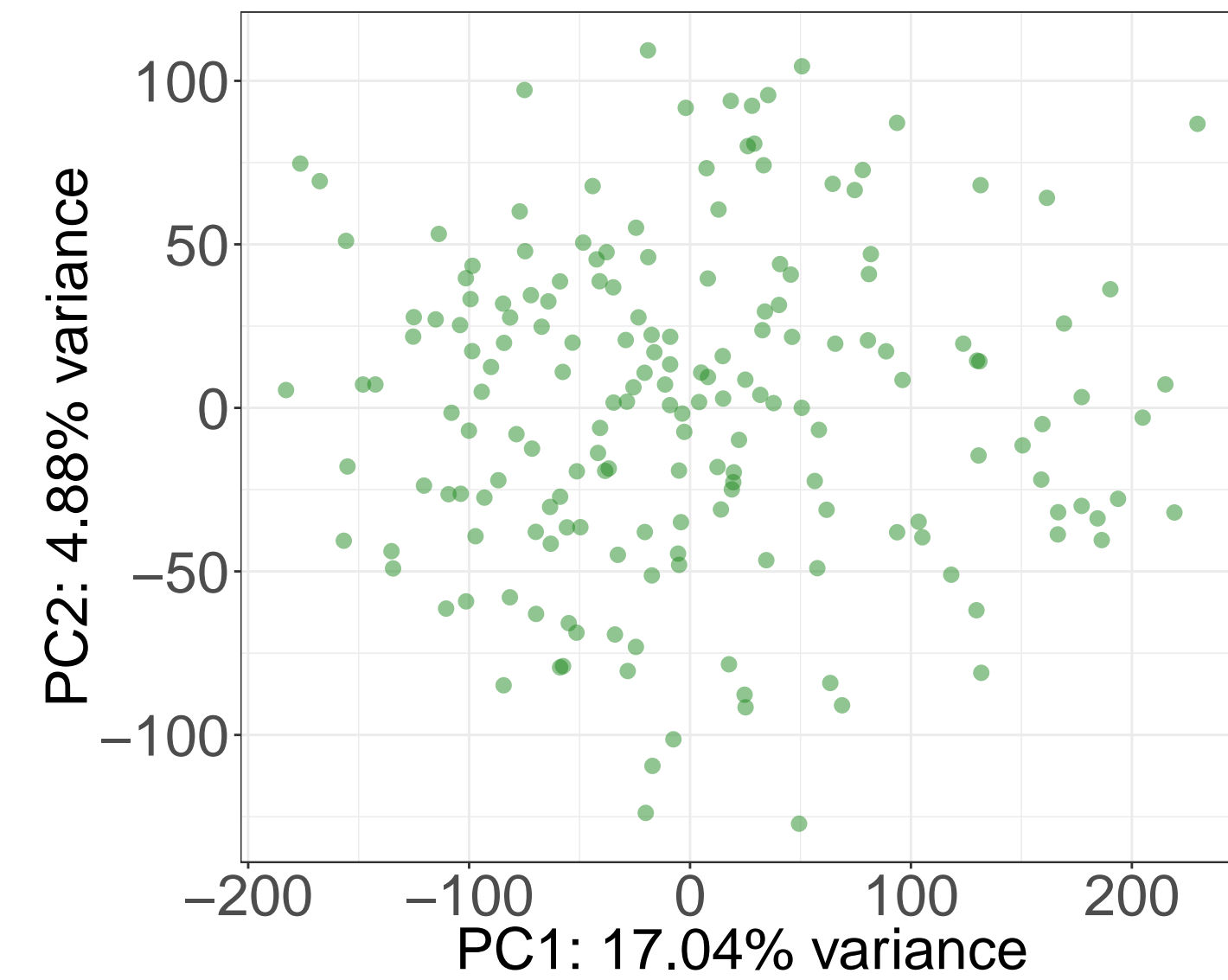
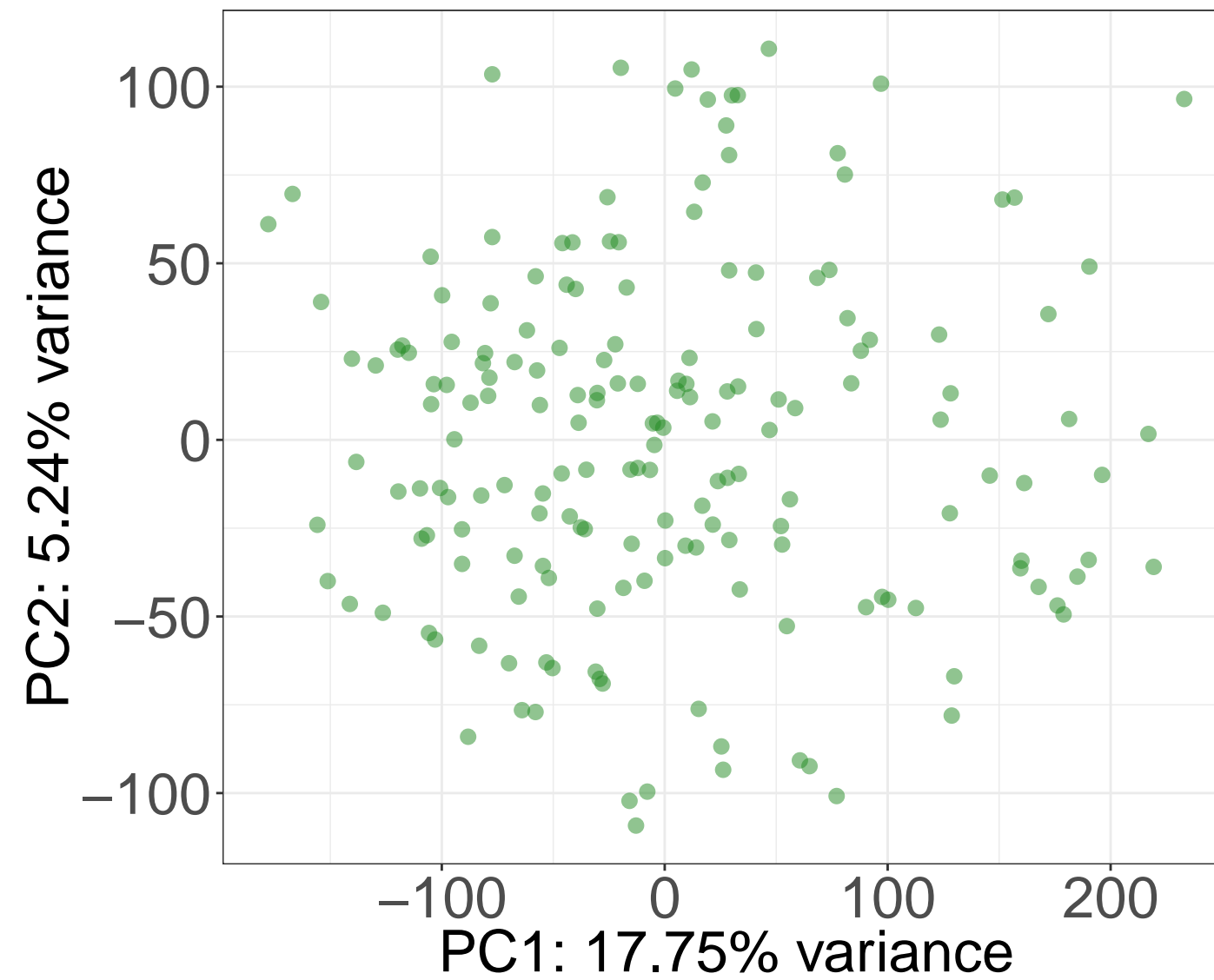
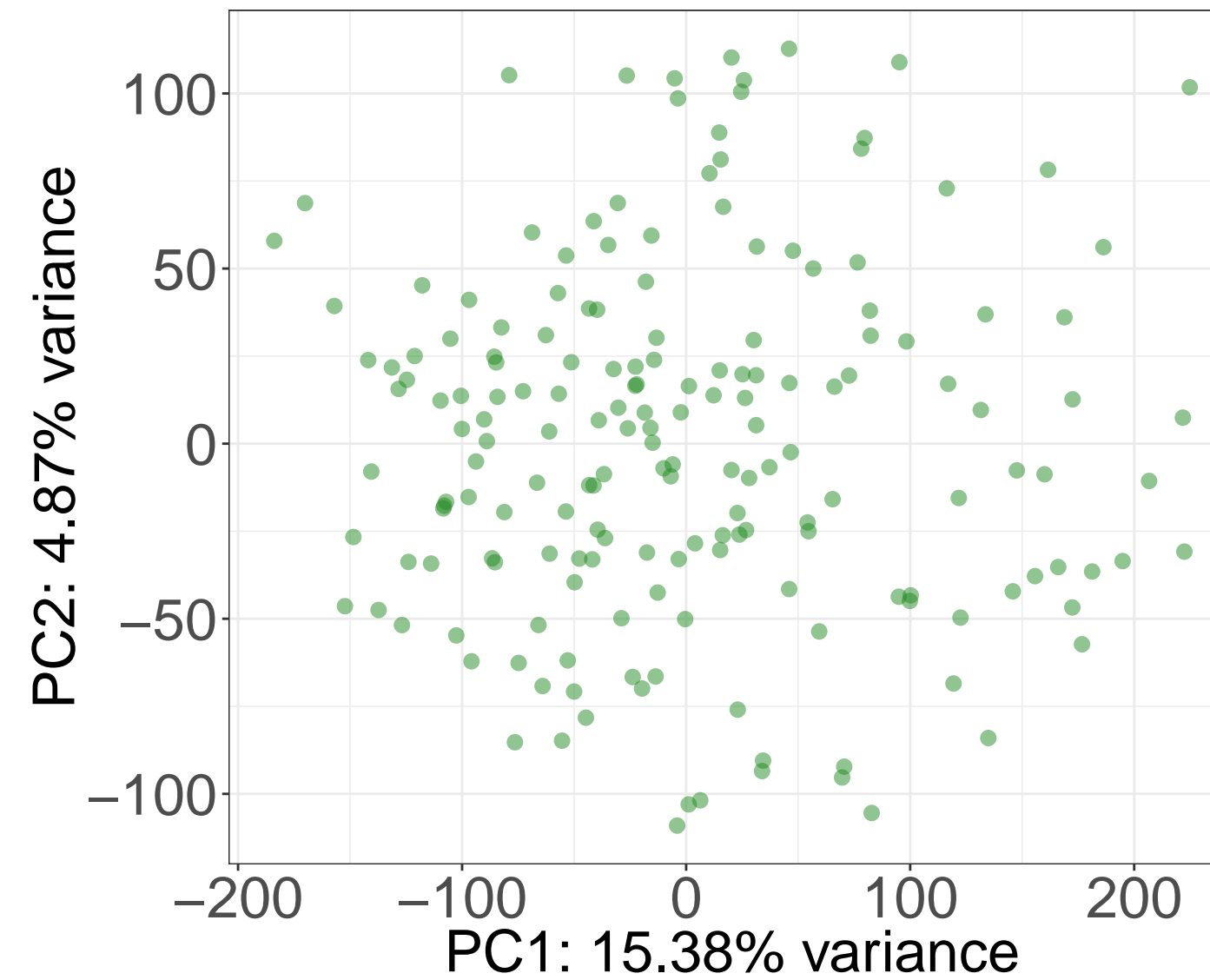
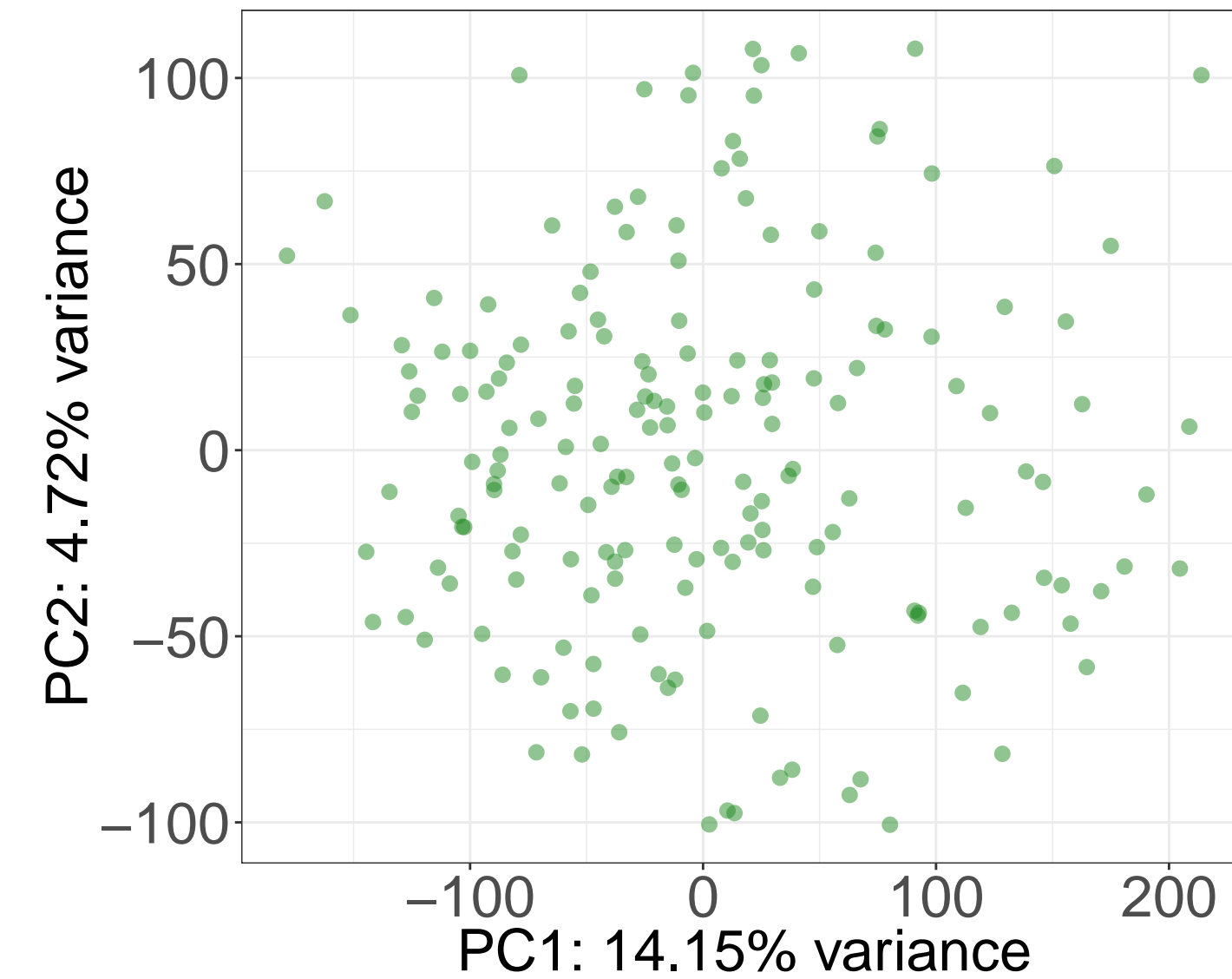
GDC**Xena/Toil****Piccolo****Recount2****MSKCC****MSKCC Batch**

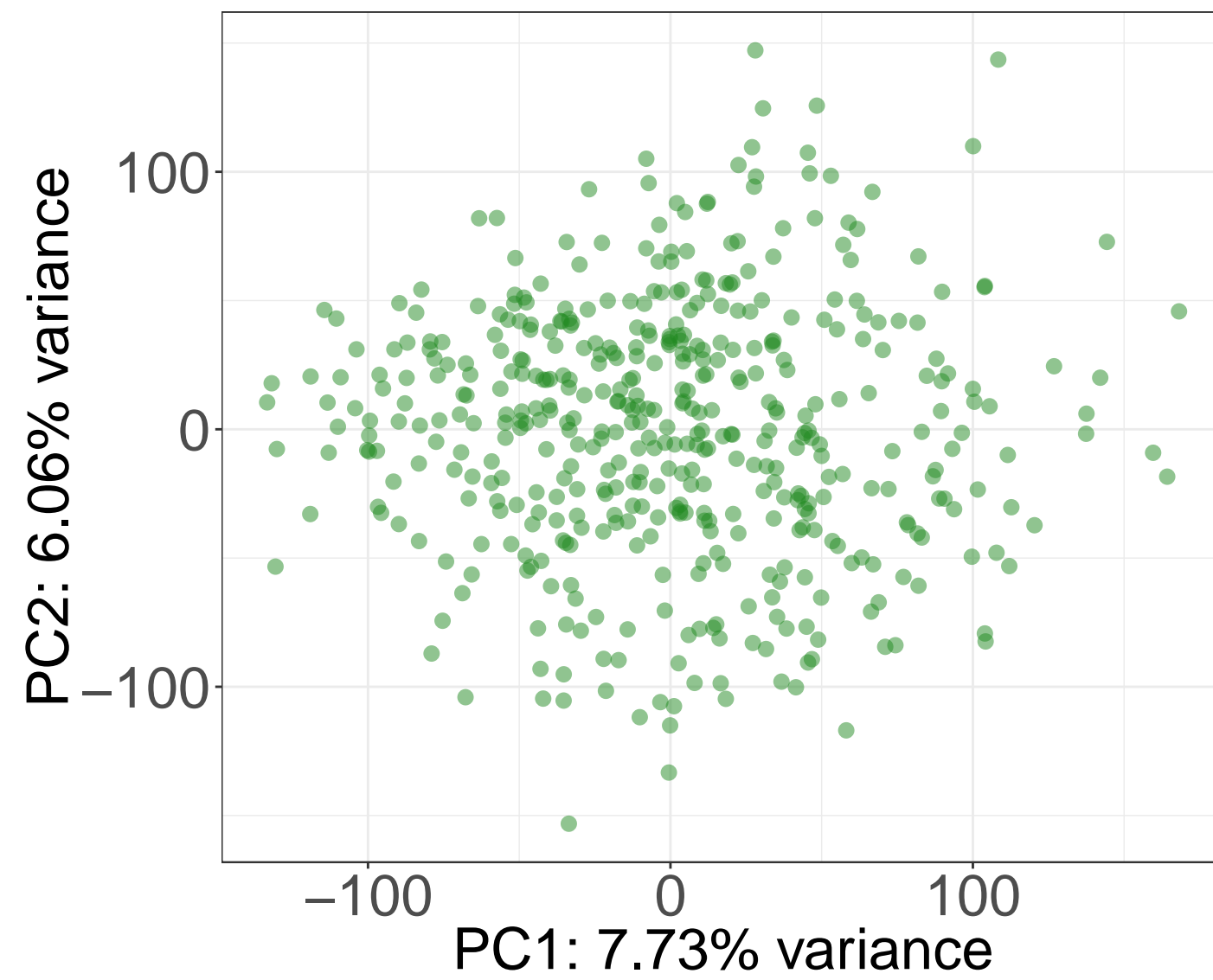
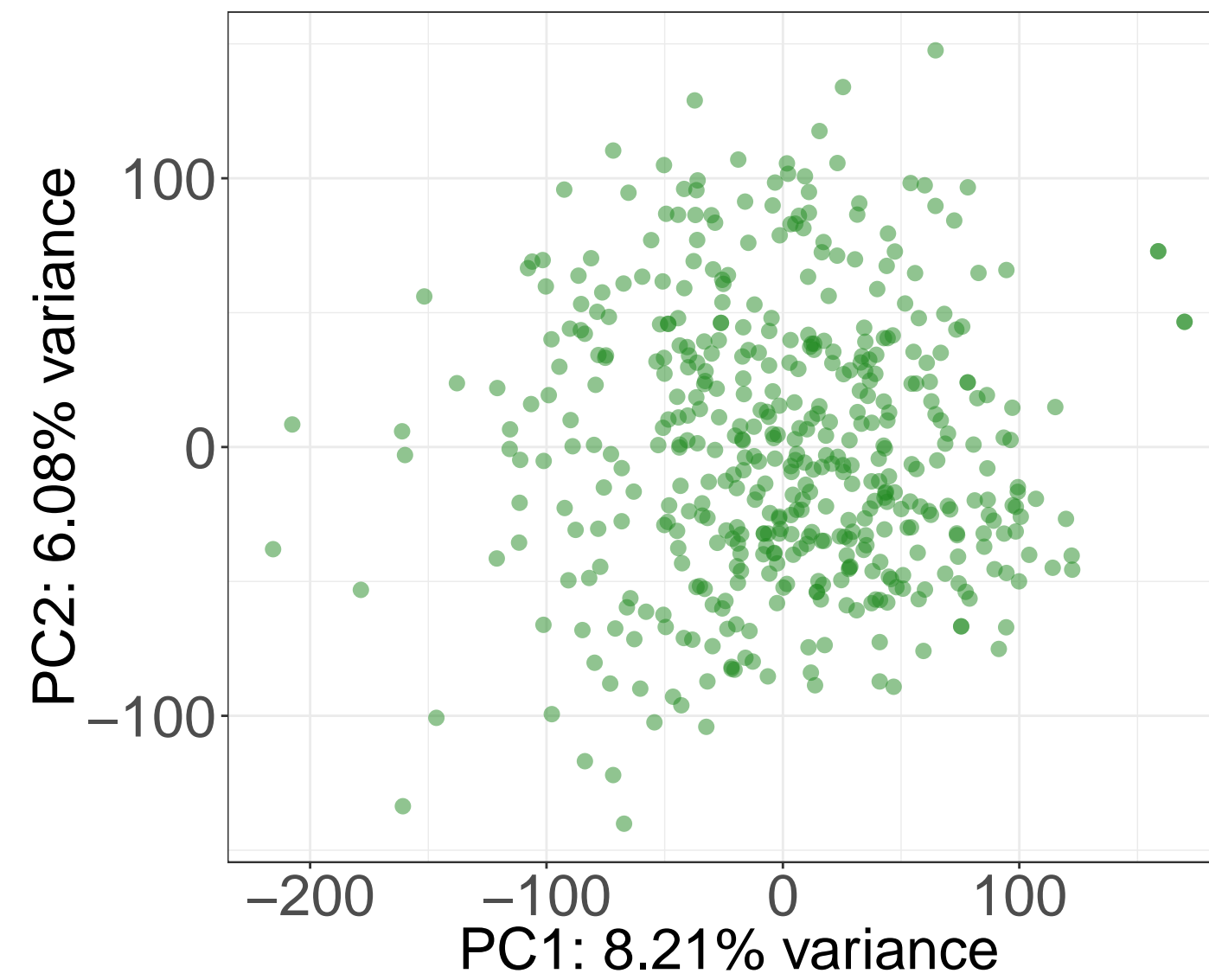
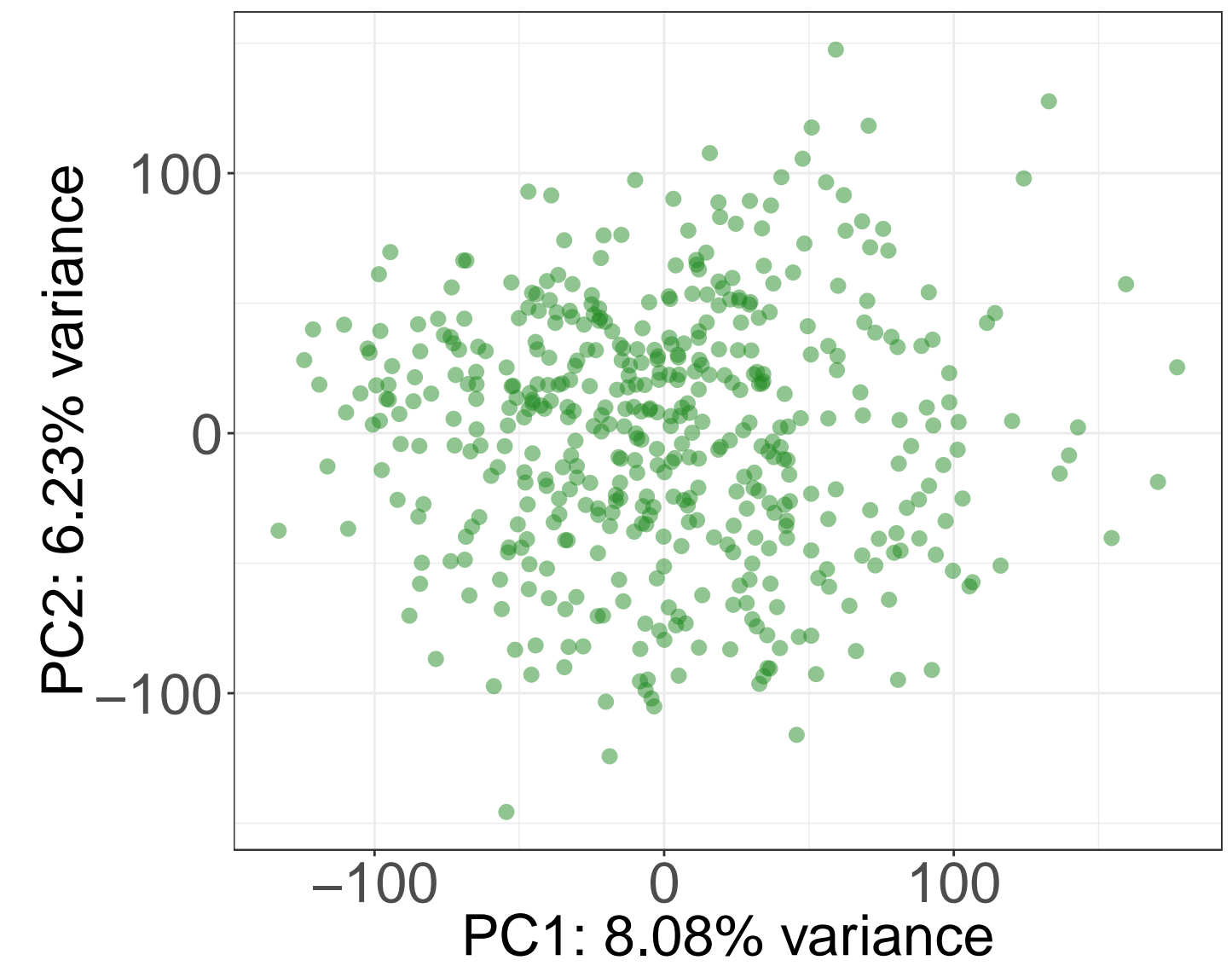
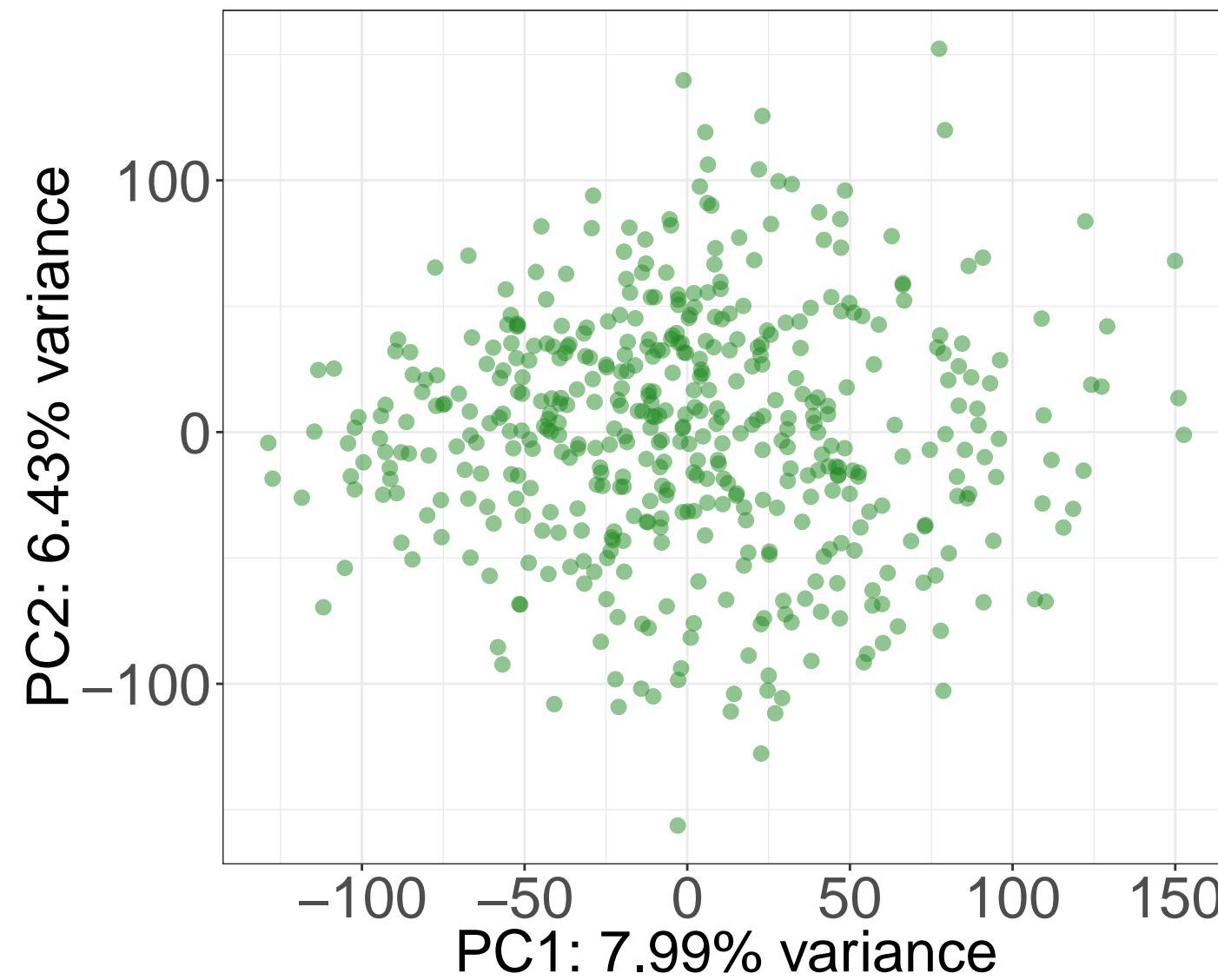
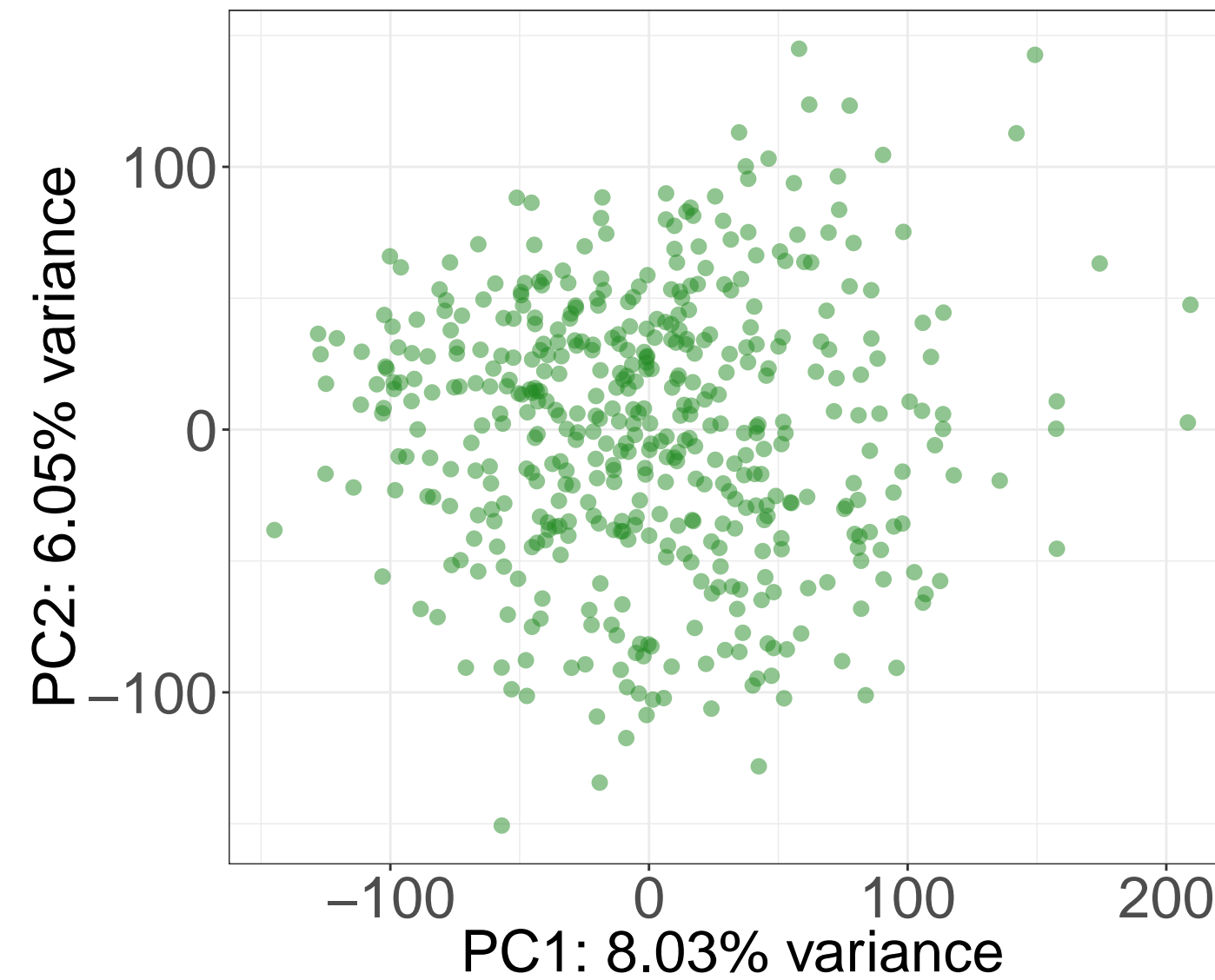
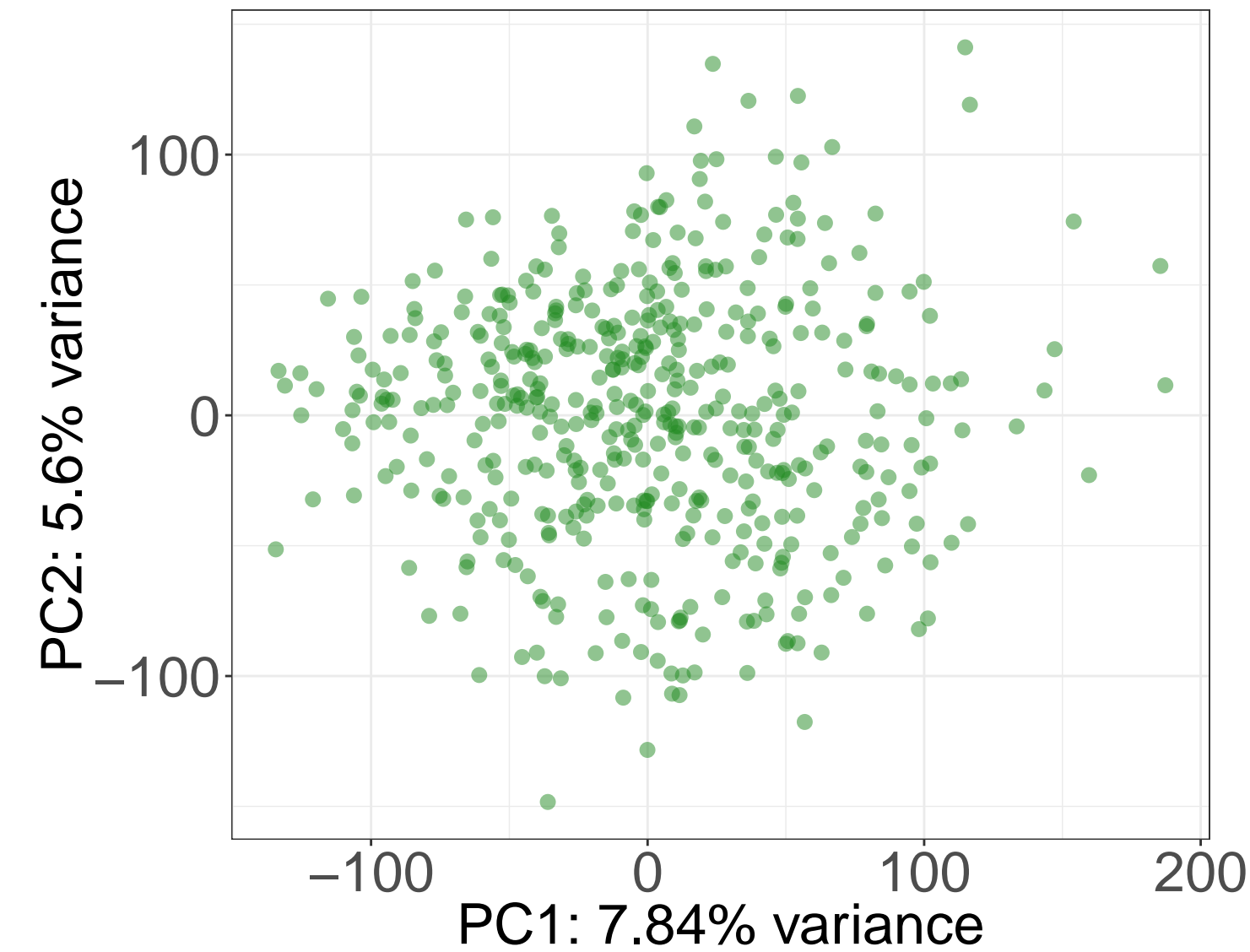
GDC**Xena/Toil****Piccolo****Recount2****MSKCC****MSKCC Batch**

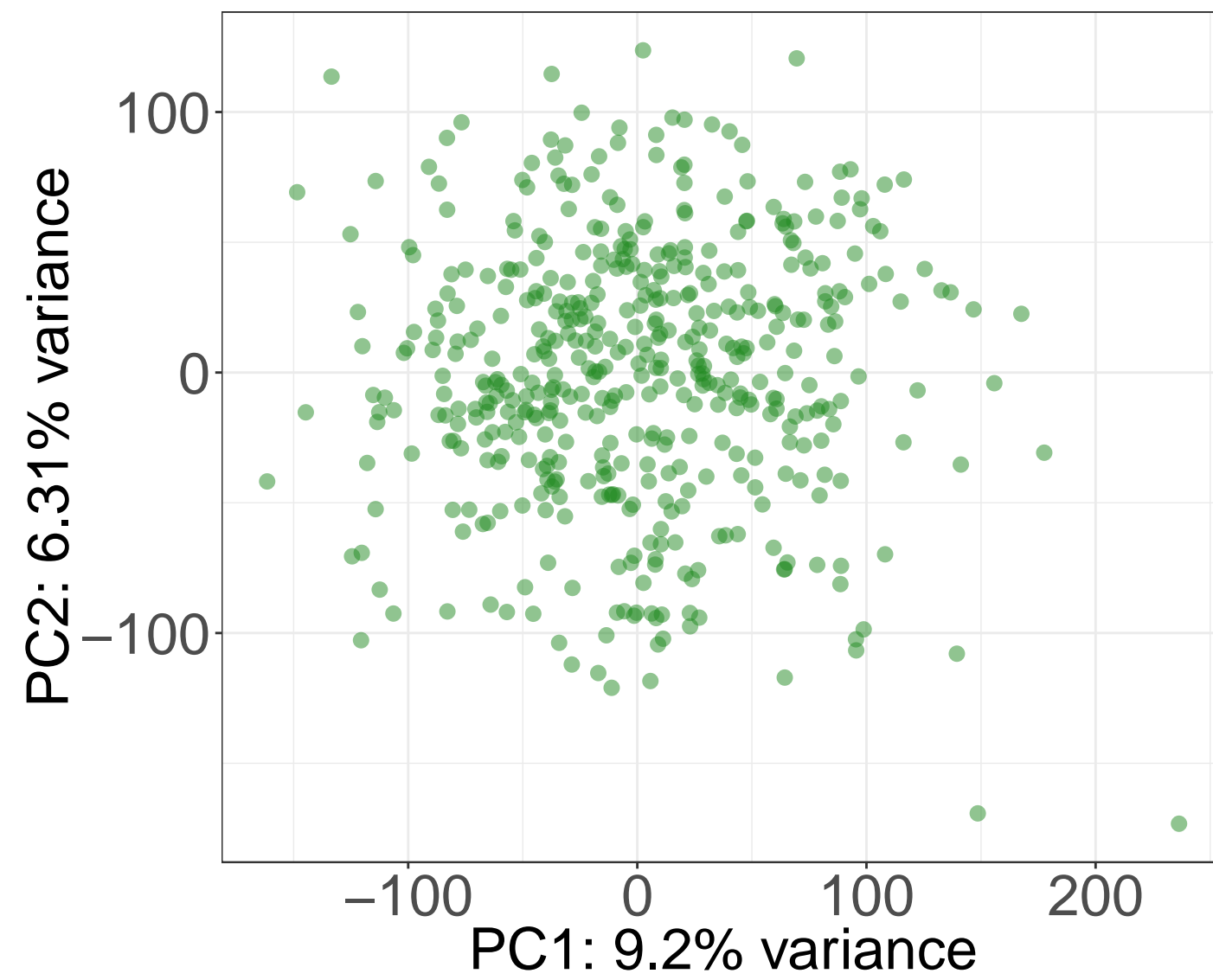
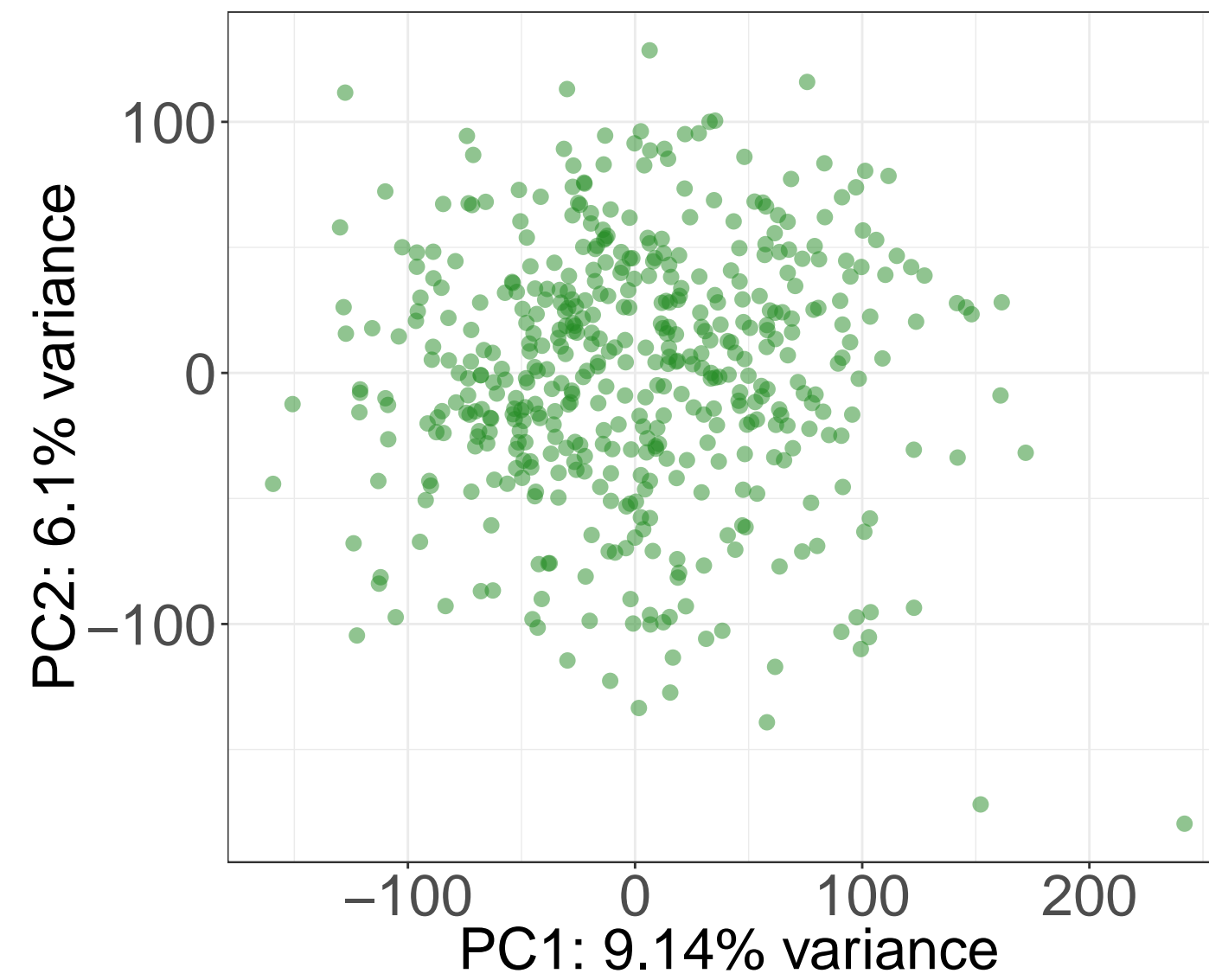
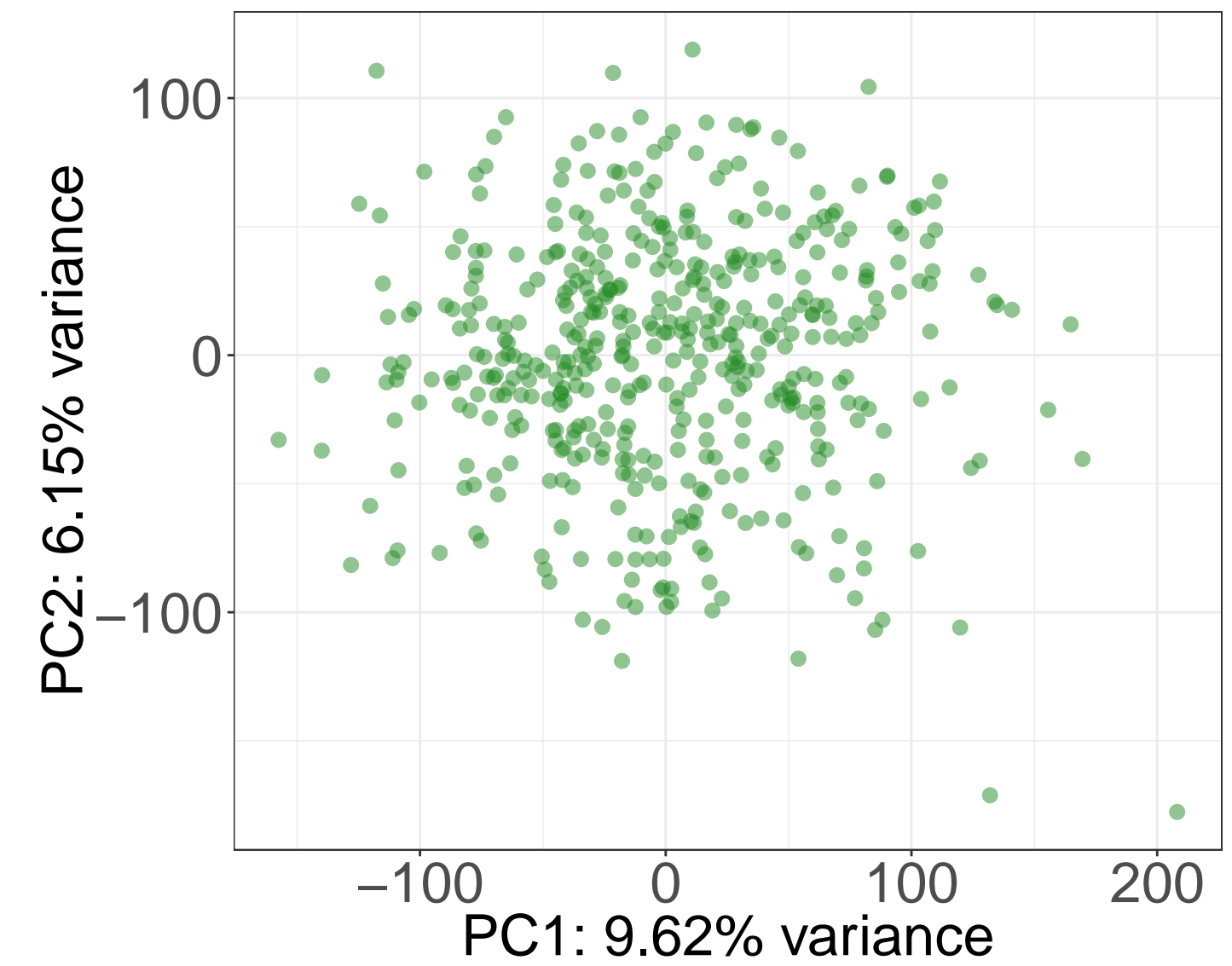
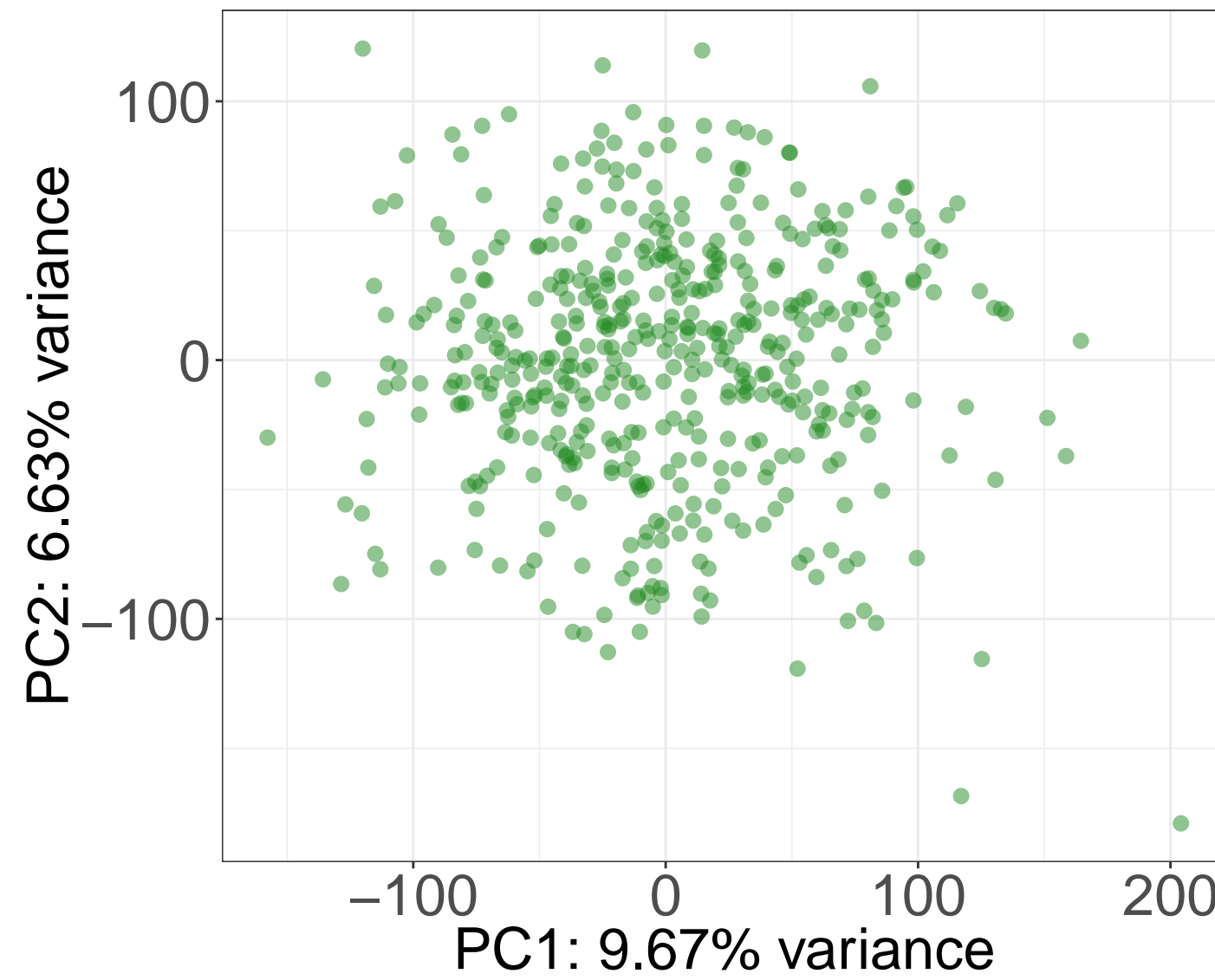
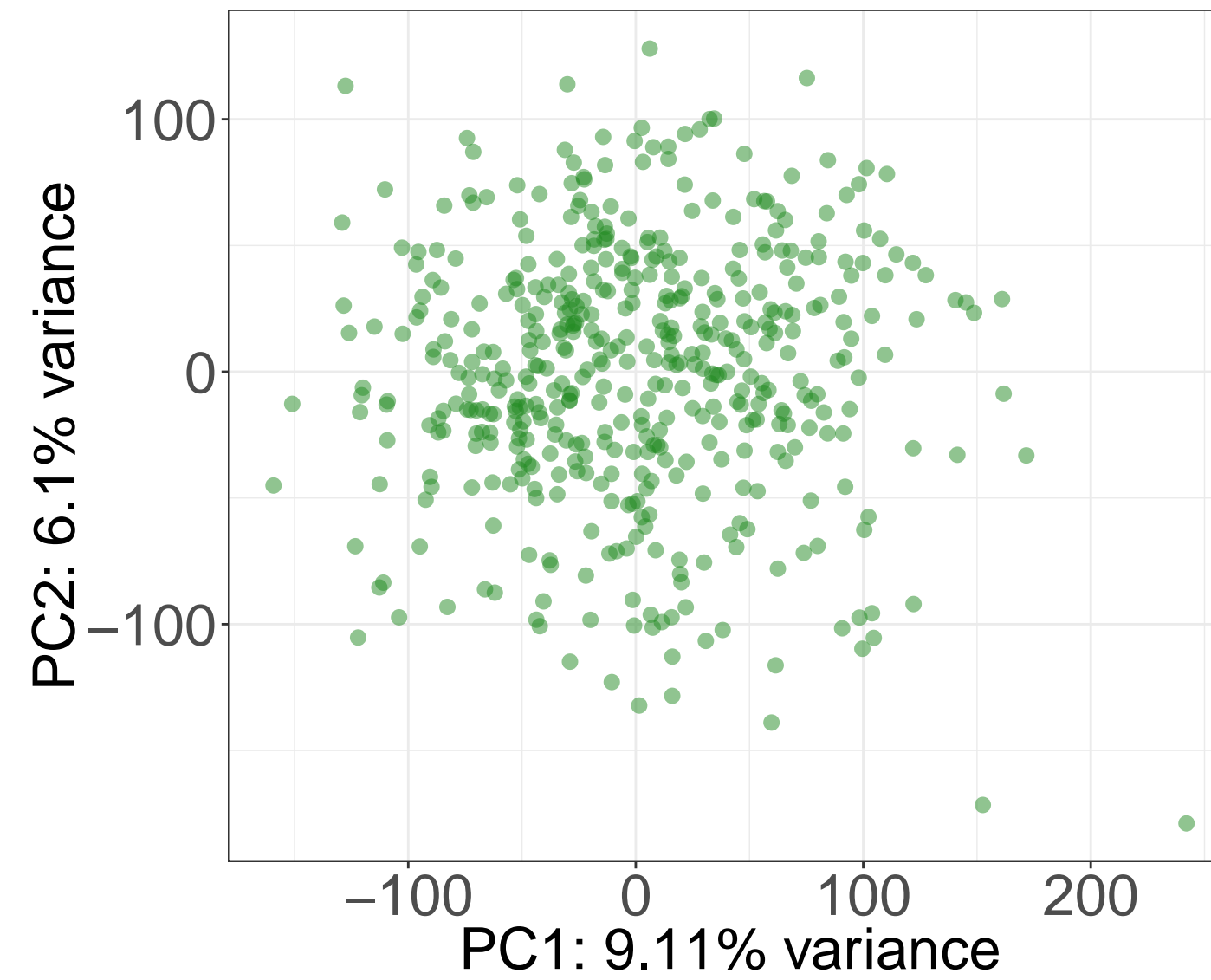
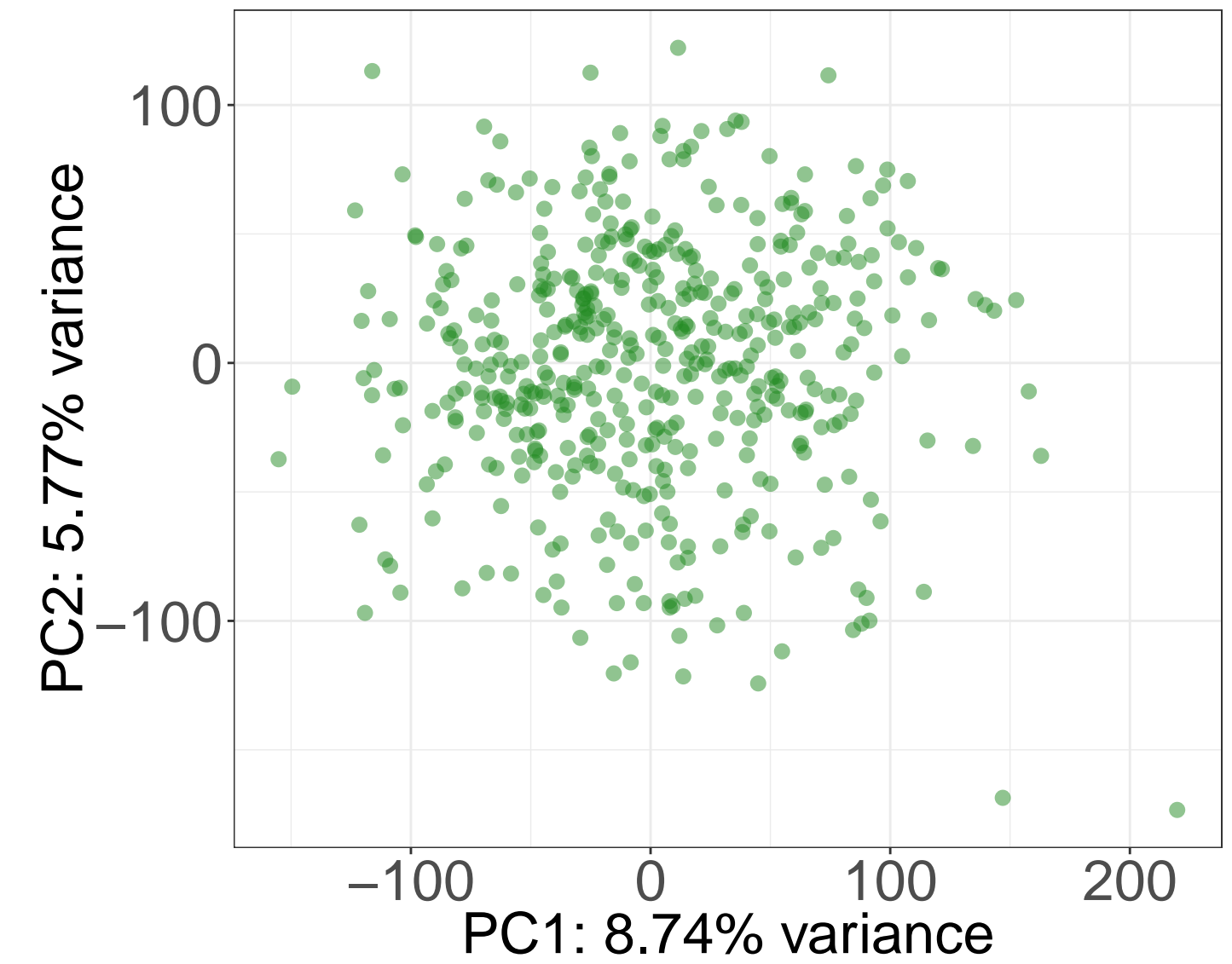
GDC**Xena/Toil****Piccolo****Recount2****MSKCC****MSKCC Batch**

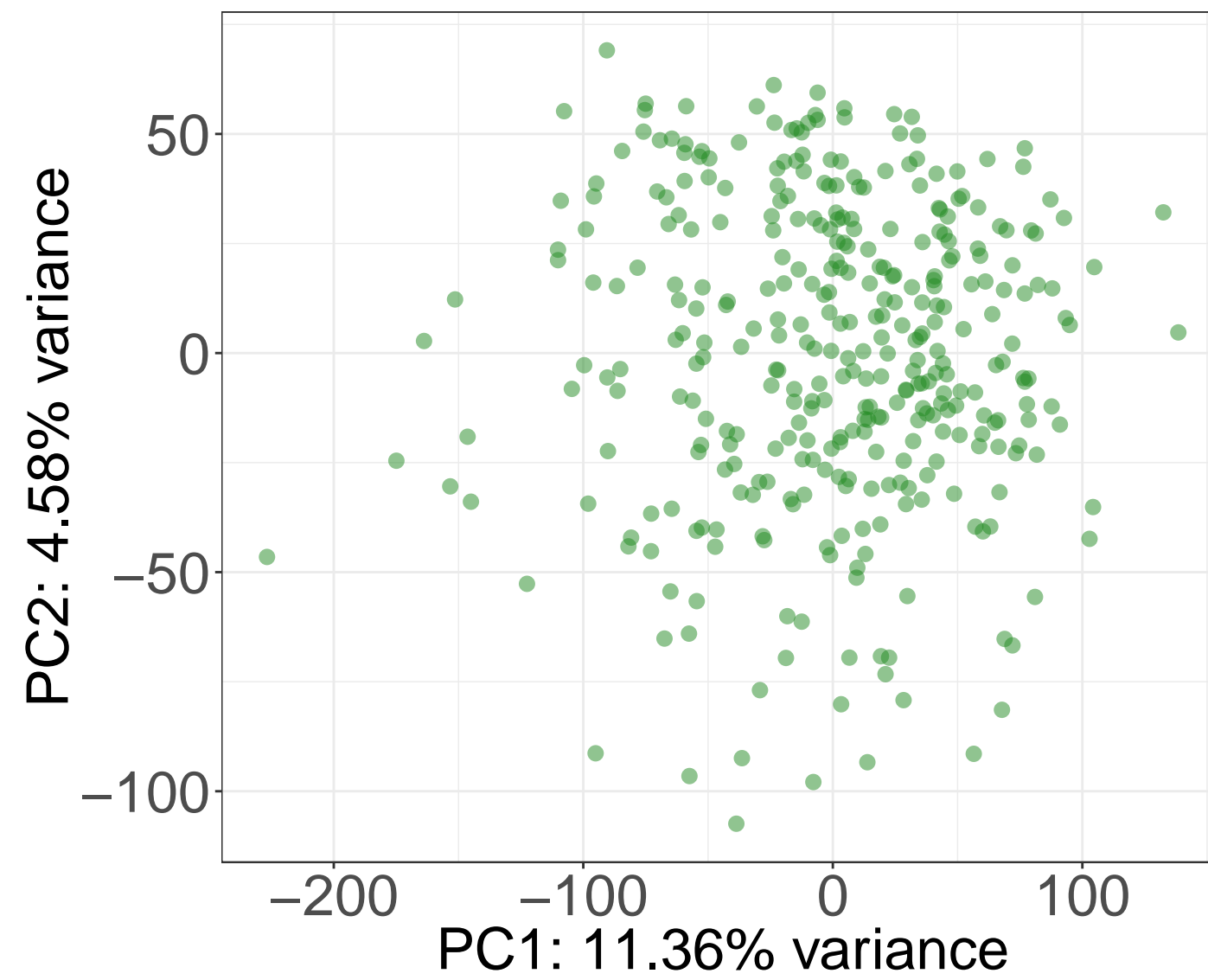
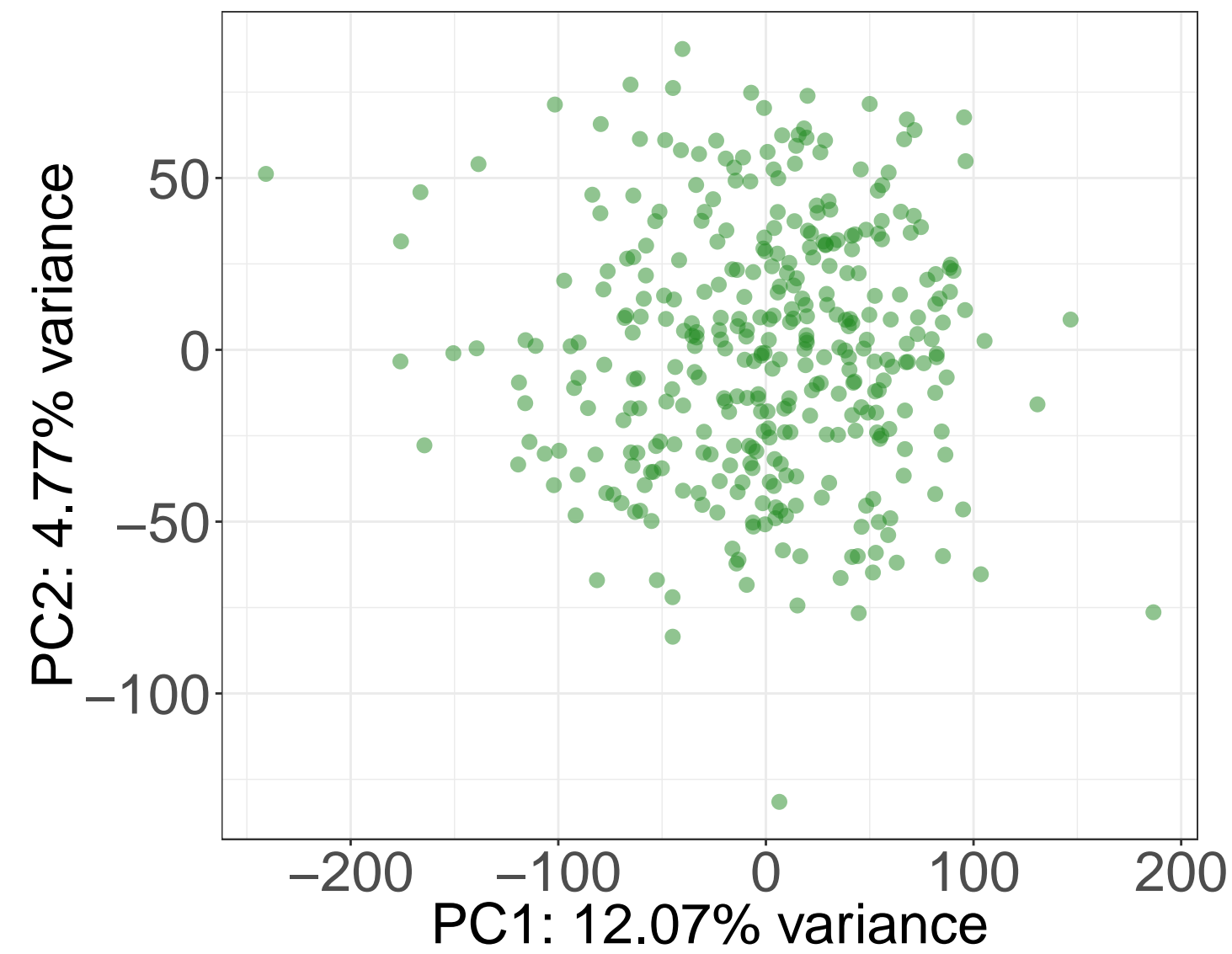
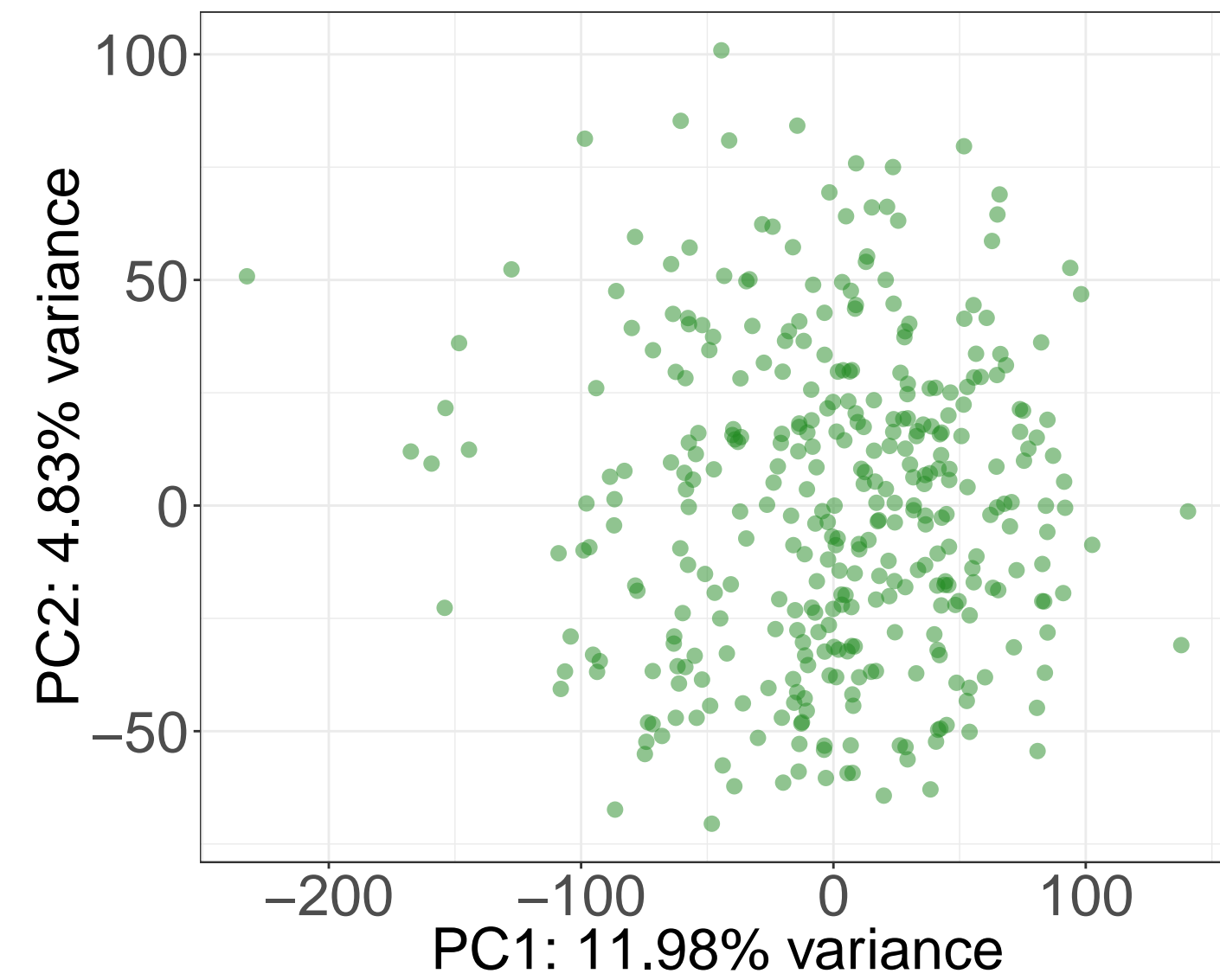
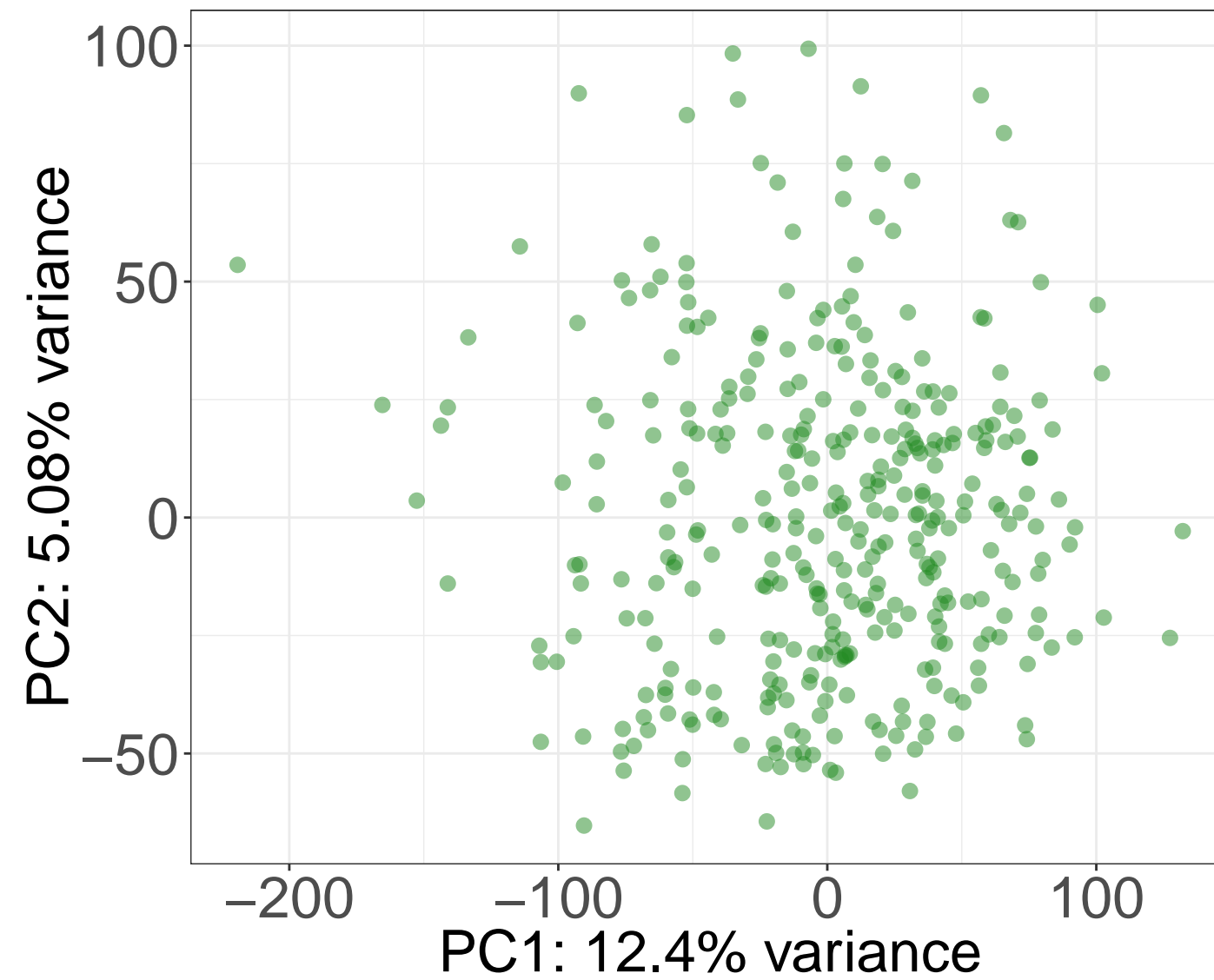
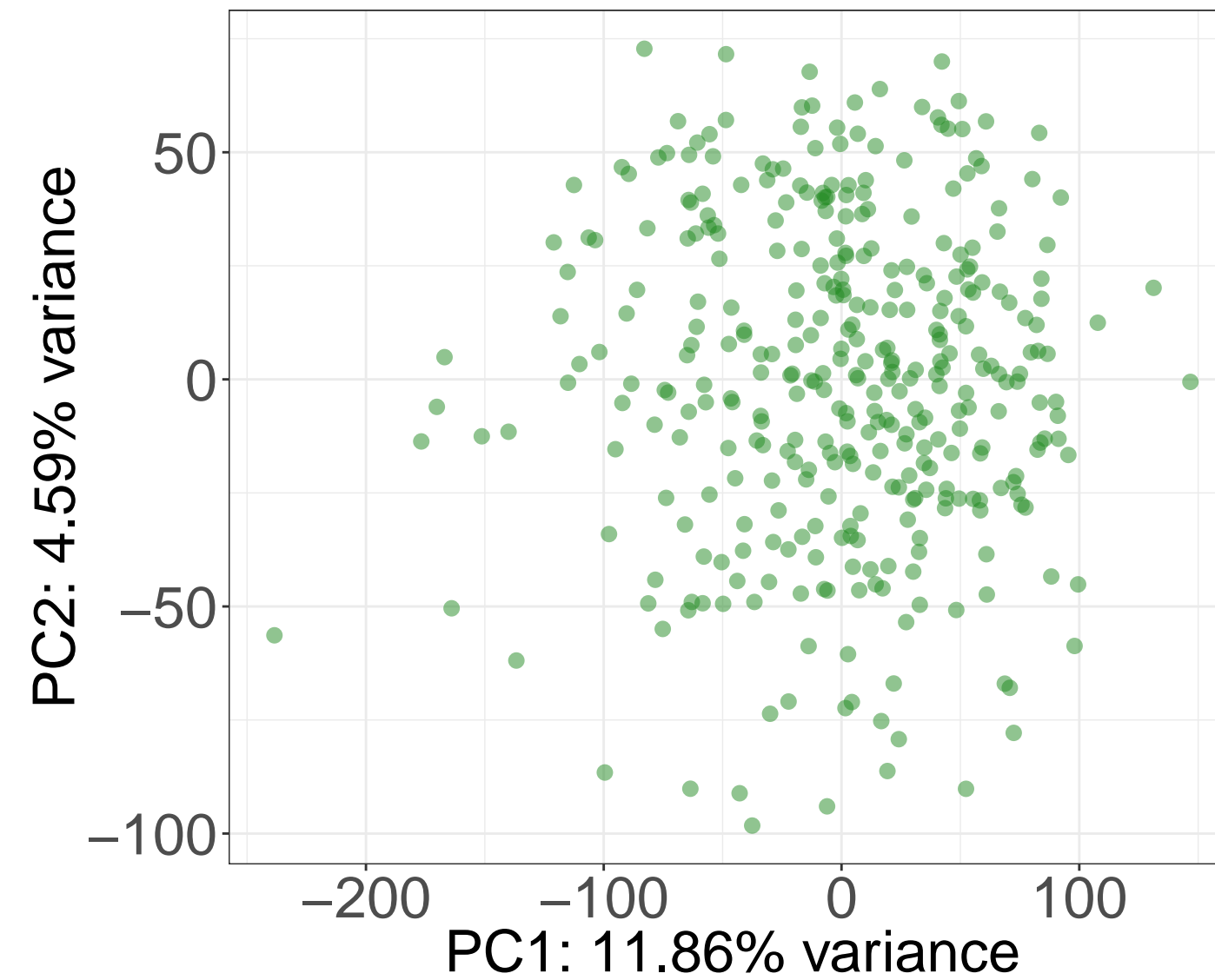
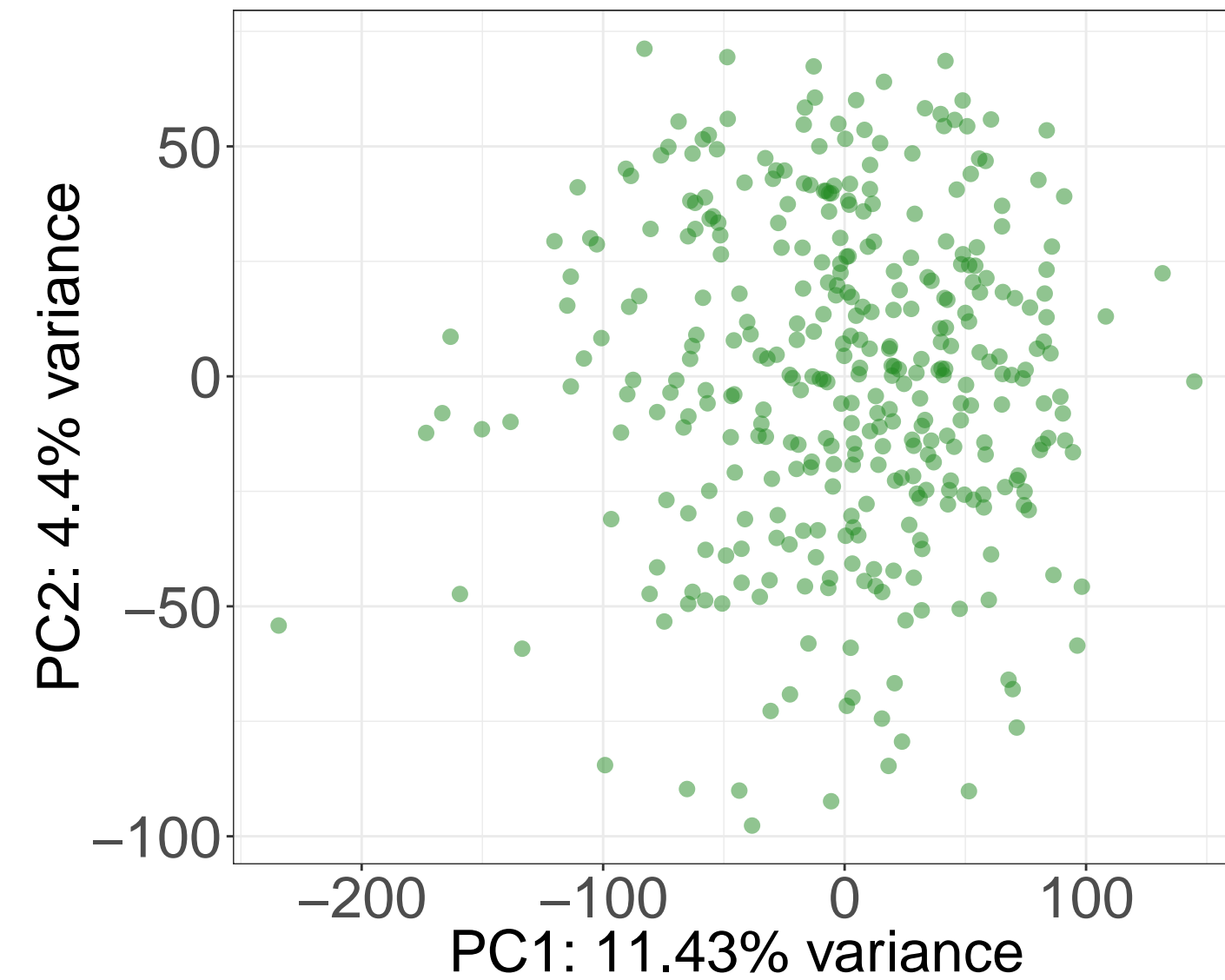
GDC**Xena/Toil****Piccolo****Recount2****MSKCC****MSKCC Batch**

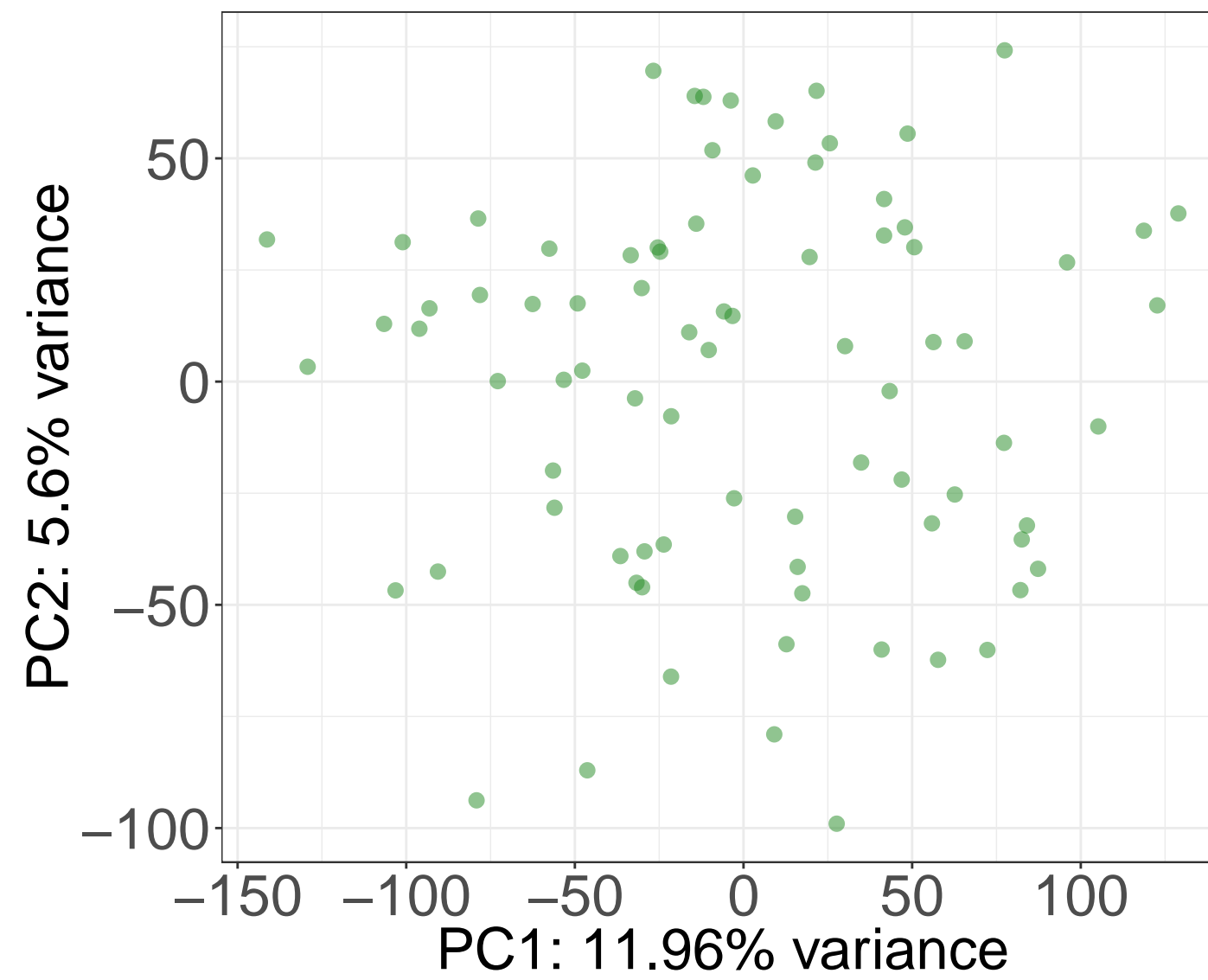
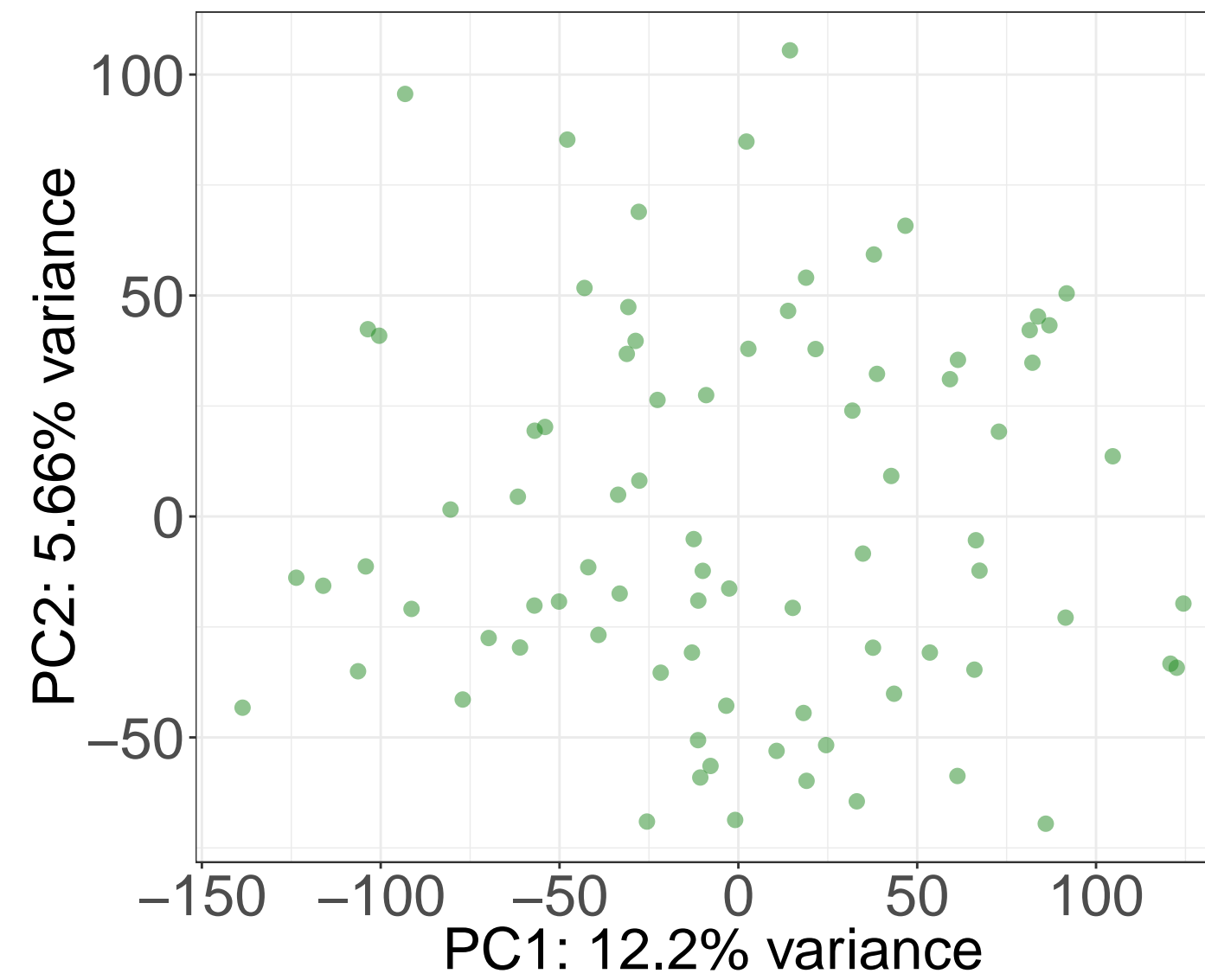
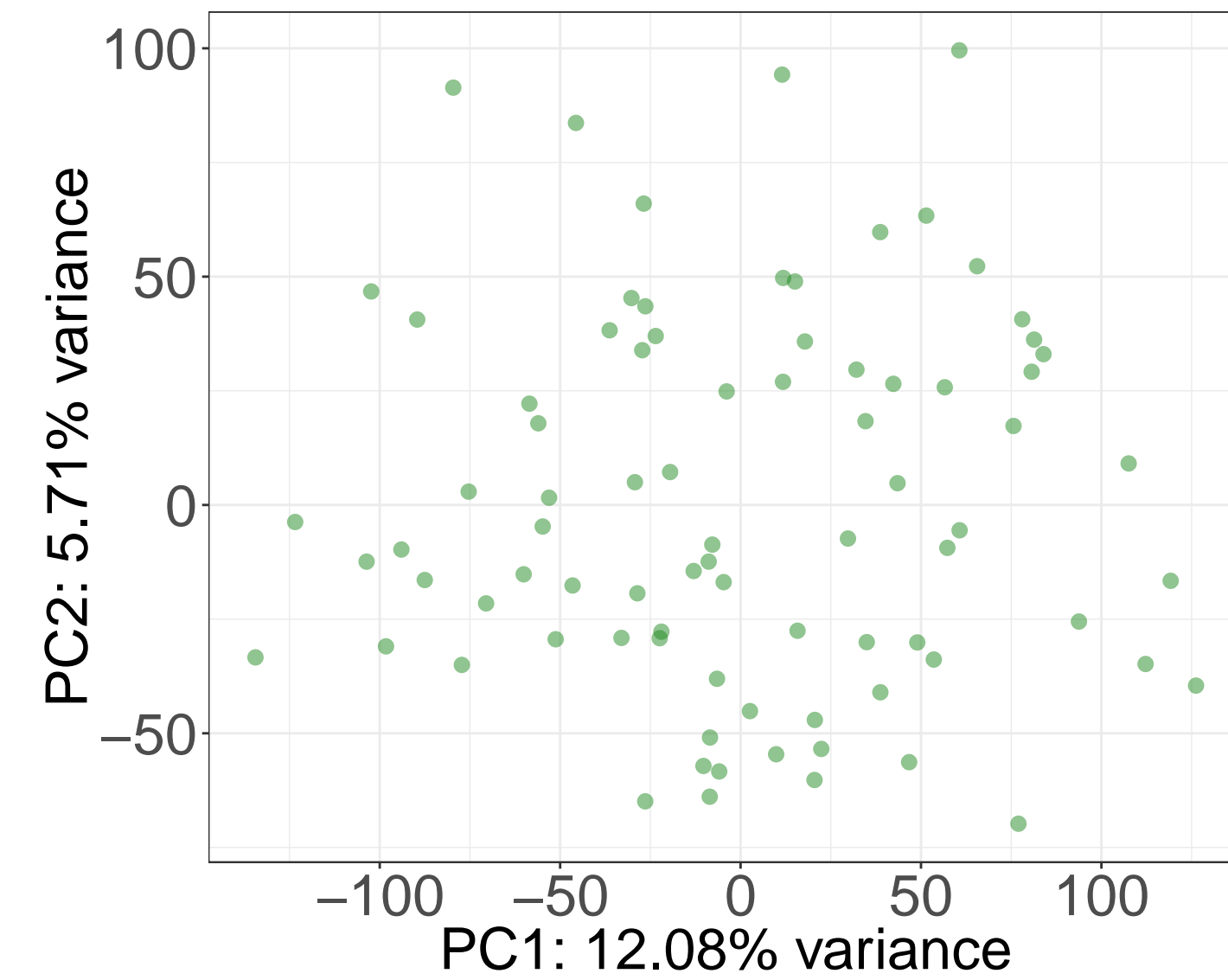
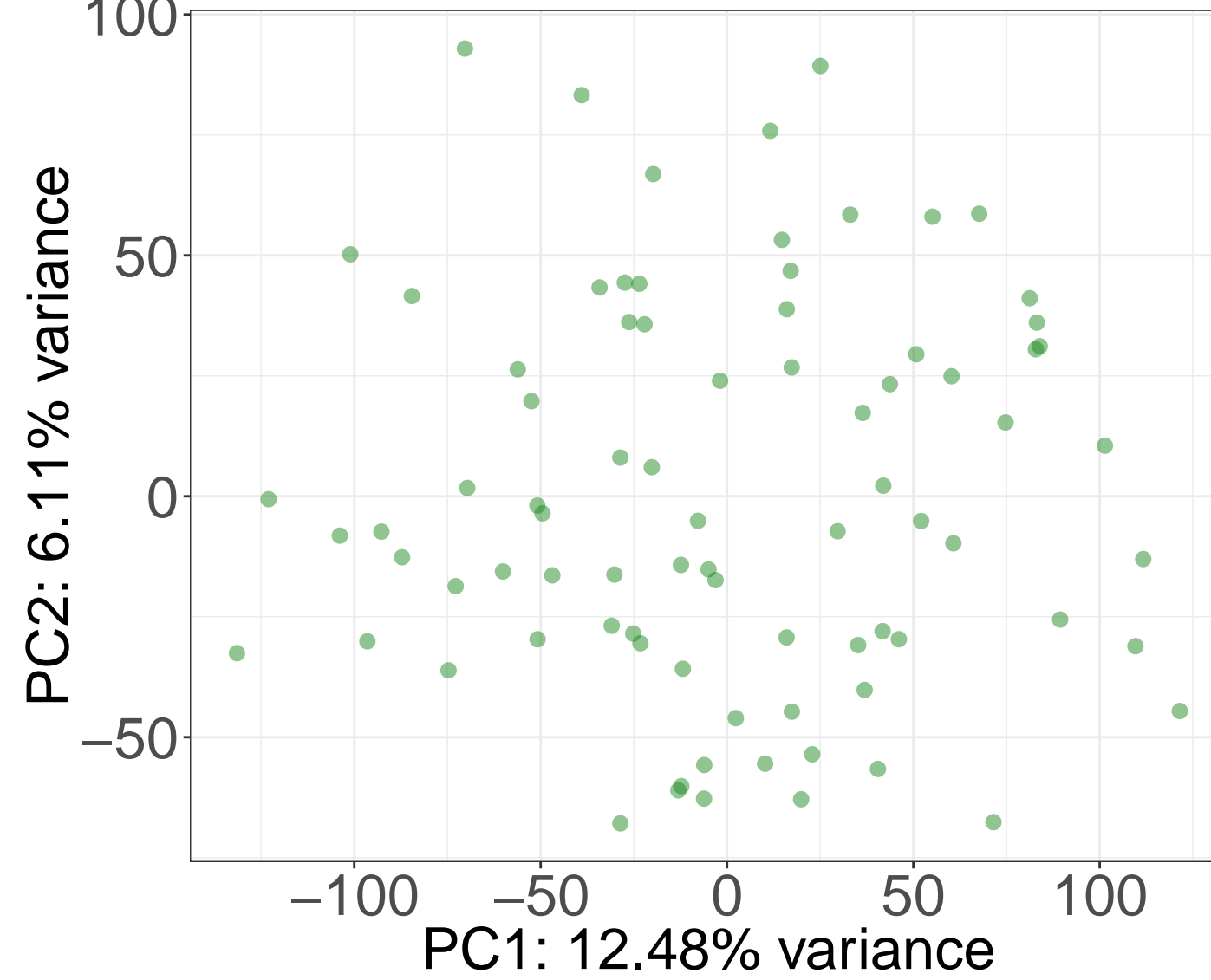
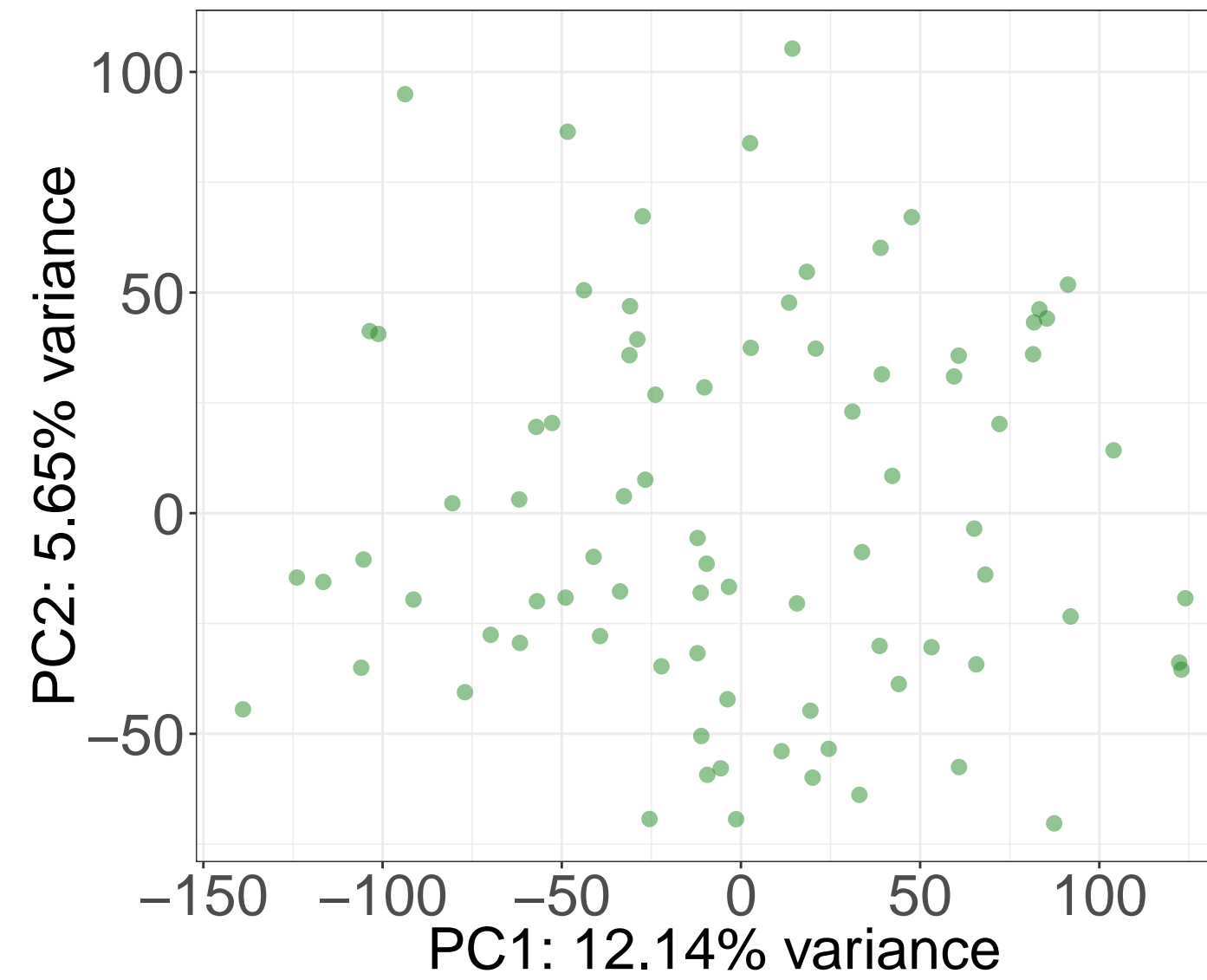
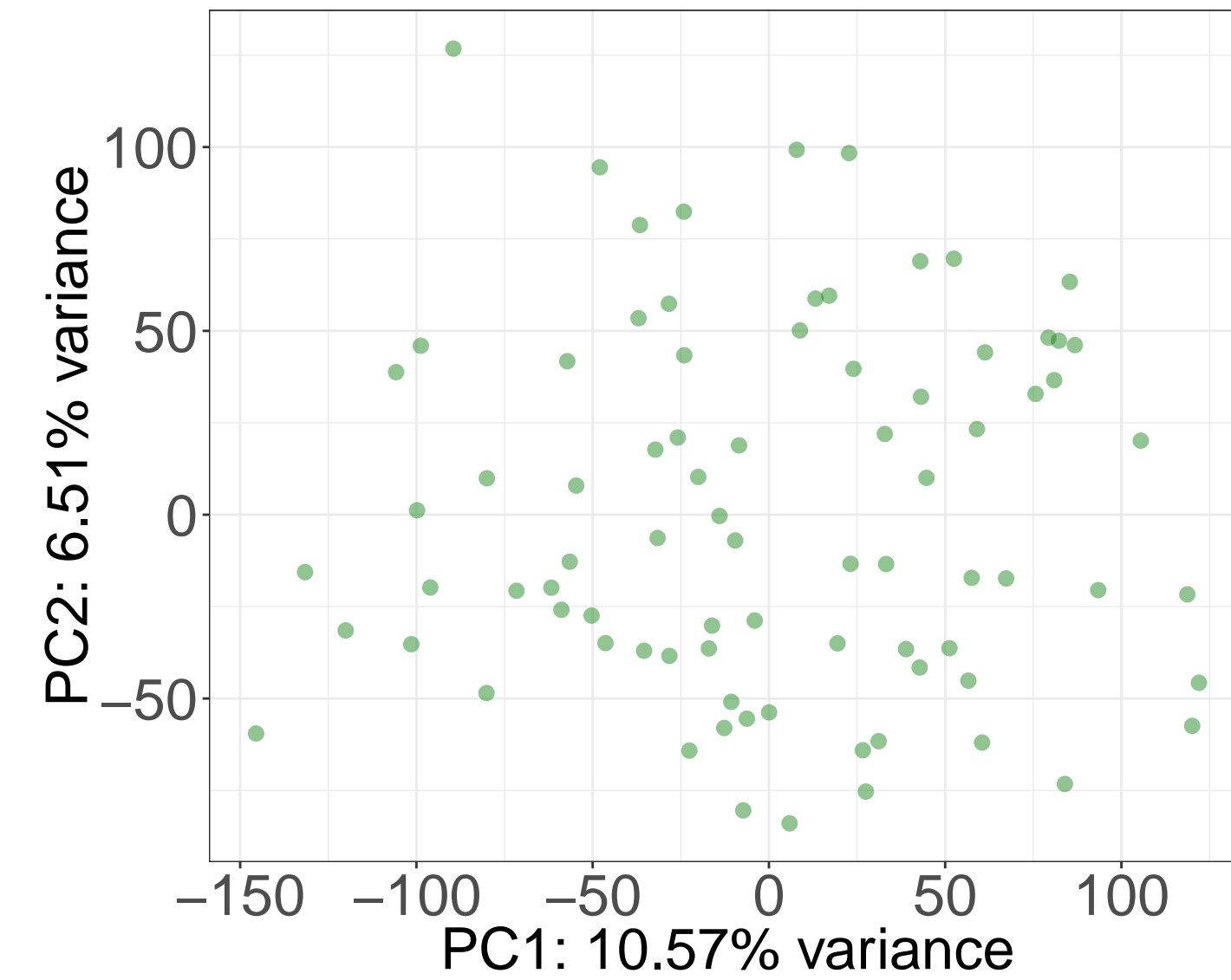
GDC**Xena/Toil****Piccolo****Recount2****MSKCC****MSKCC Batch**

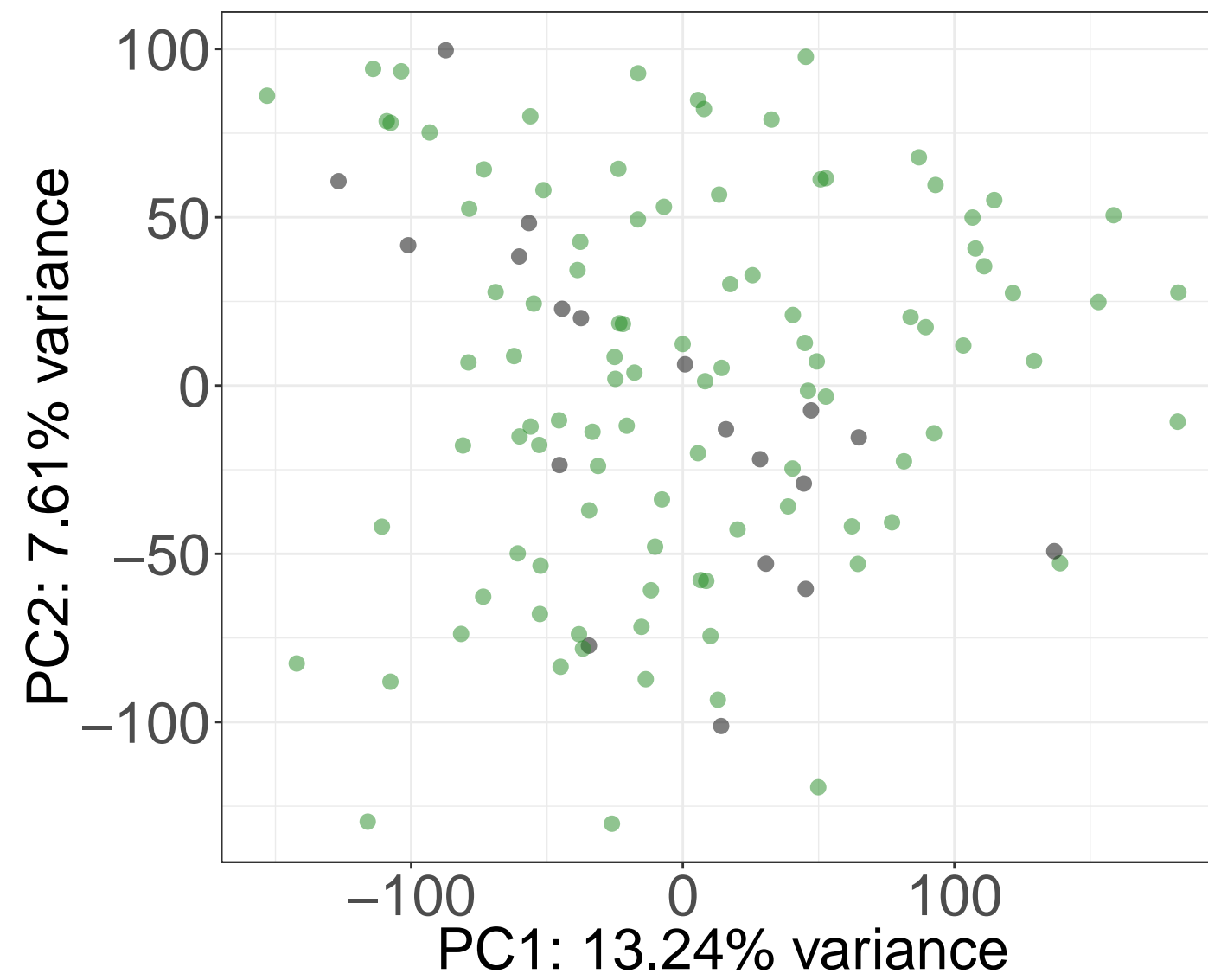
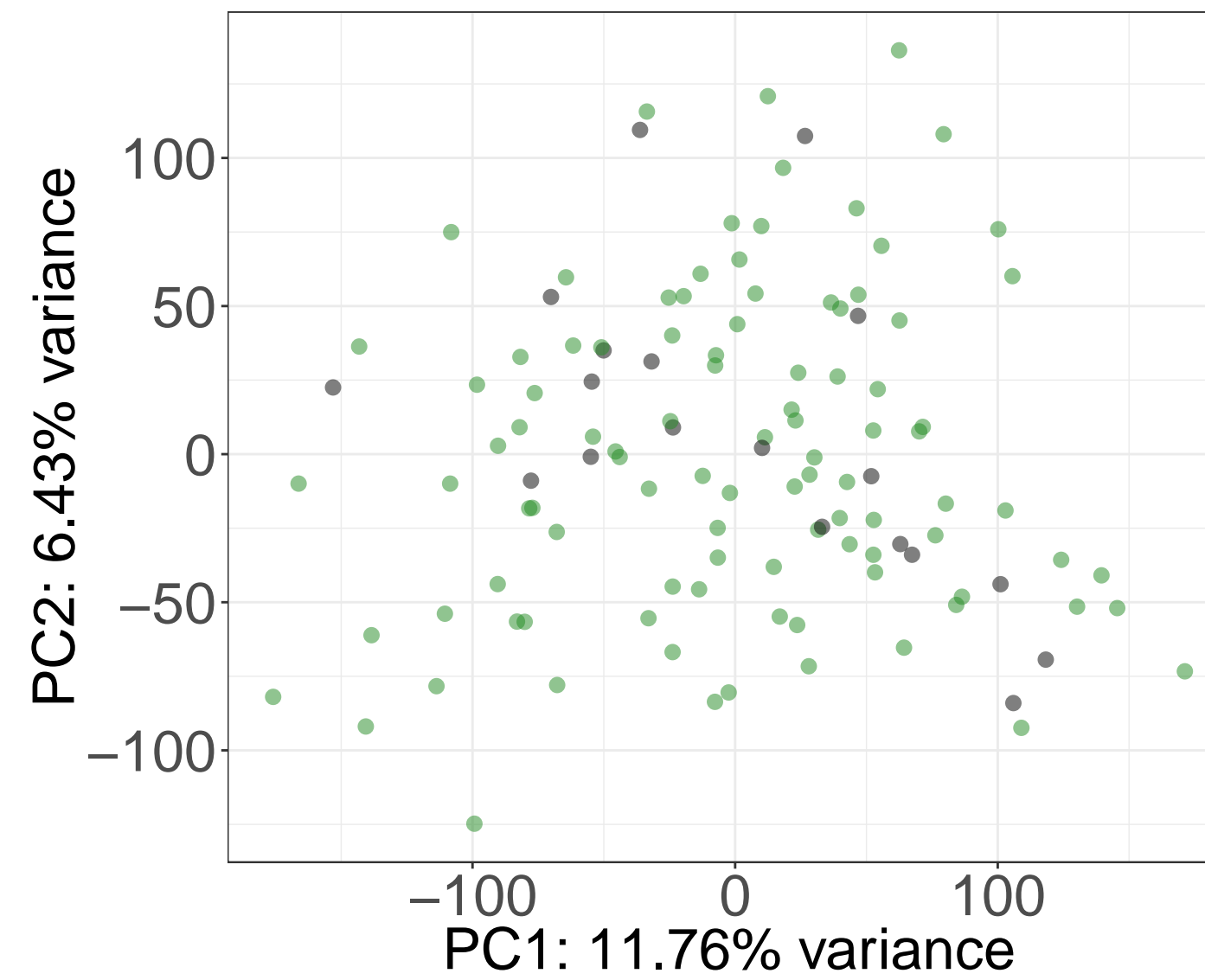
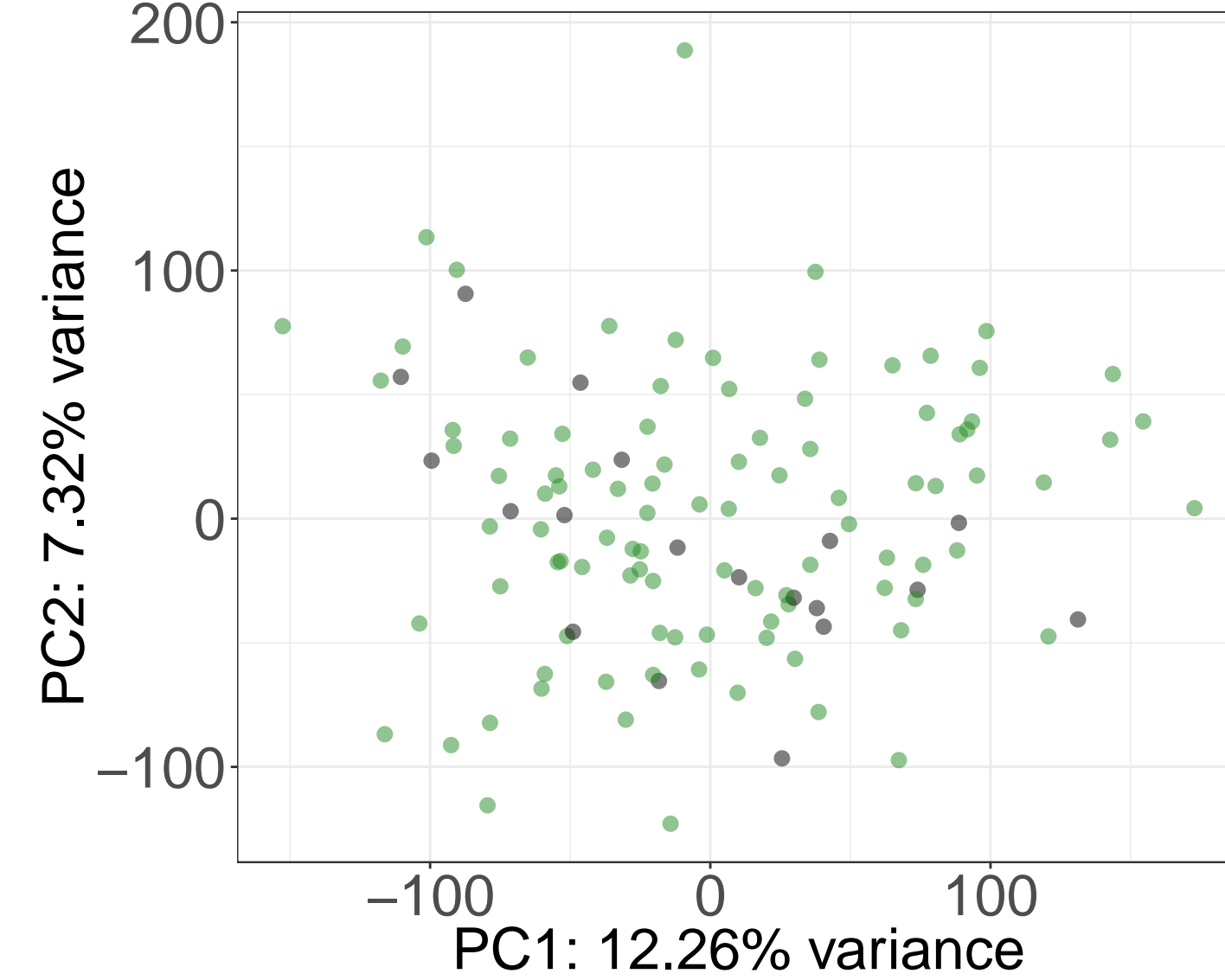
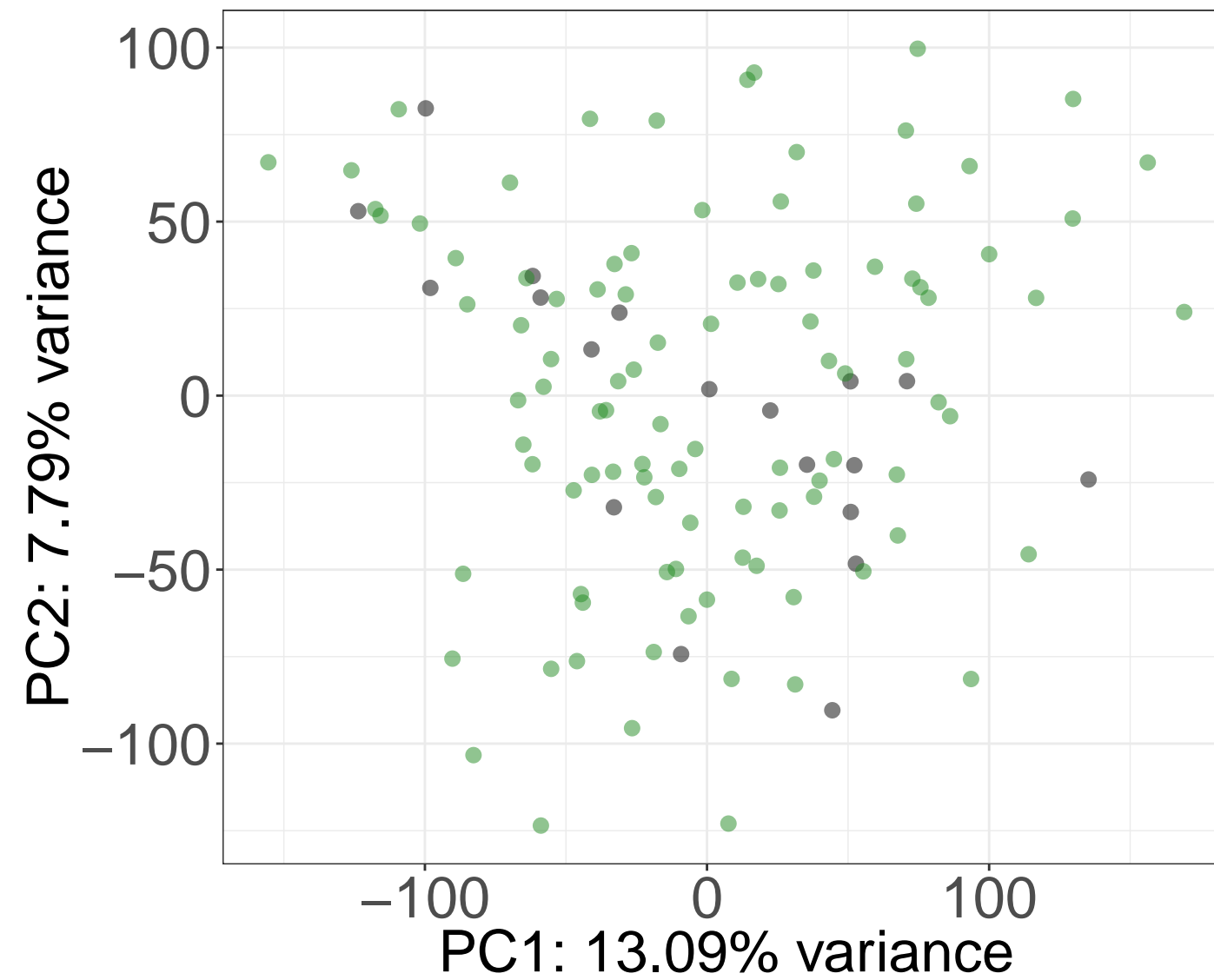
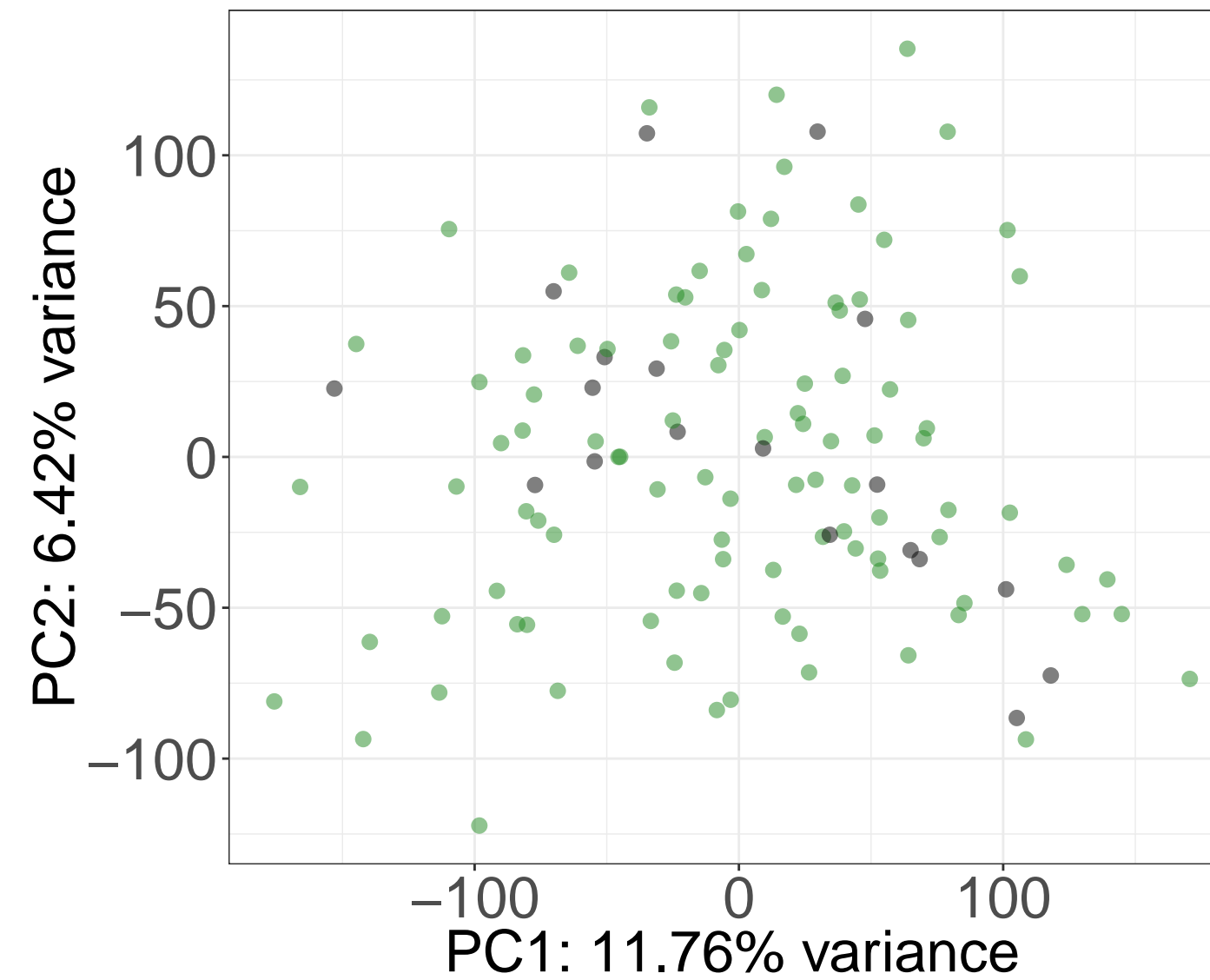
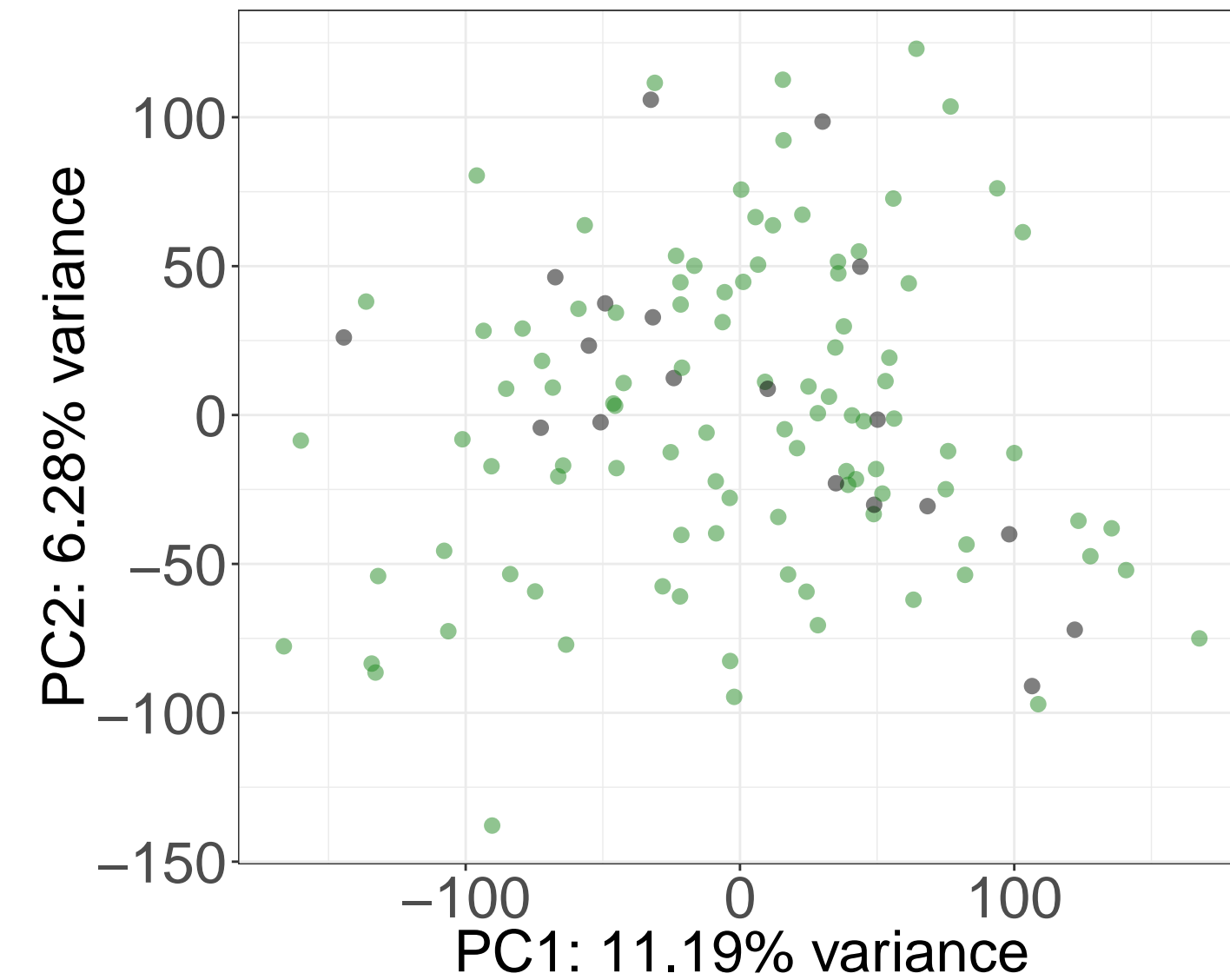
GDC**Xena/Toil****Piccolo****Recount2****MSKCC****MSKCC Batch**

GDC**Xena/Toil****Piccolo****Recount2****MSKCC****MSKCC Batch**

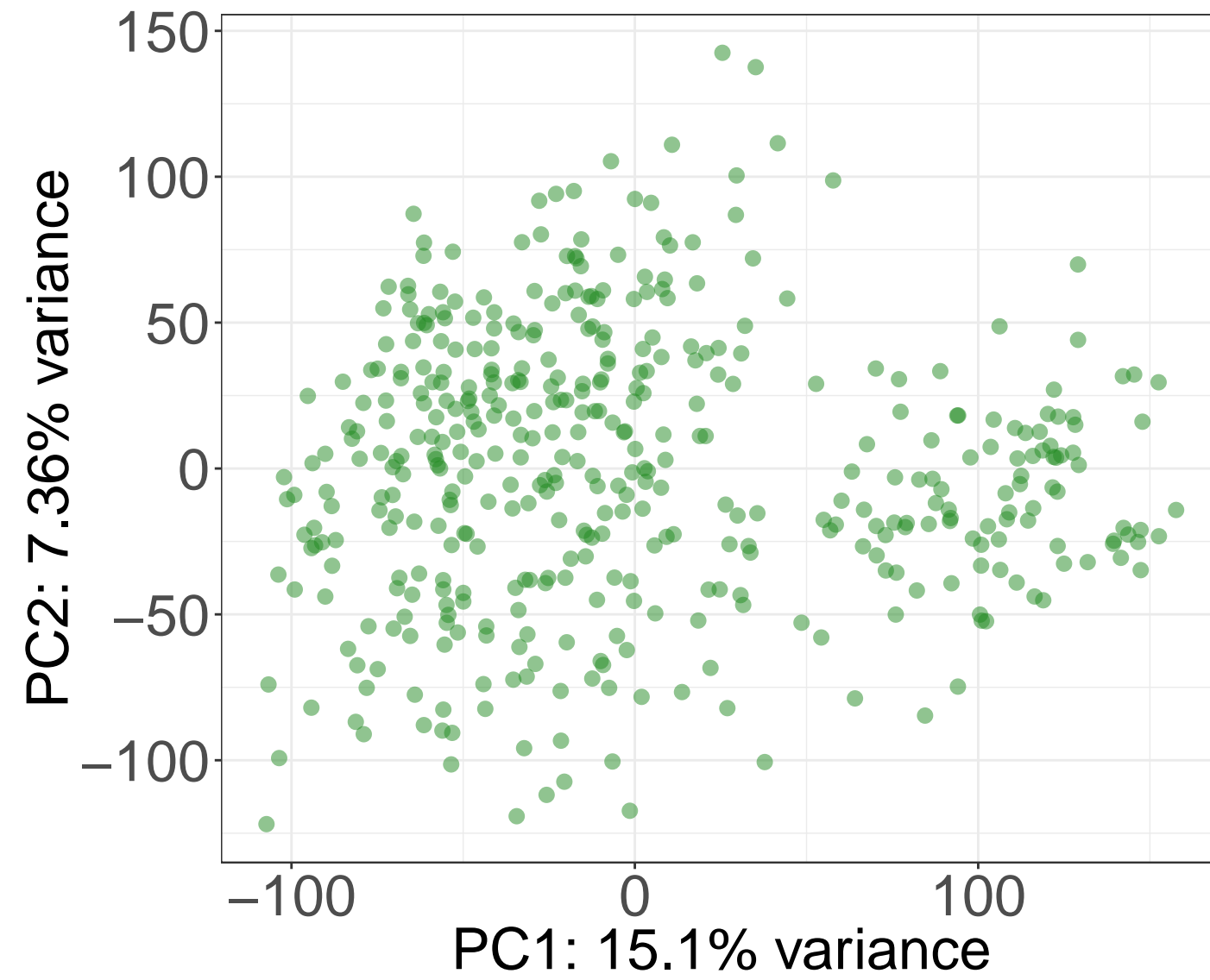
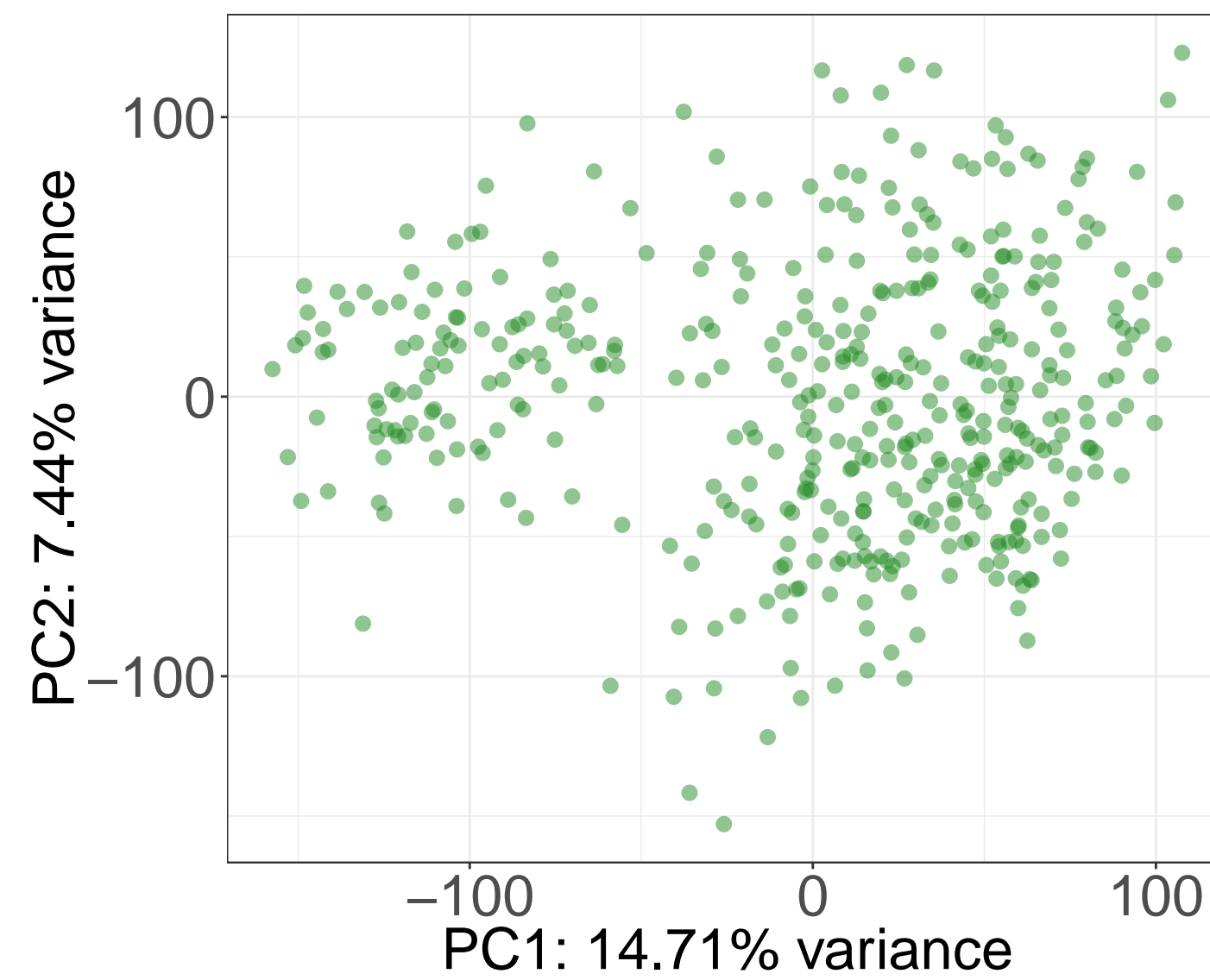
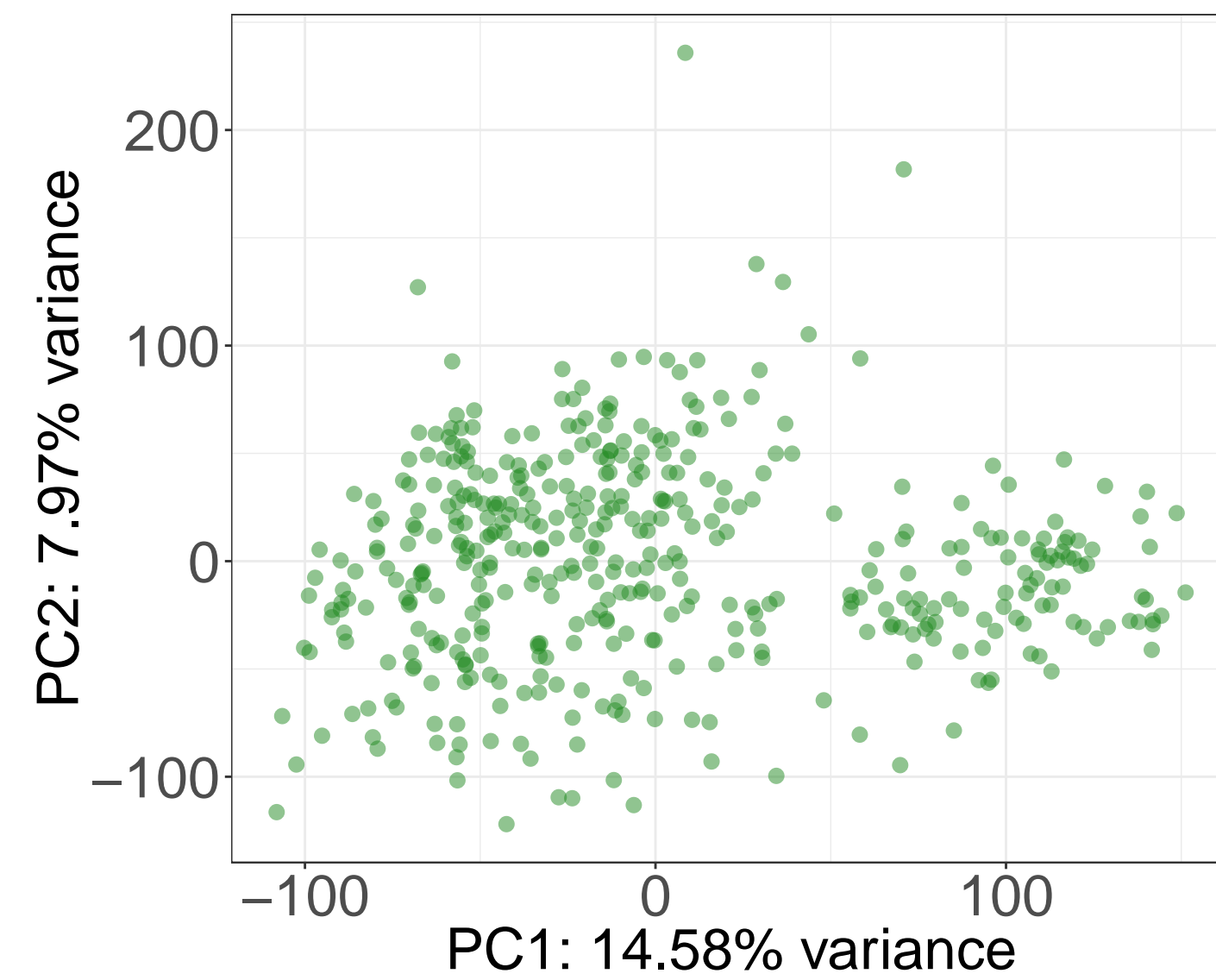
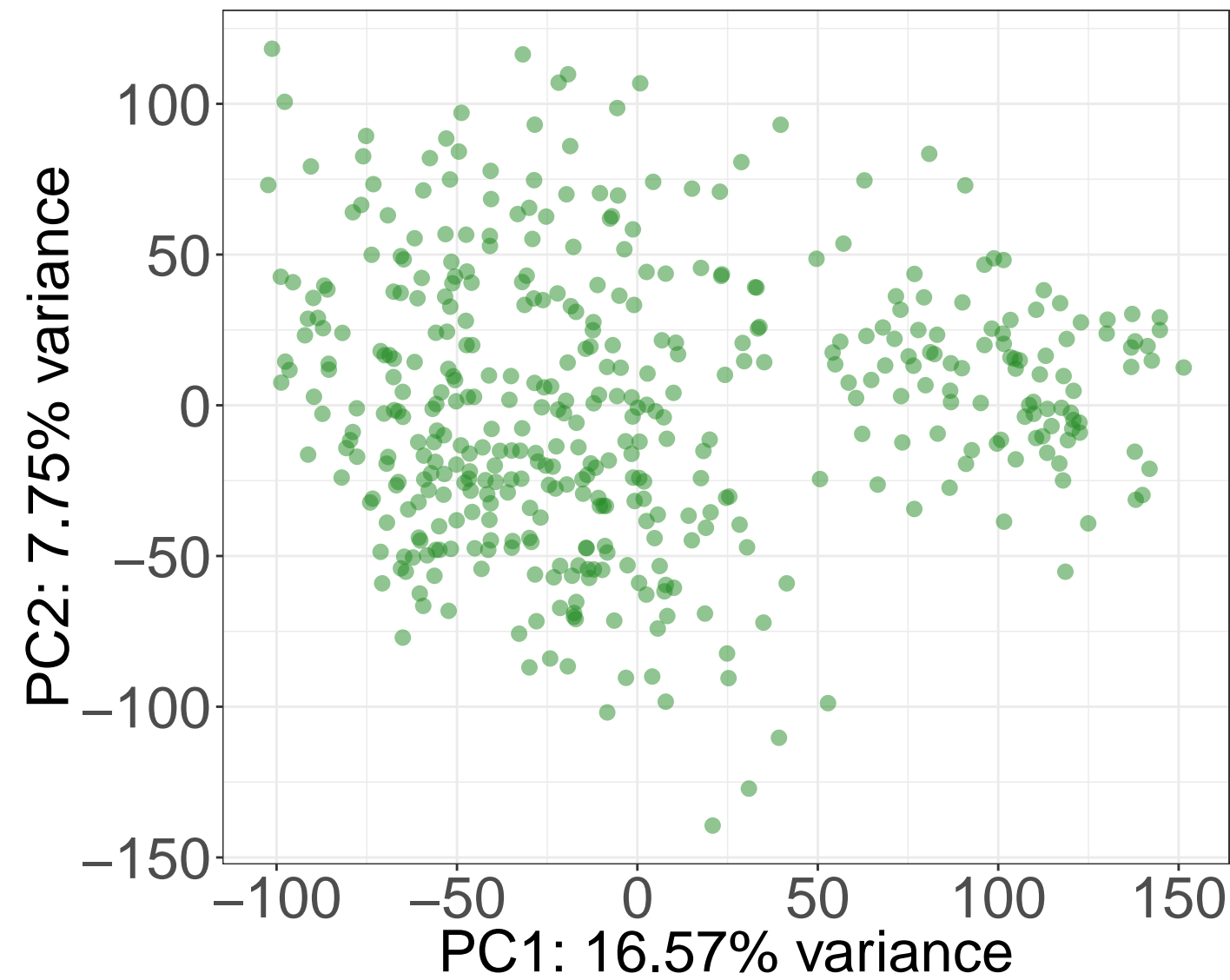
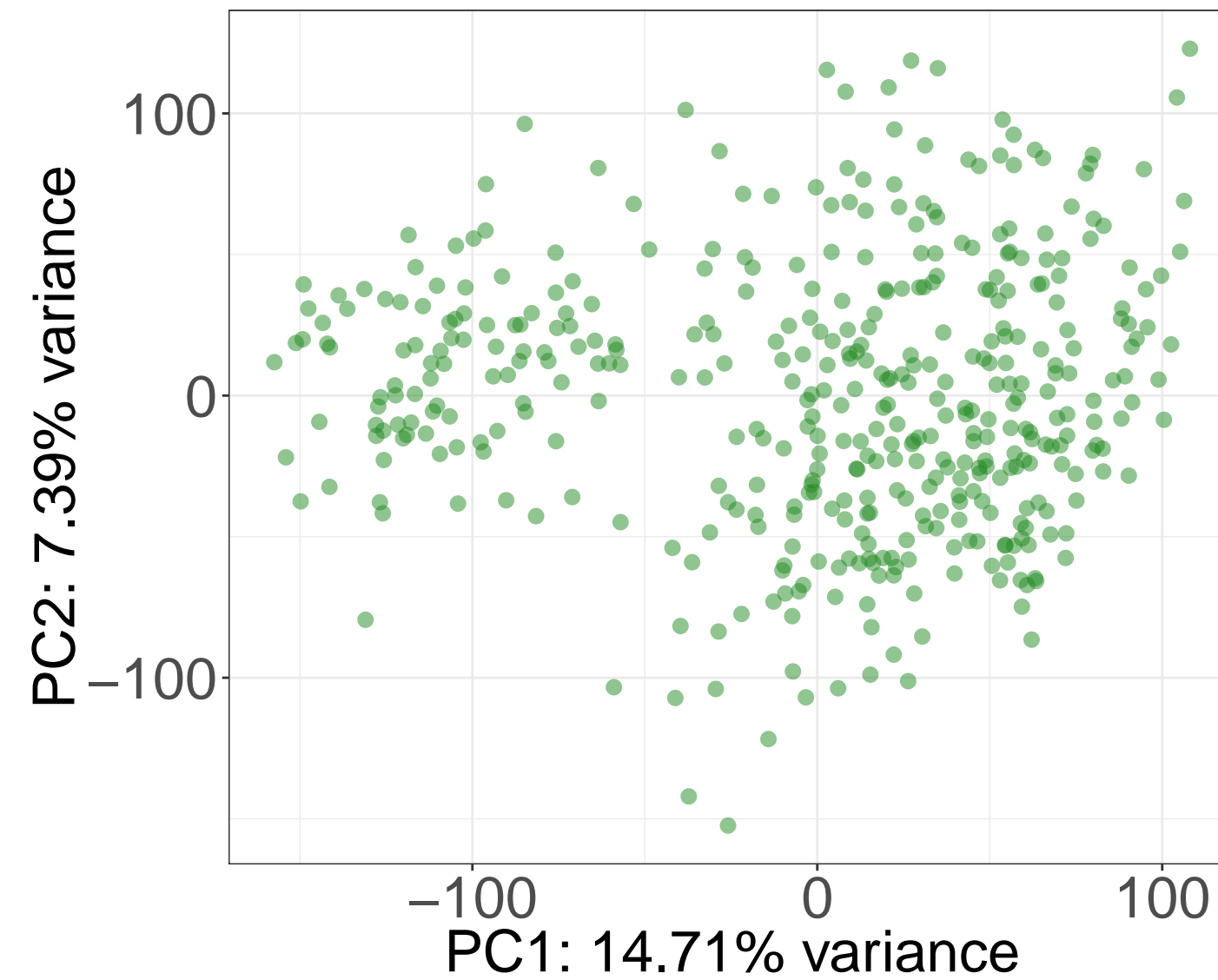
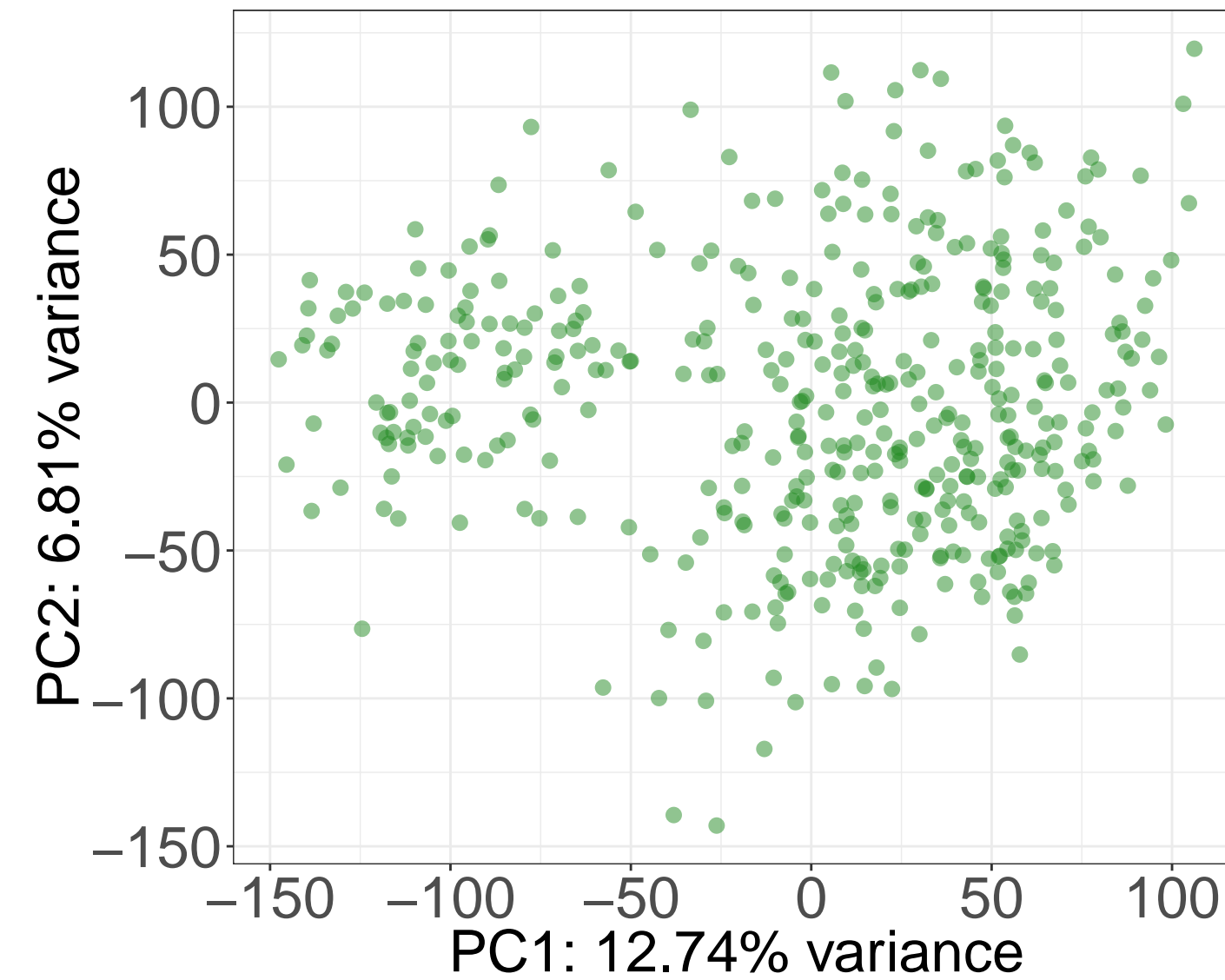
GDC**Xena/Toil****Piccolo****Recount2****MSKCC****MSKCC Batch**

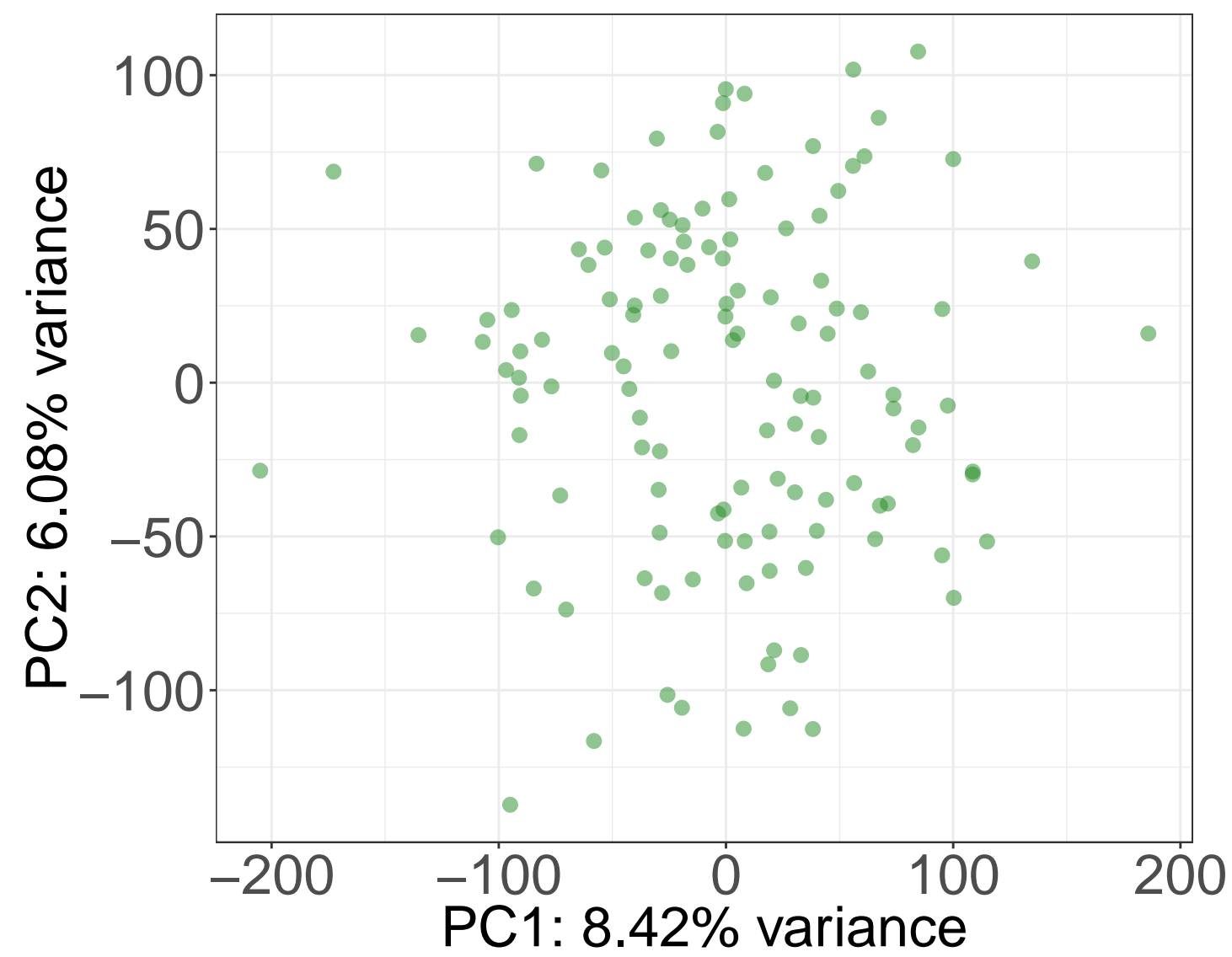
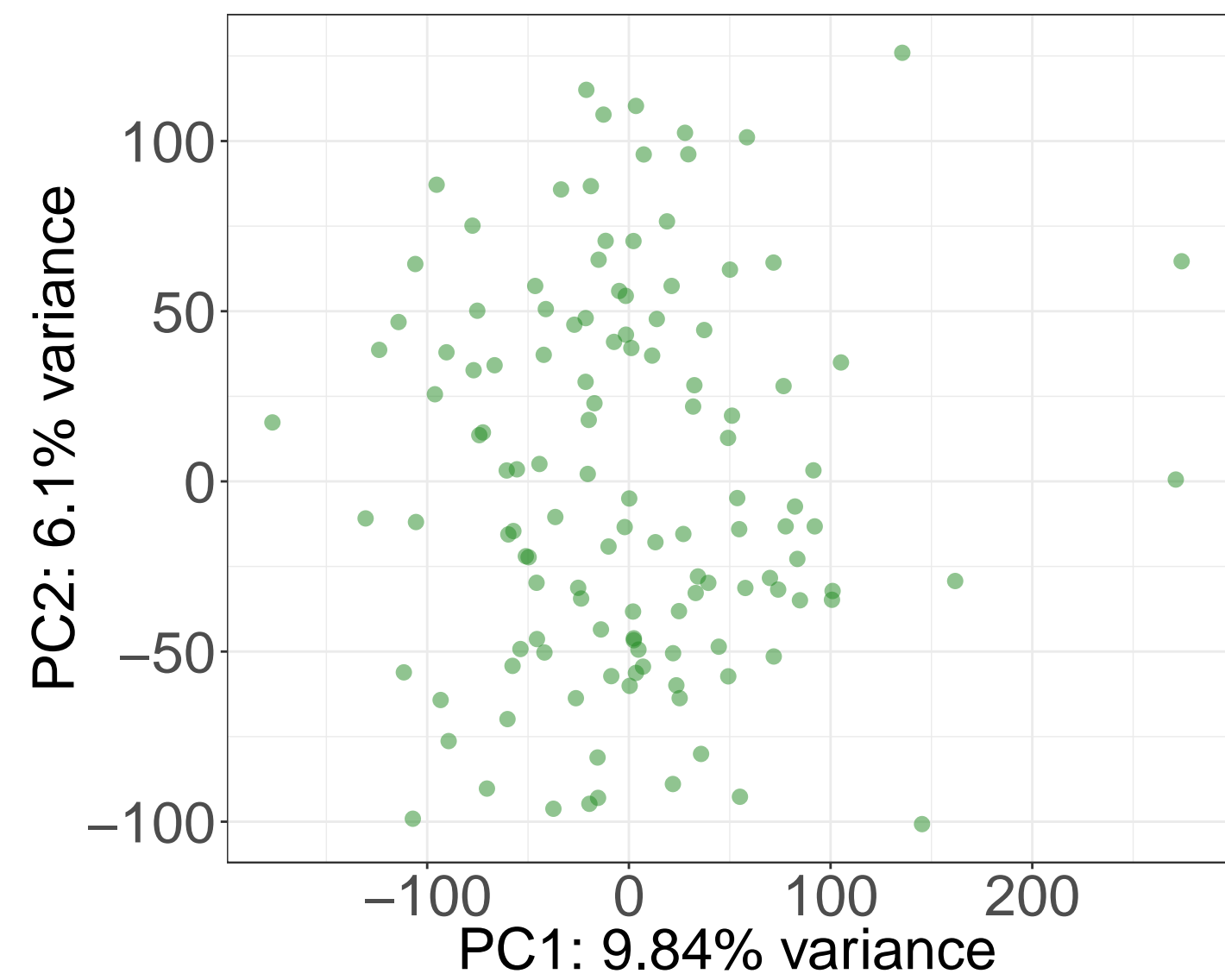
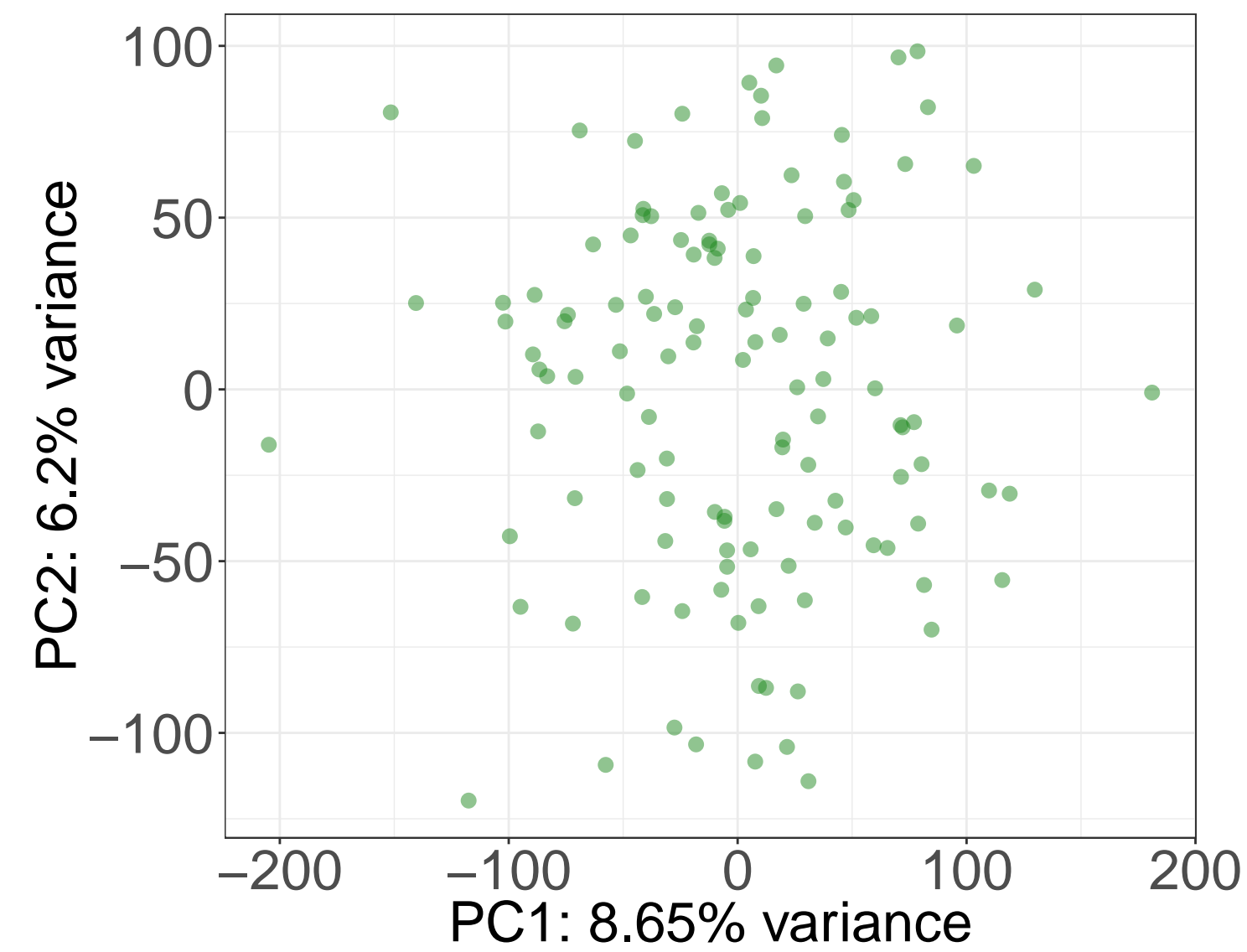
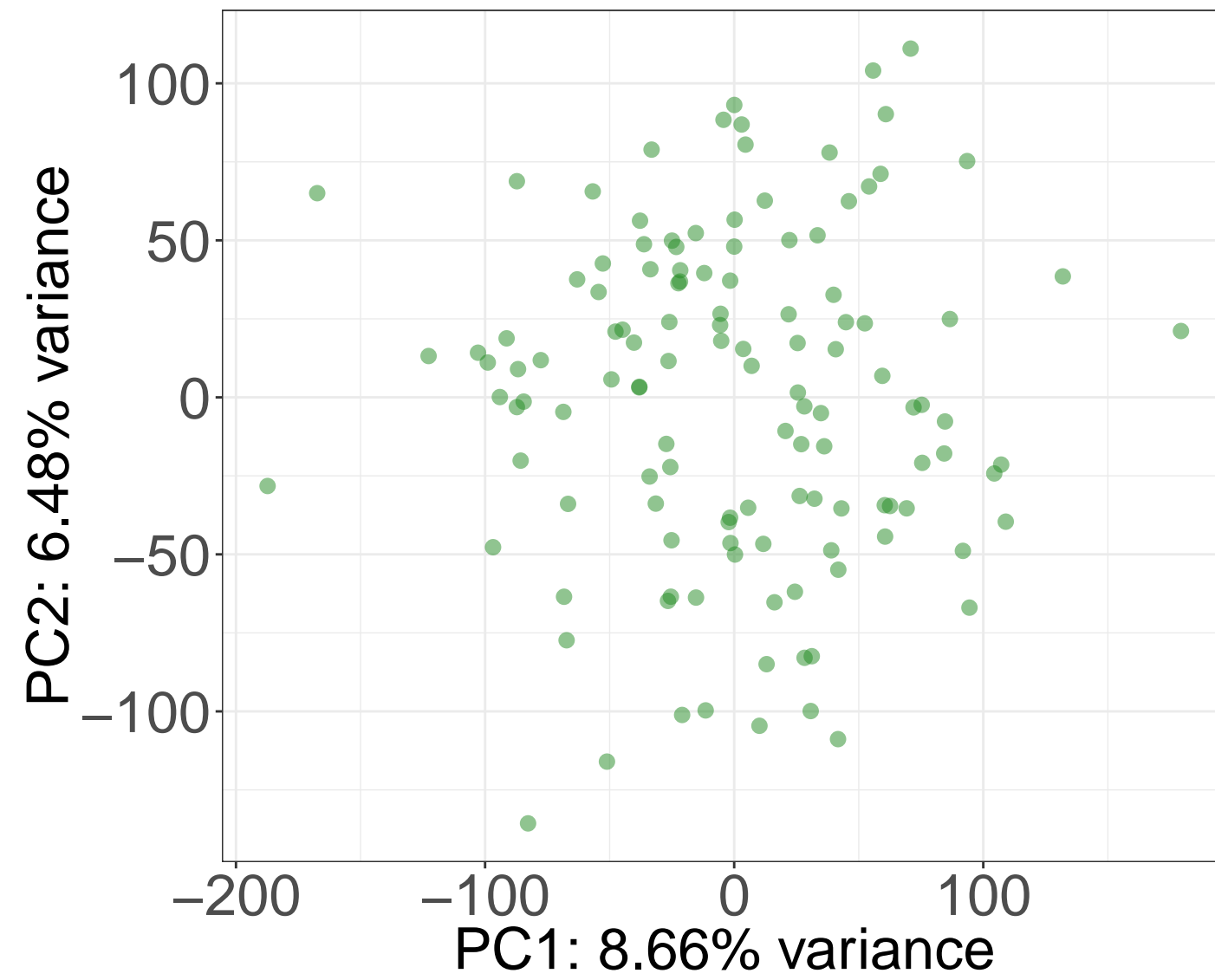
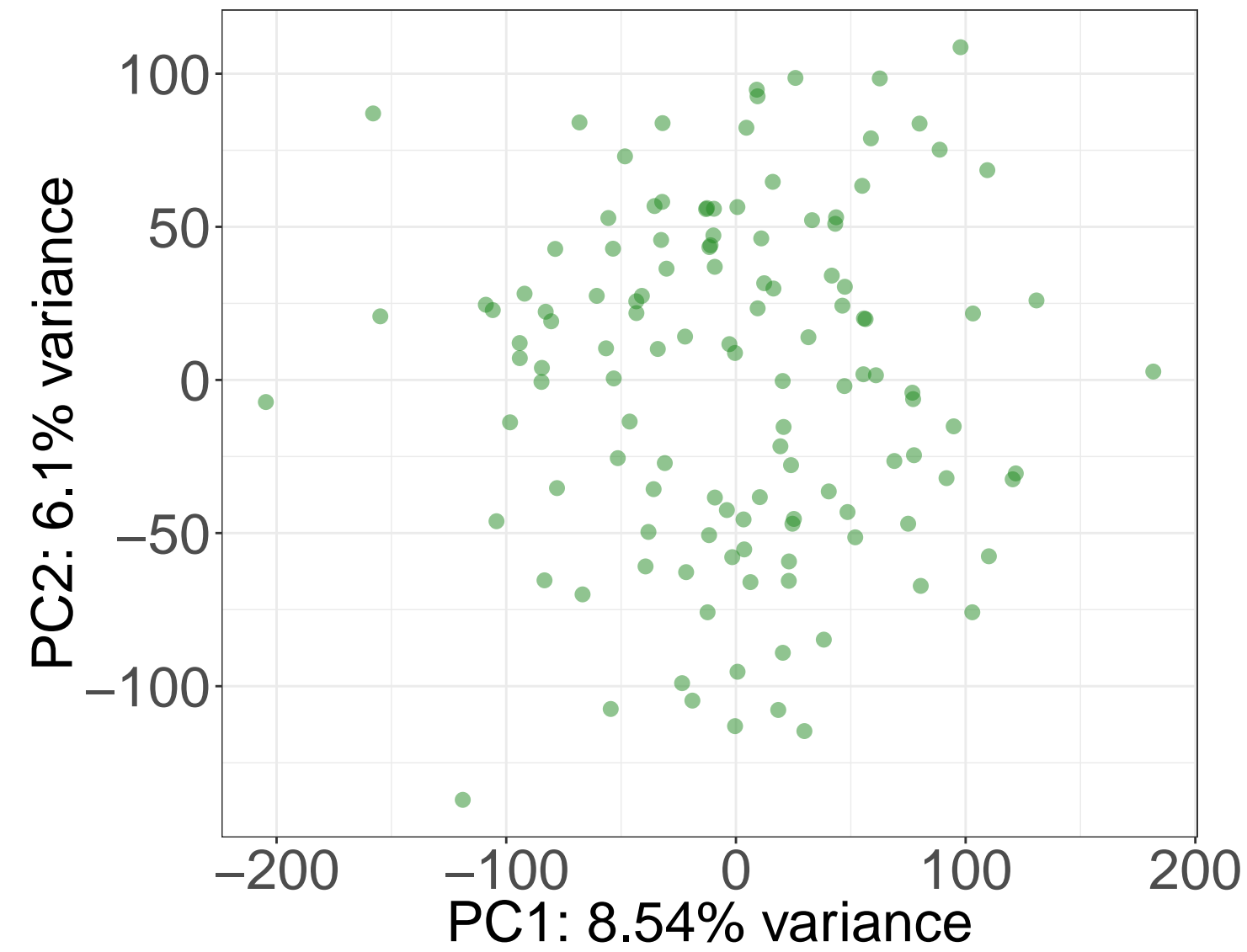
GDC**Xena/Toil****Piccolo****Recount2****MSKCC****MSKCC Batch**

GDC**Xena/Toil****Piccolo****Recount2****MSKCC****MSKCC Batch**

GDC**Xena/Toil****Piccolo****Recount2****MSKCC****MSKCC Batch**

● 13 ● 31

GDC**Xena/Toil****Piccolo****Recount2****MSKCC****MSKCC Batch**

GDC**Xena/Toil****Piccolo****Recount2****MSKCC****MSKCC Batch**