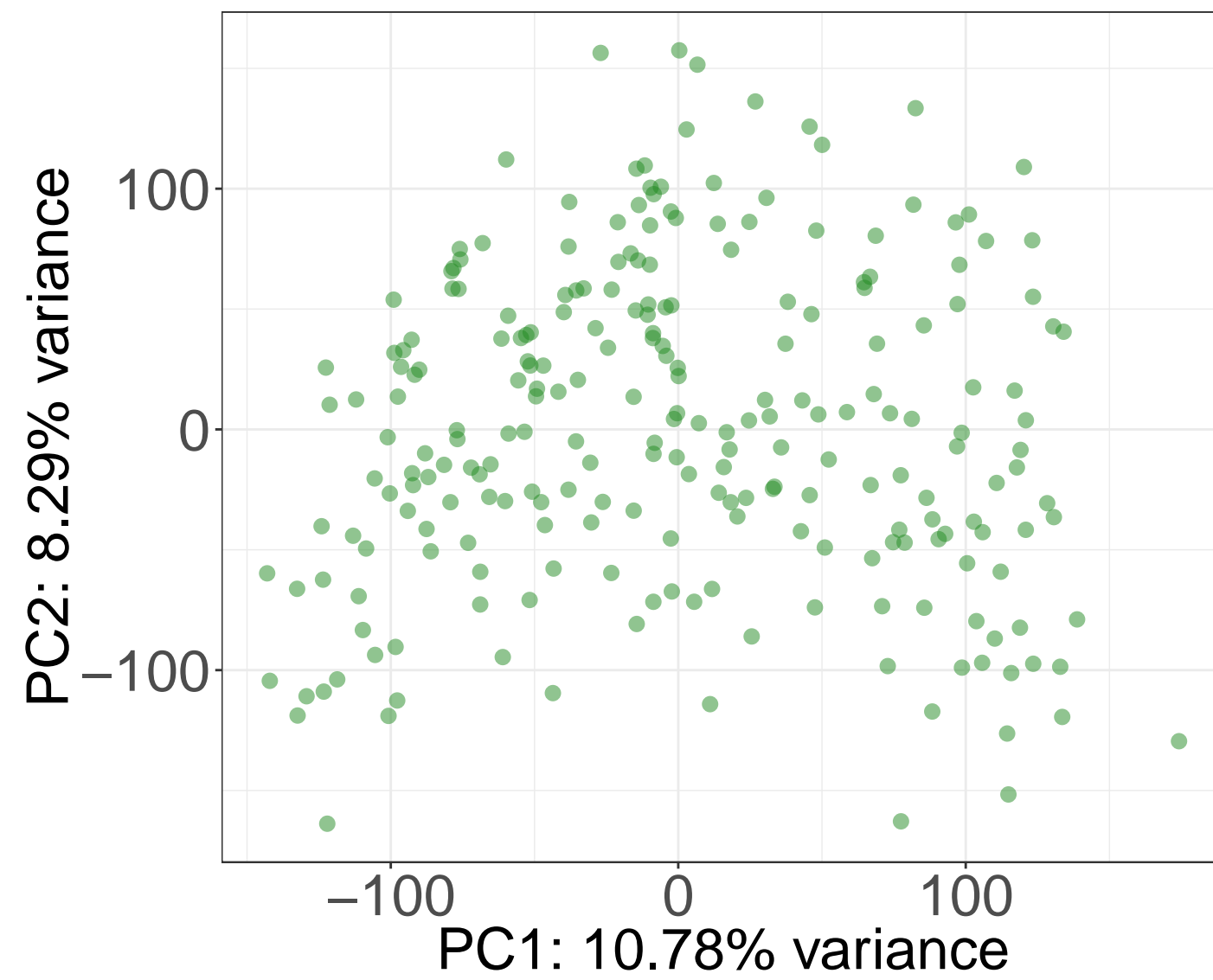
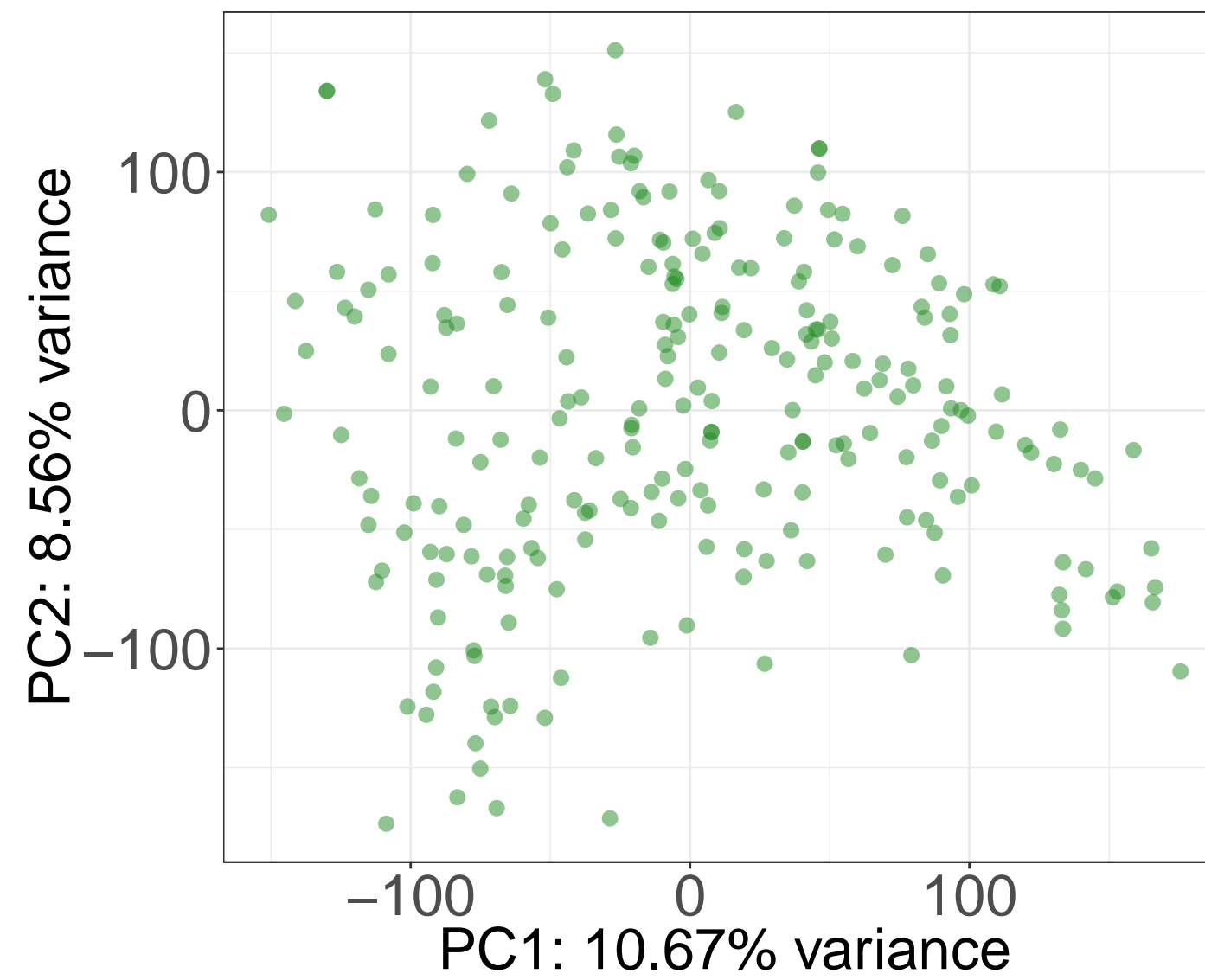
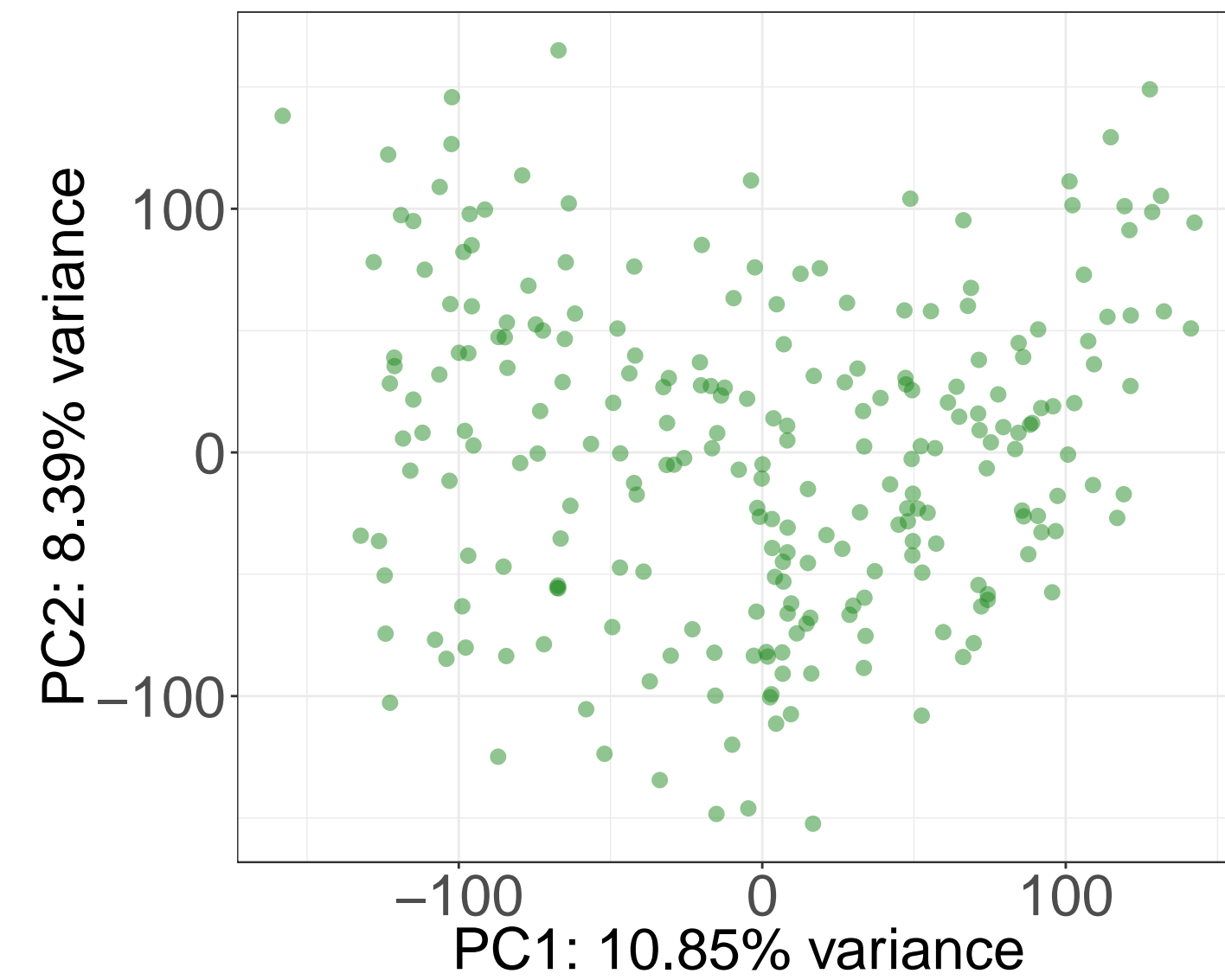
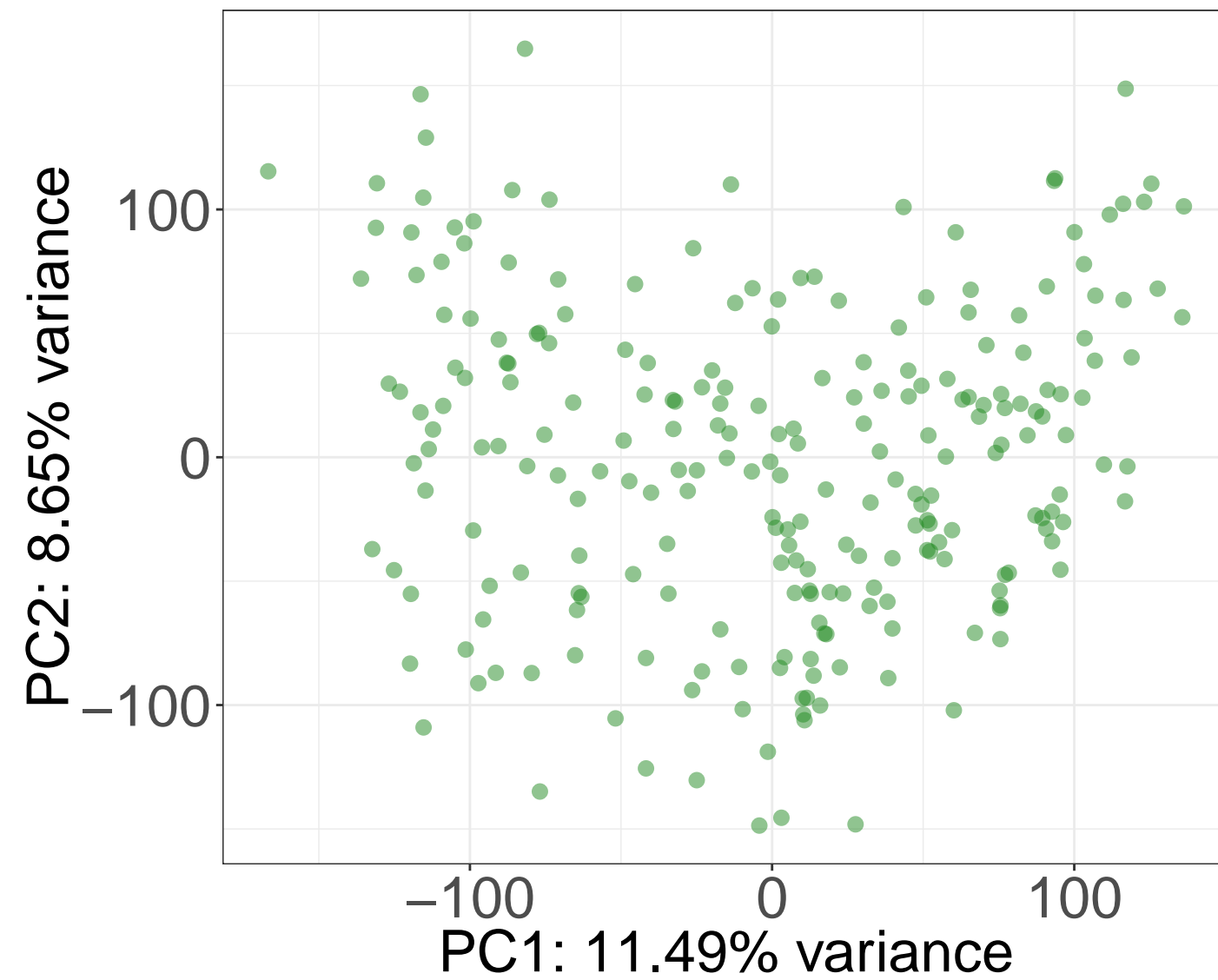
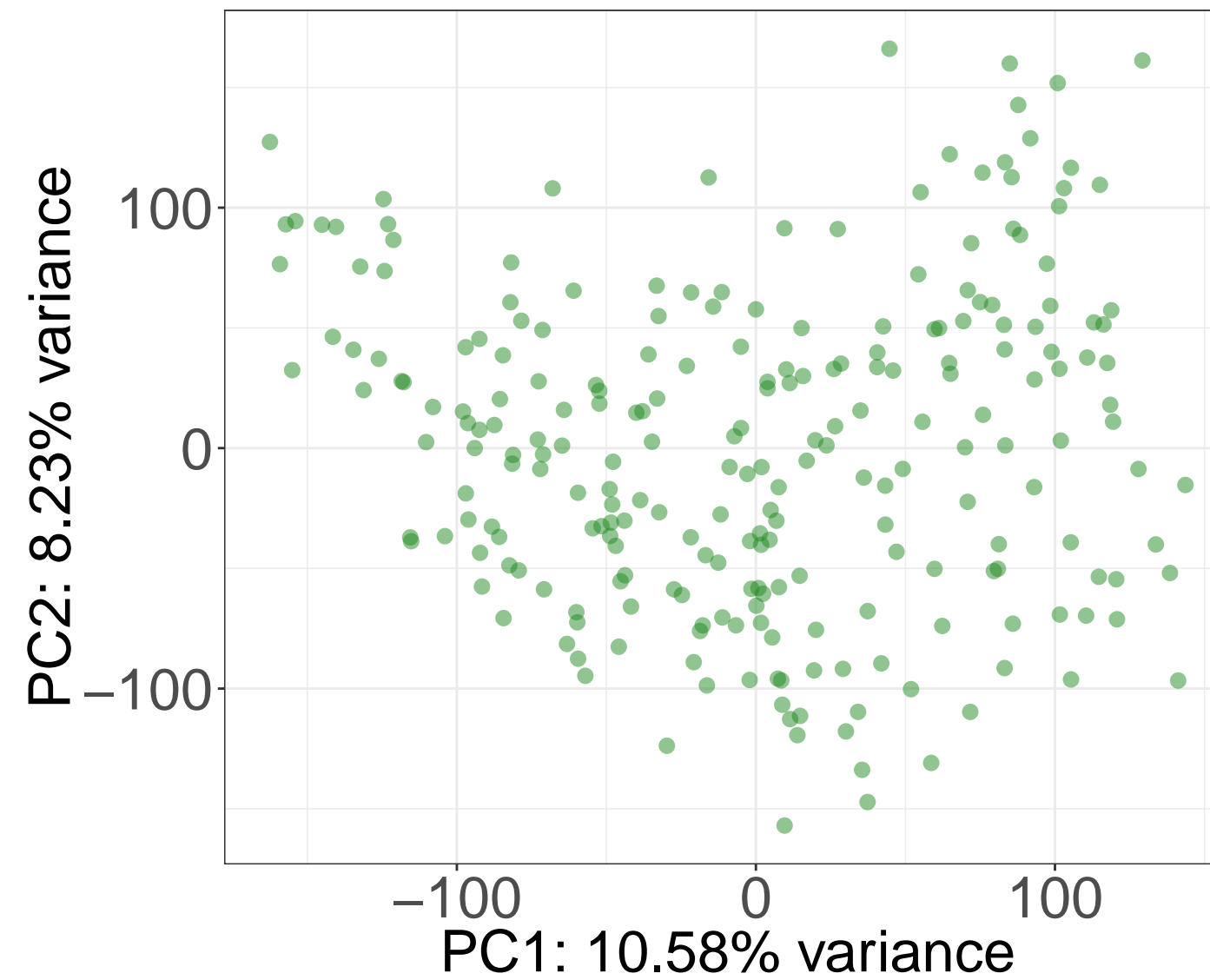
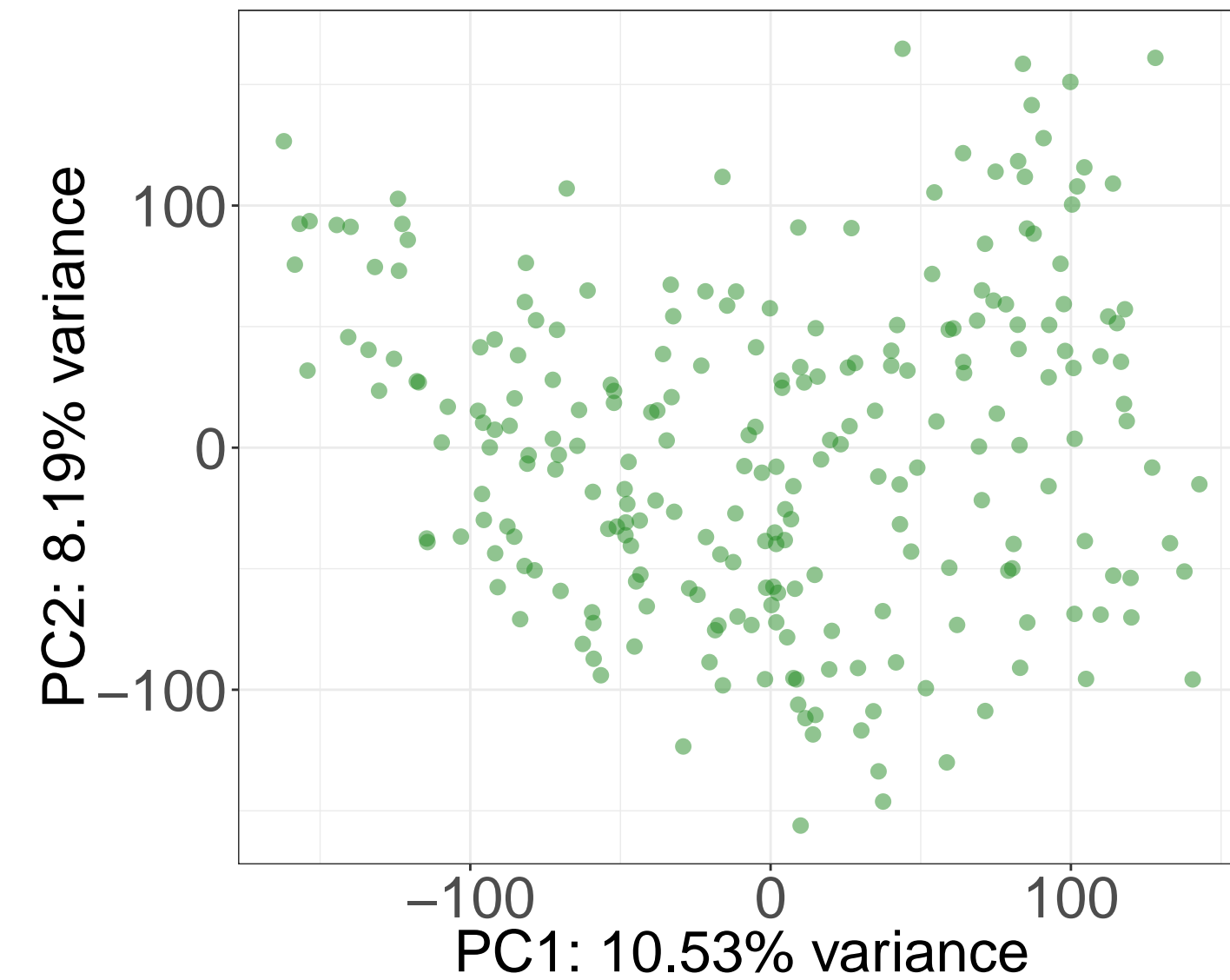
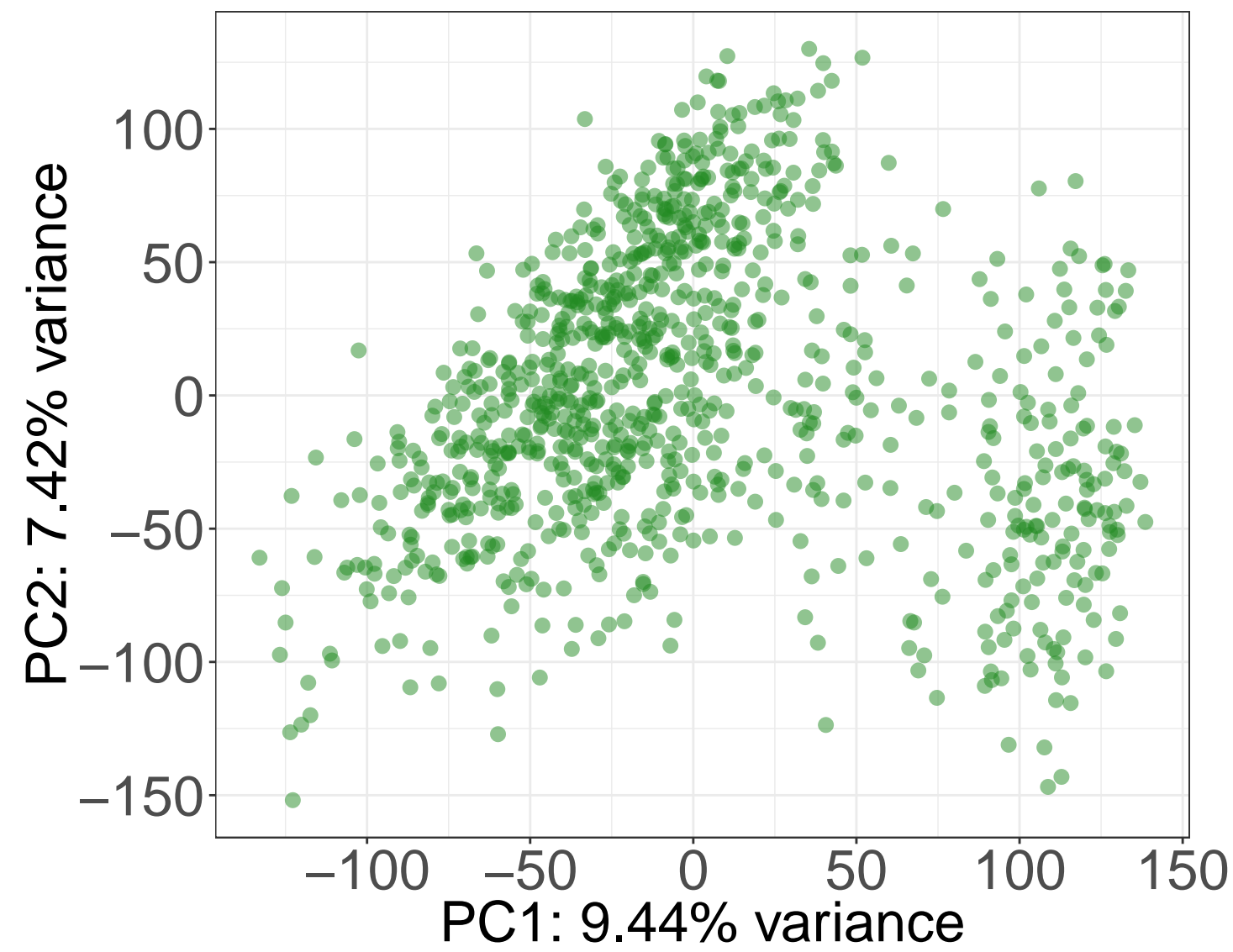
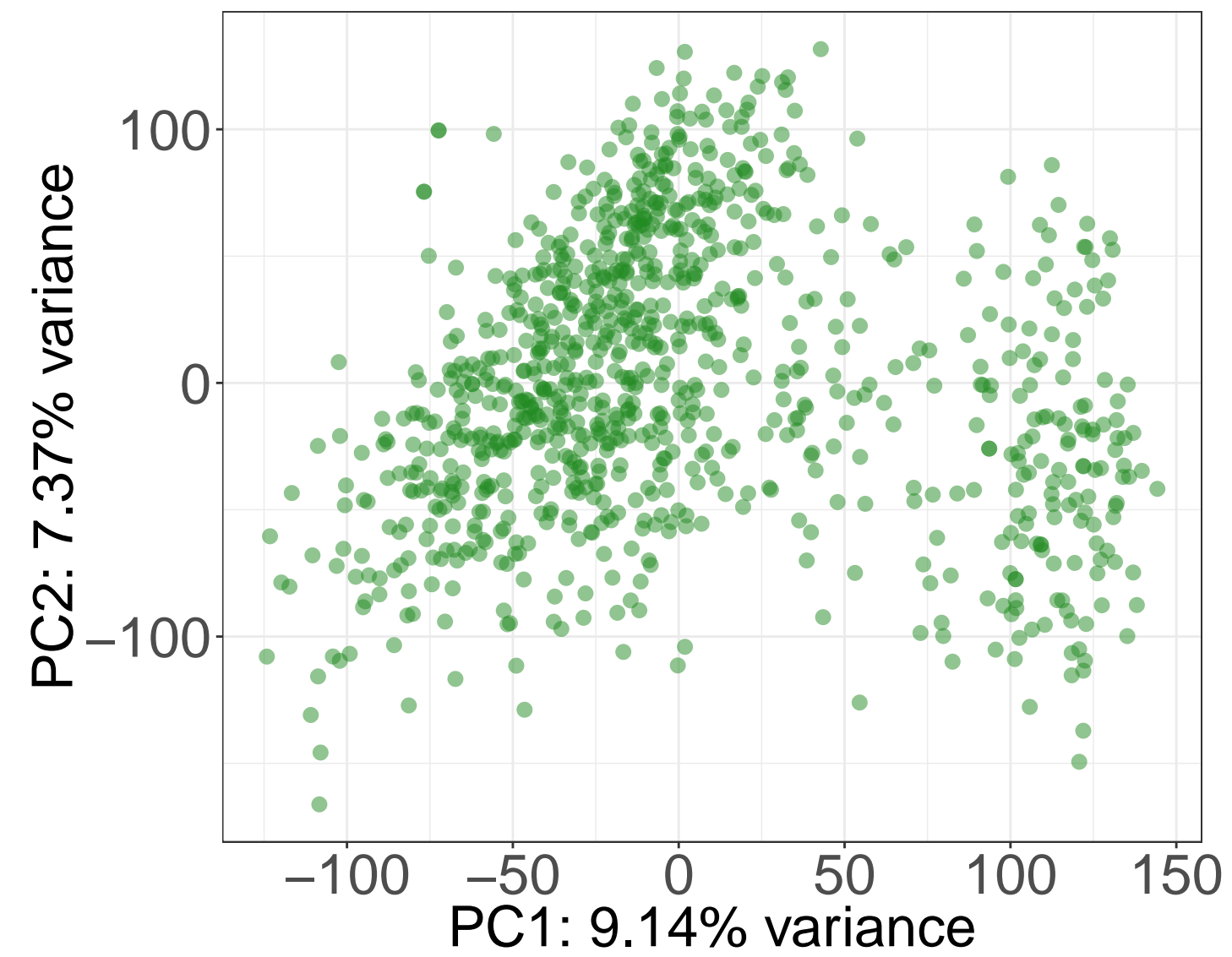
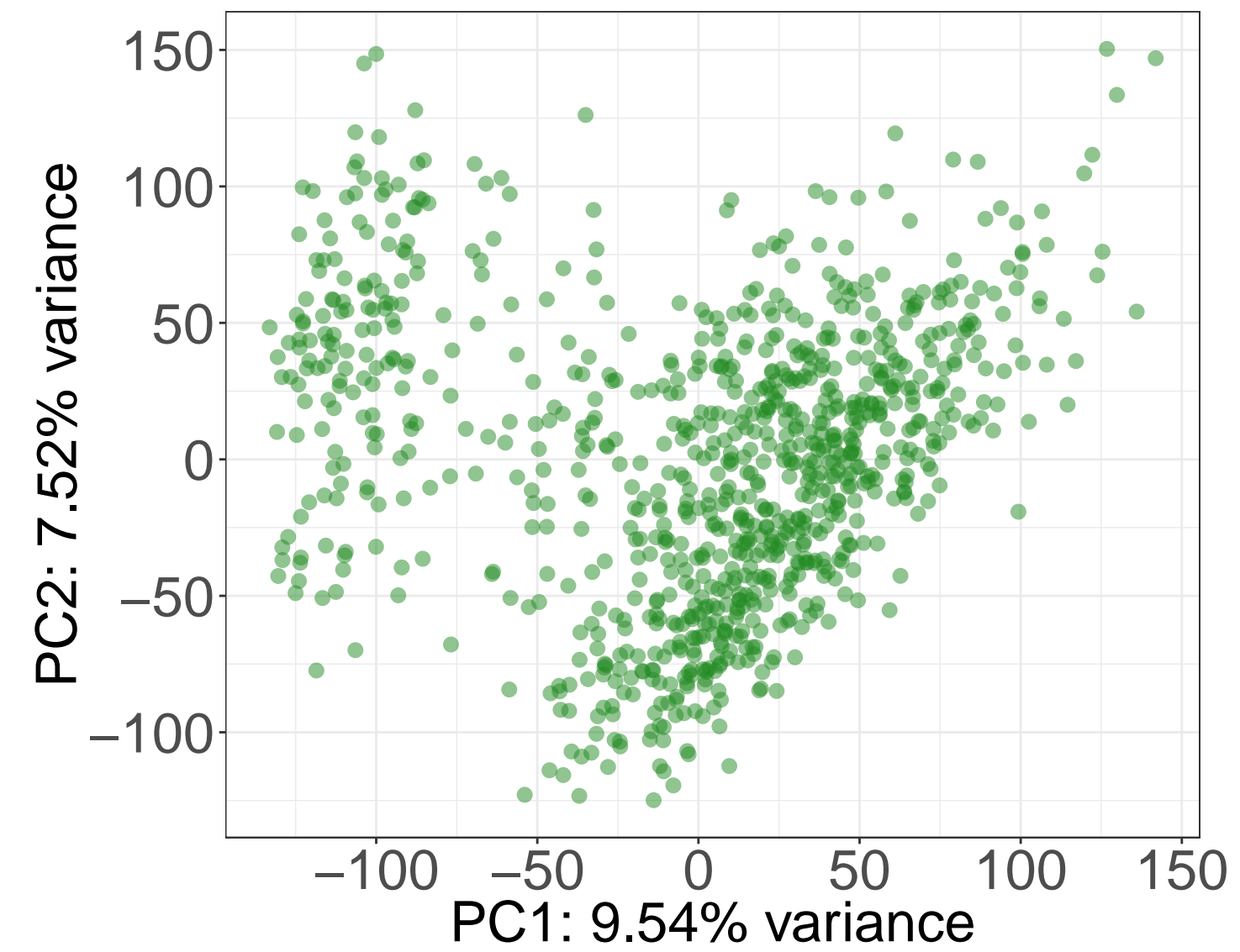
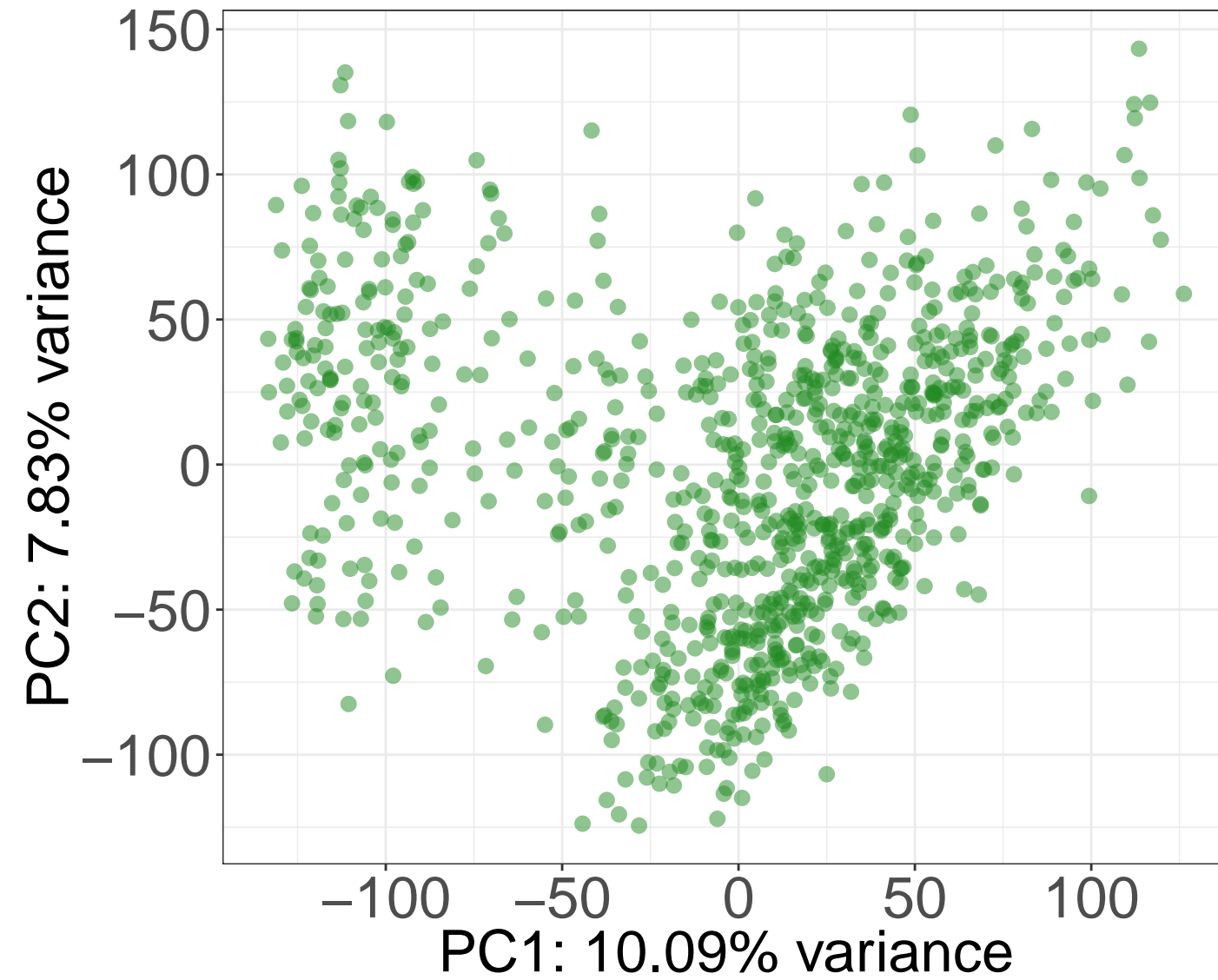
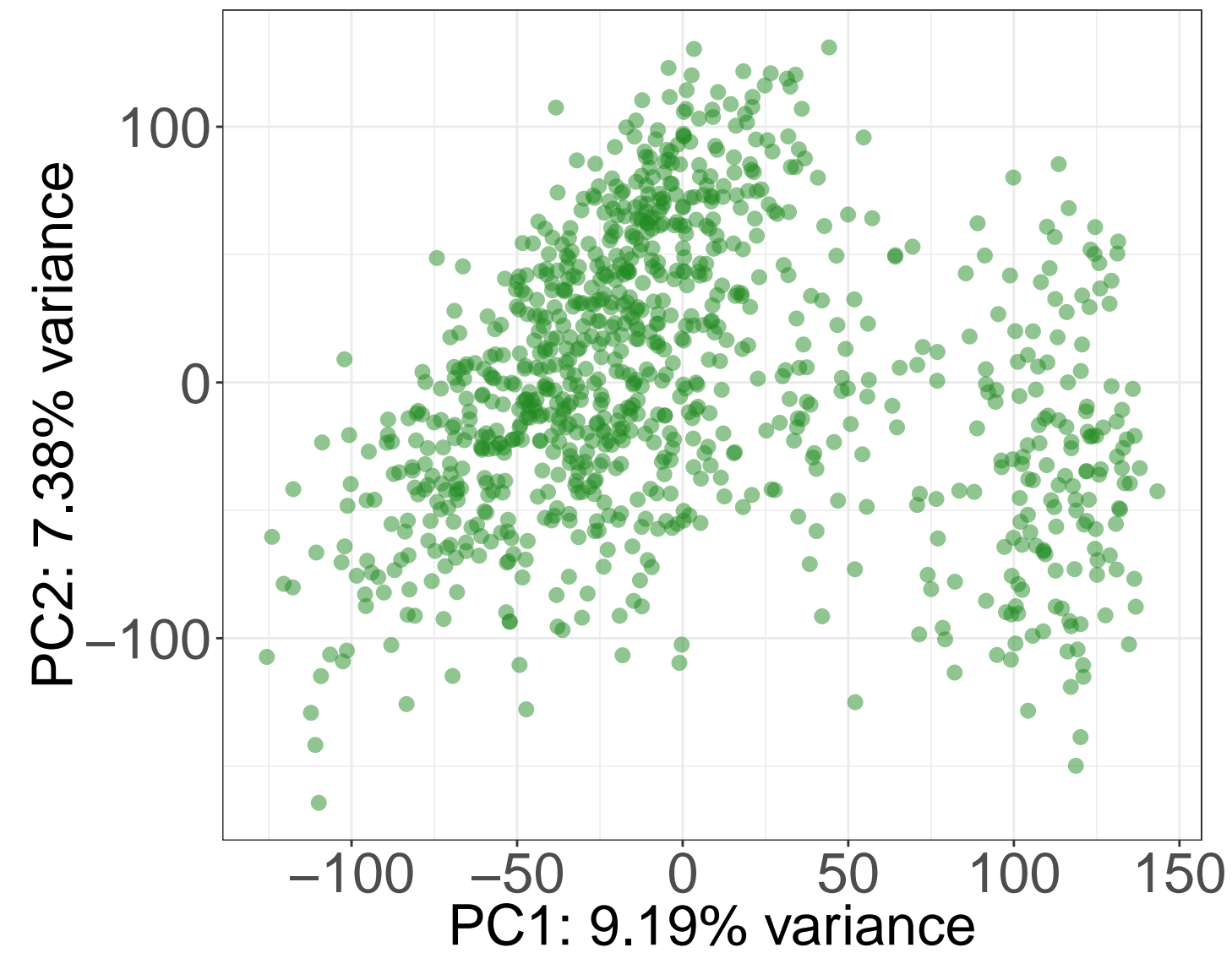
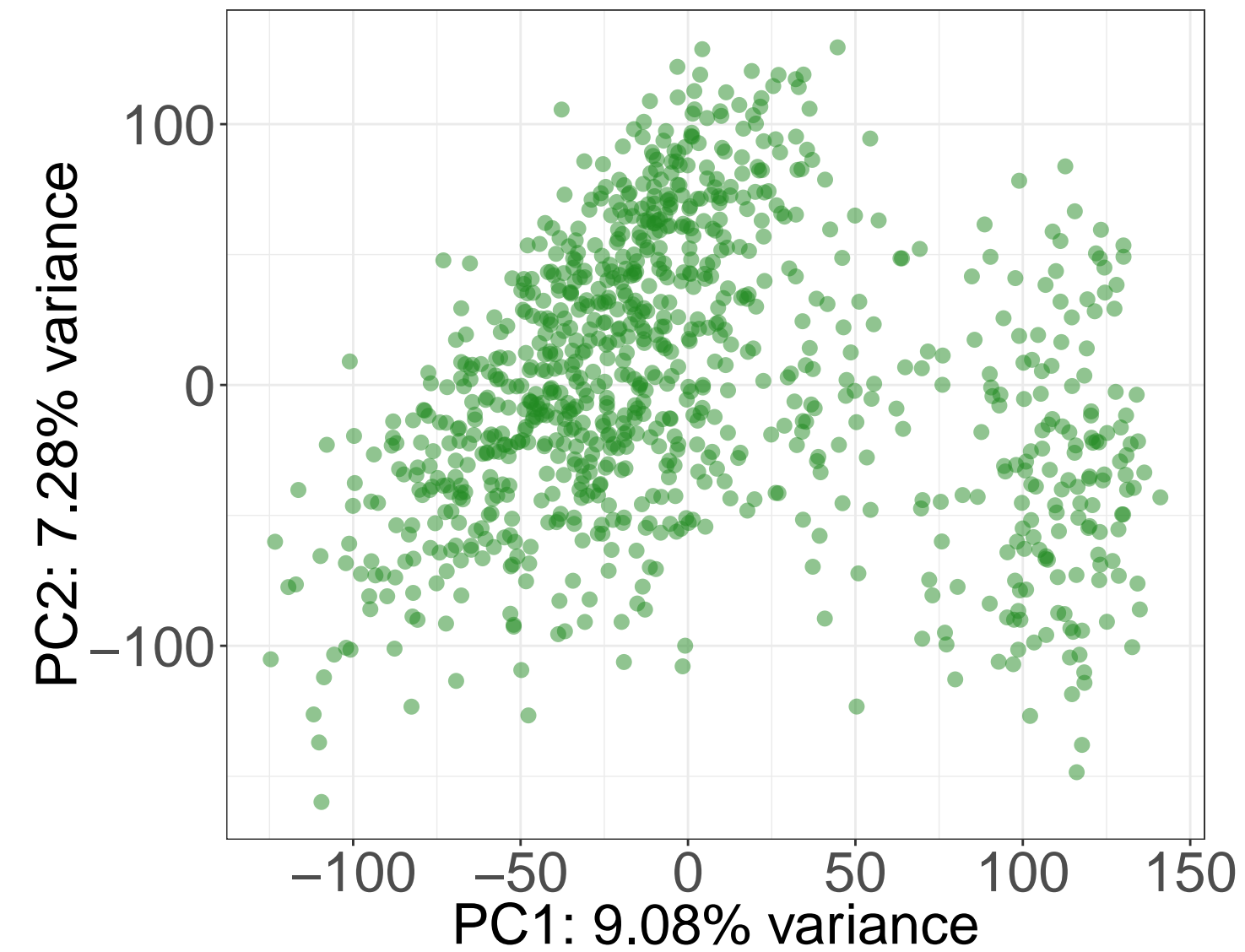
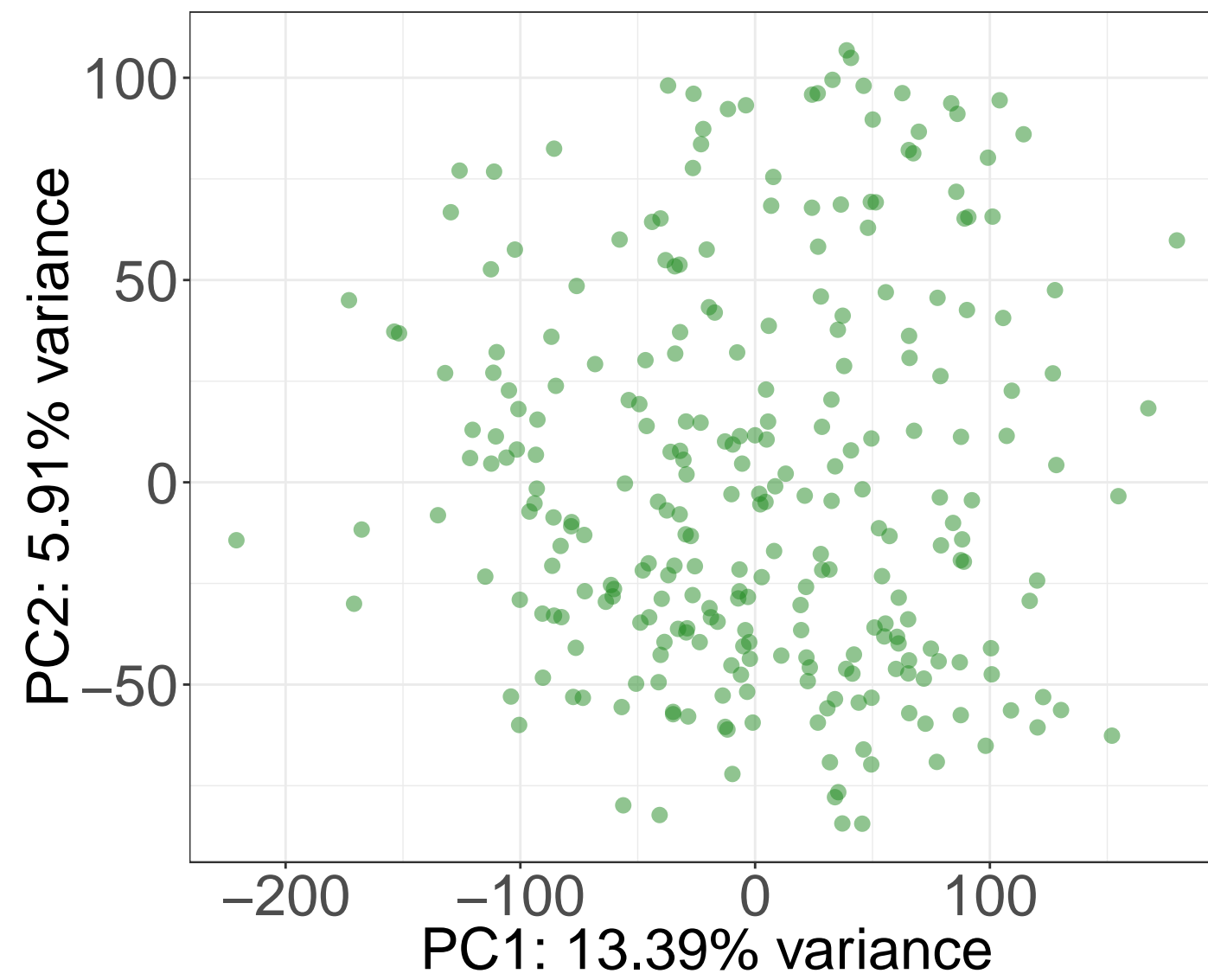
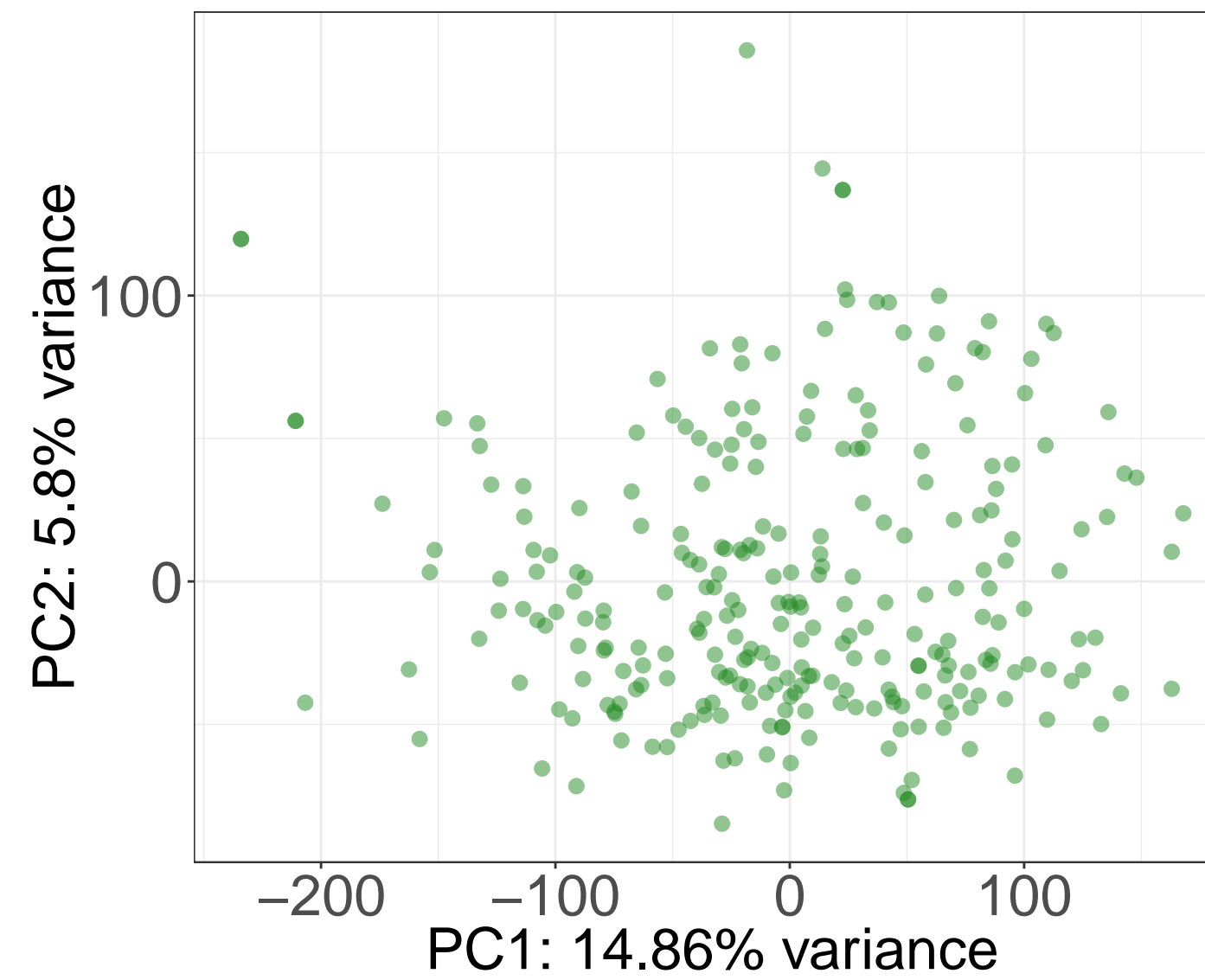
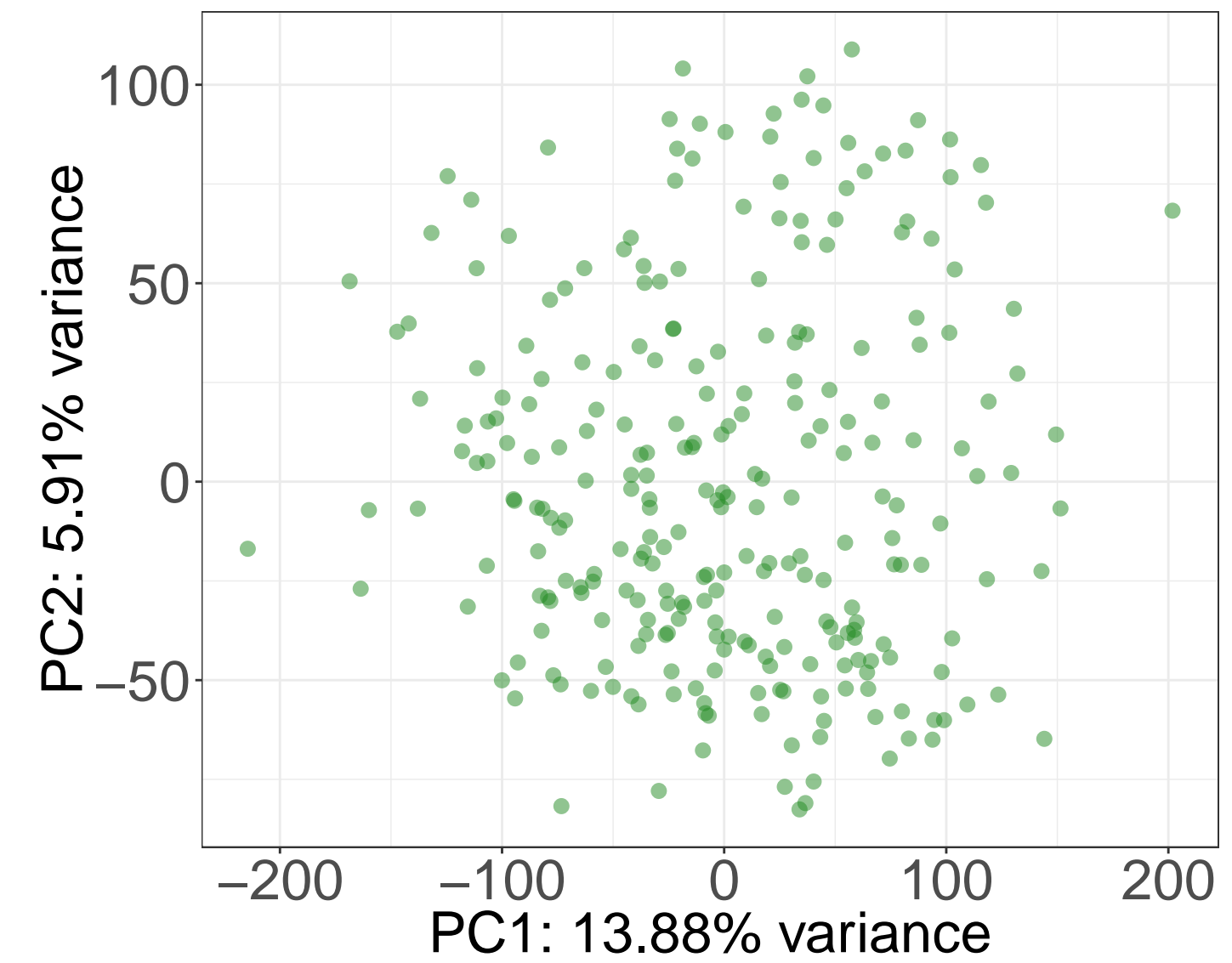
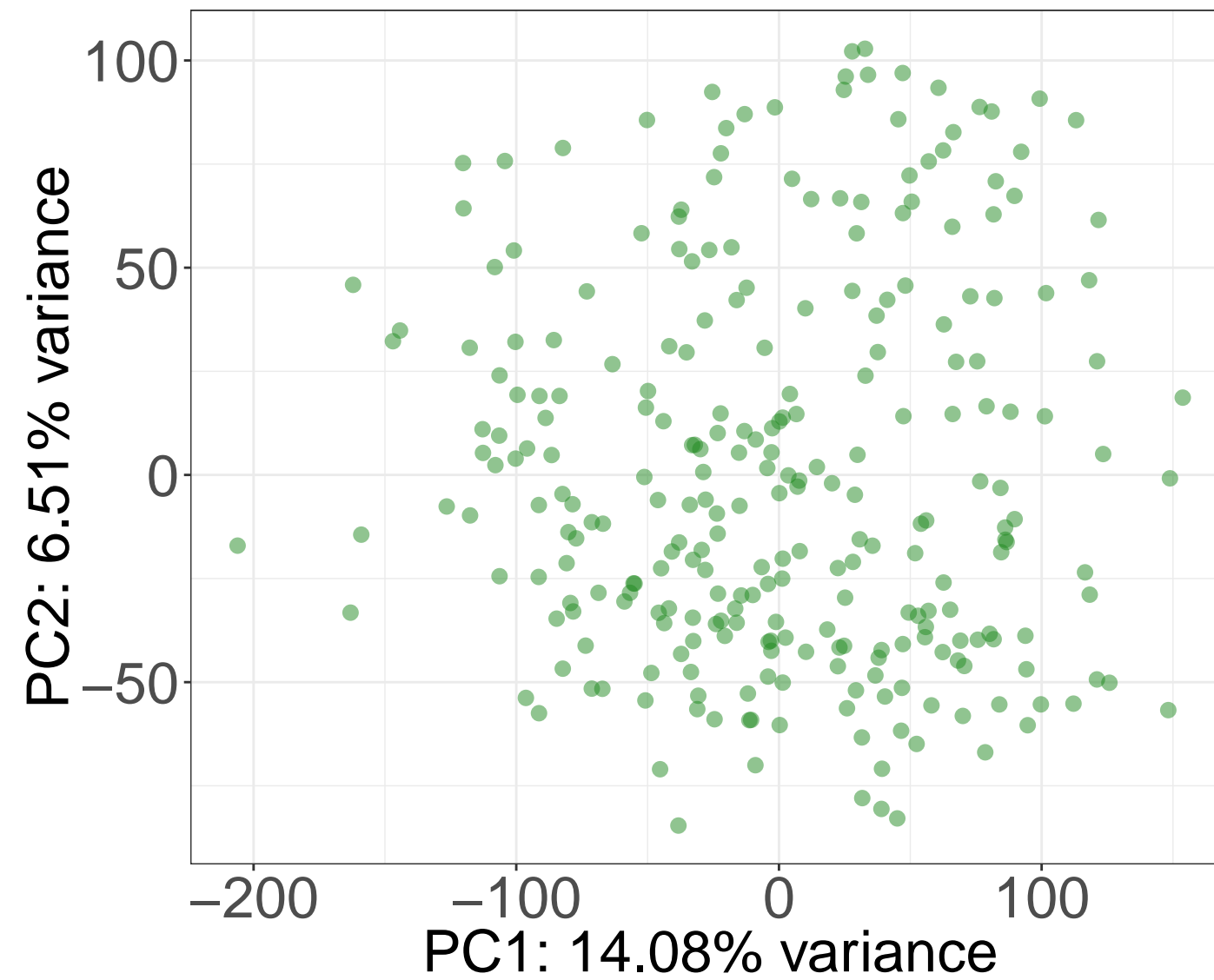
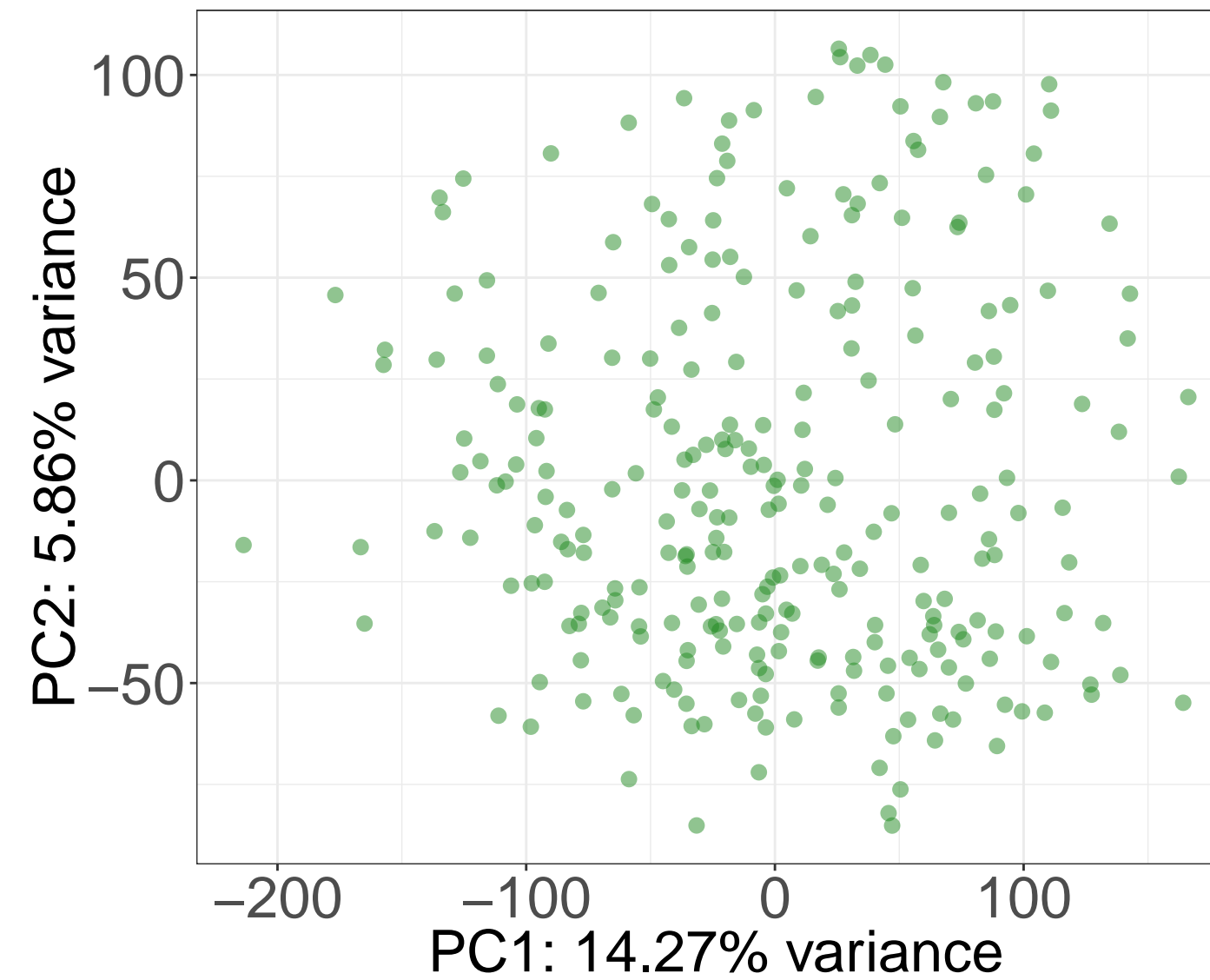
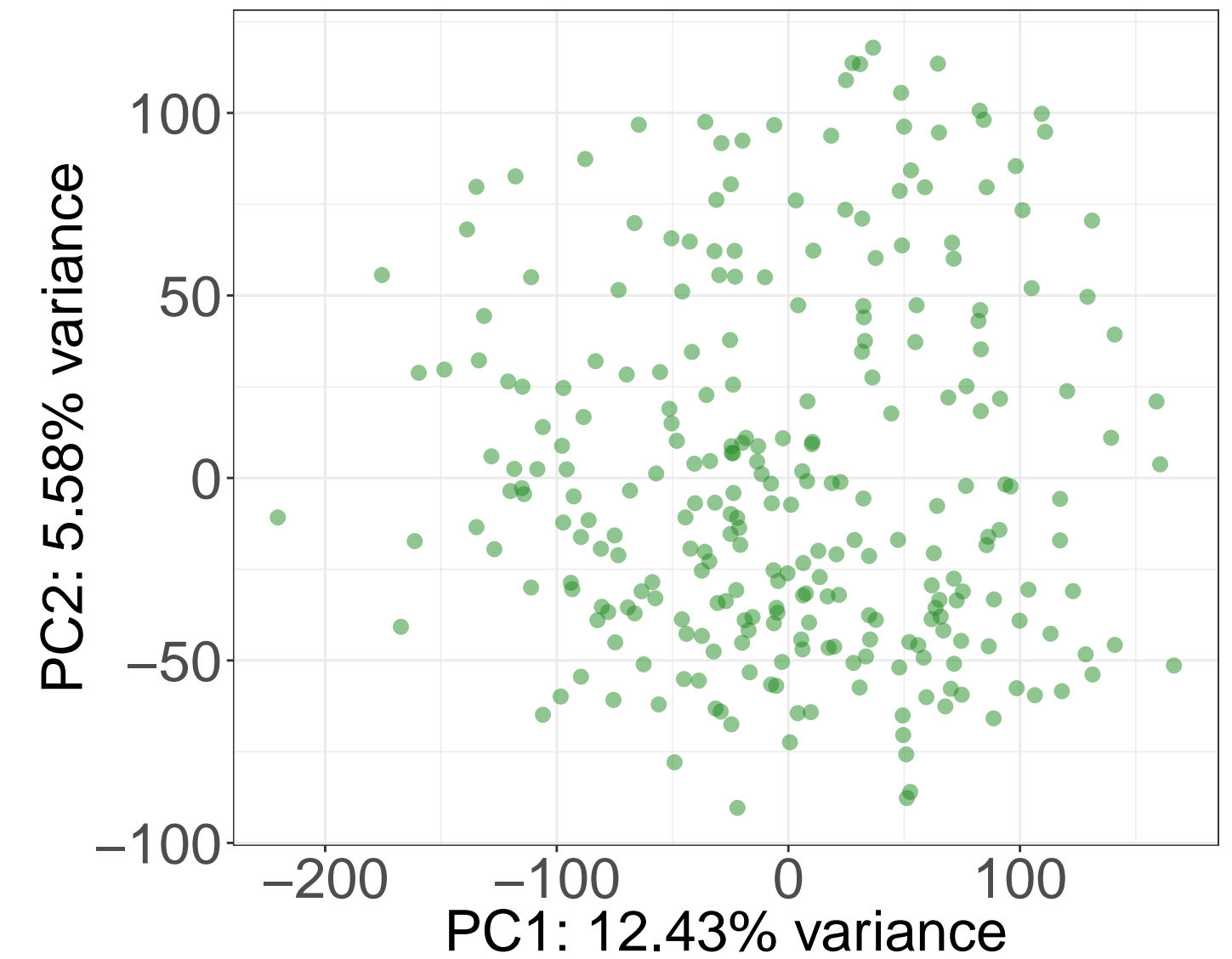
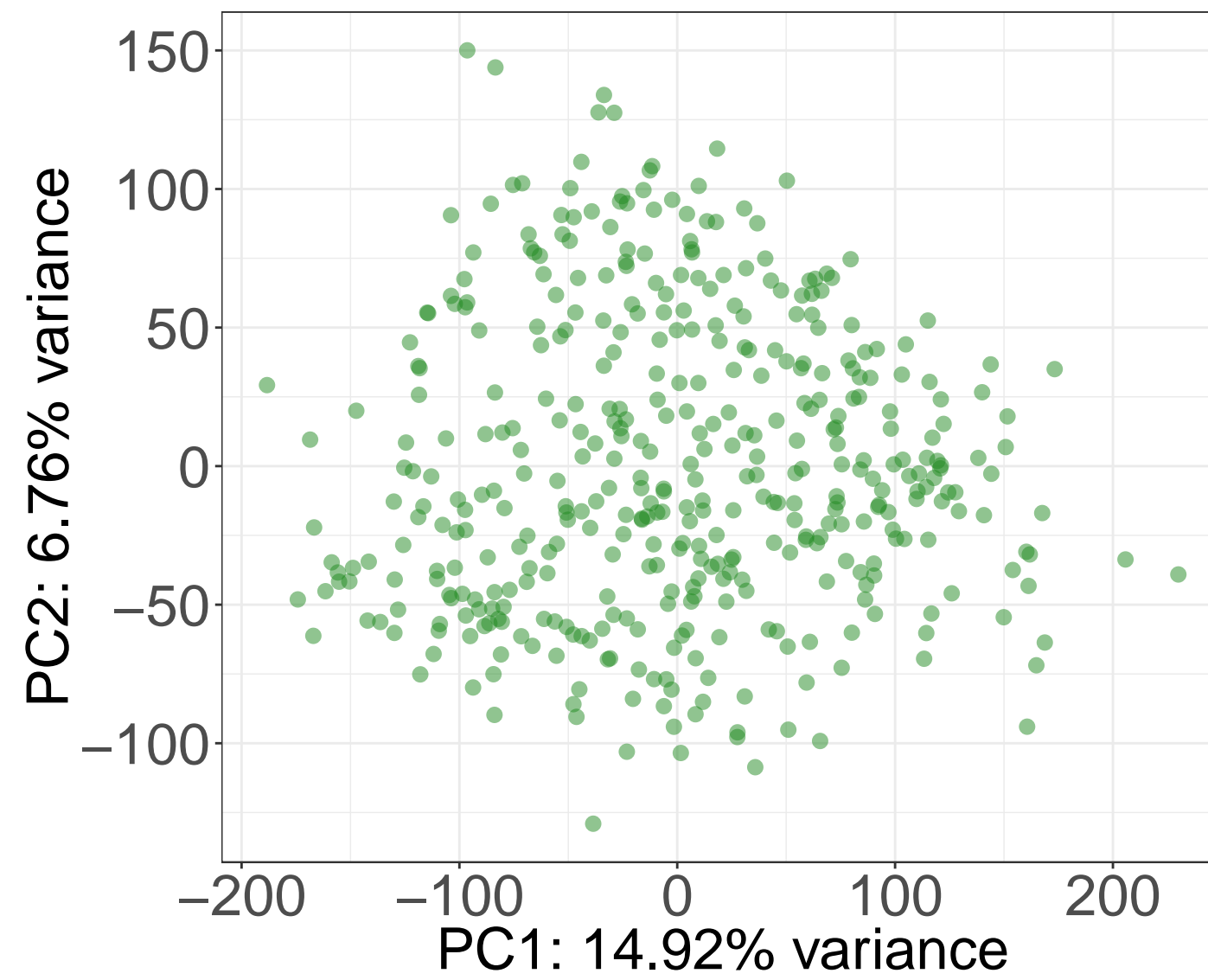
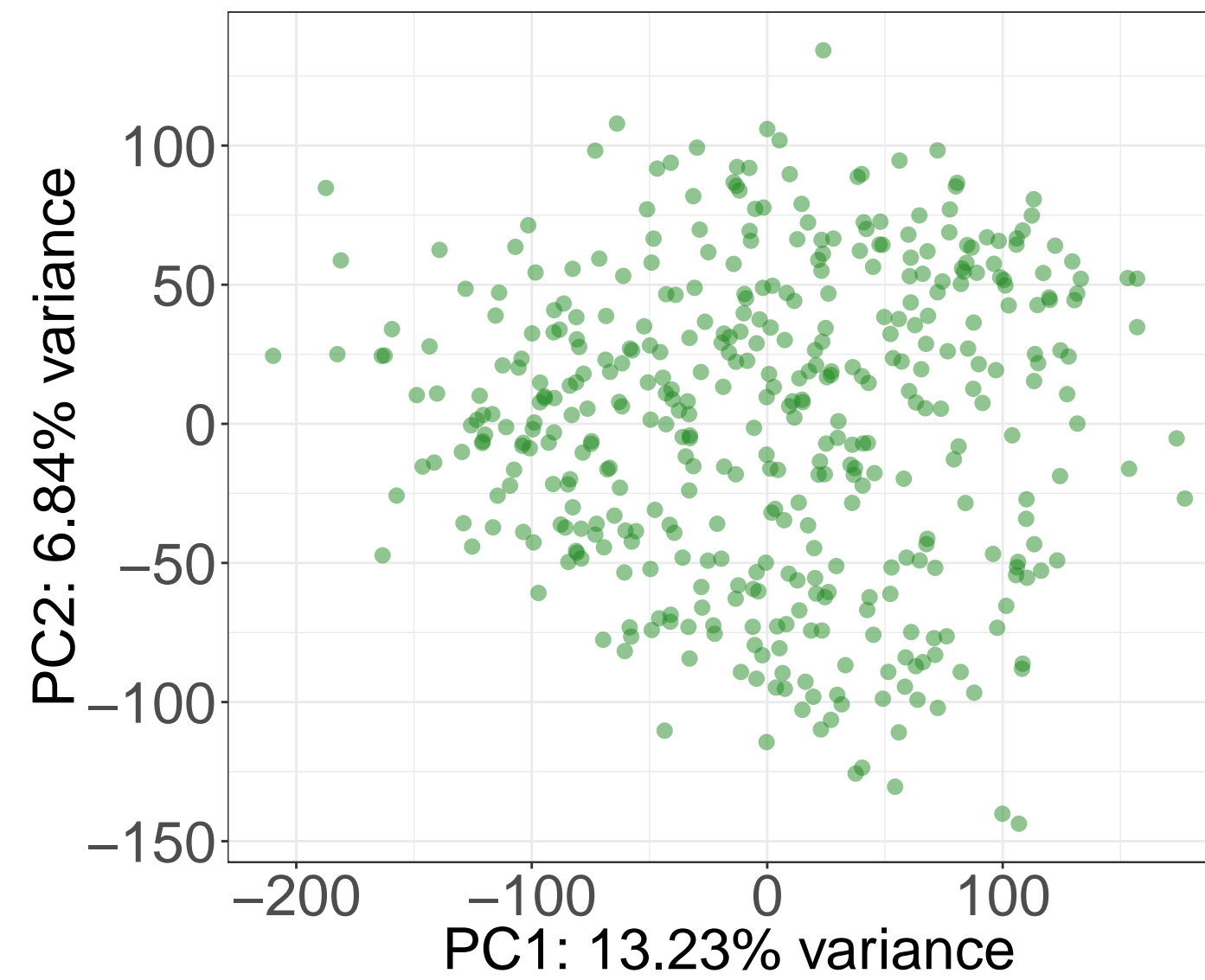
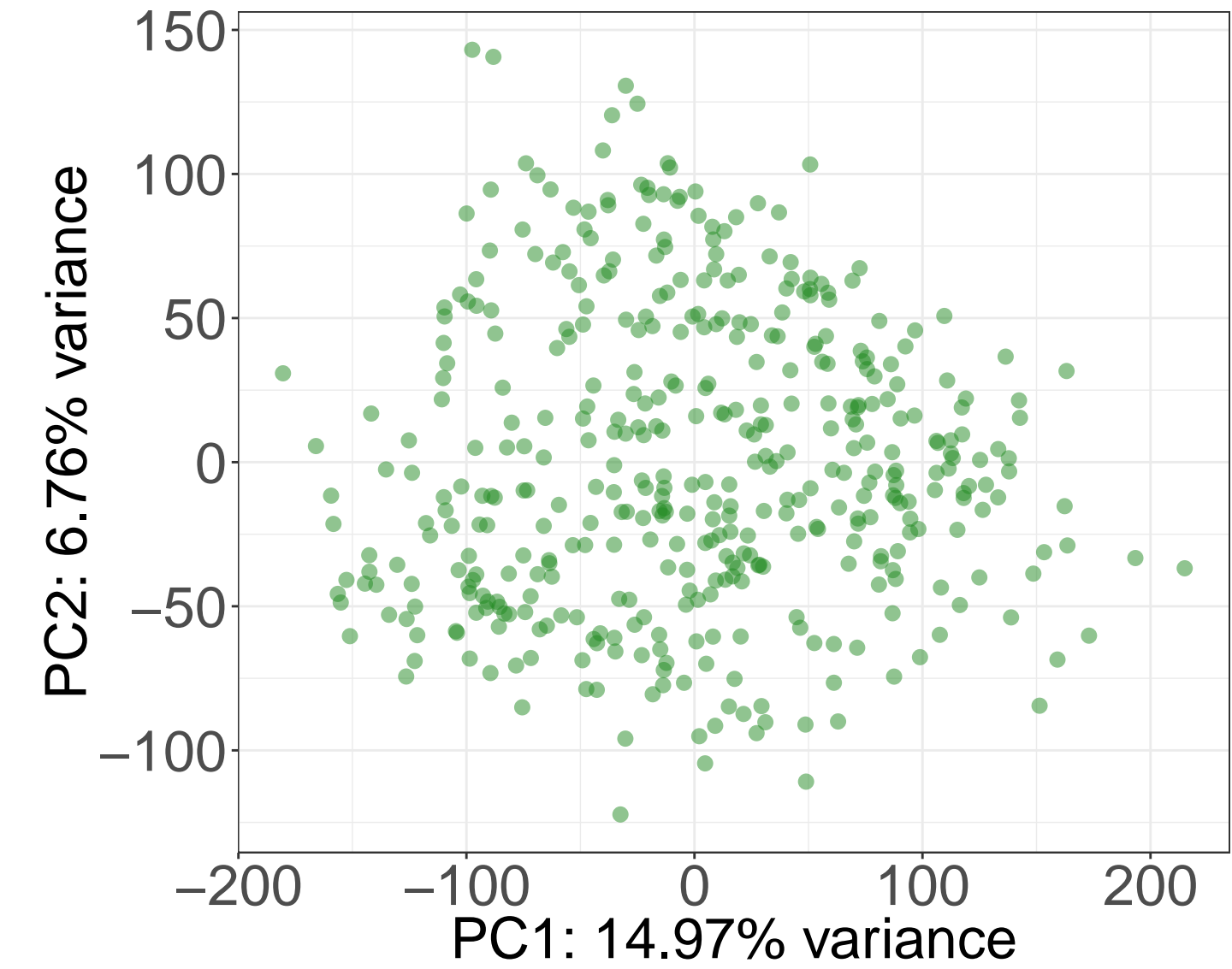
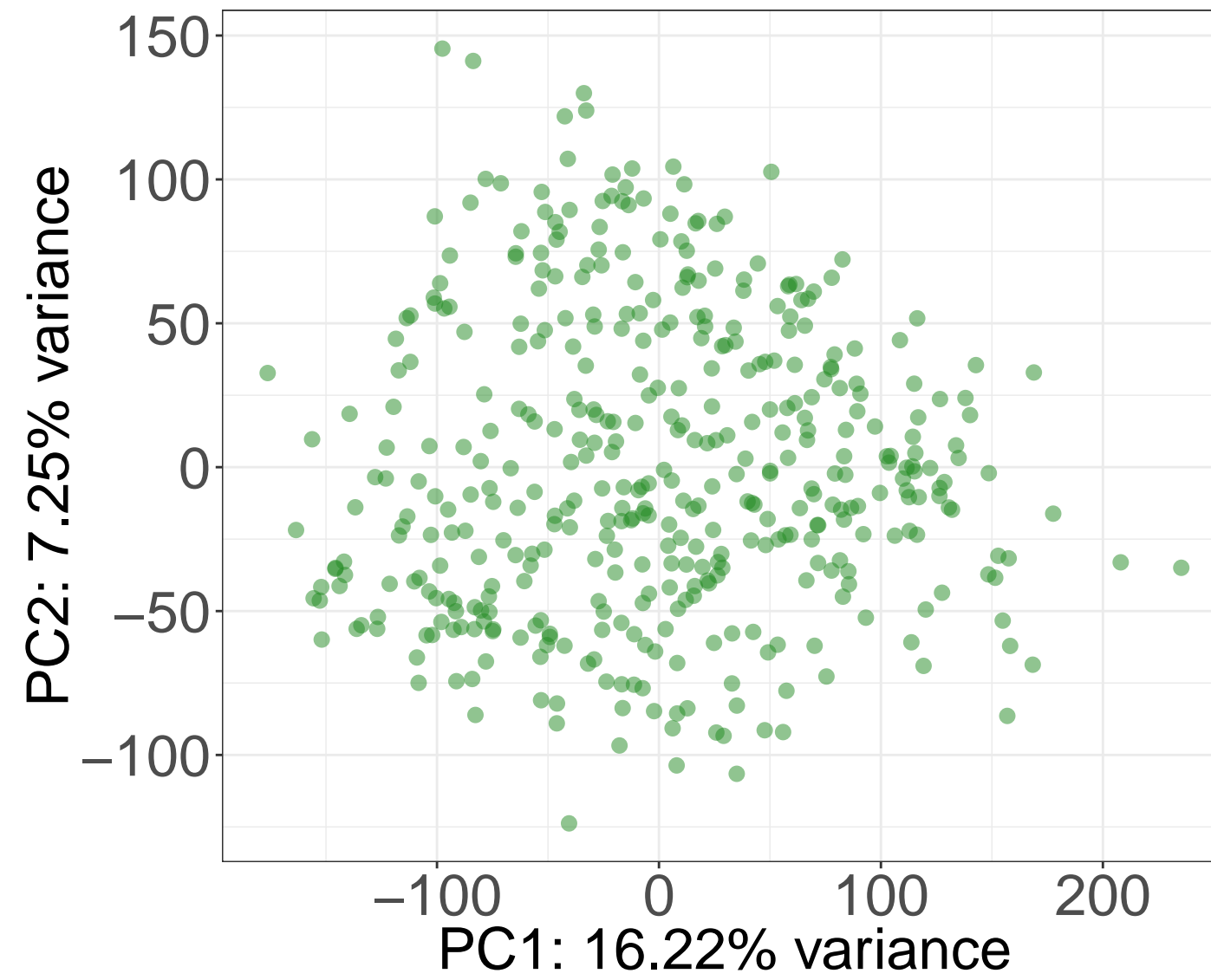
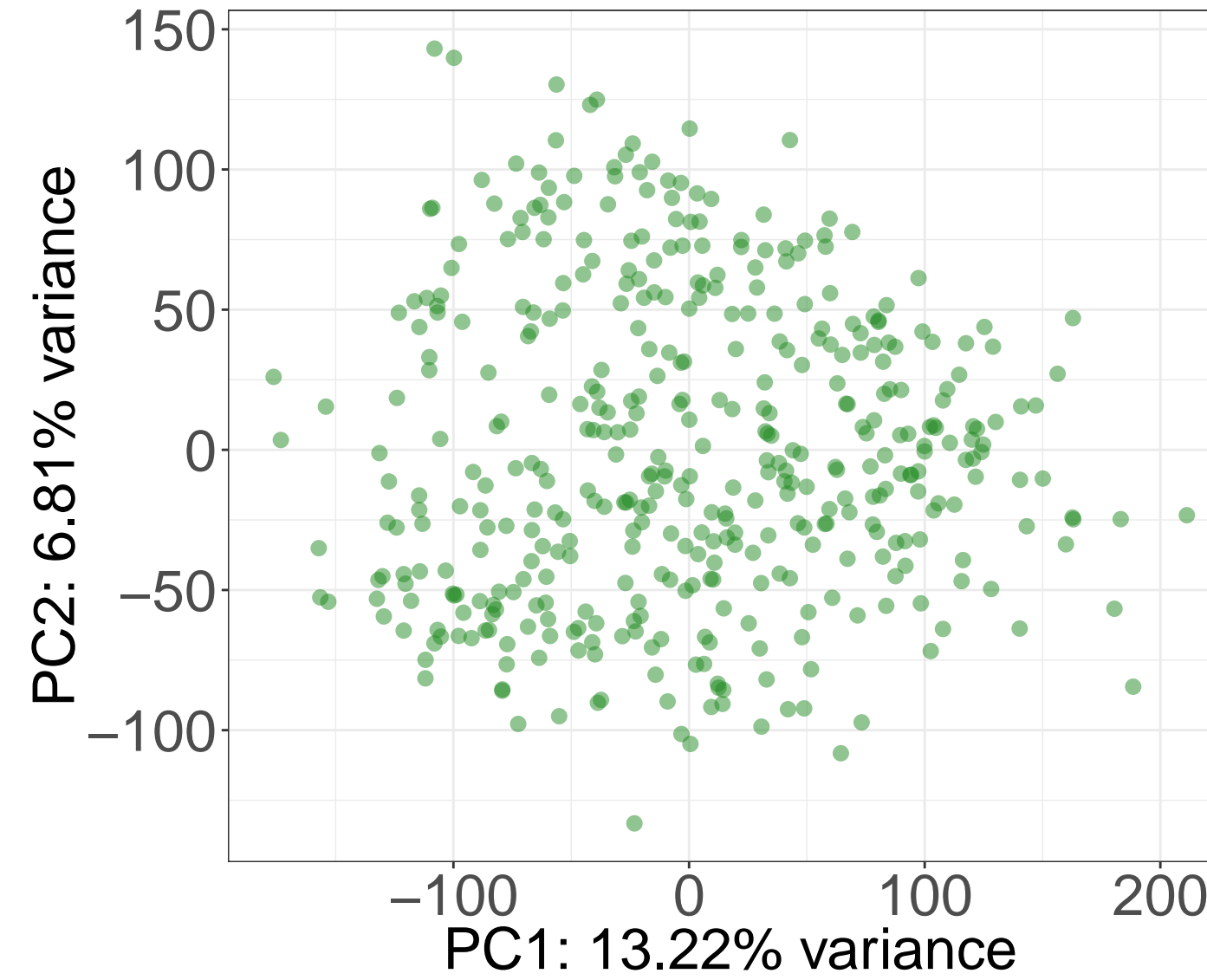
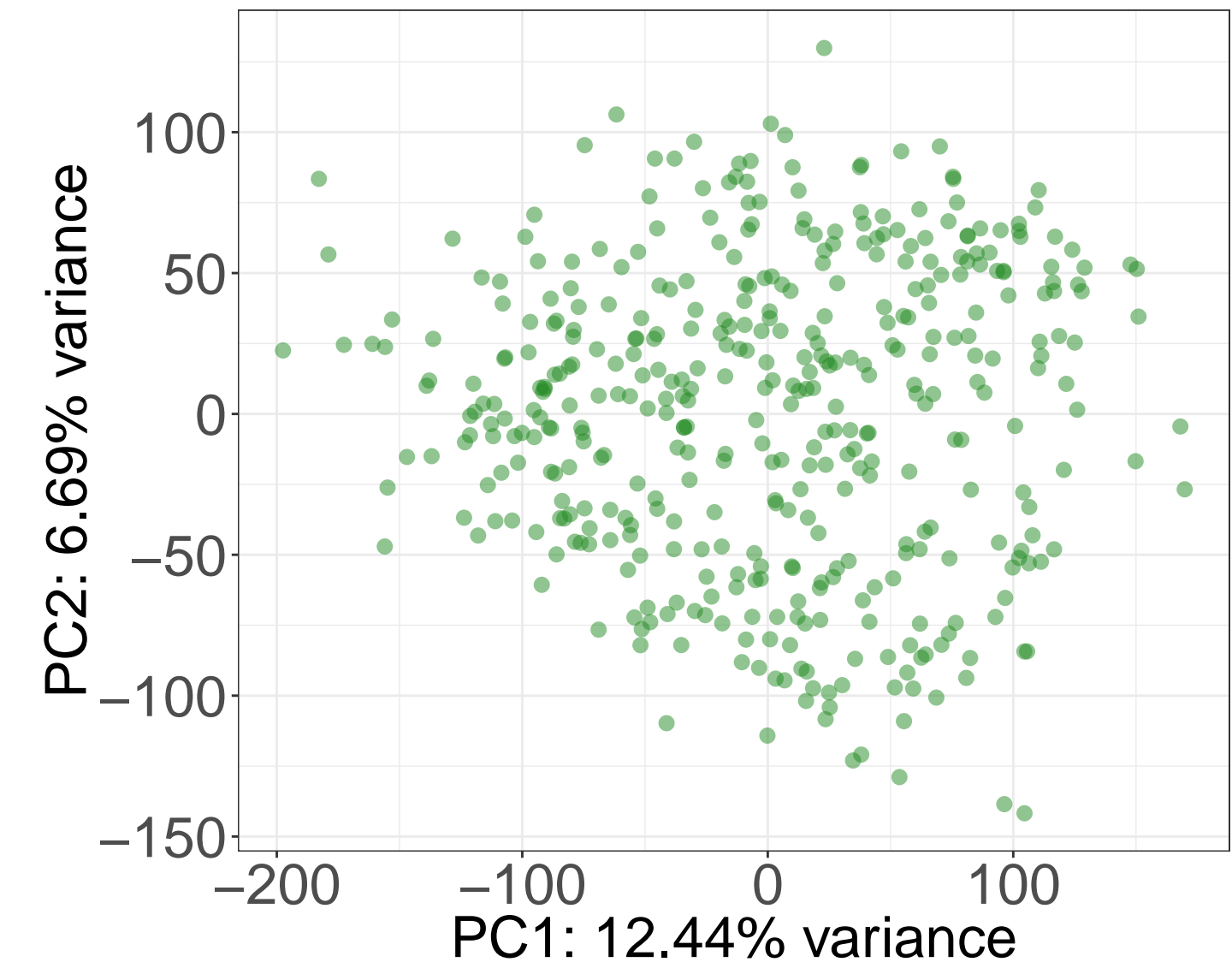
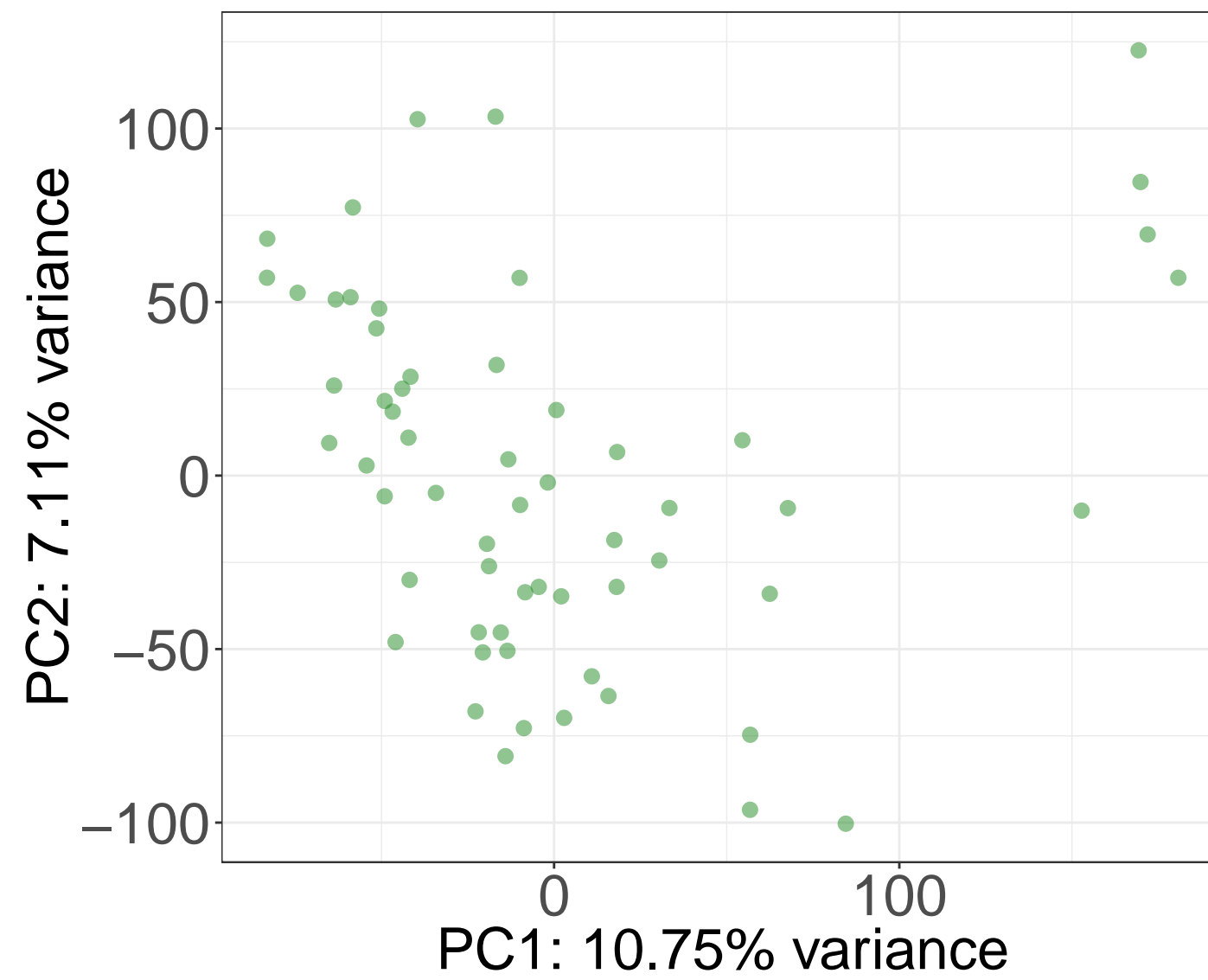
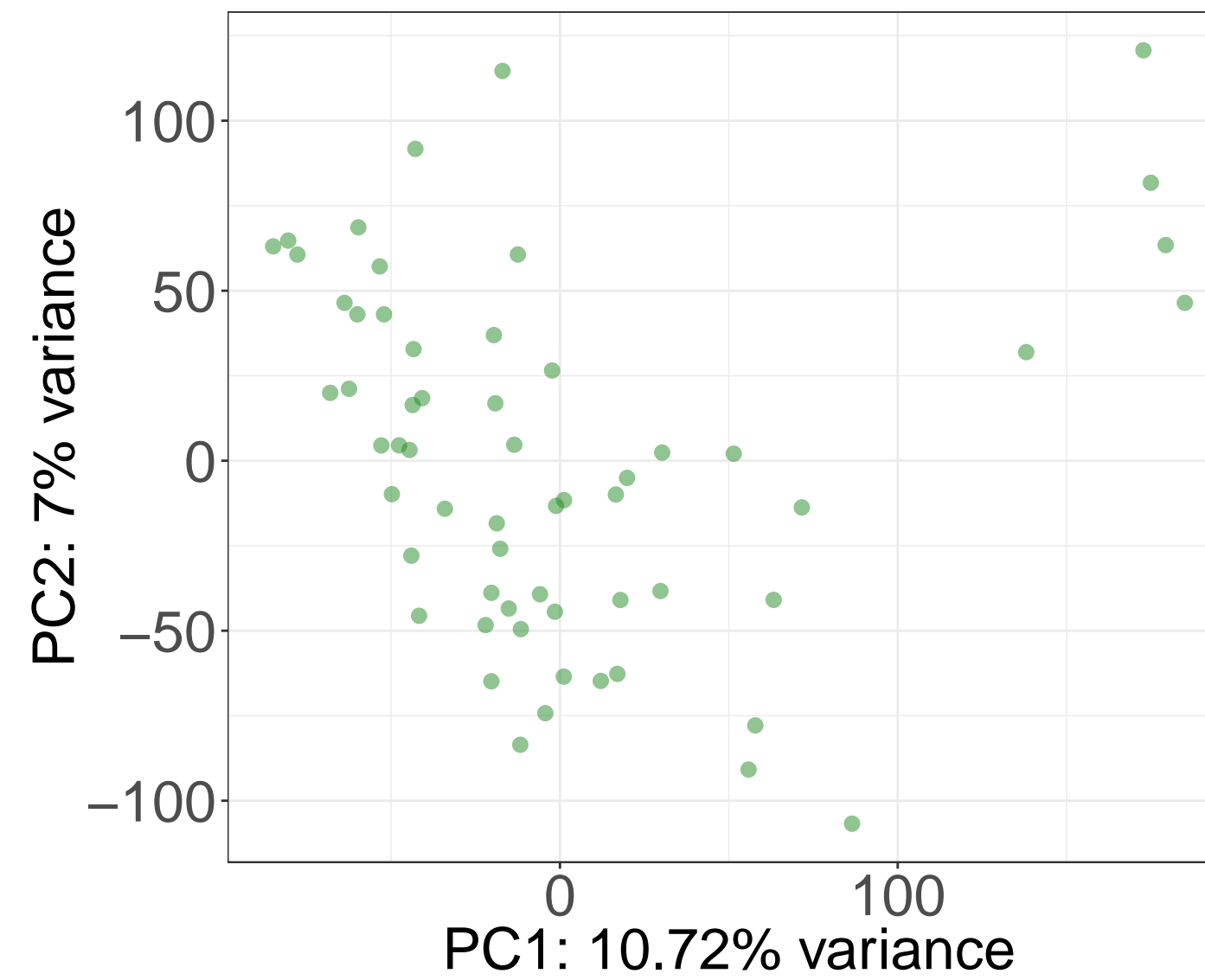
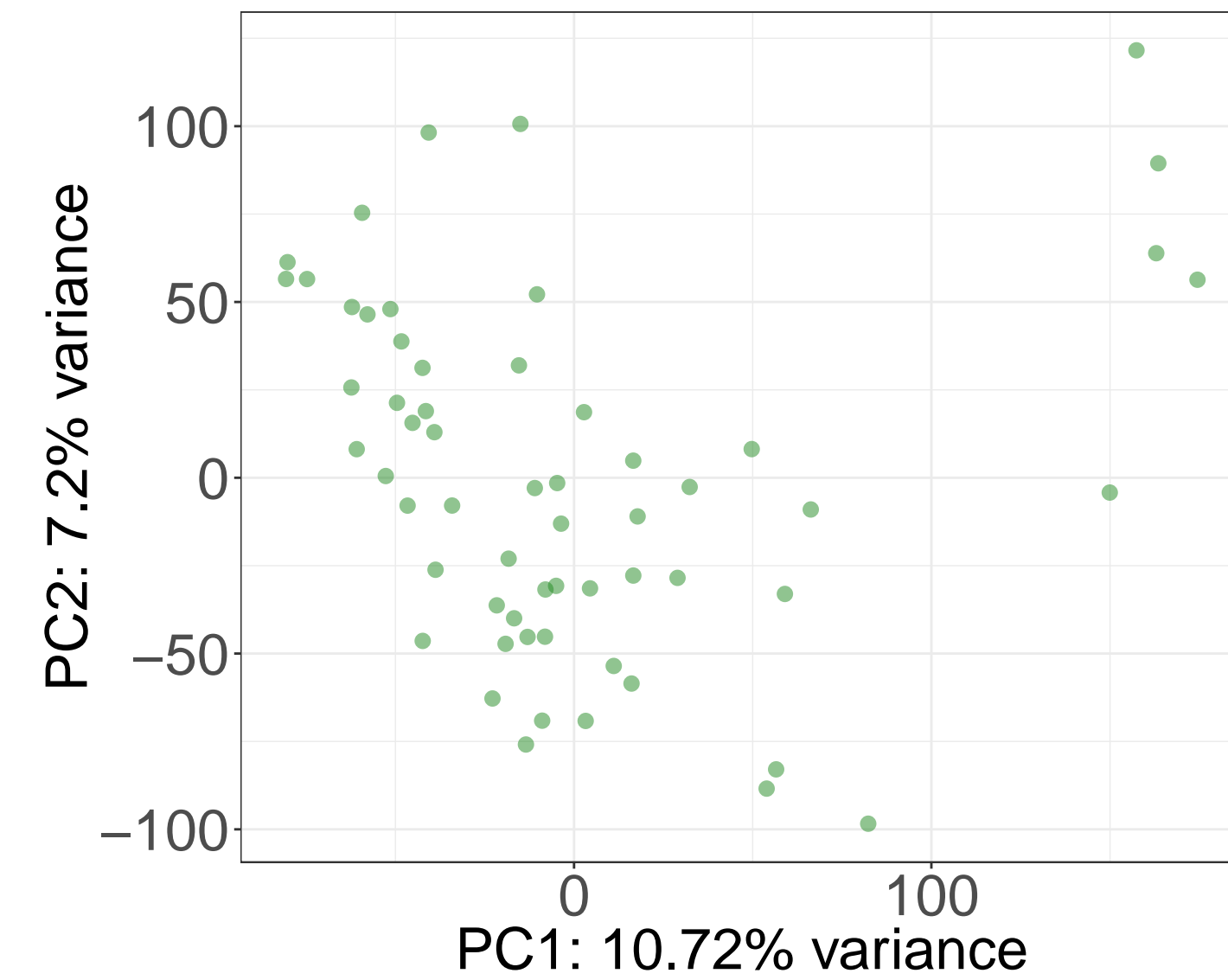
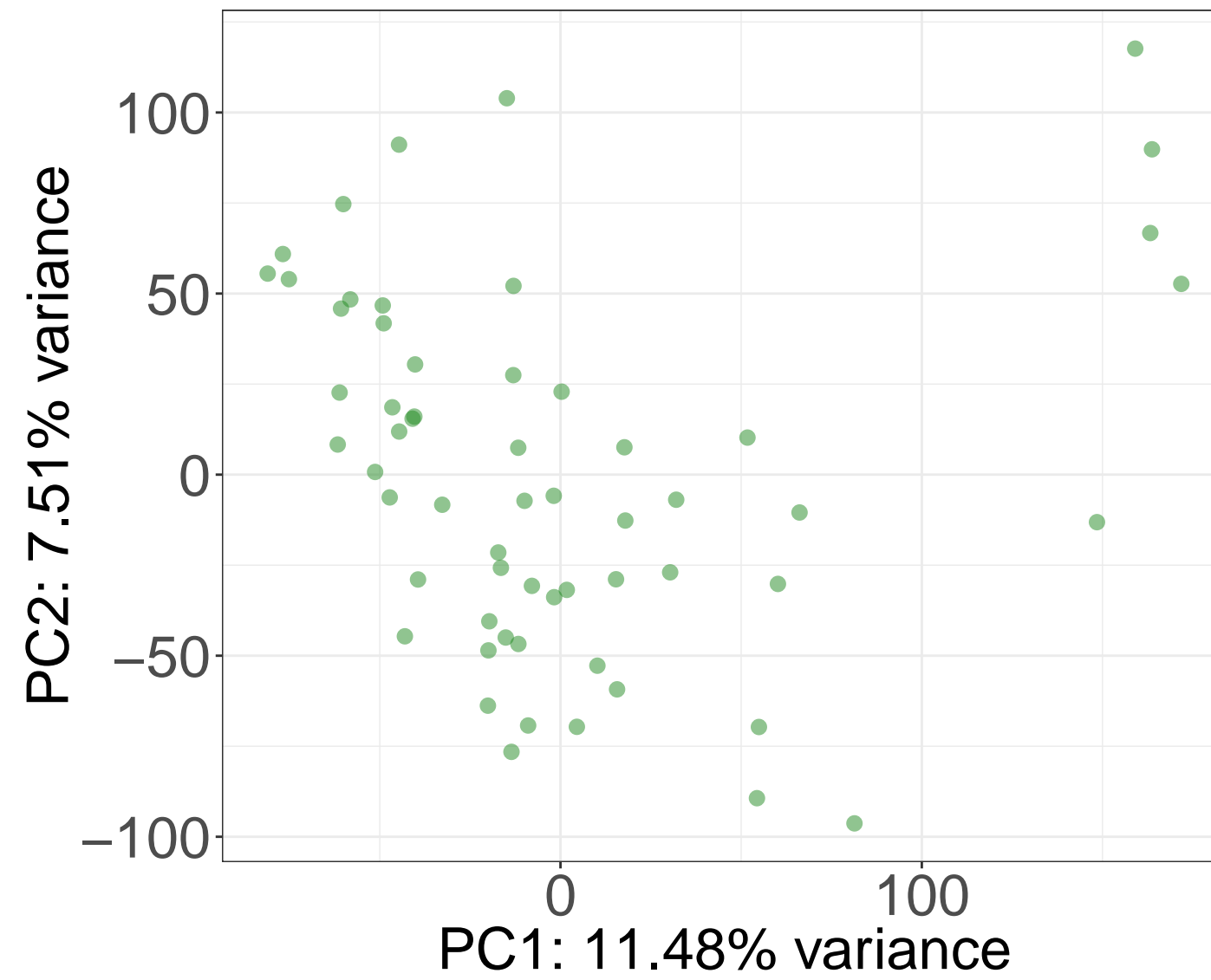
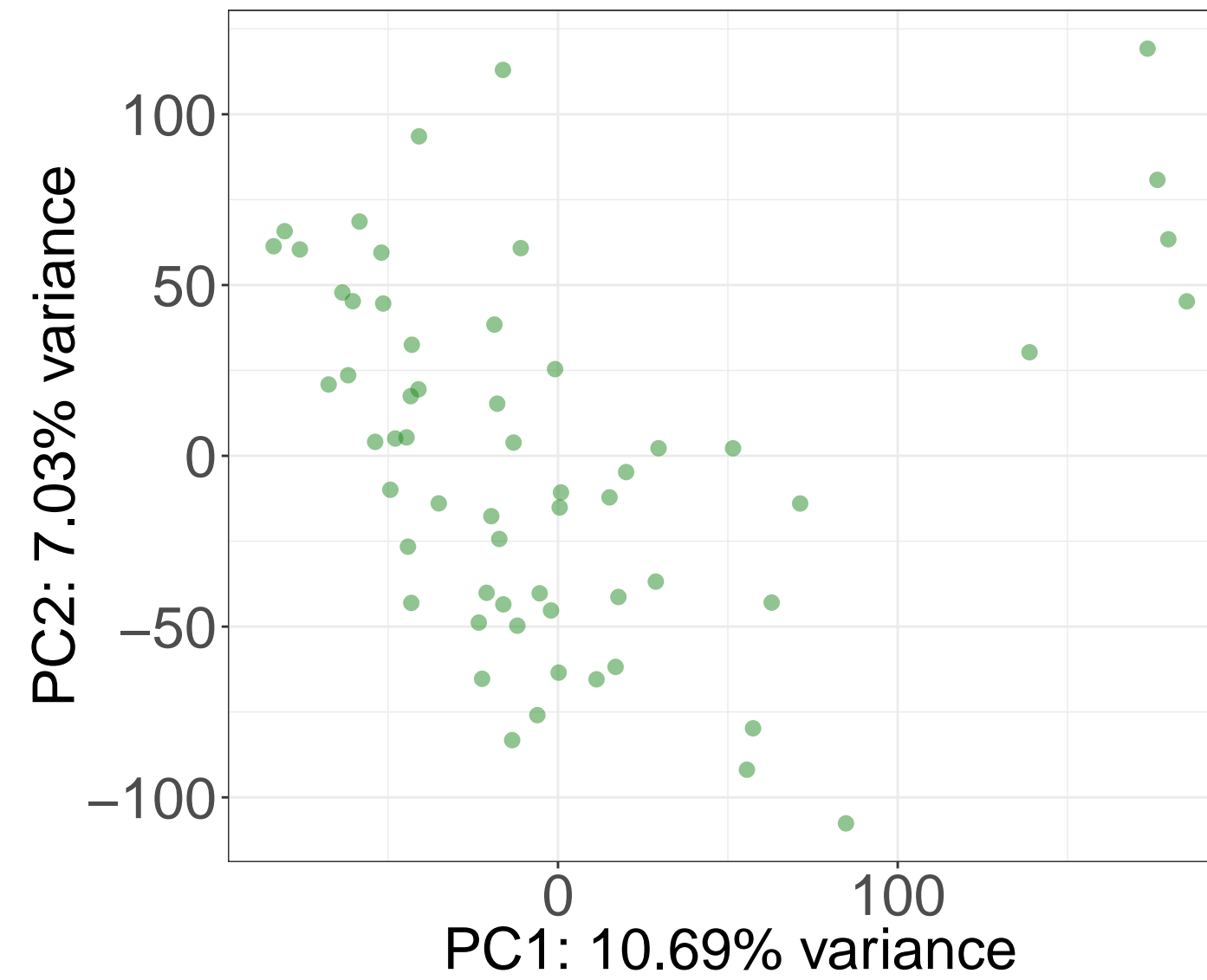
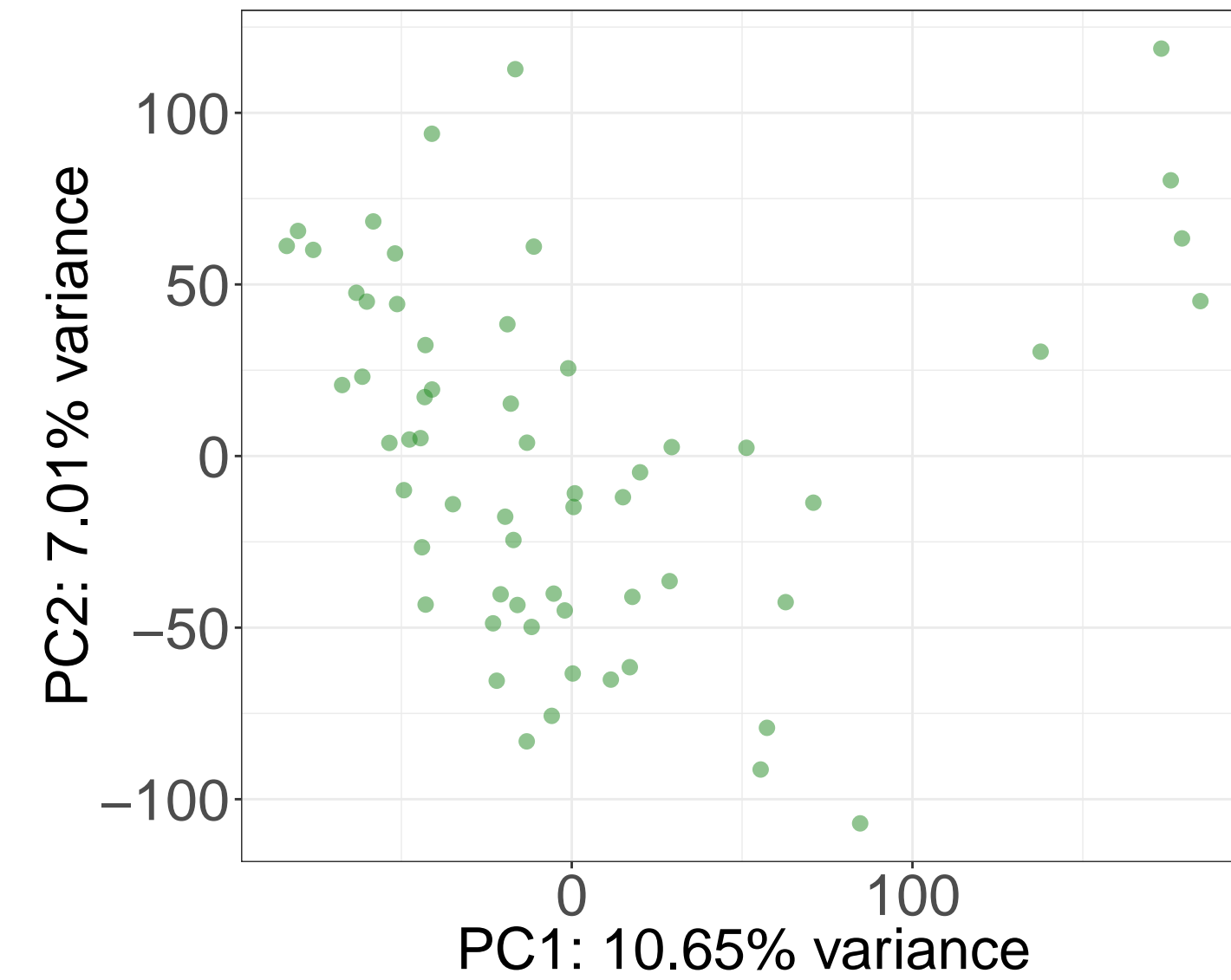


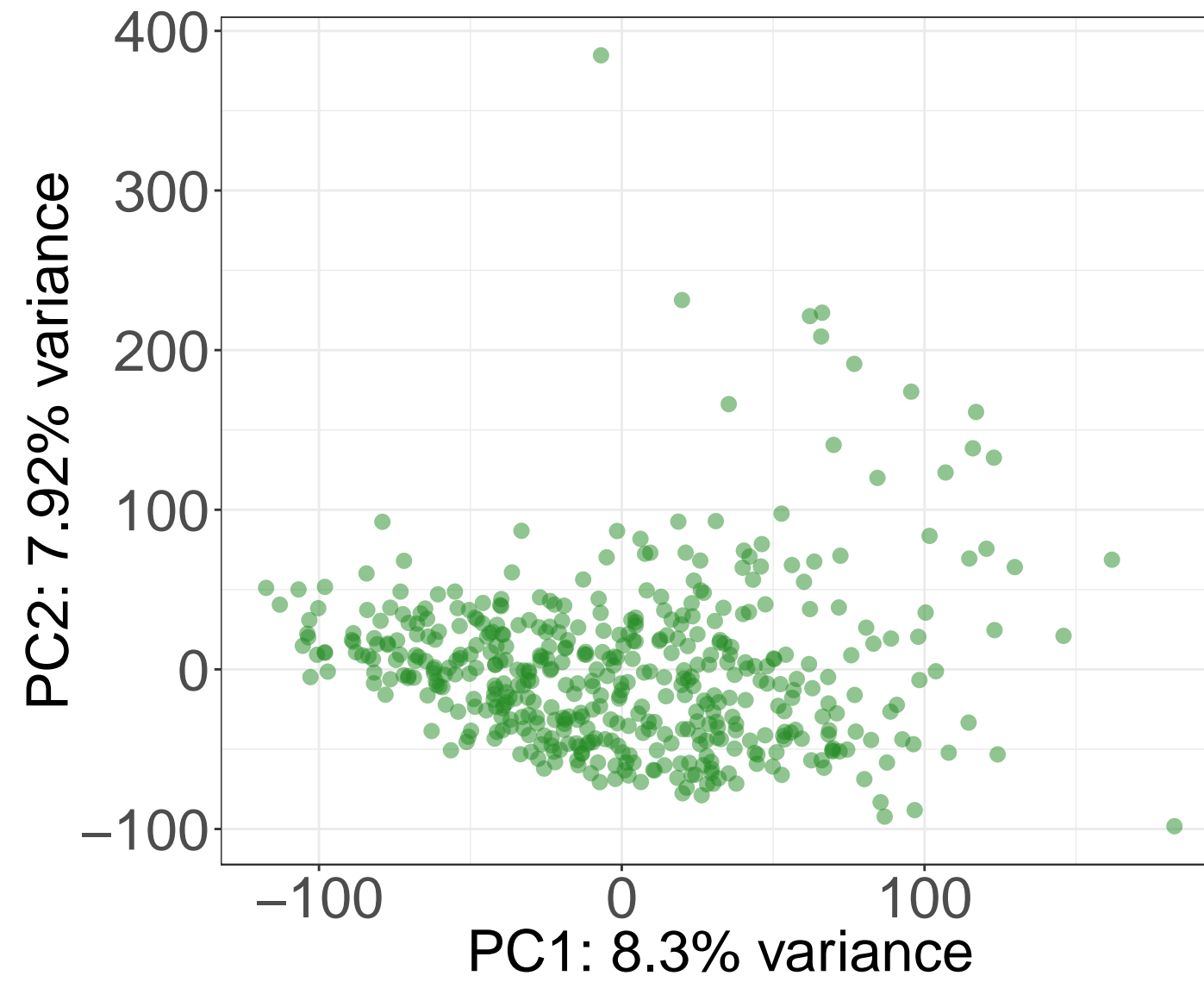
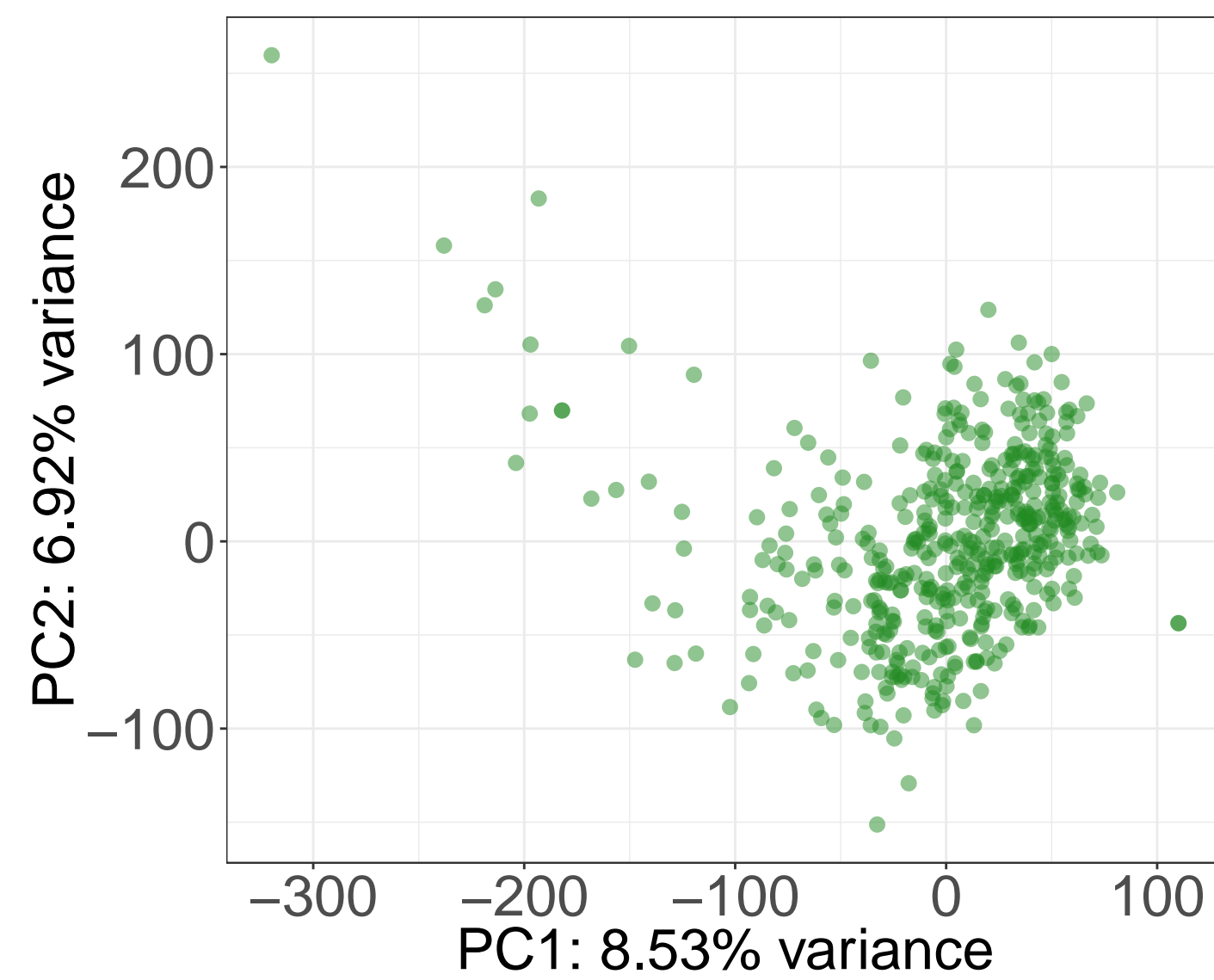
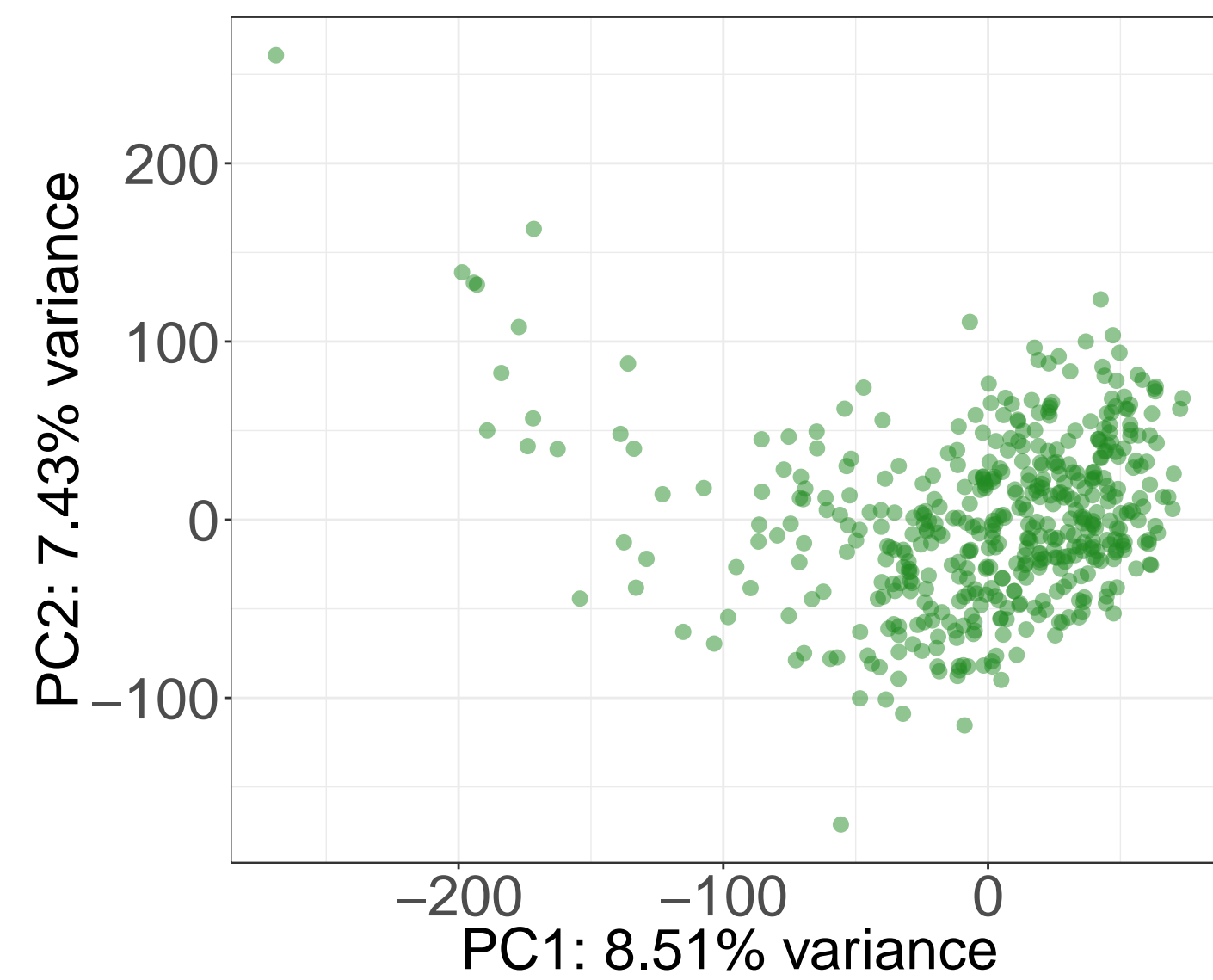
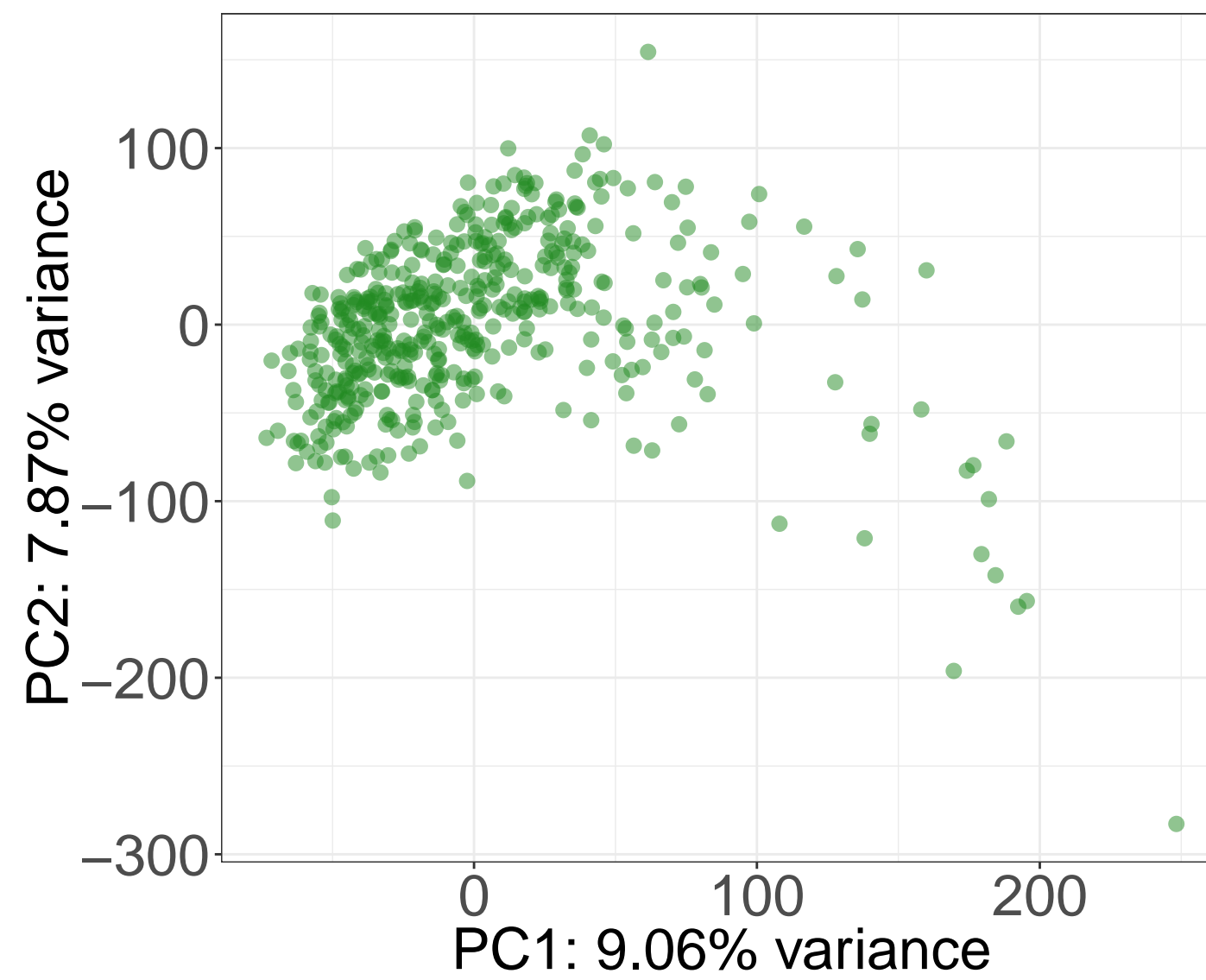
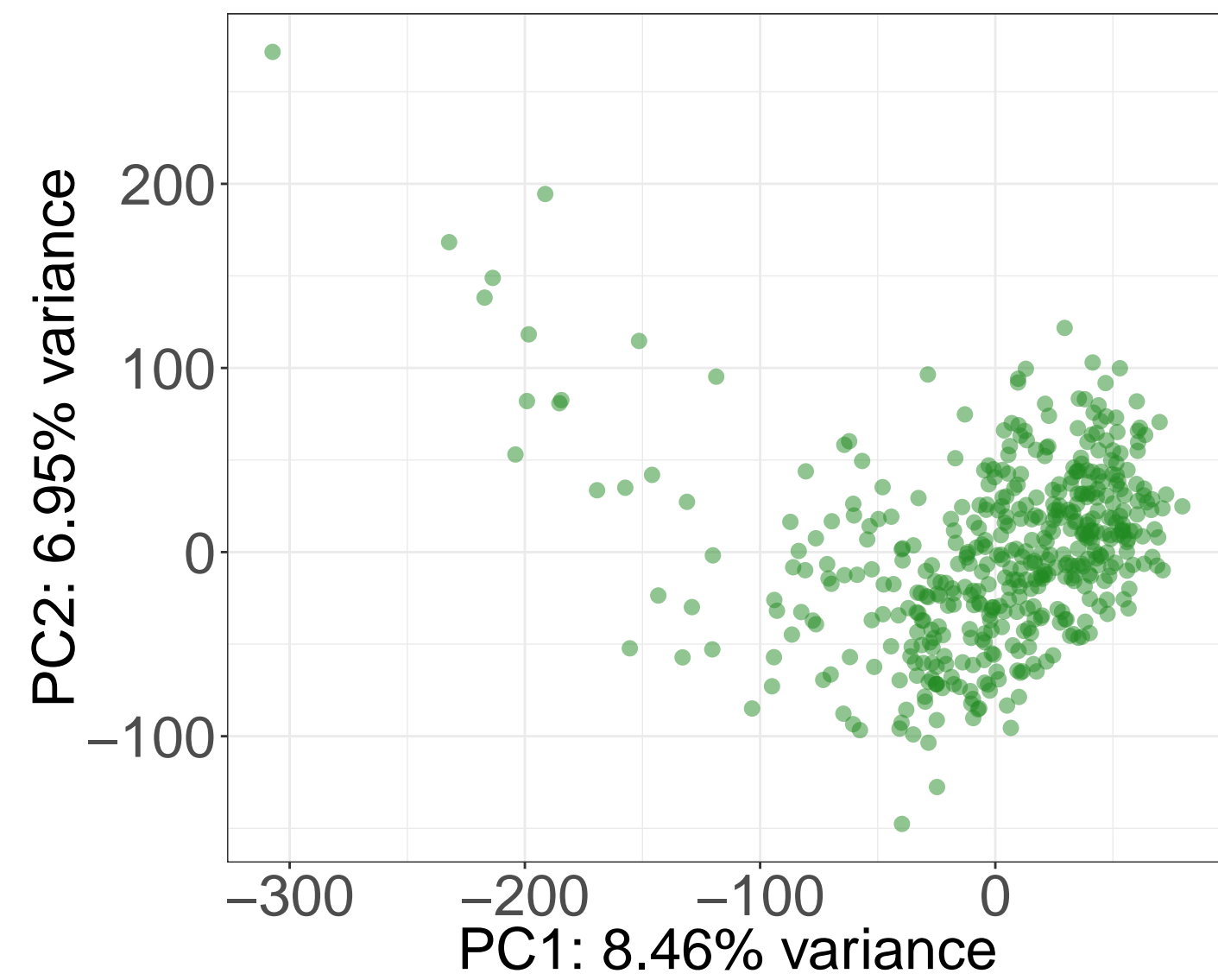
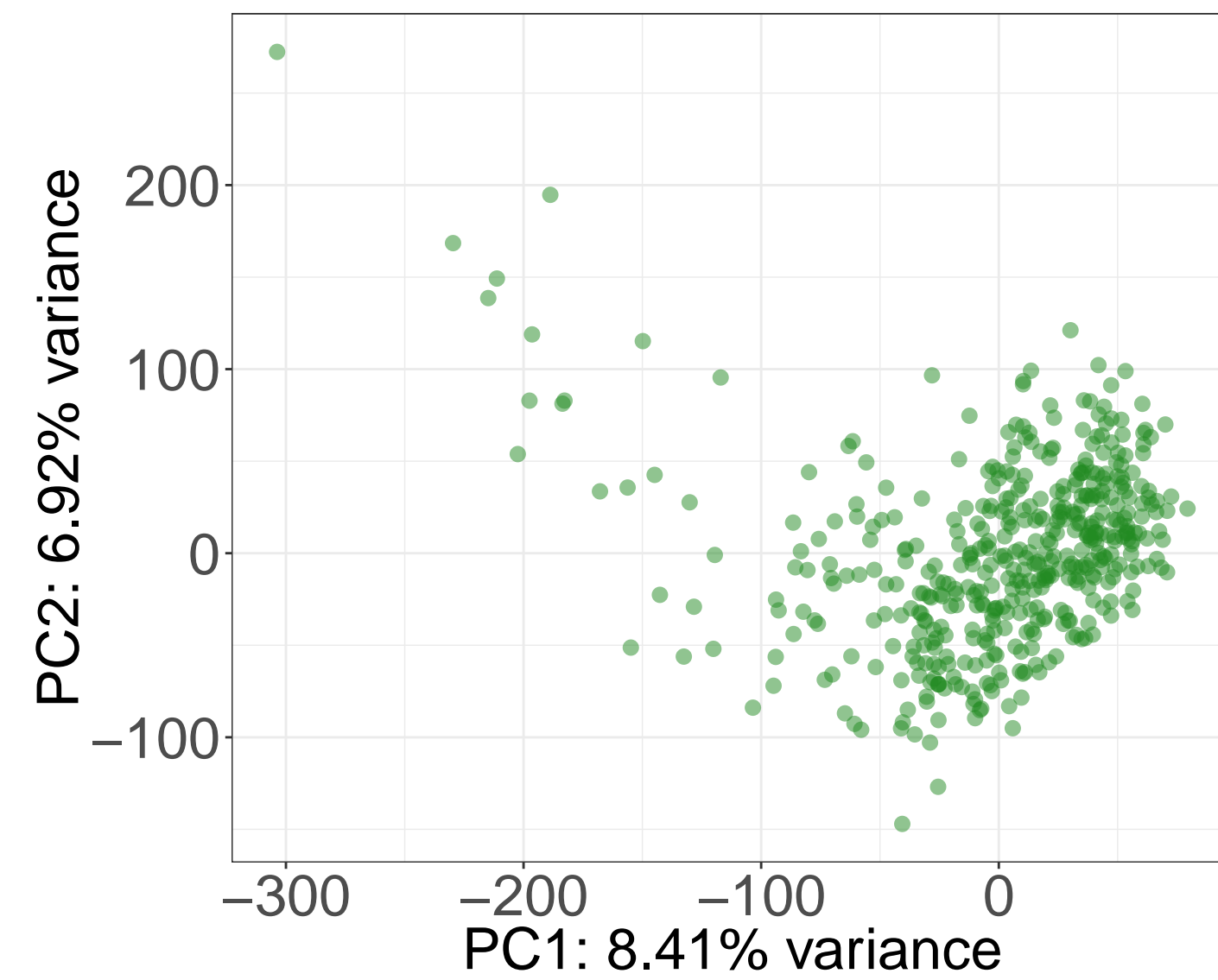
GDC**Xena****Piccolo****Recount2****MSKCC****MSKCC Batch**

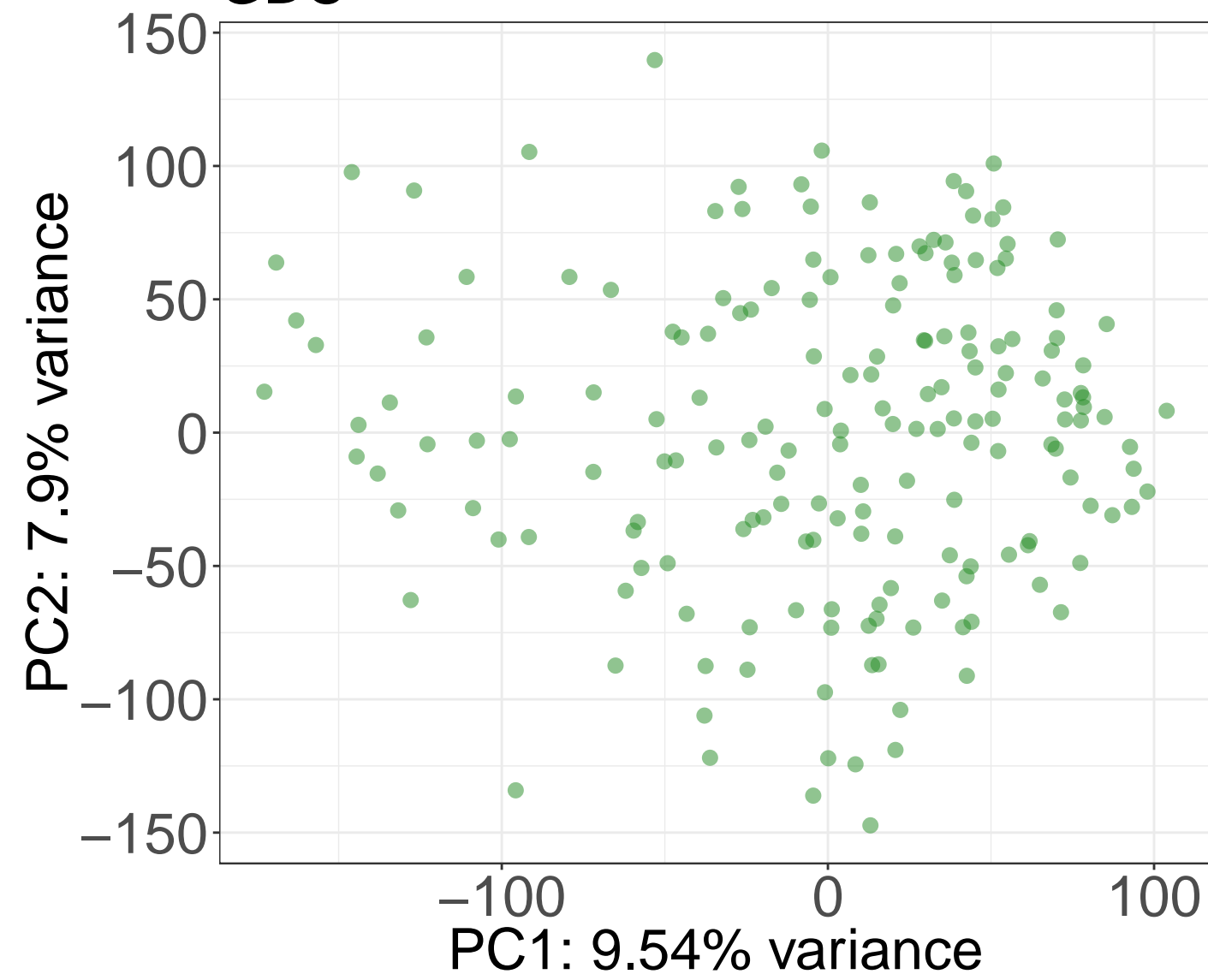
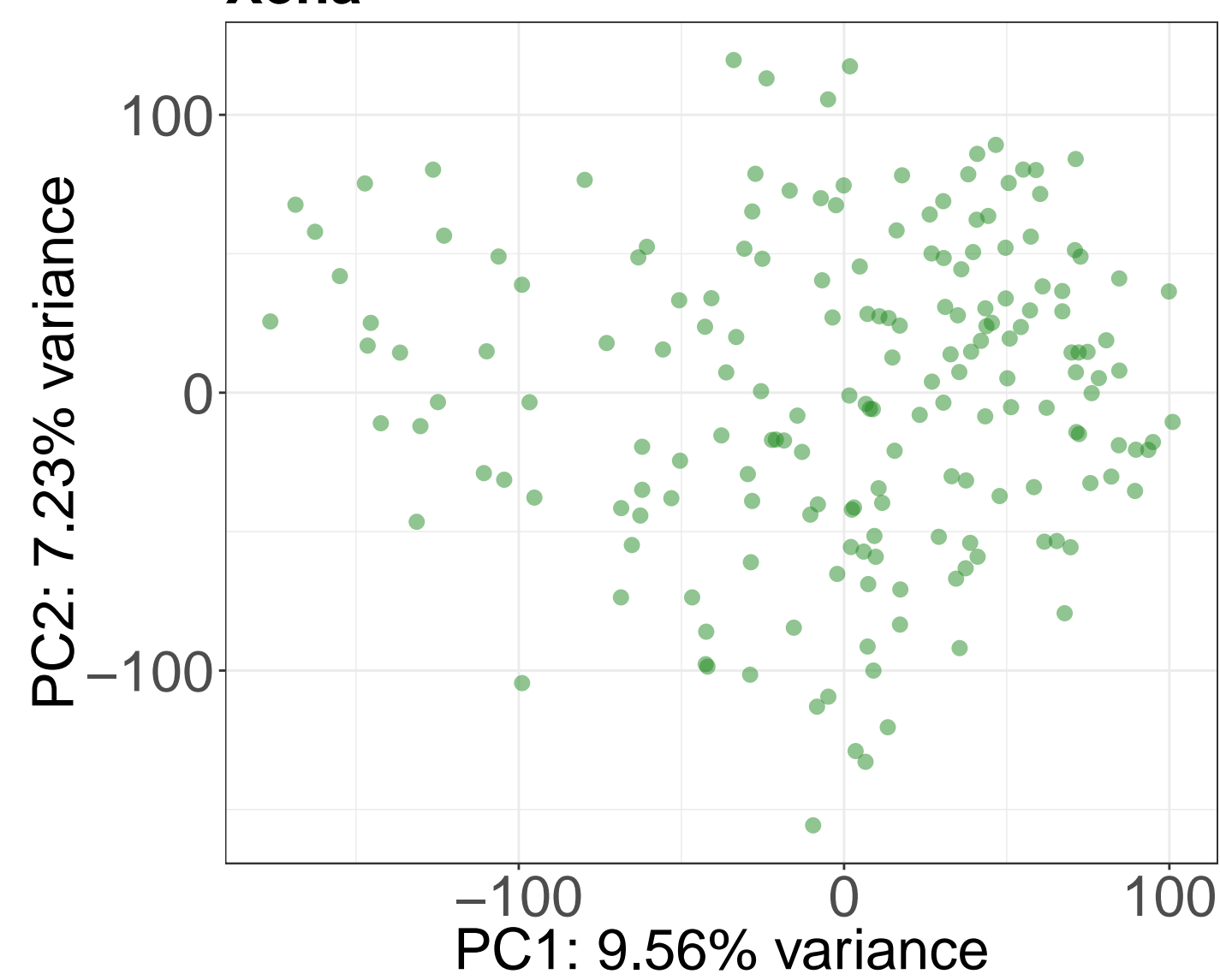
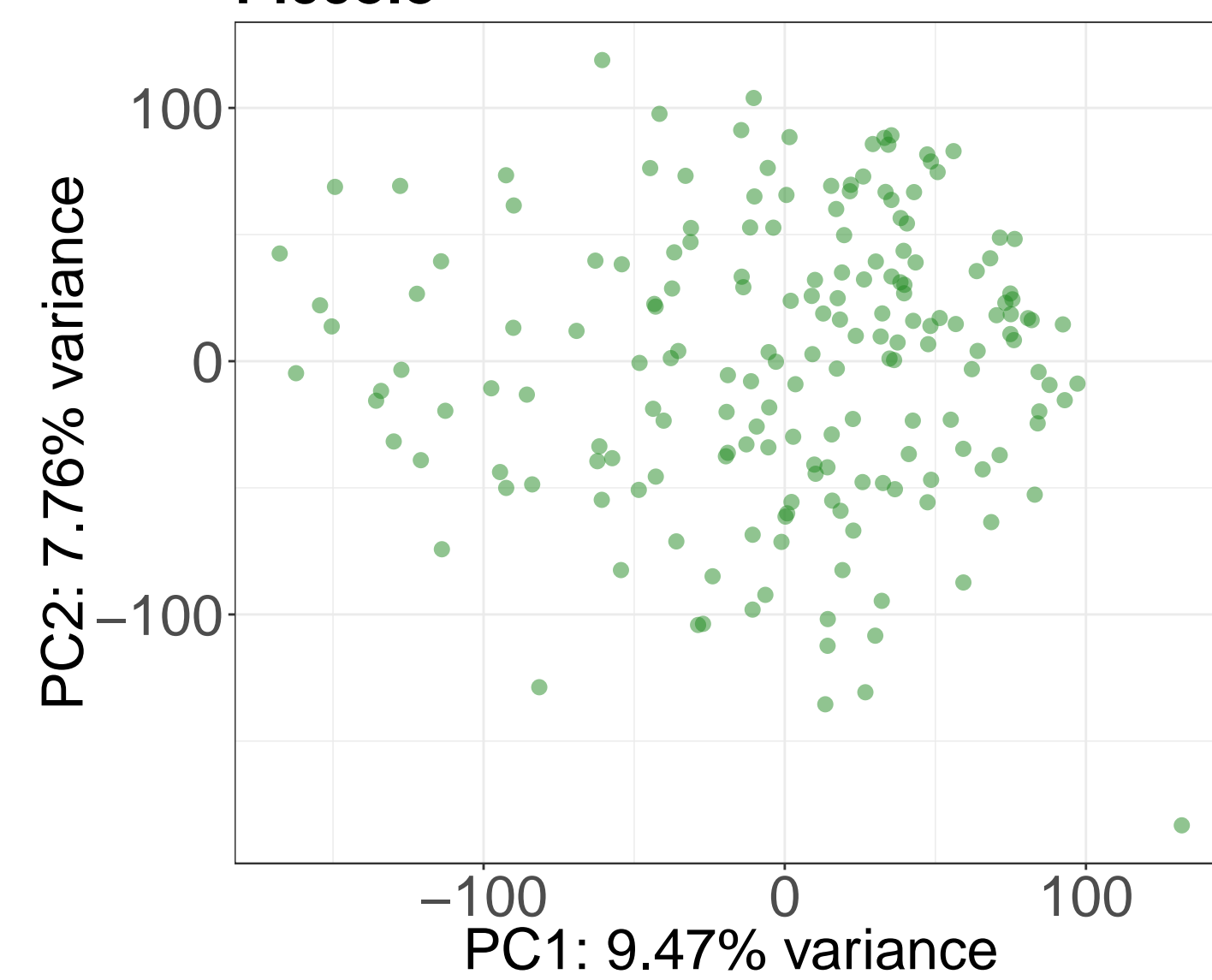
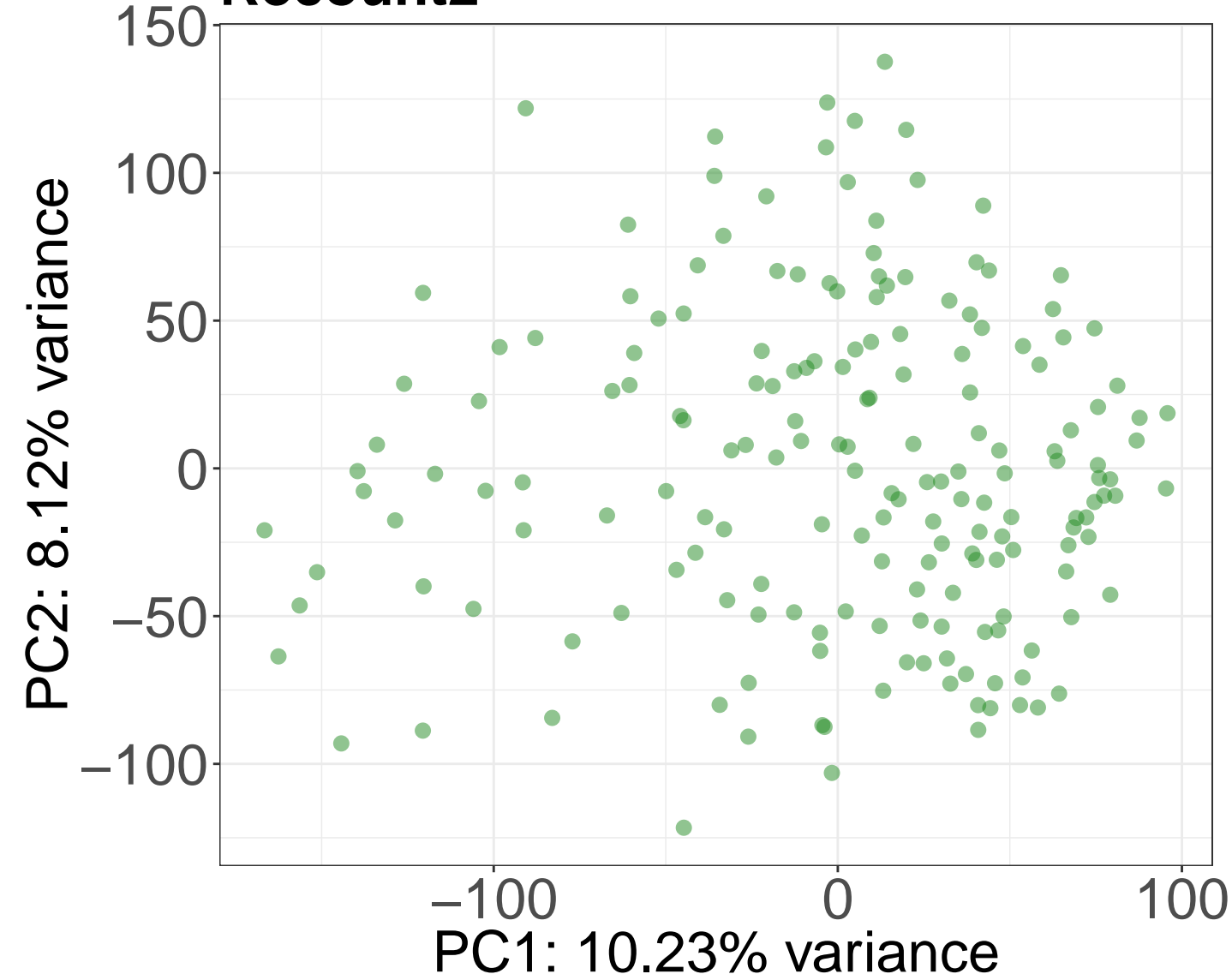
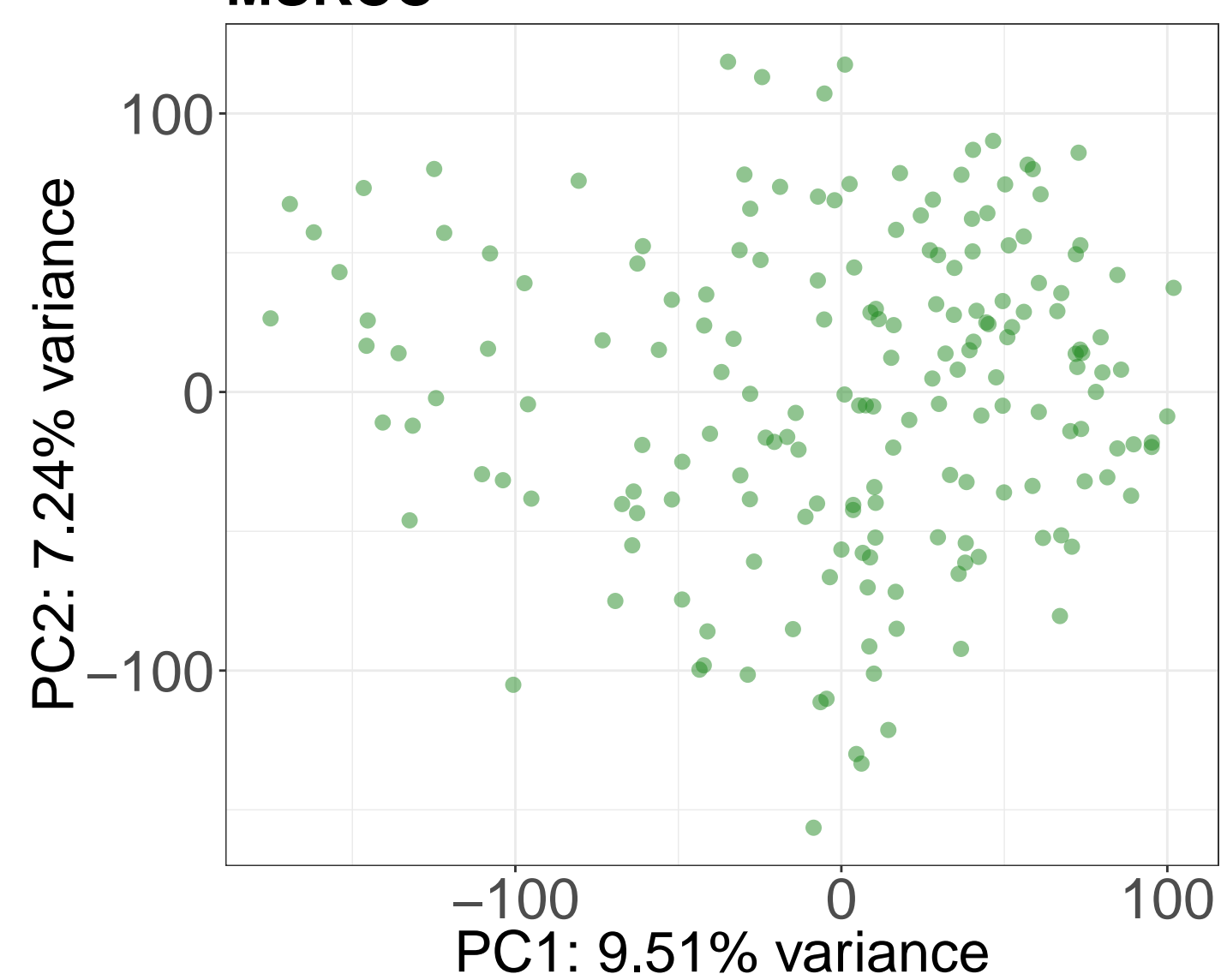
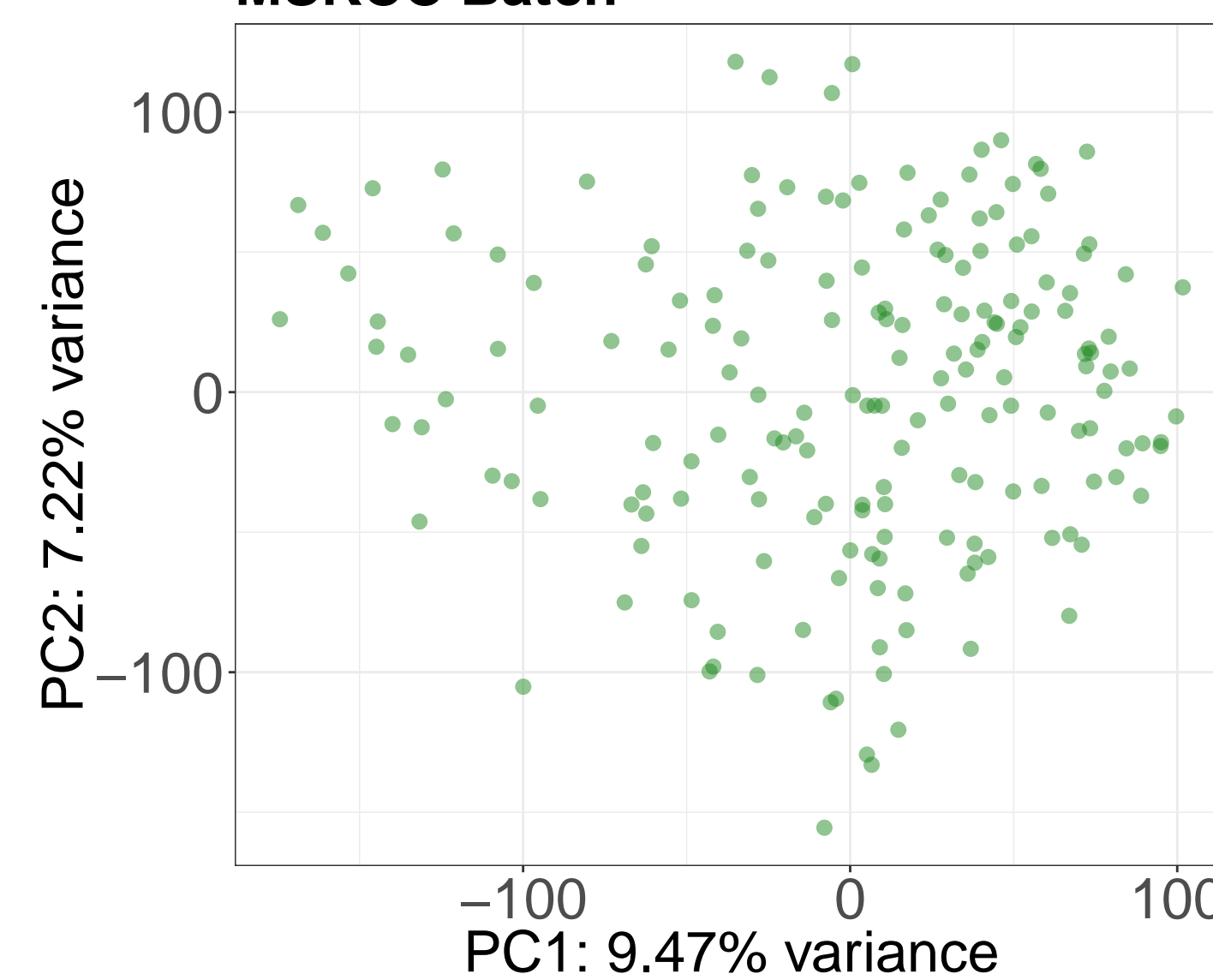
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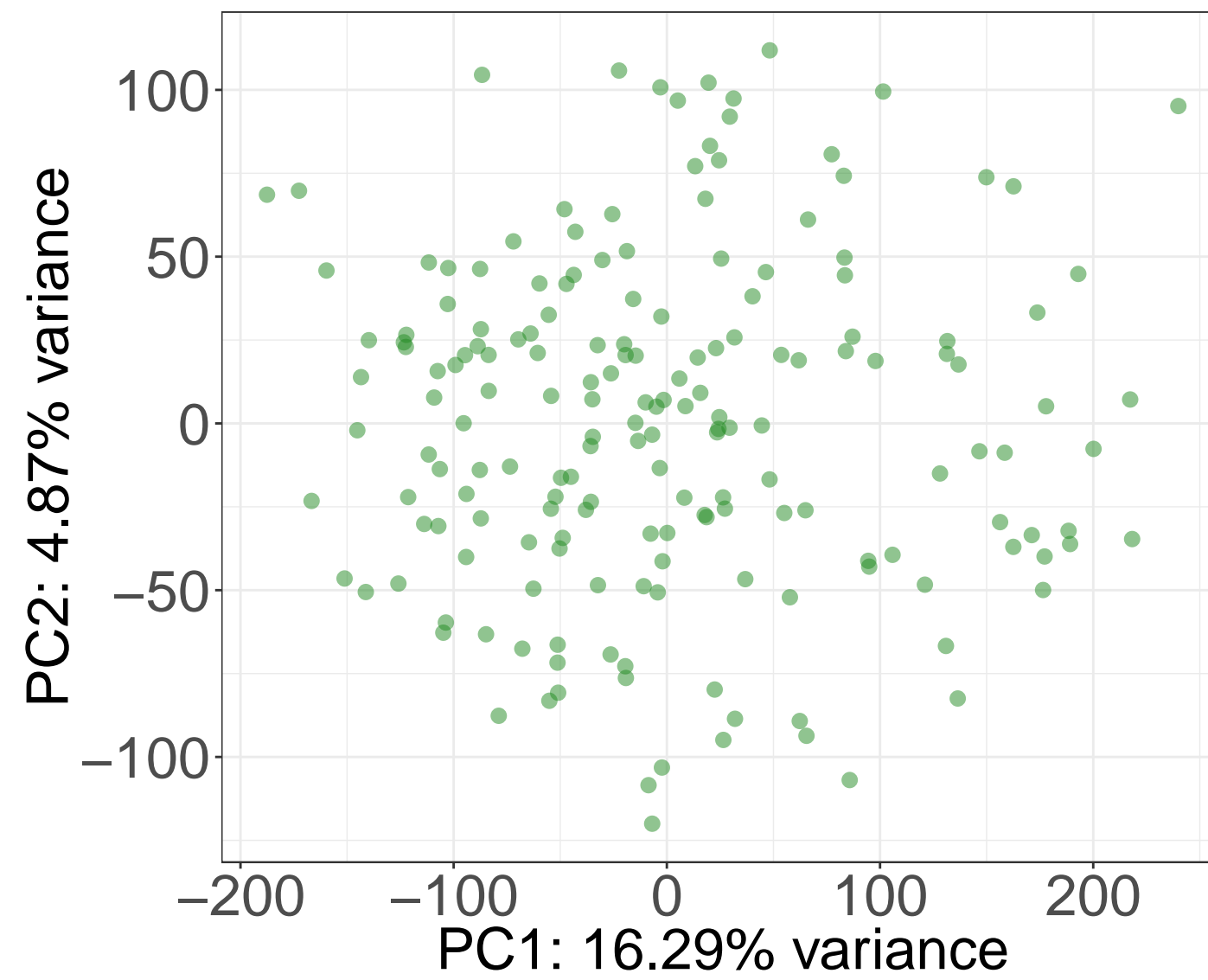
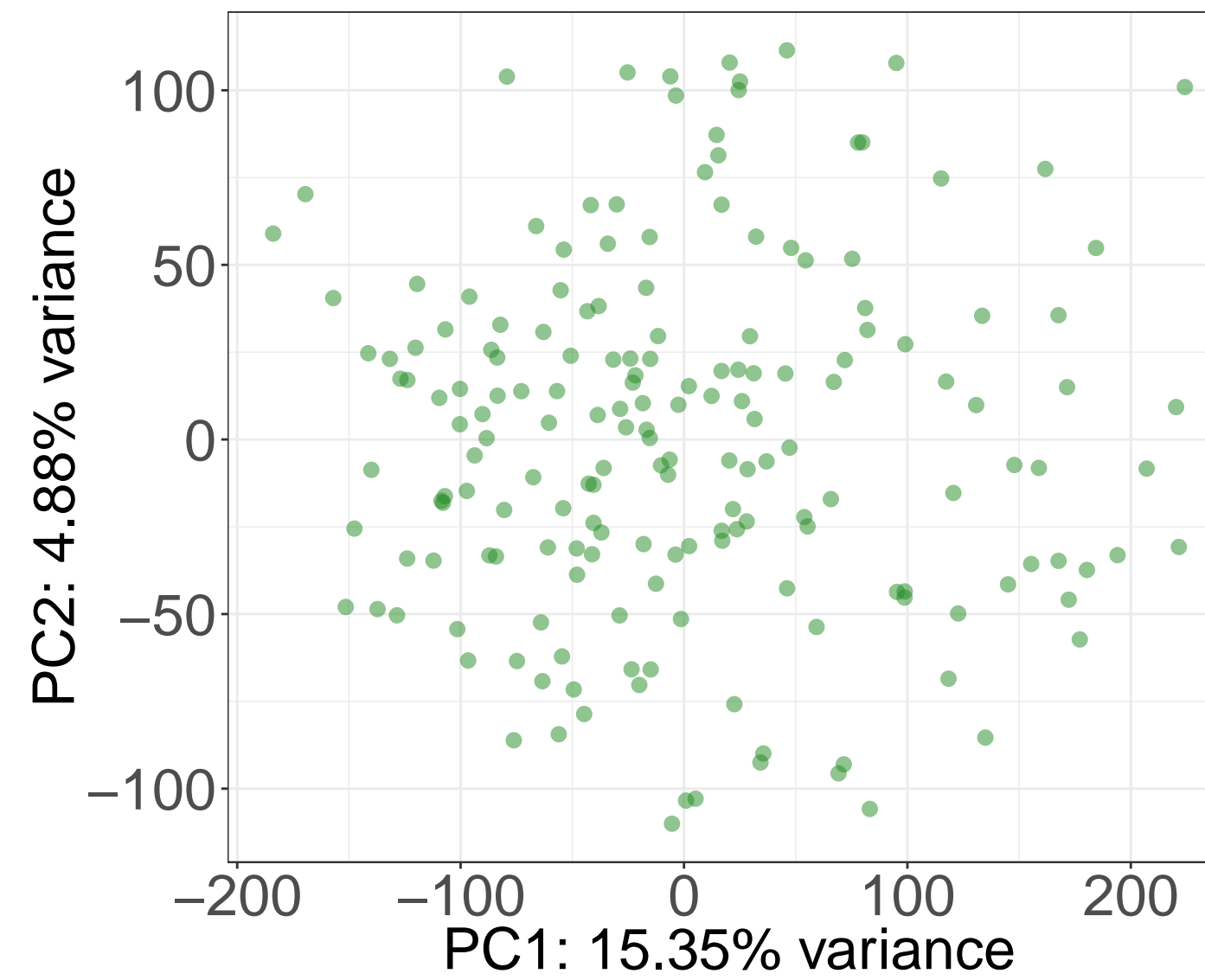
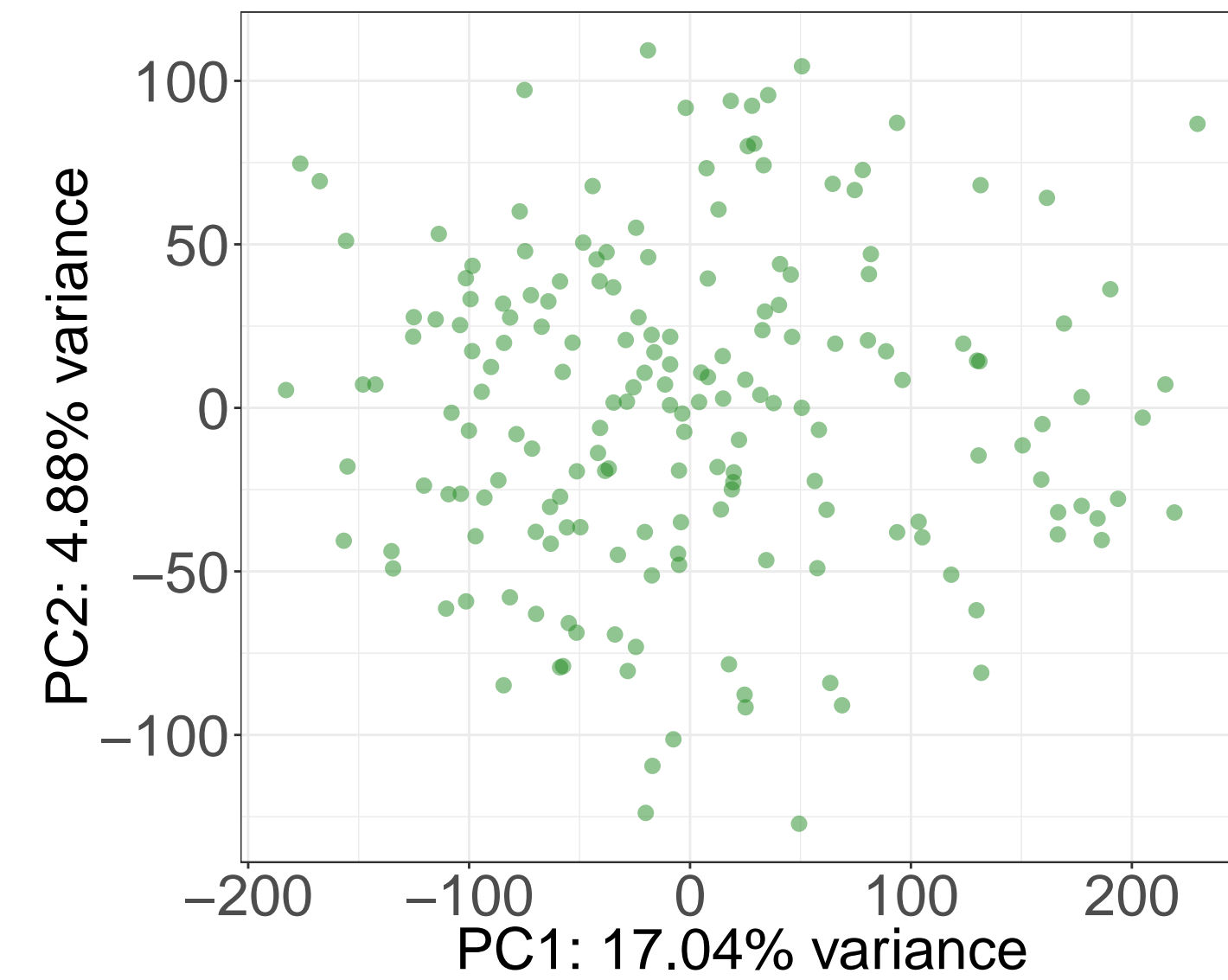
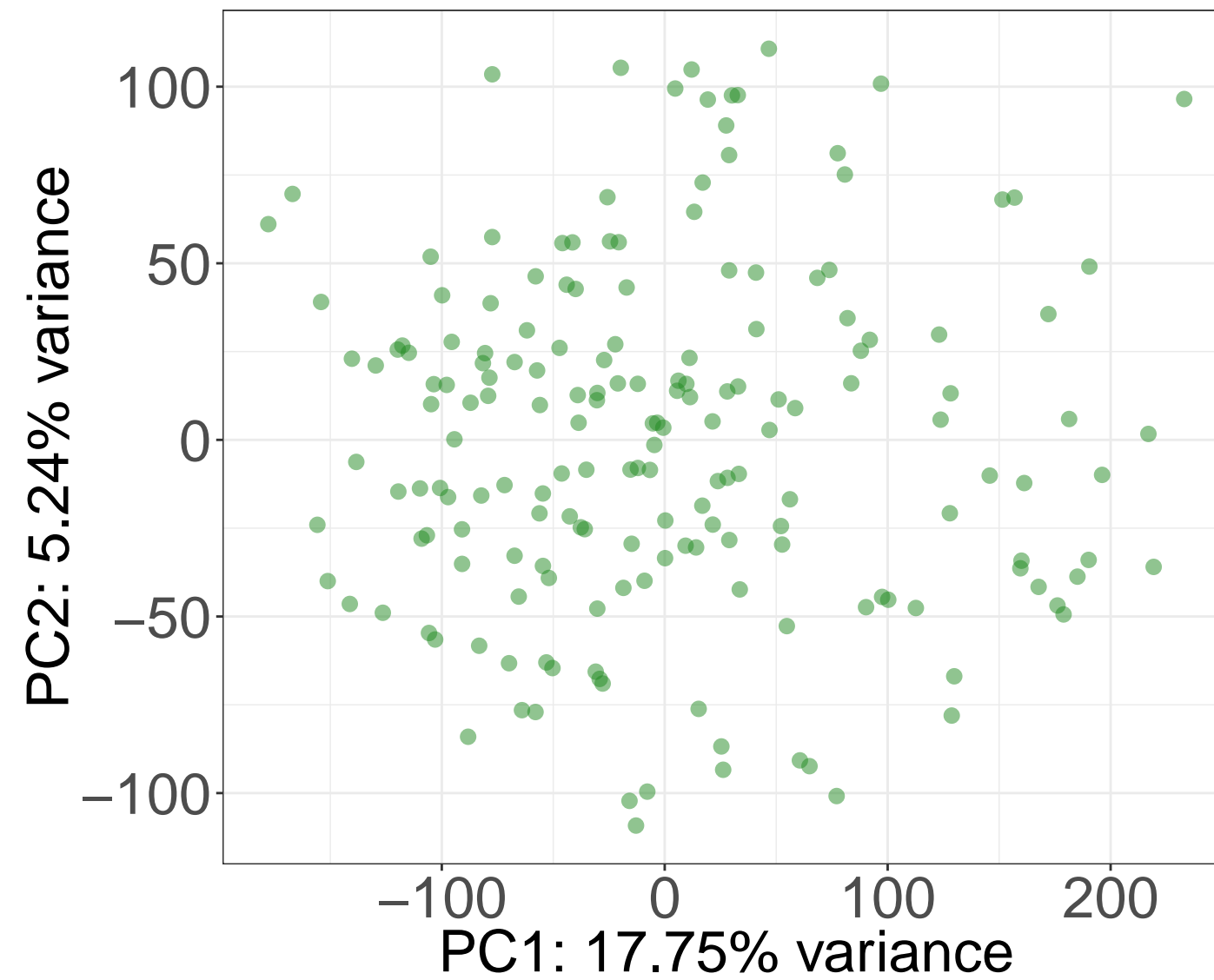
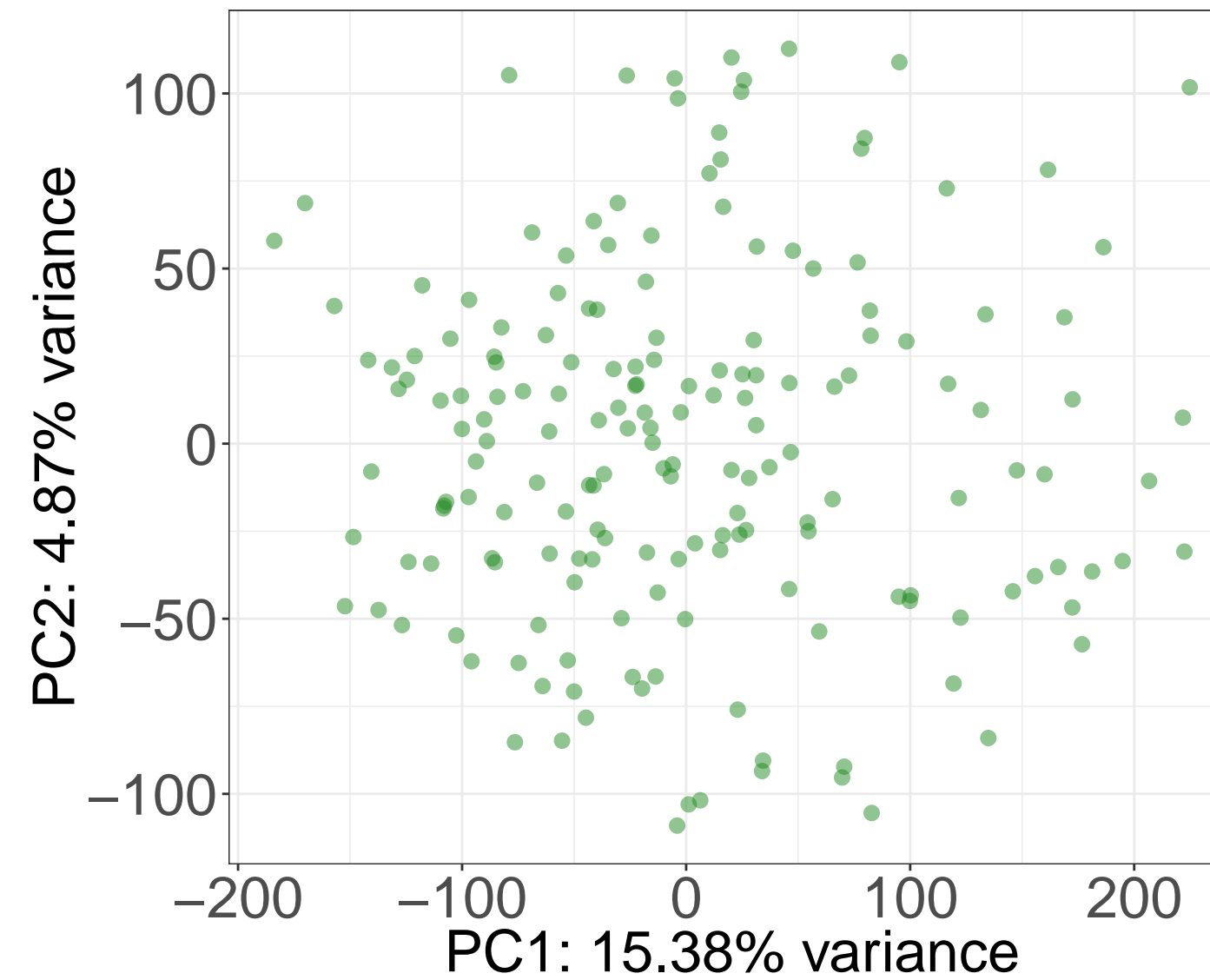
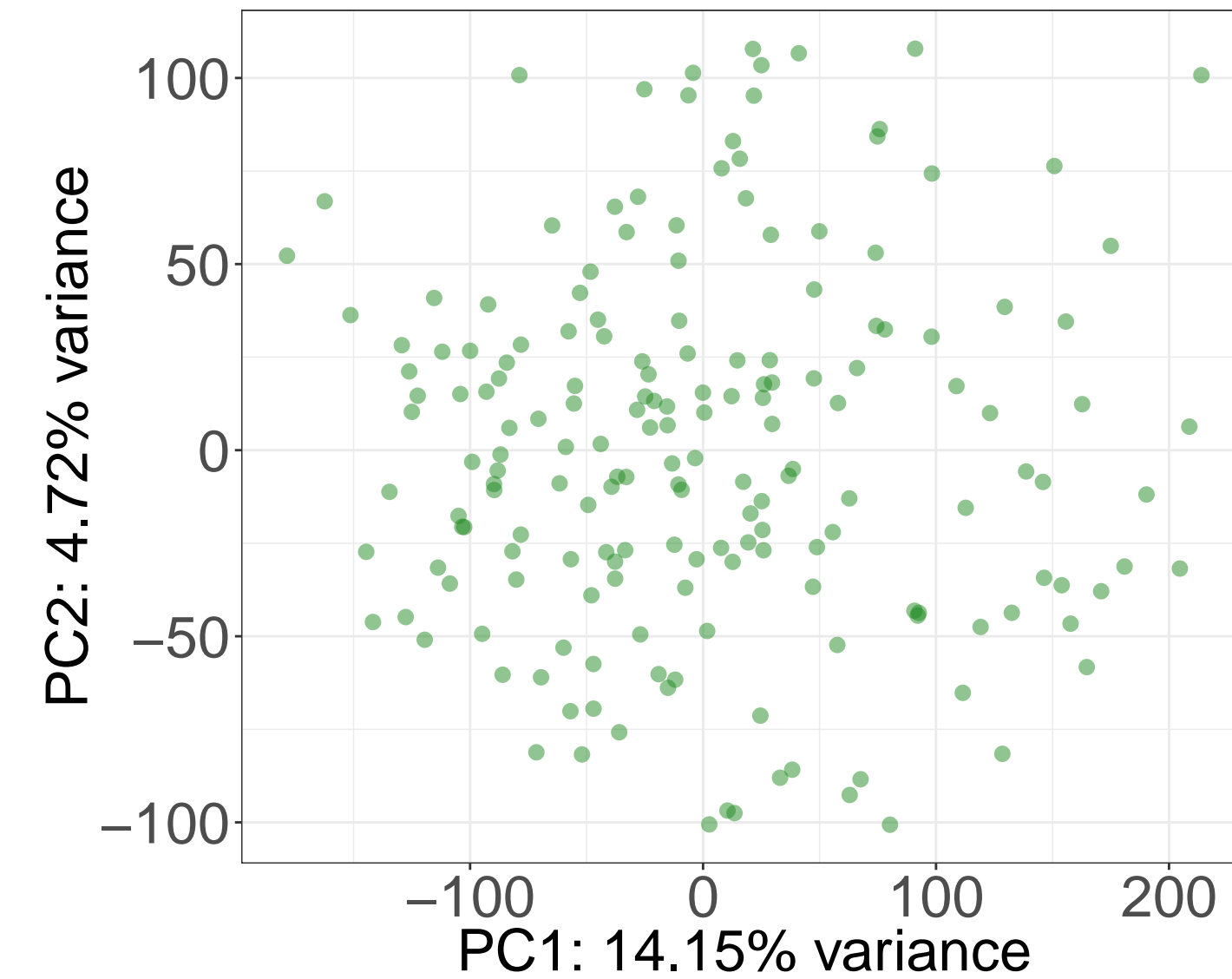
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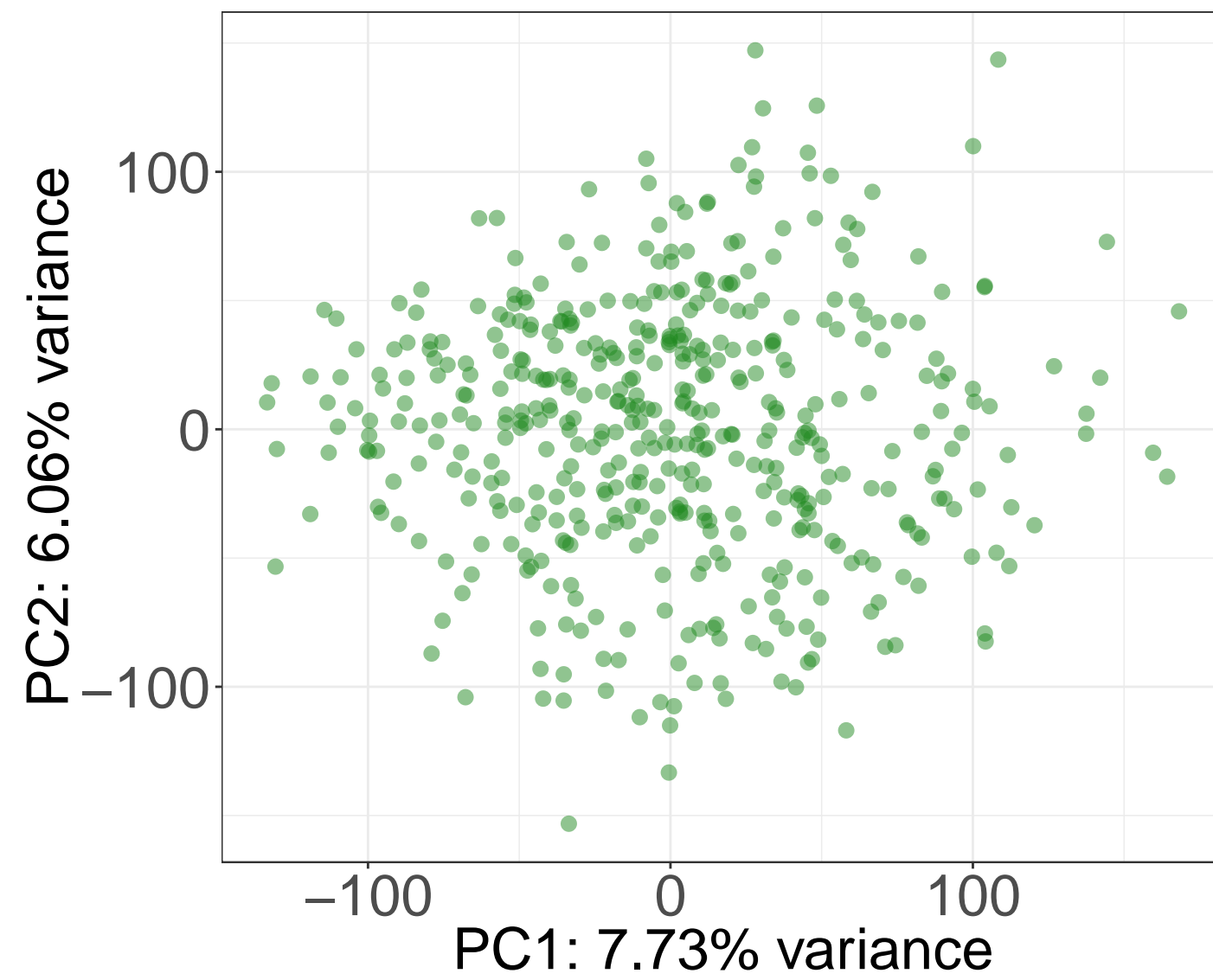
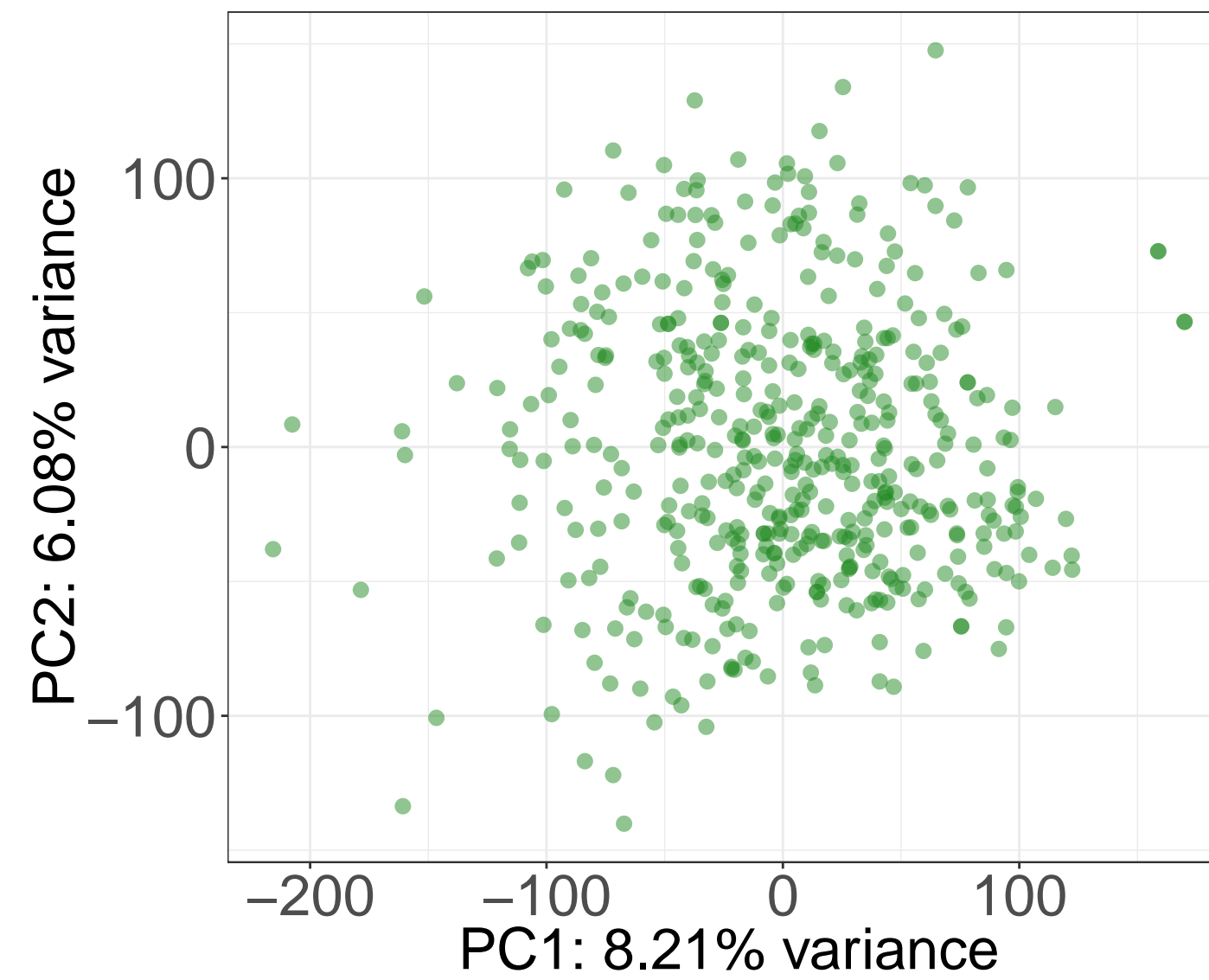
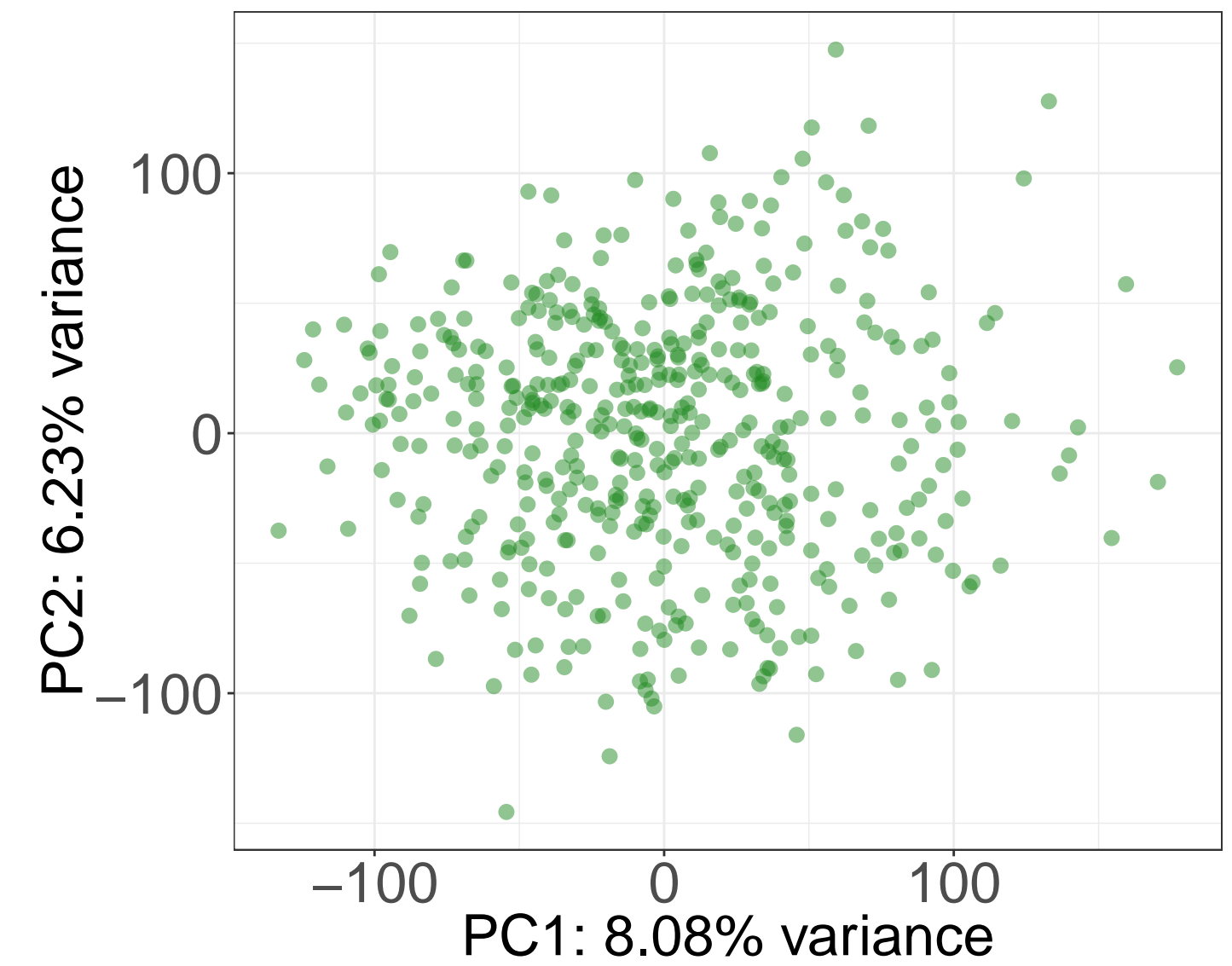
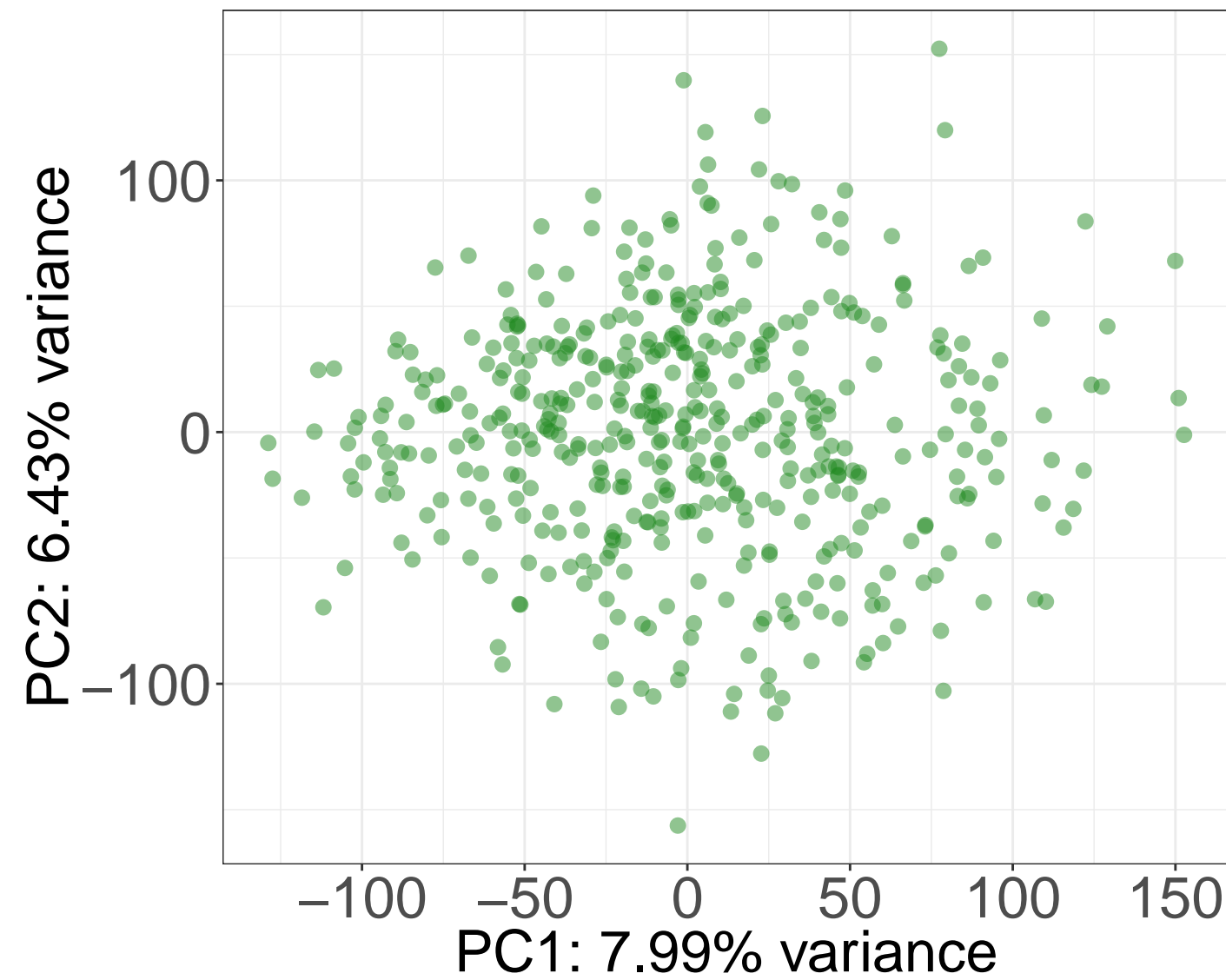
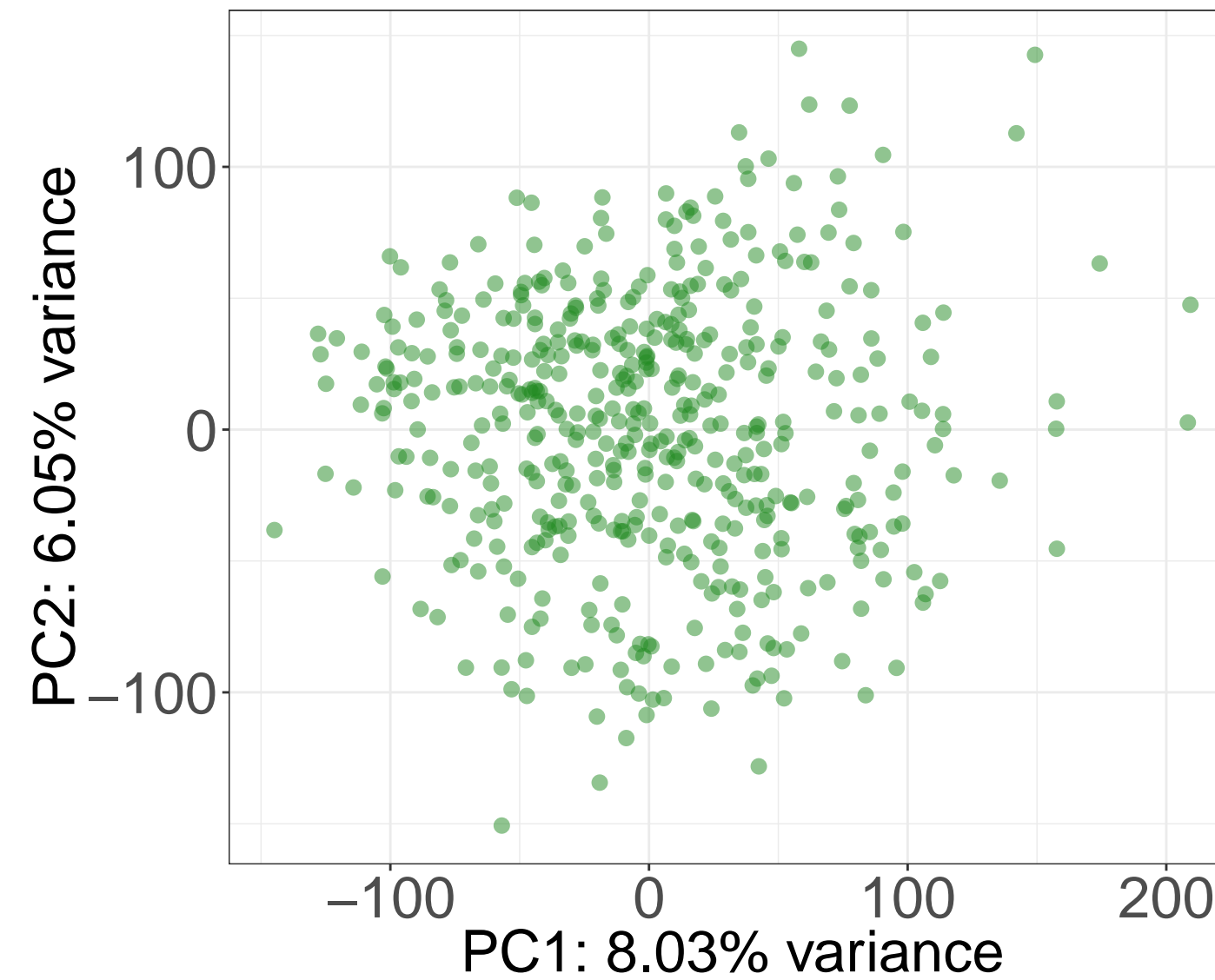
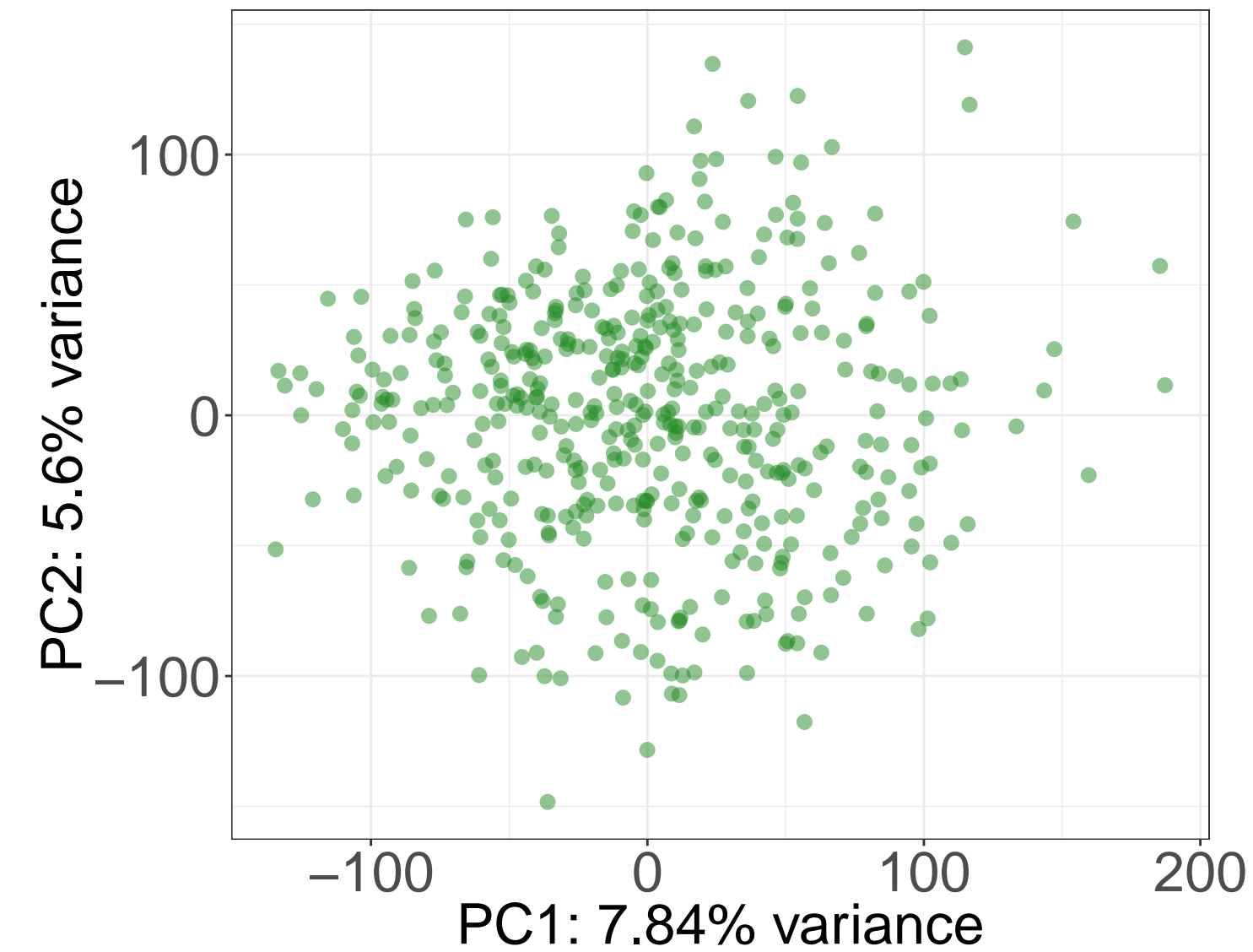
GDC**Xena****Piccolo****Recount2****MSKCC****MSKCC Batch**

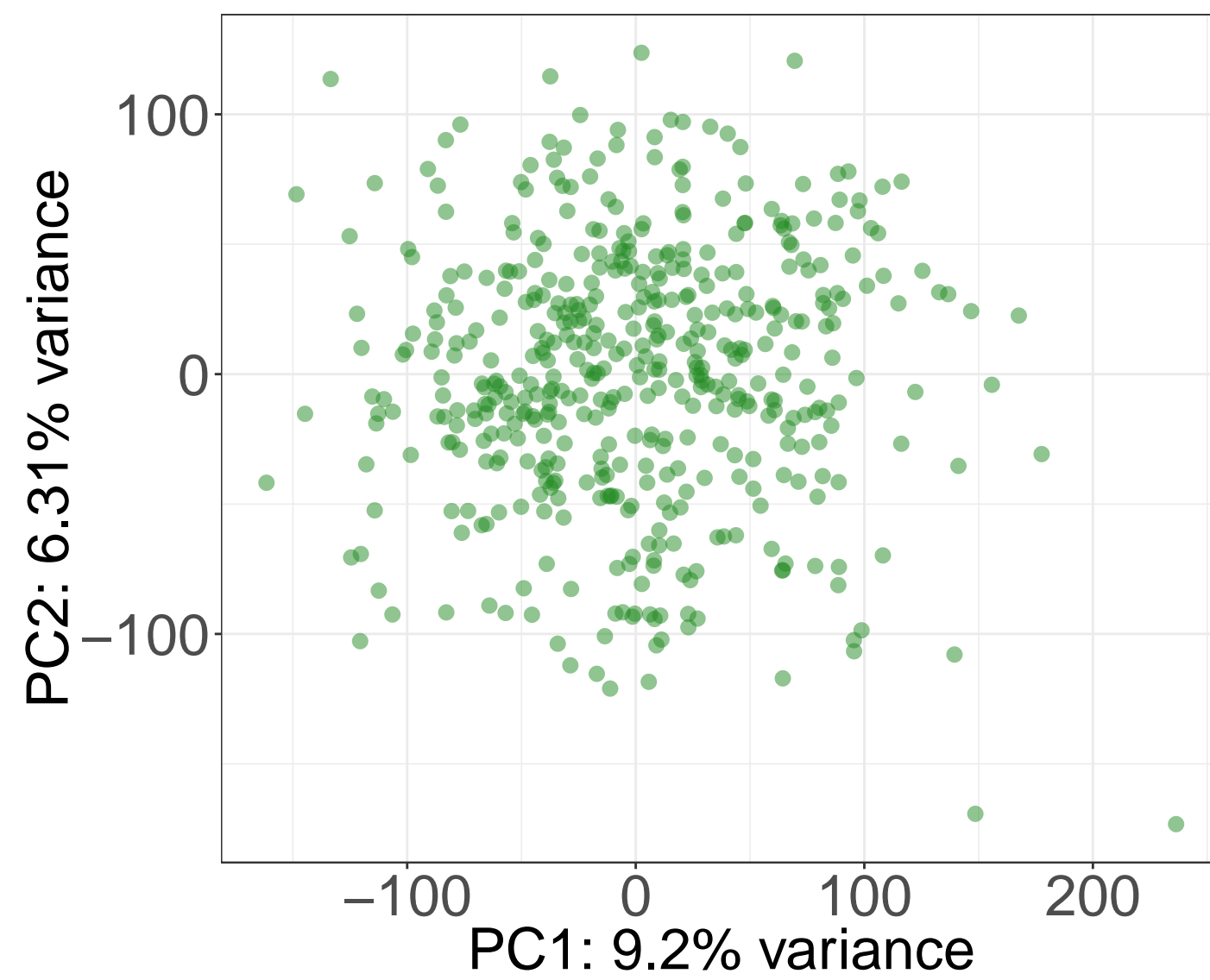
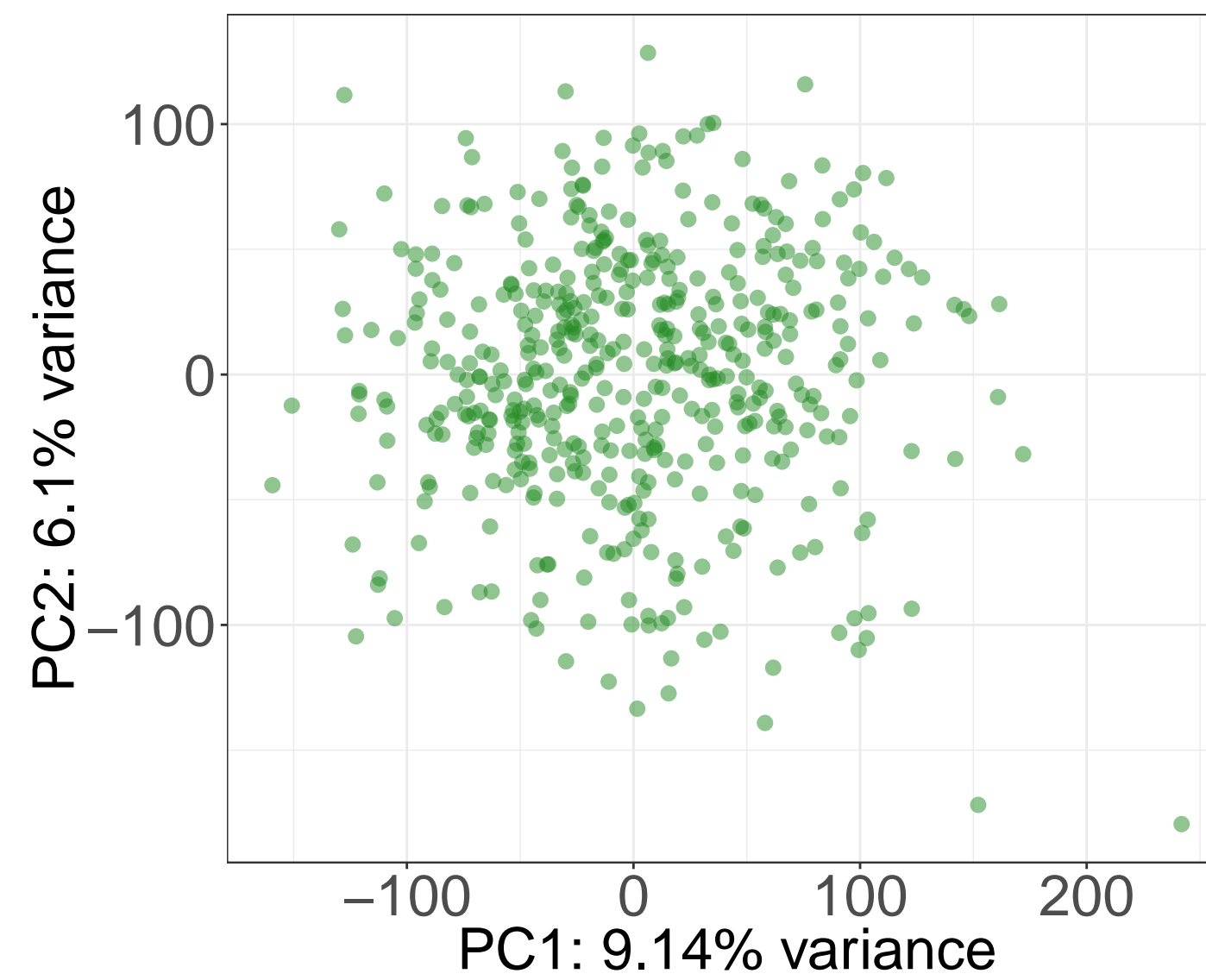
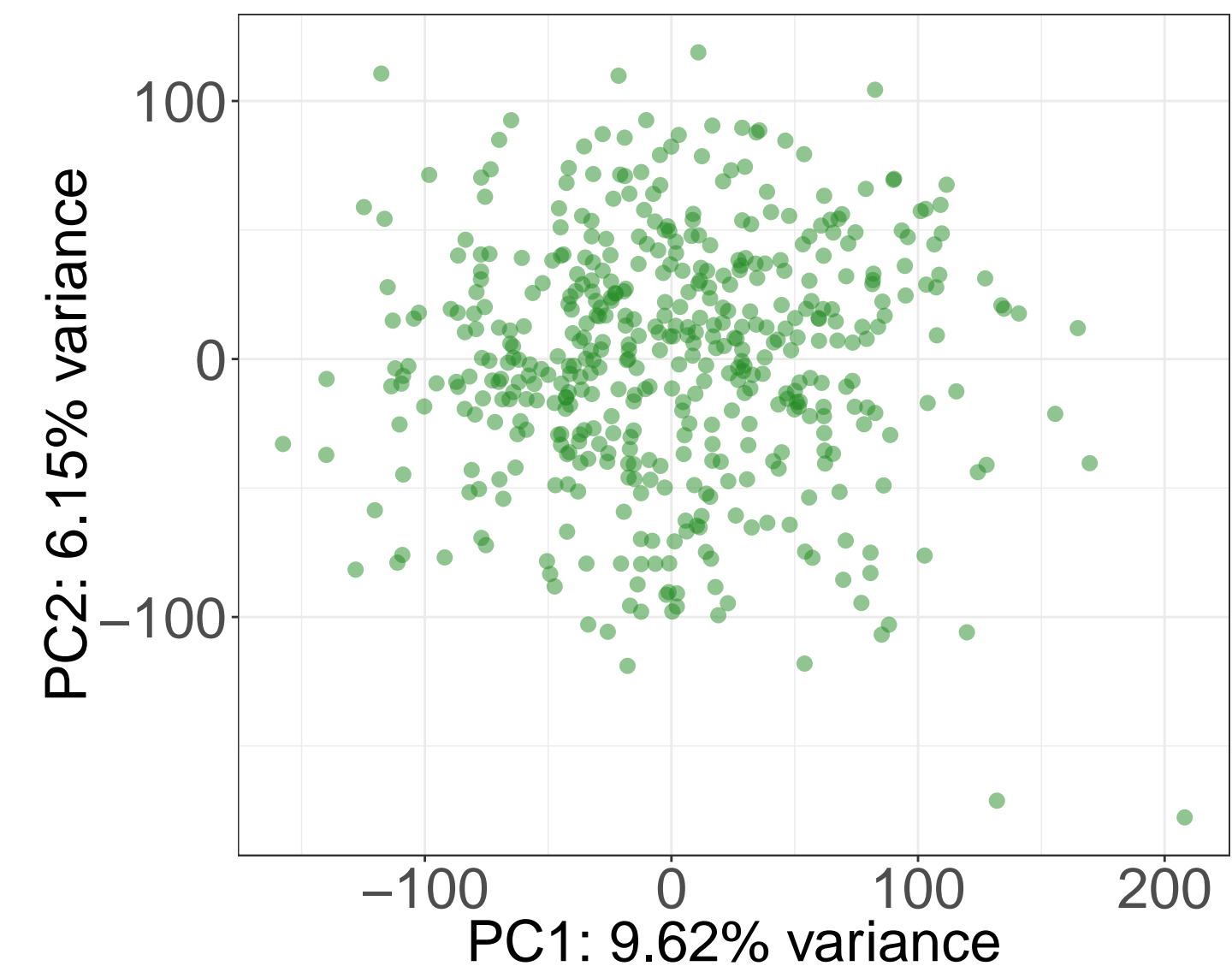
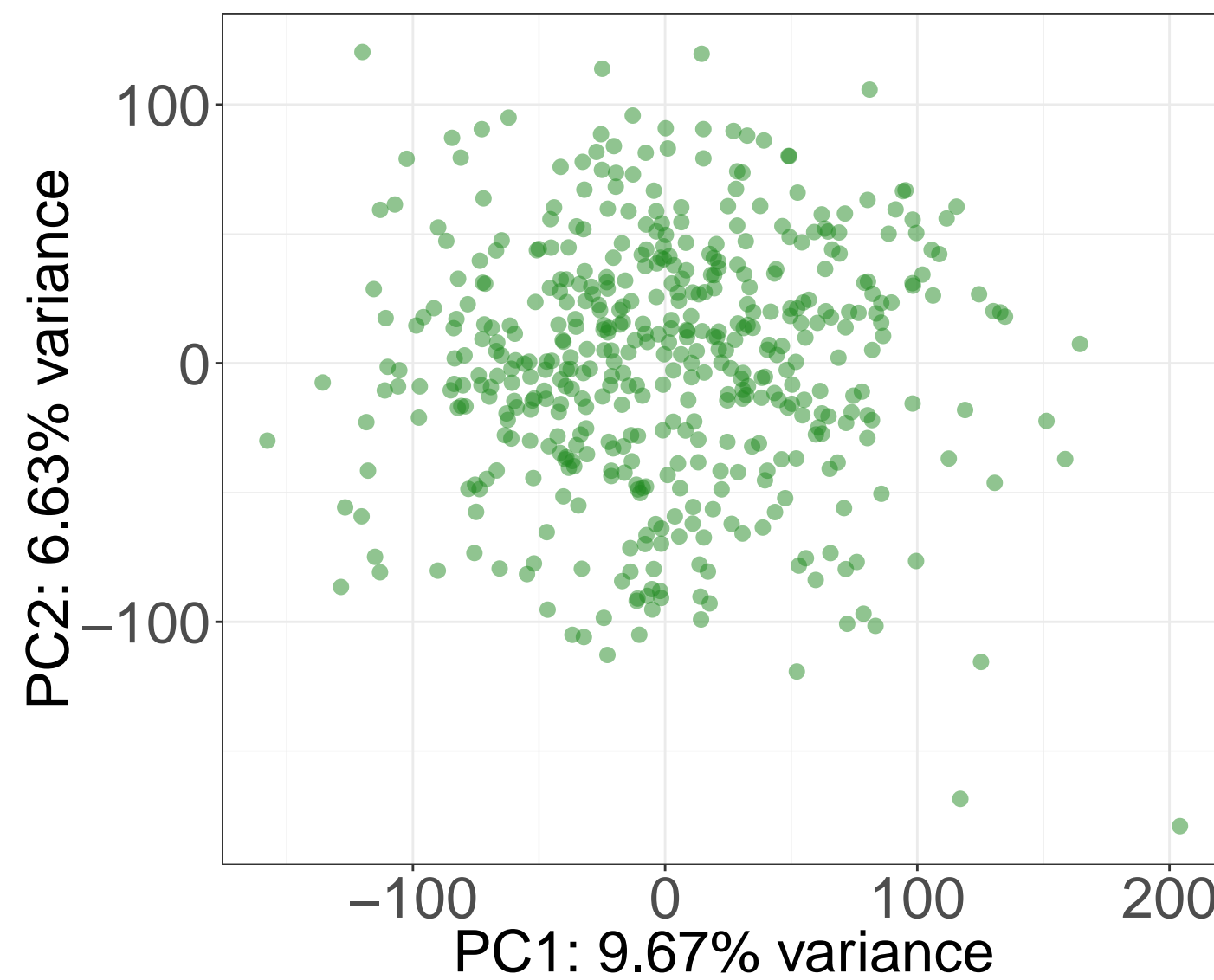
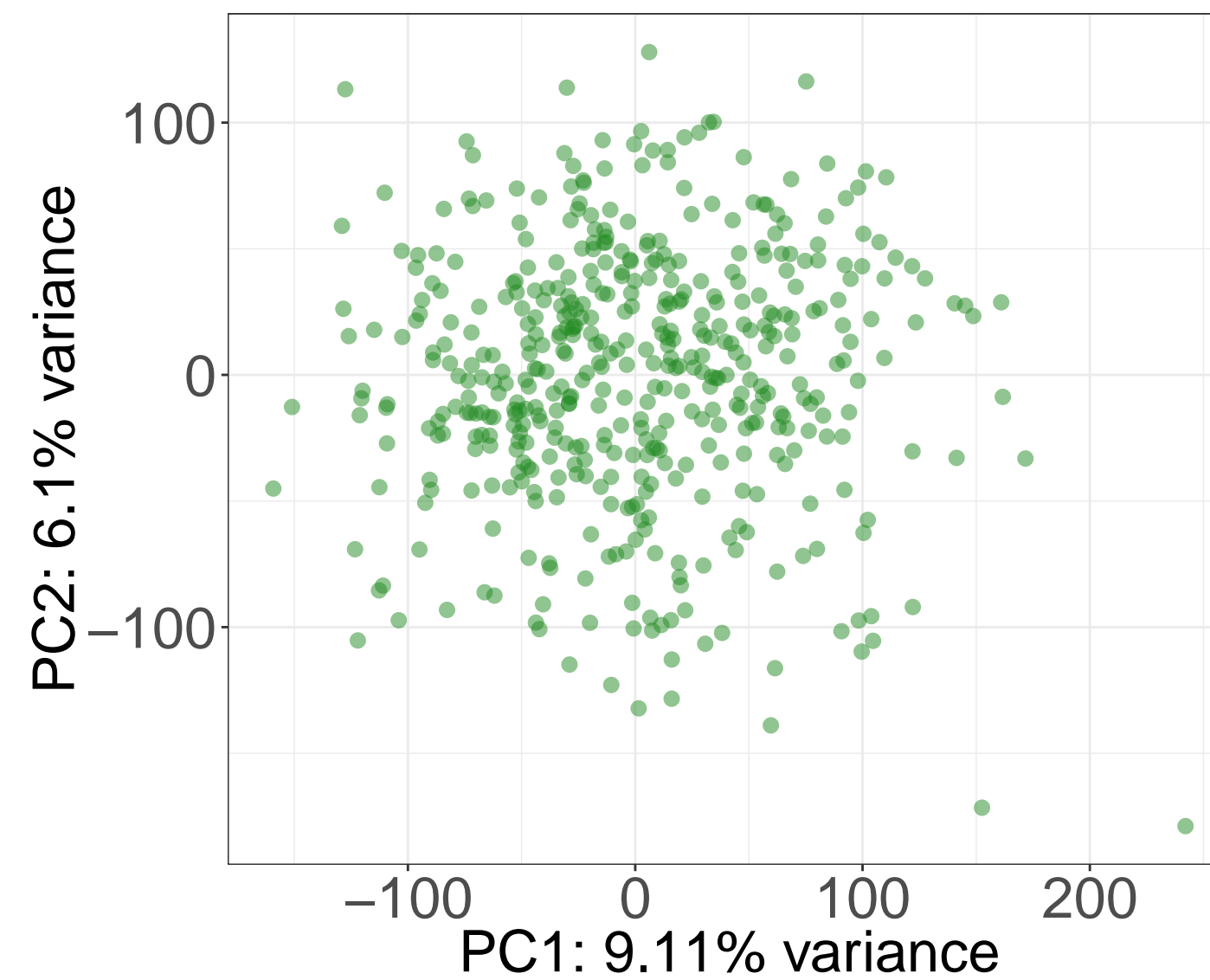
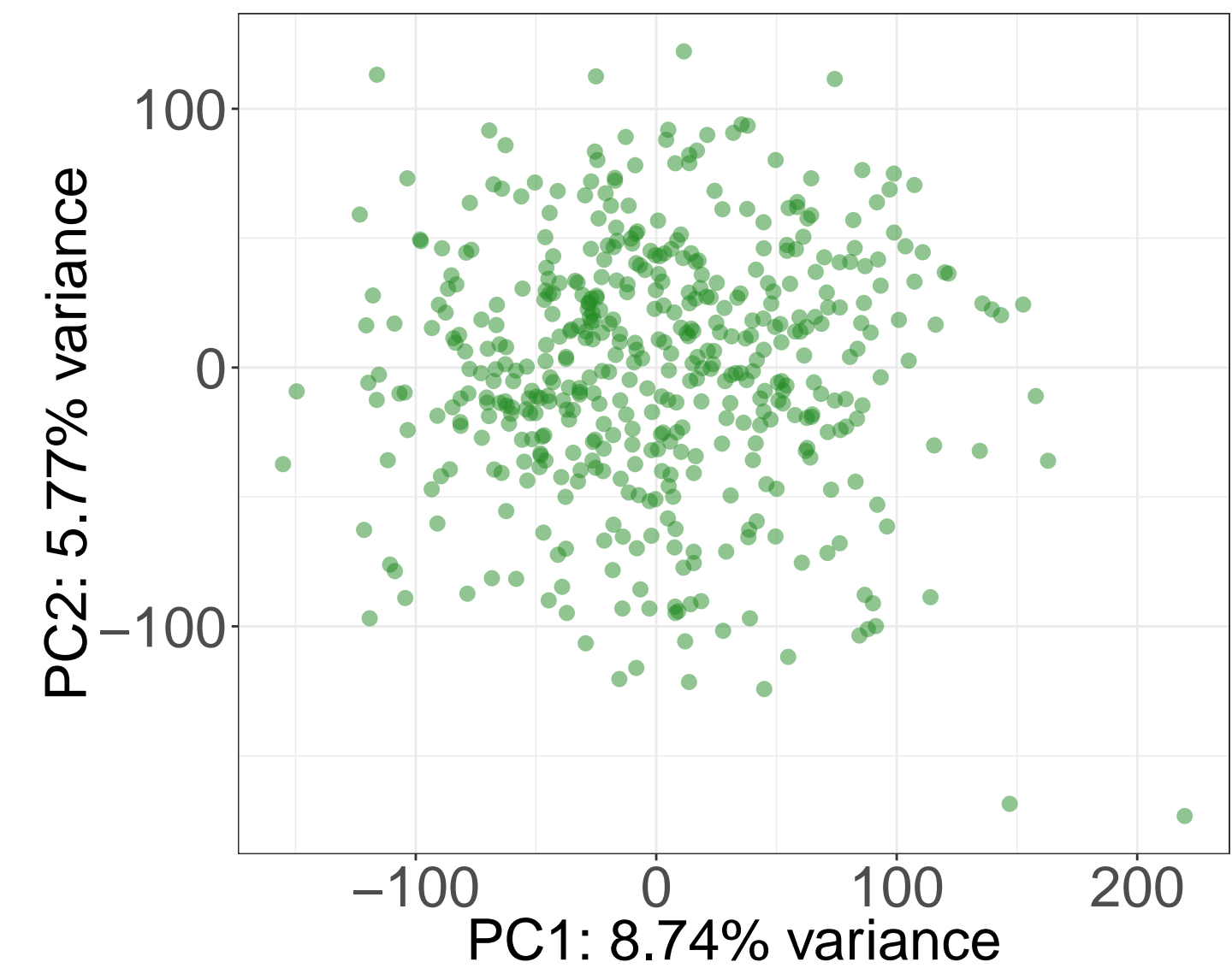
GDC**Xena****Piccolo****Recount2****MSKCC****MSKCC Batch**

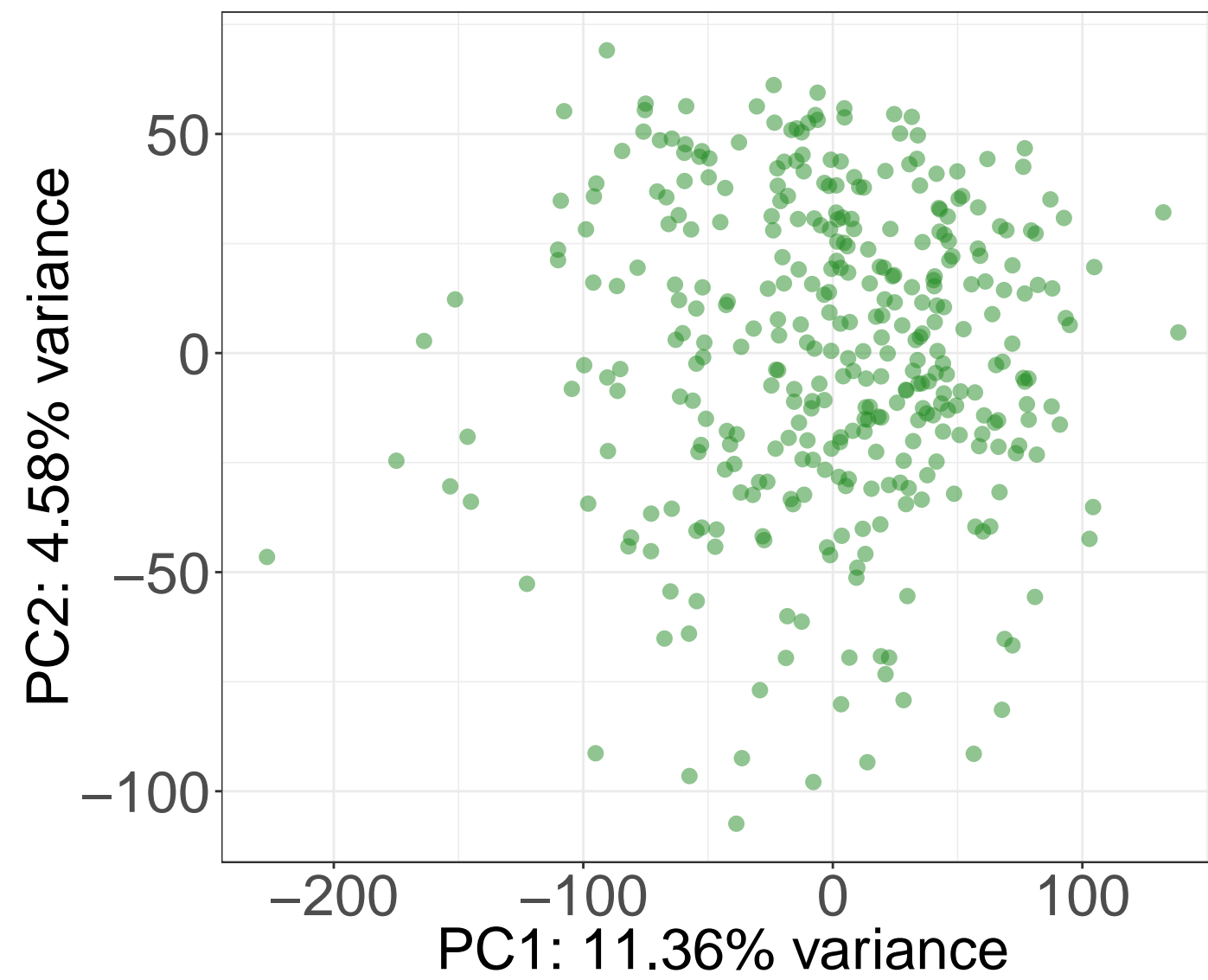
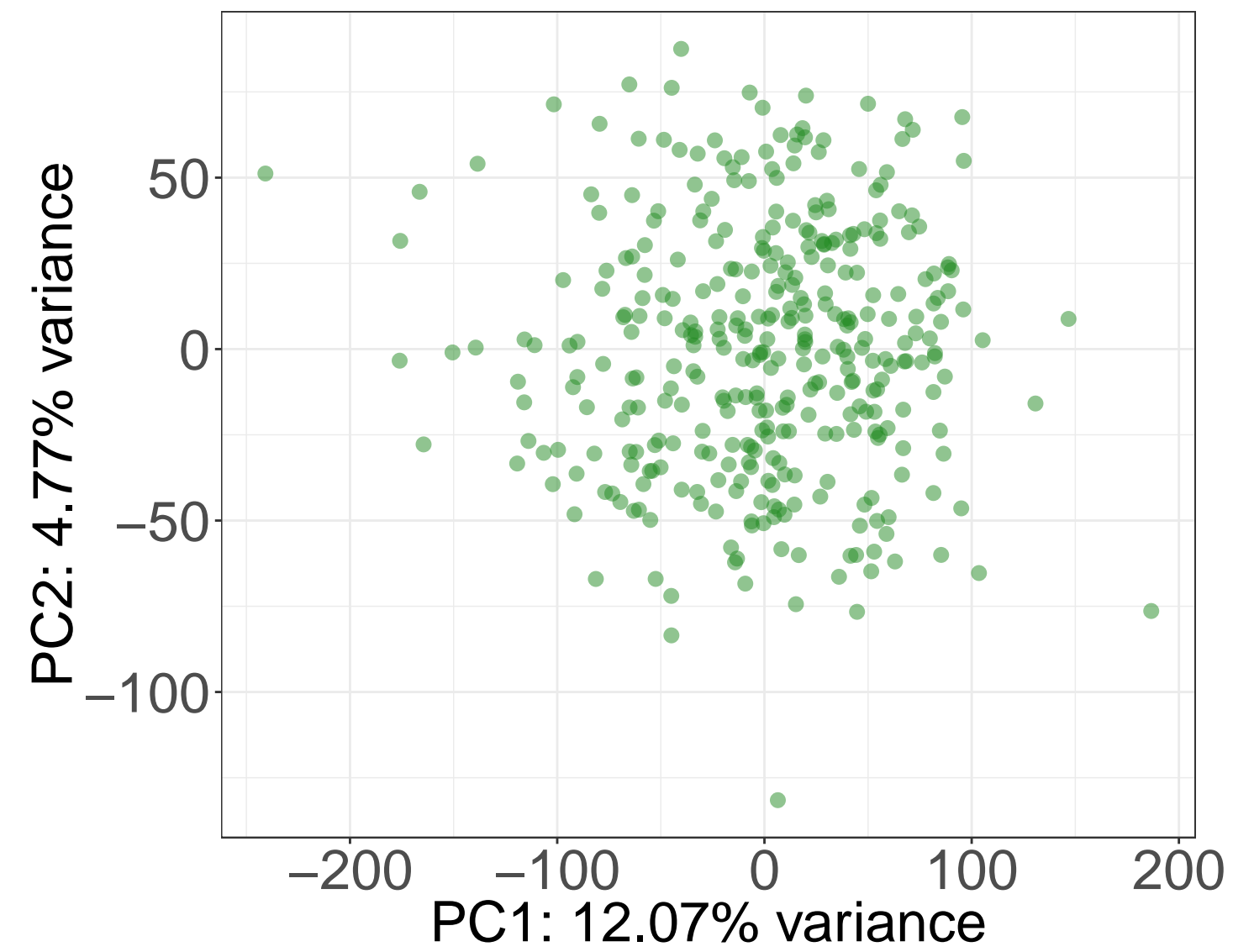
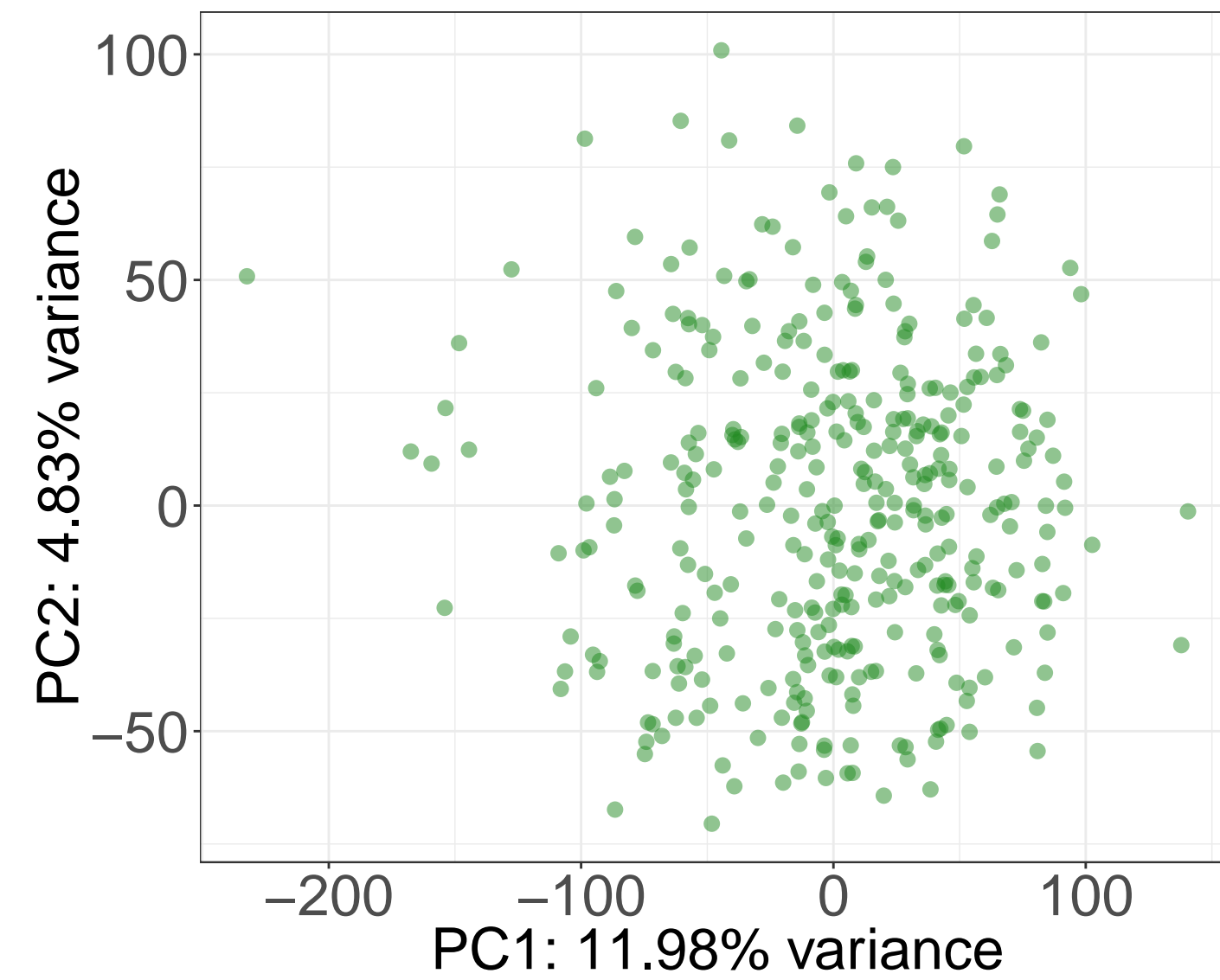
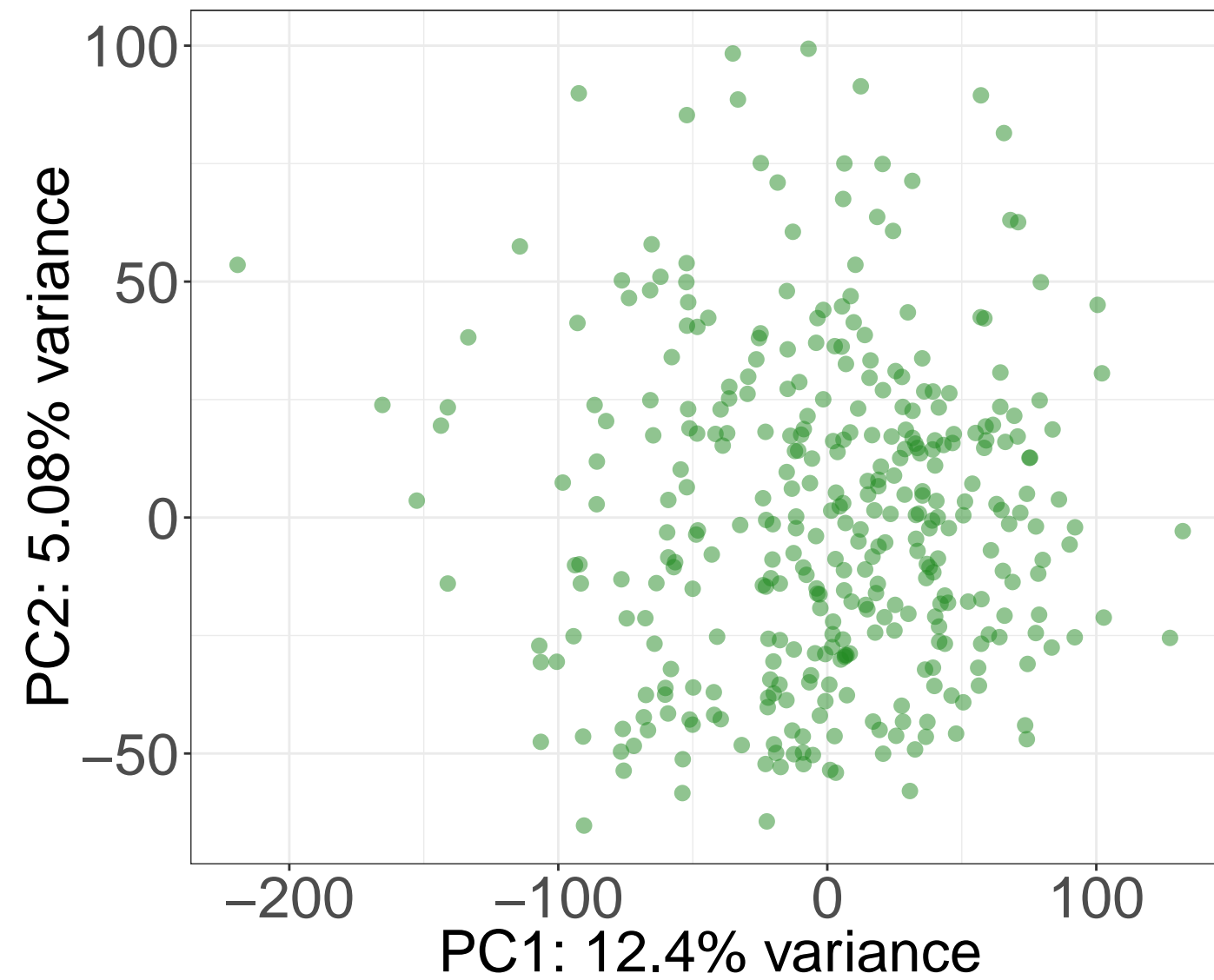
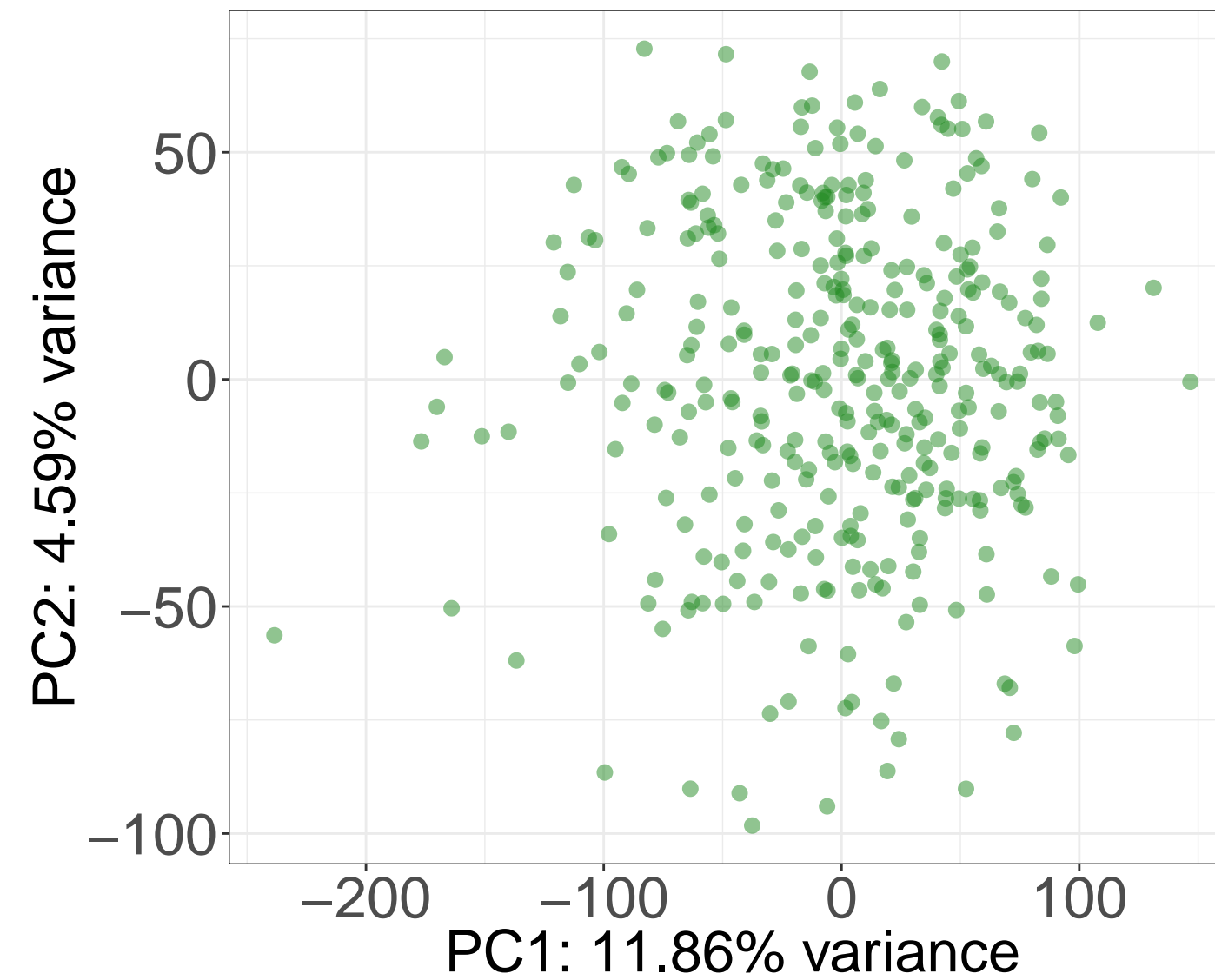
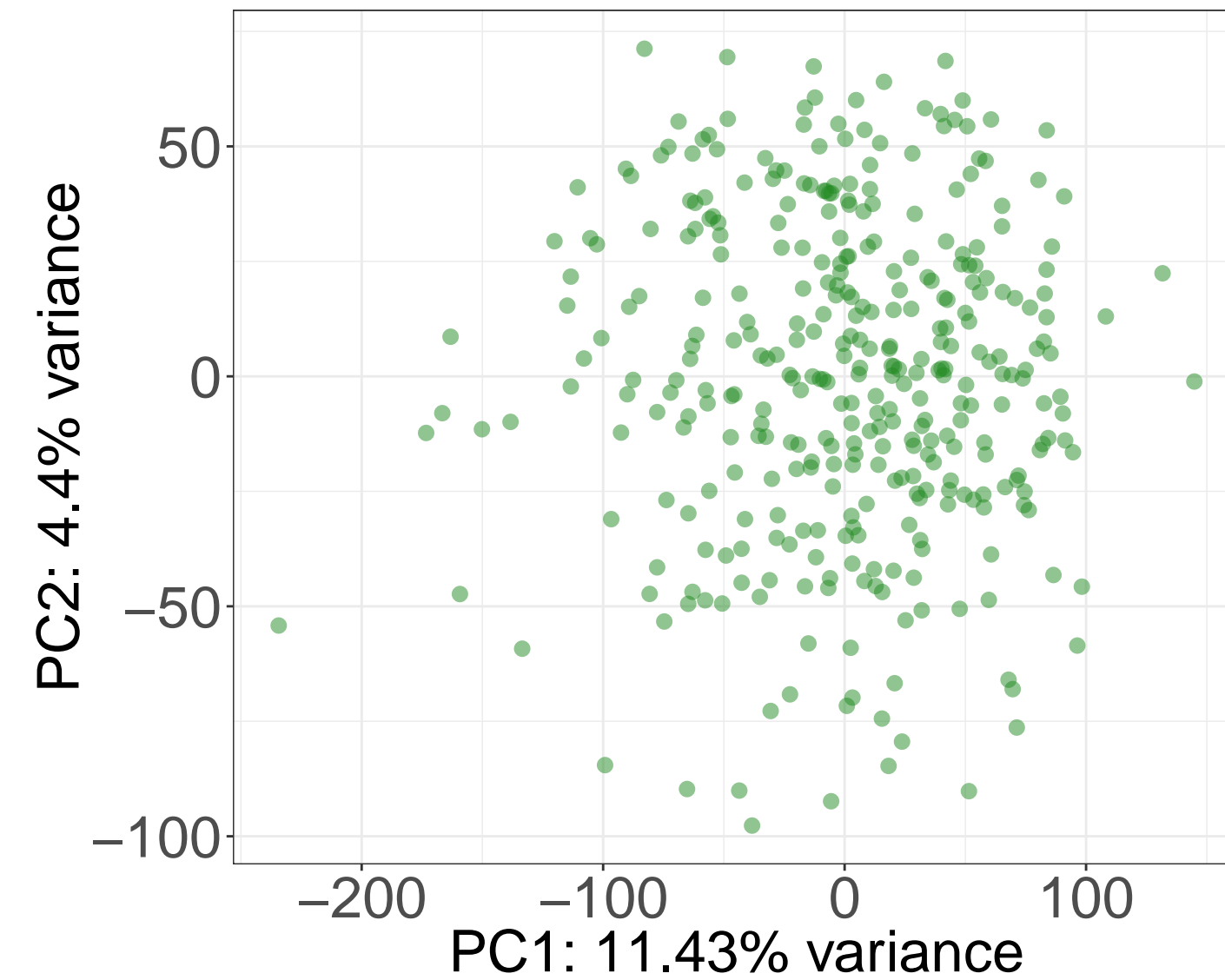
GDC**Xena****Piccolo****Recount2****MSKCC****MSKCC Batch**

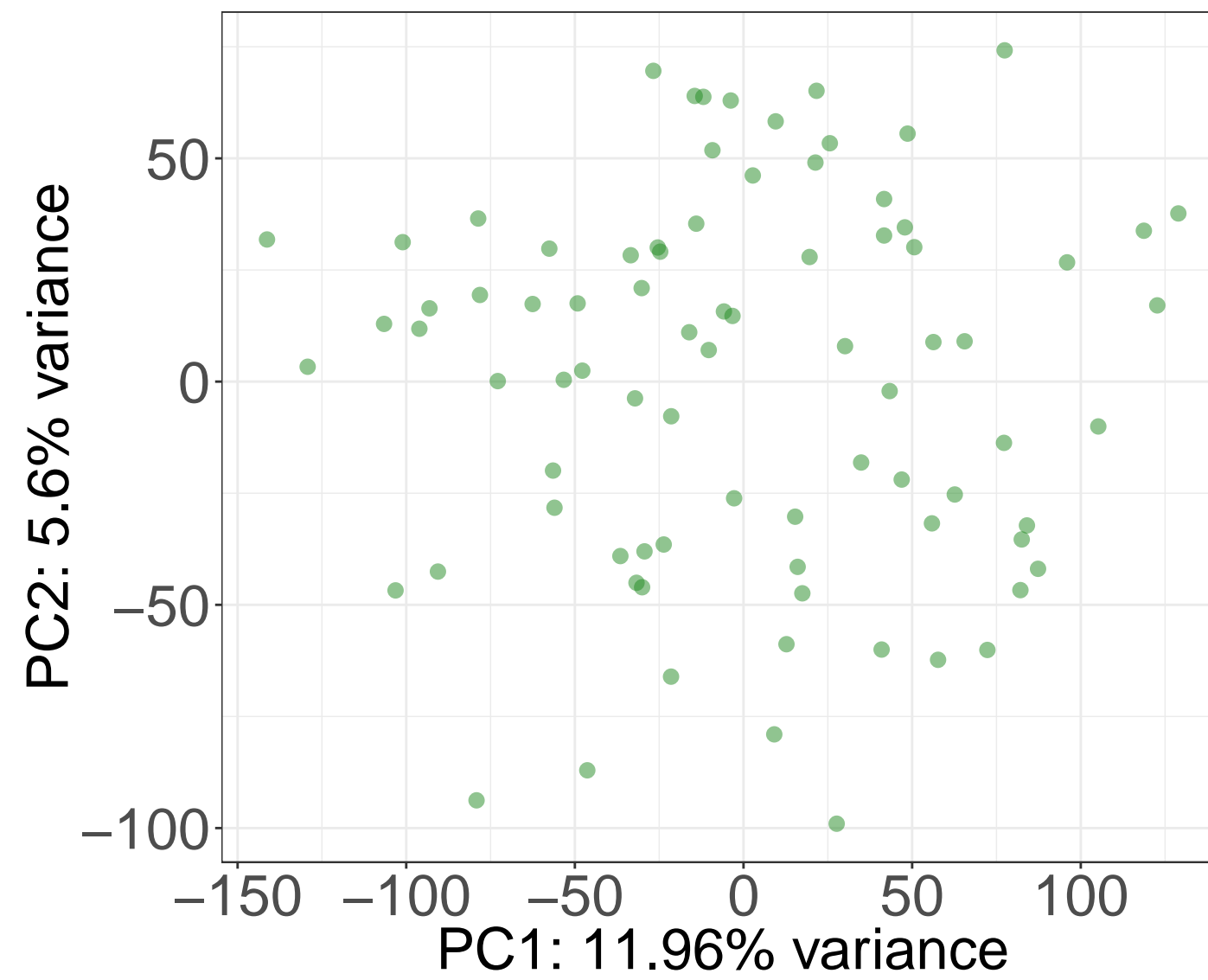
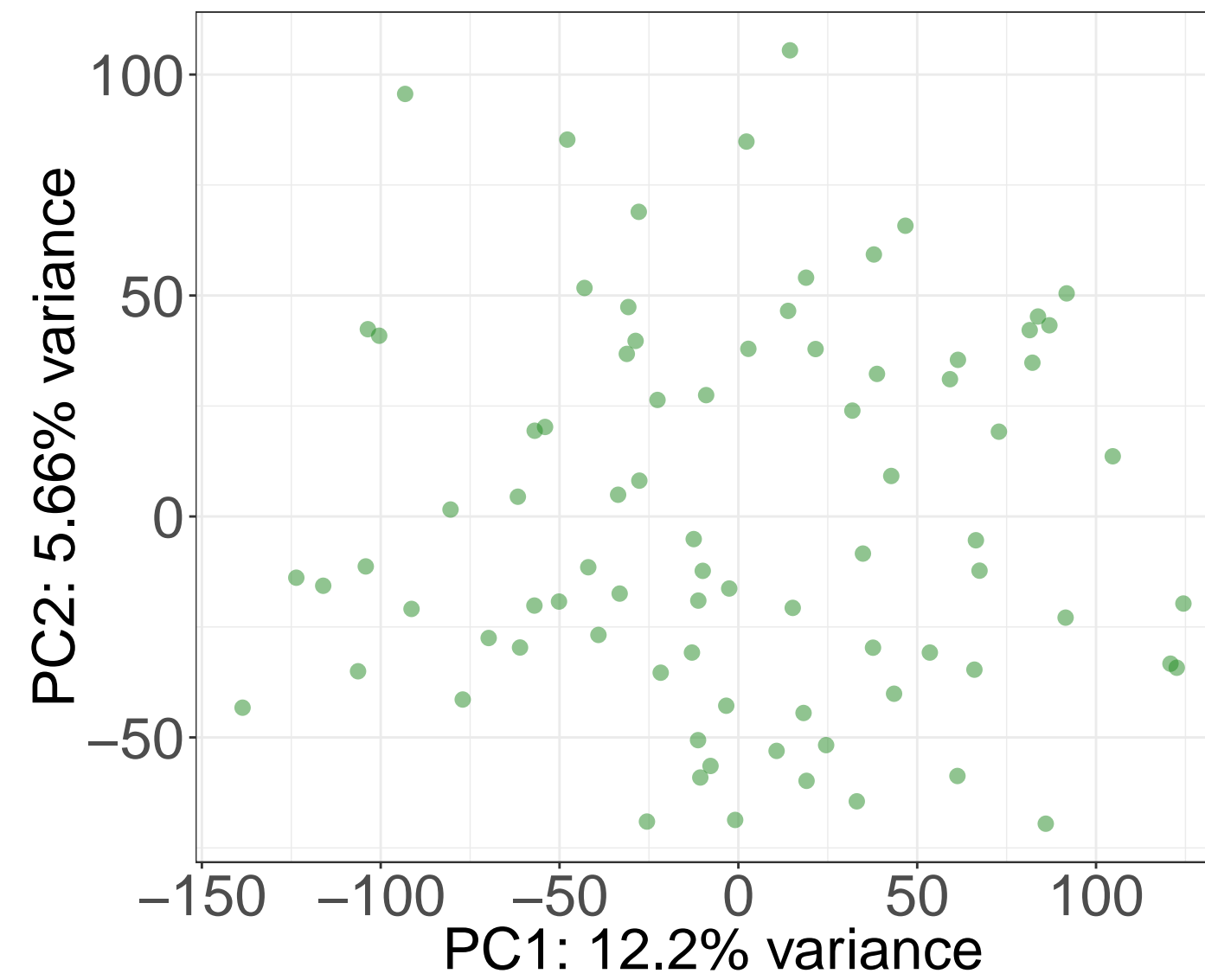
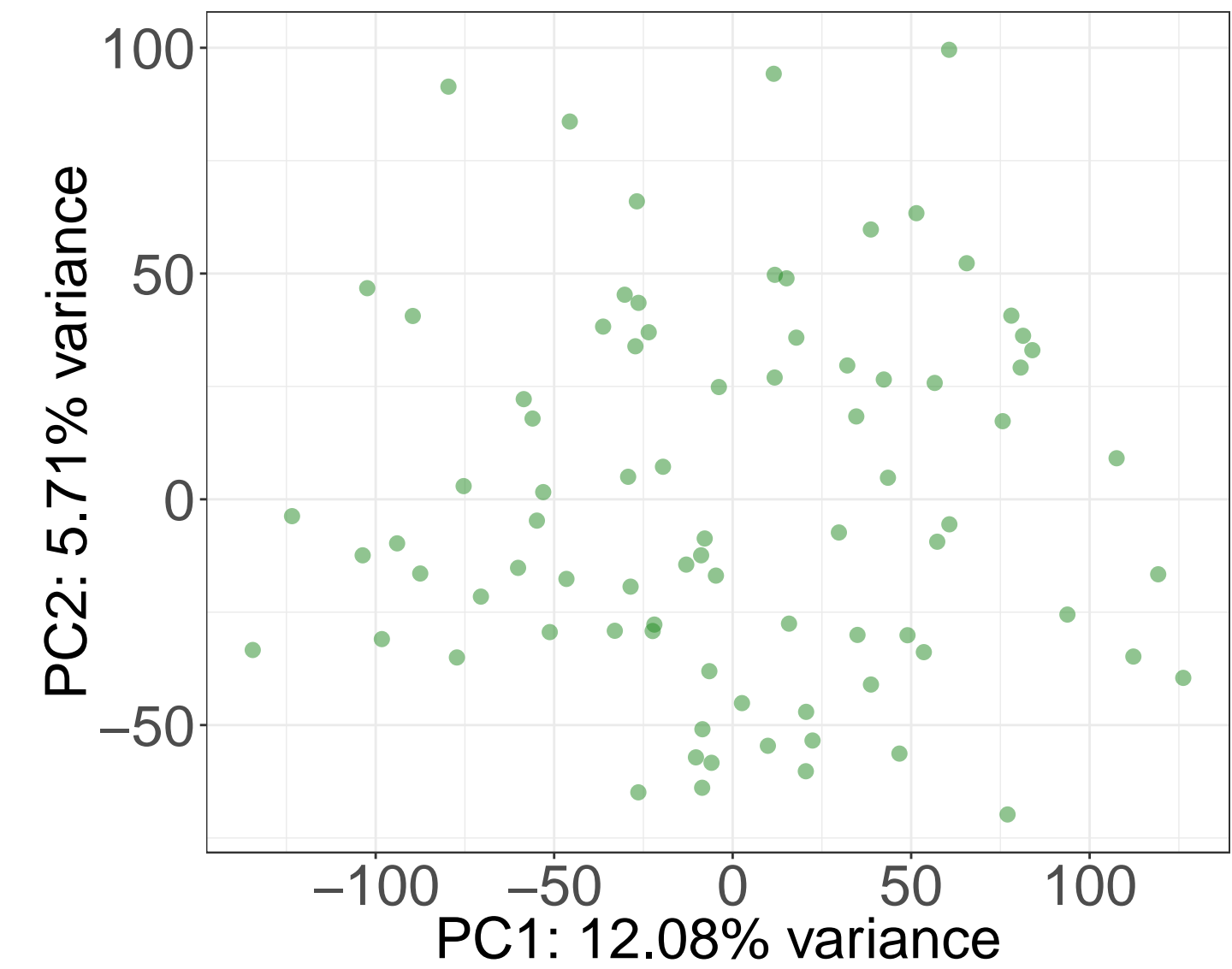
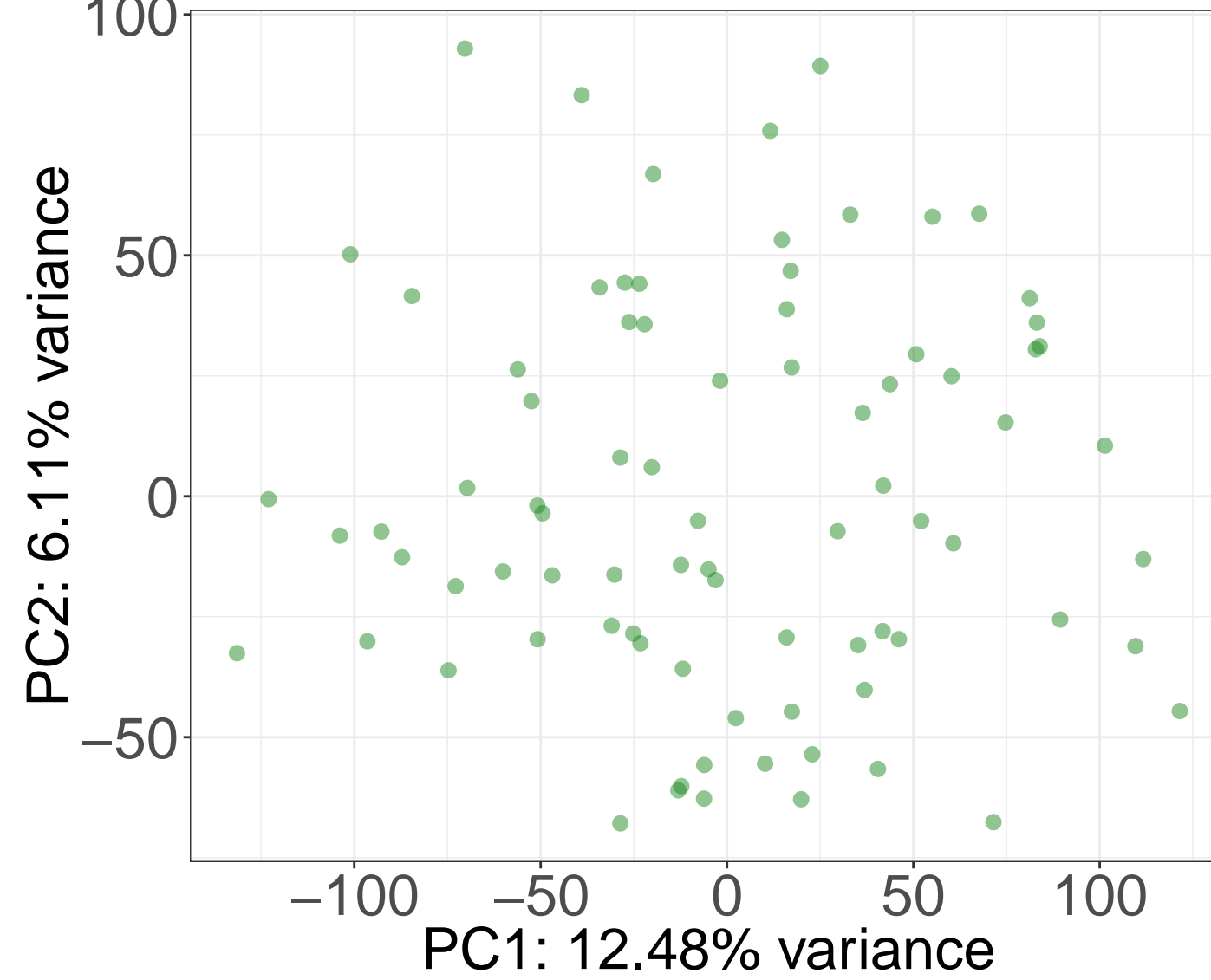
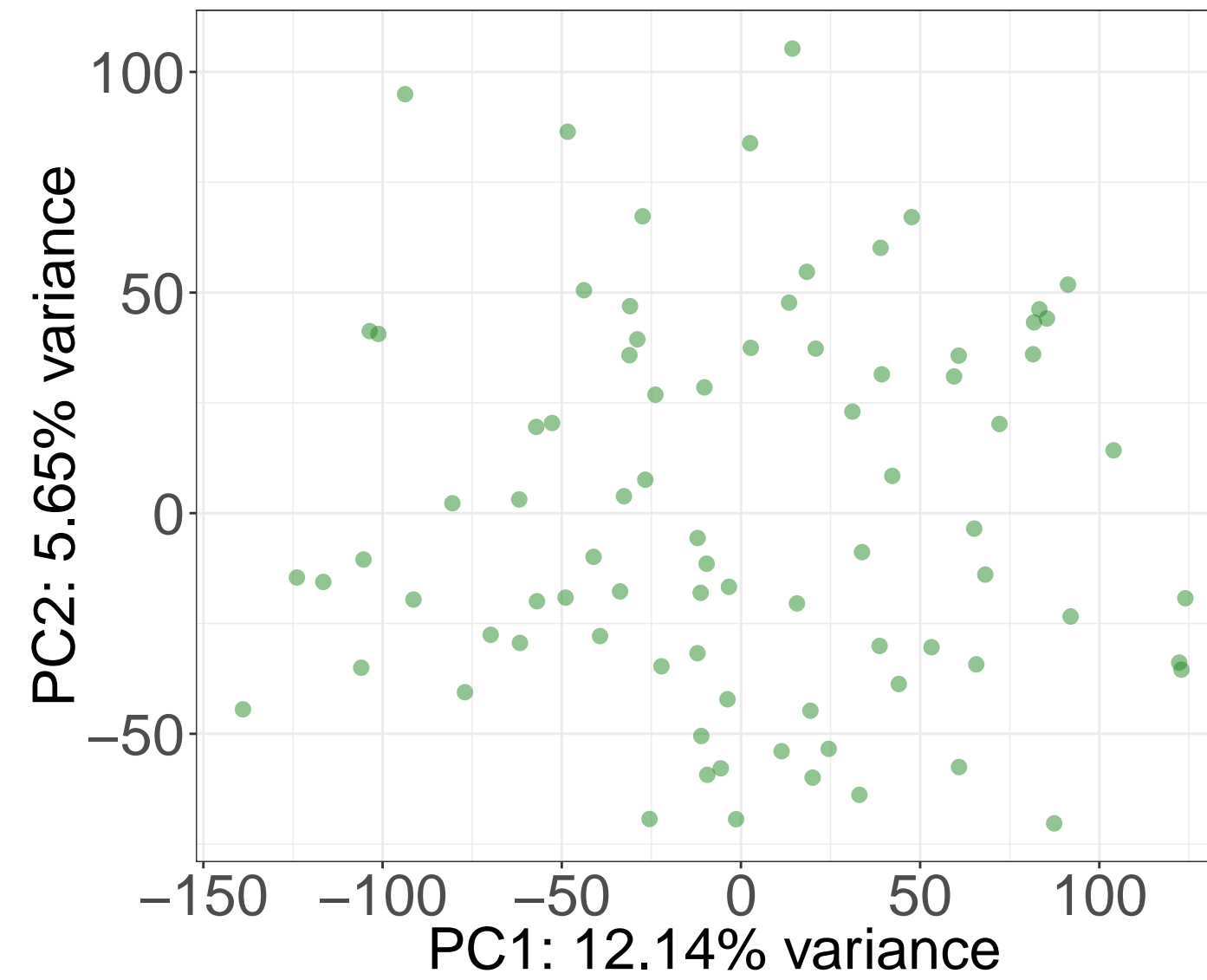
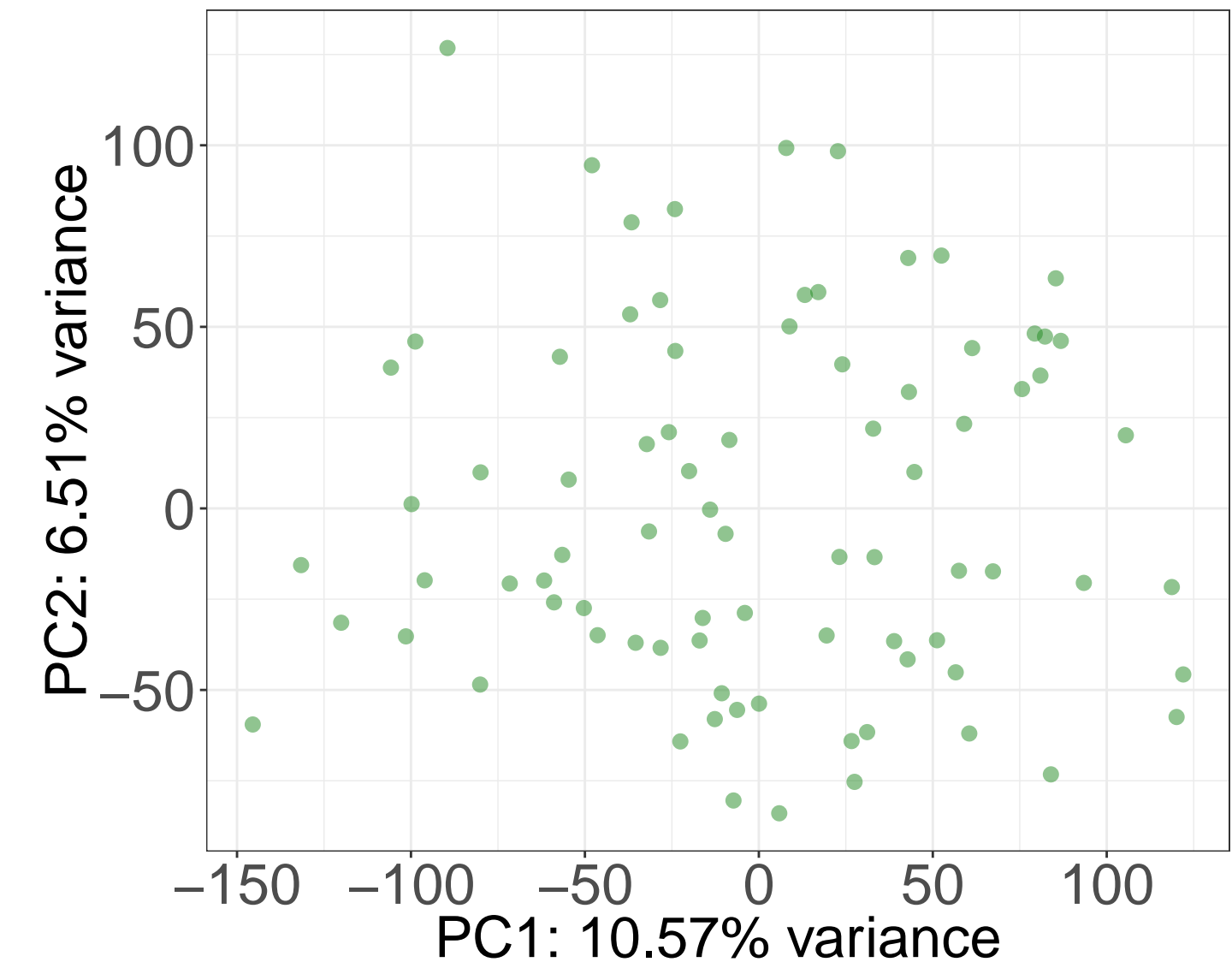
GDC**Xena****Piccolo****Recount2****MSKCC****MSKCC Batch**

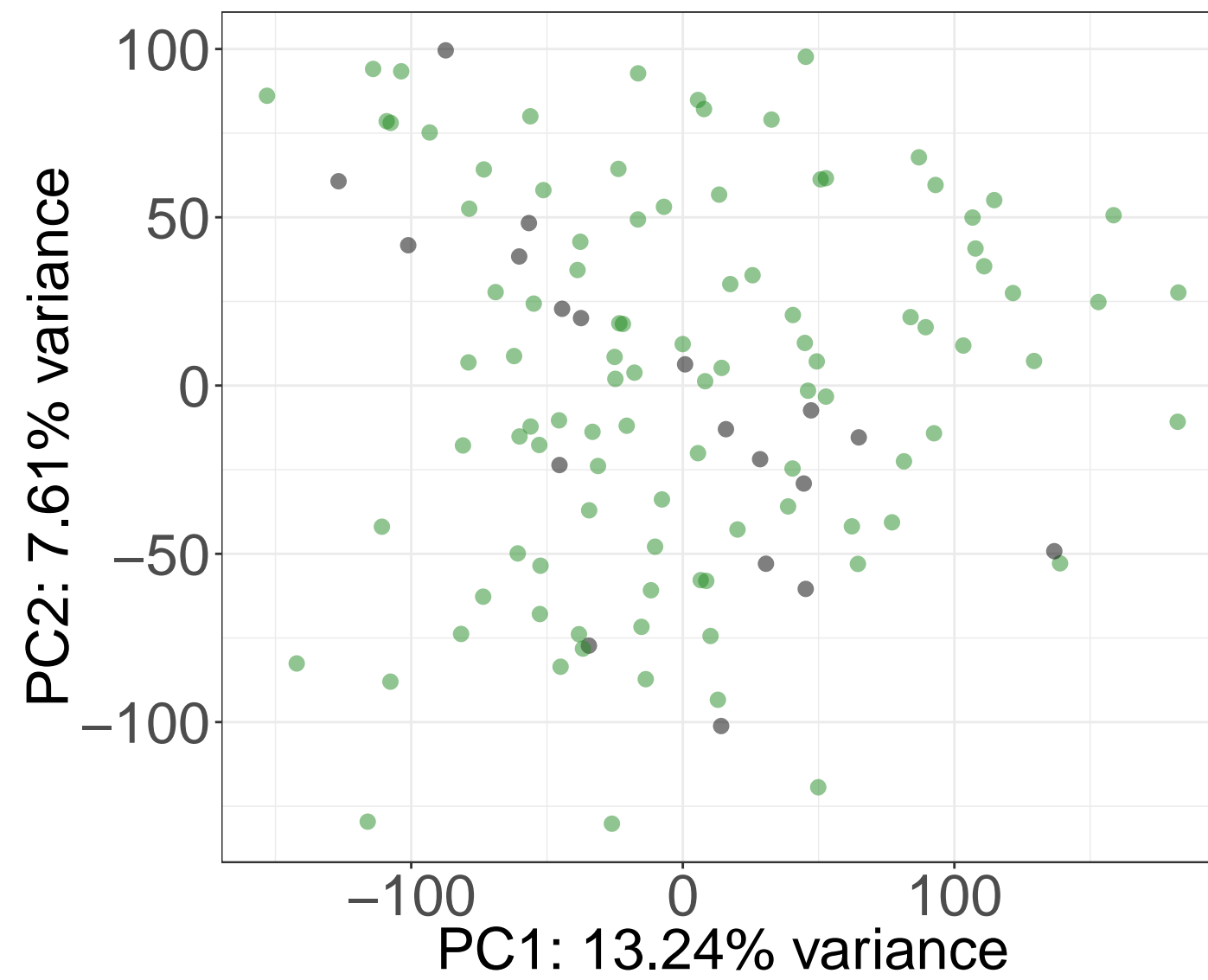
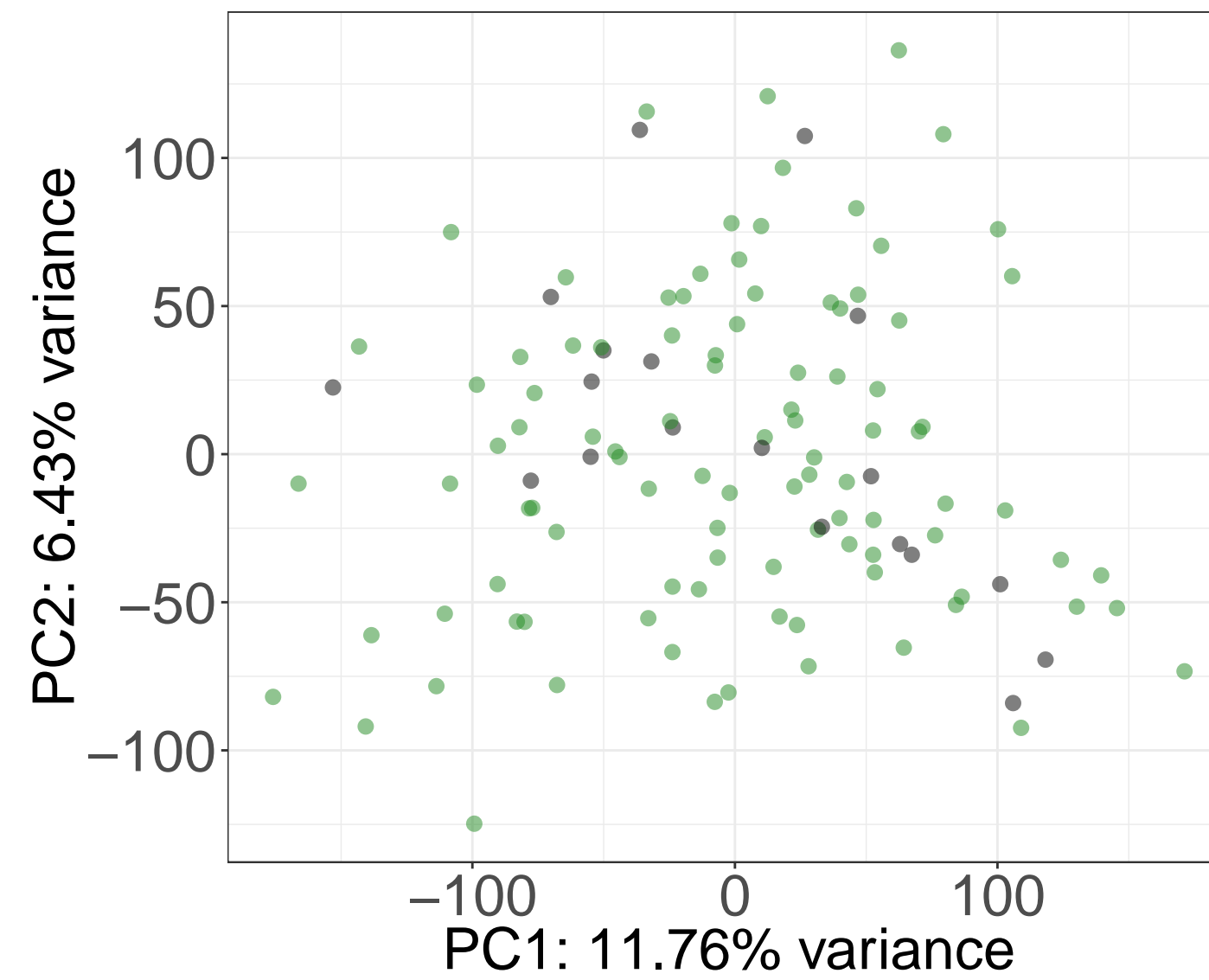
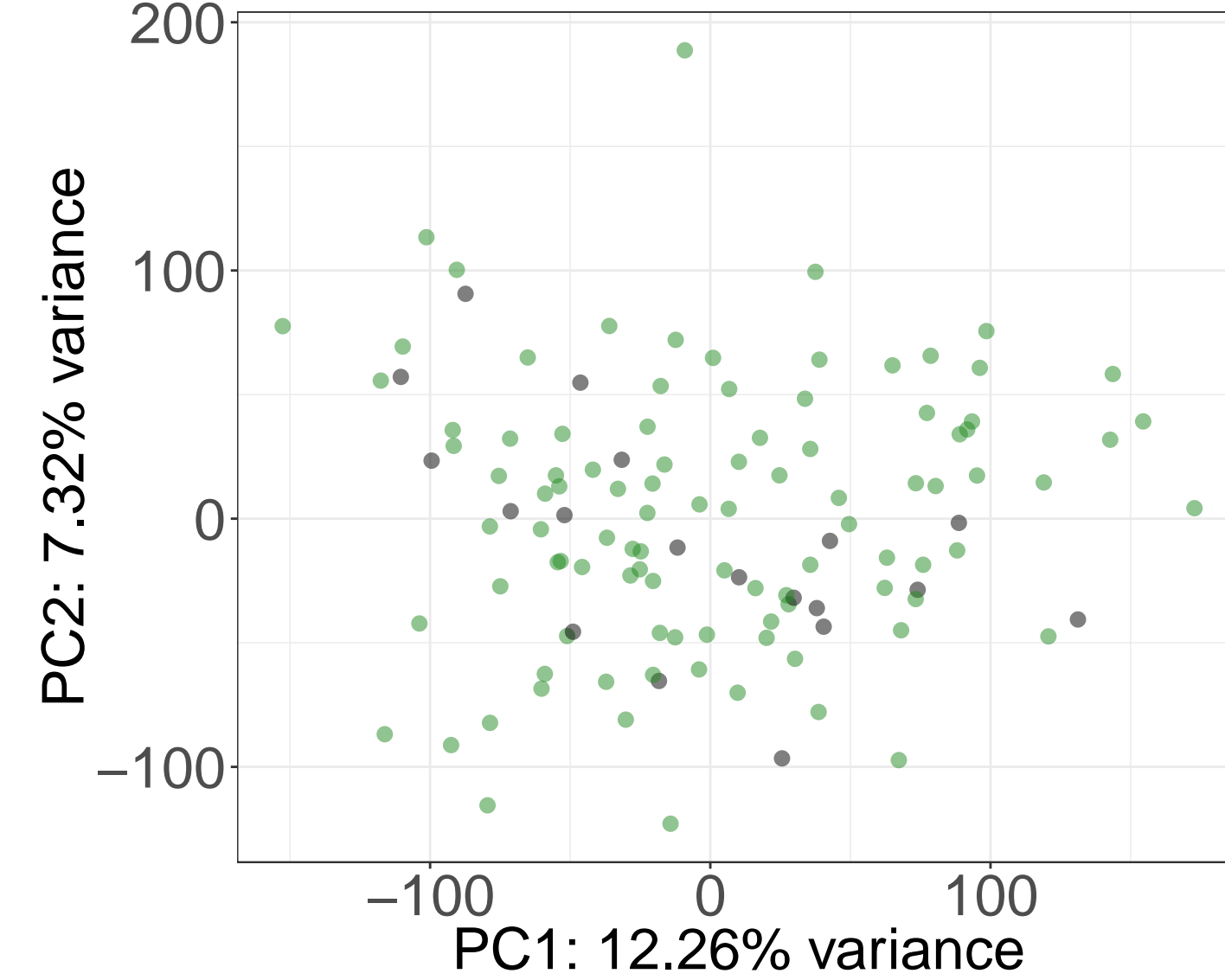
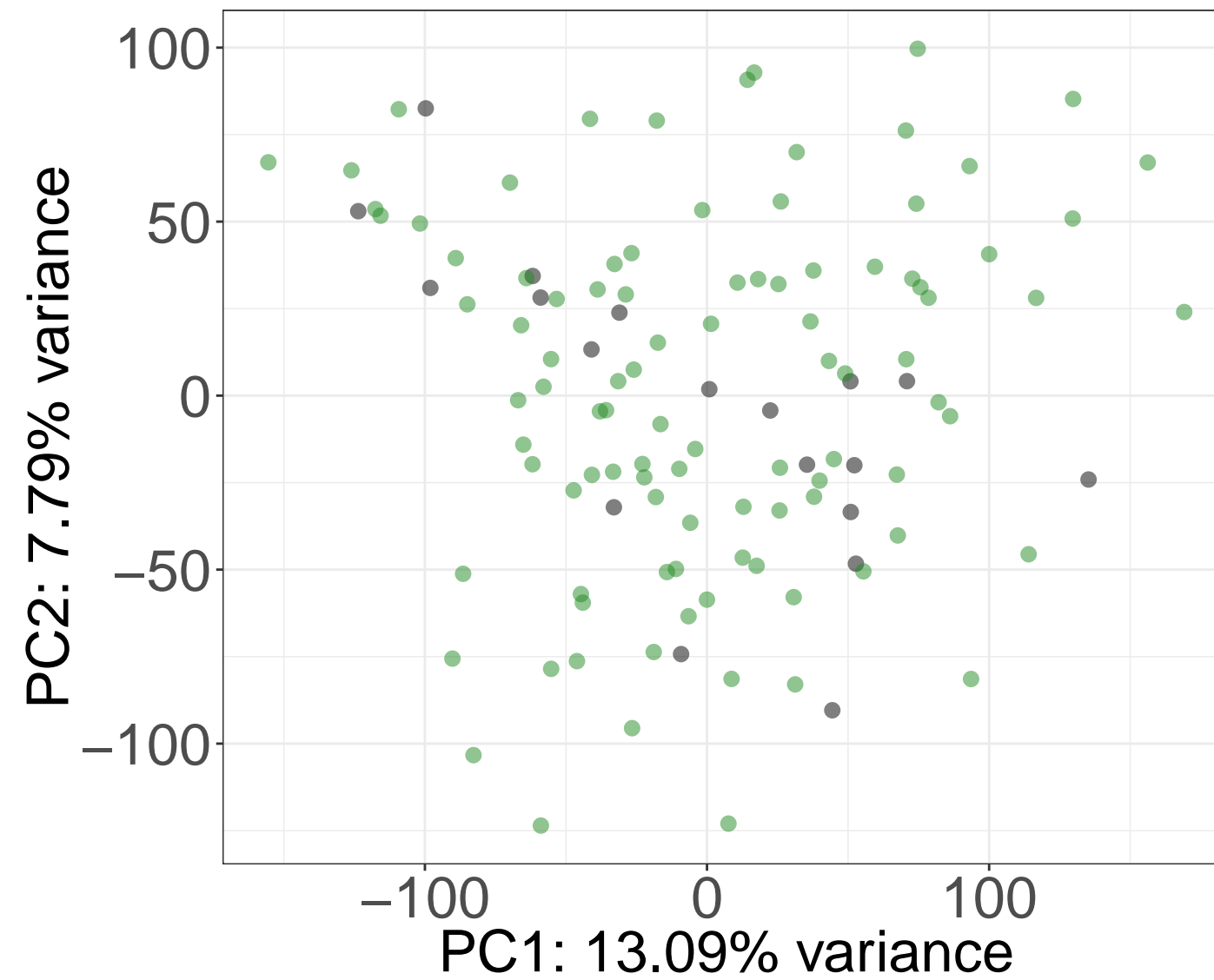
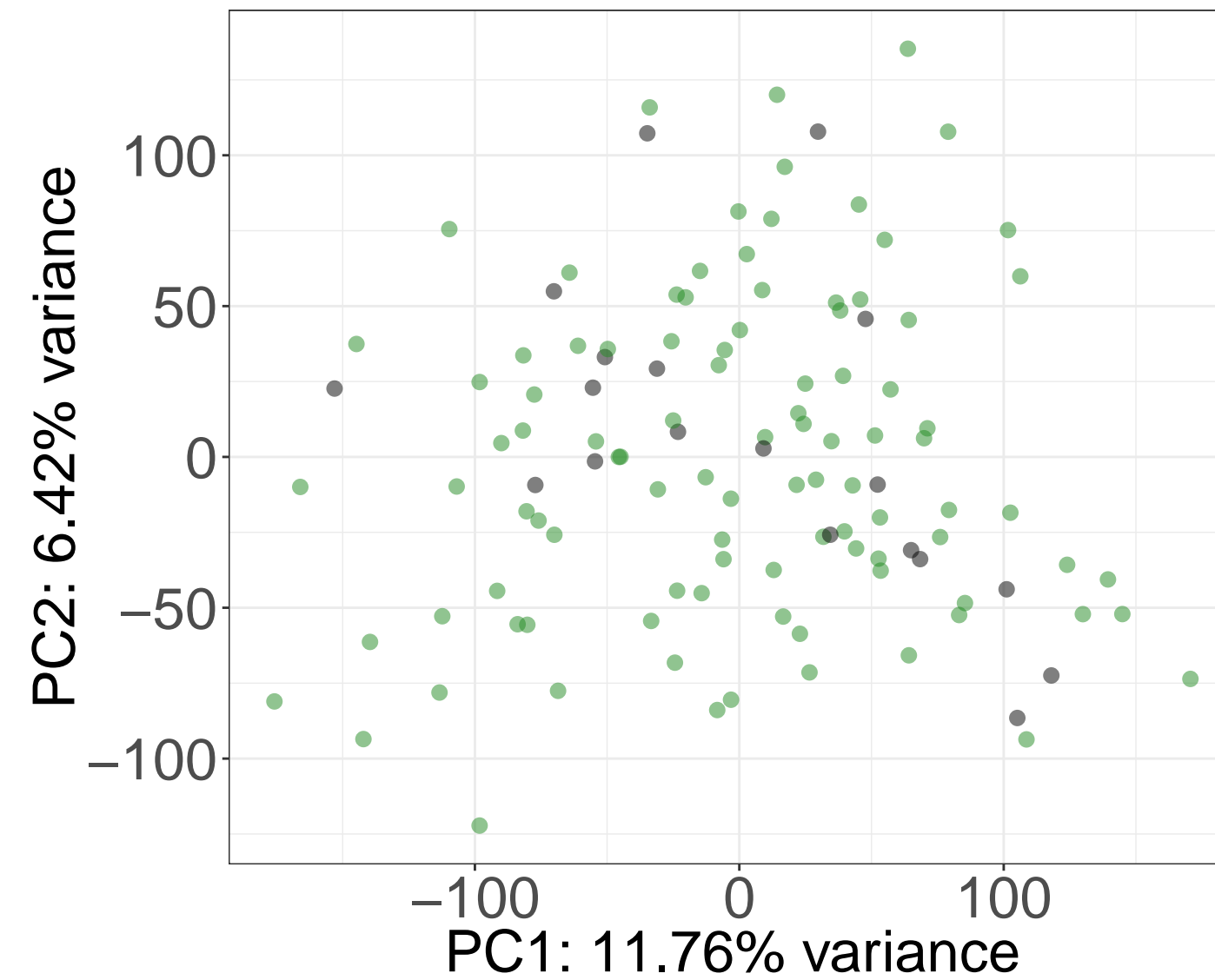
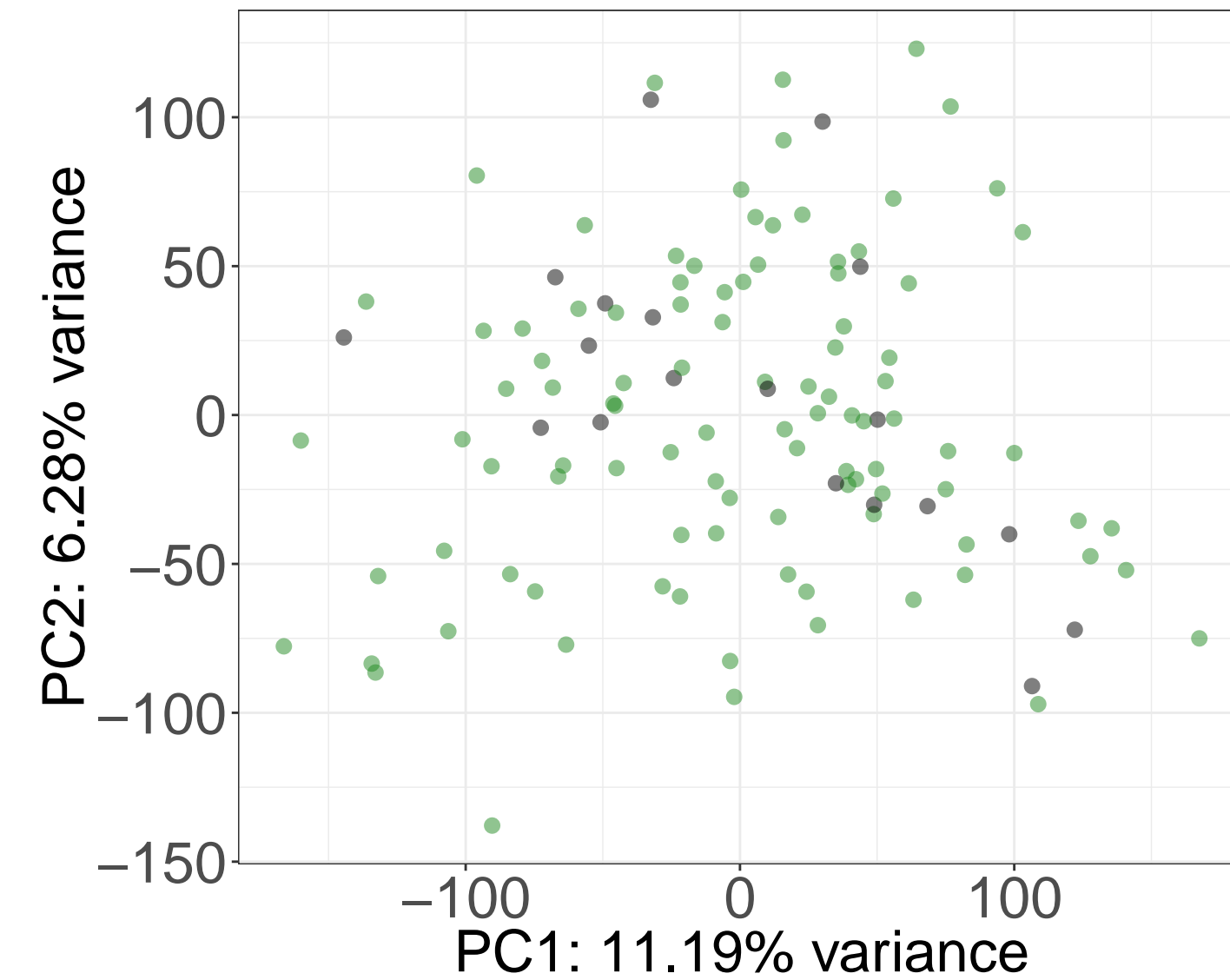
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GDC**Xena****Piccolo****Recount2****MSKCC****MSKCC Batch**

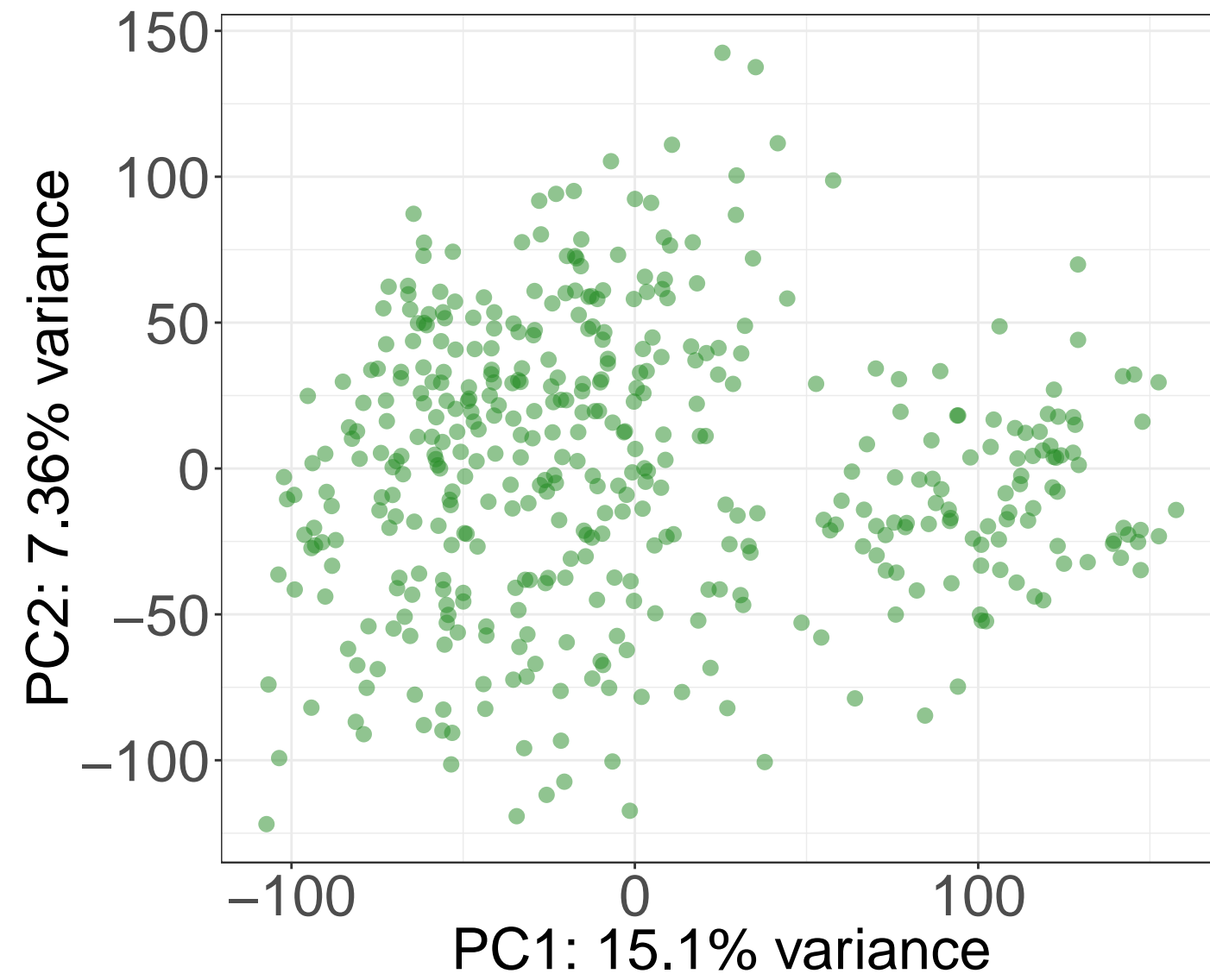
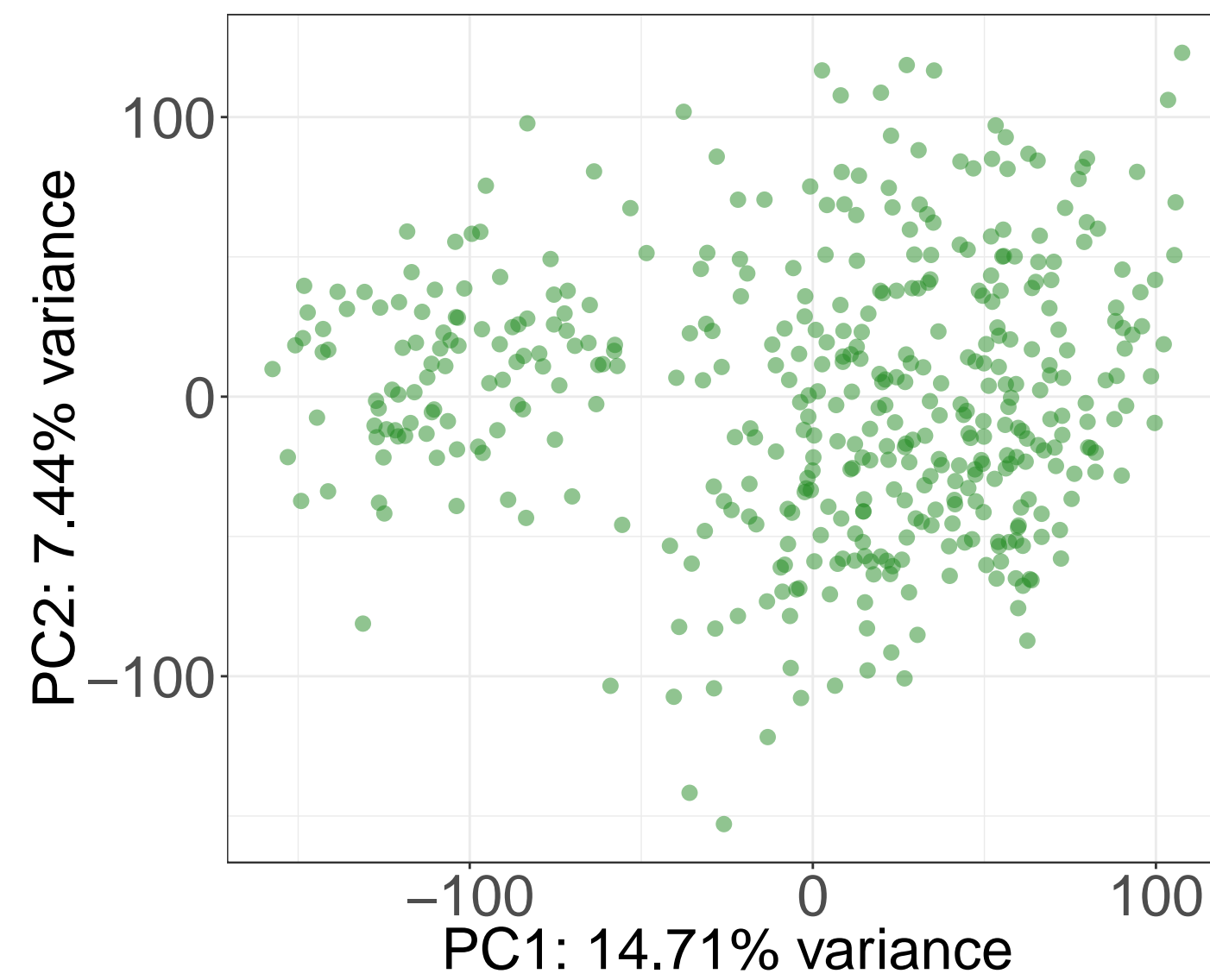
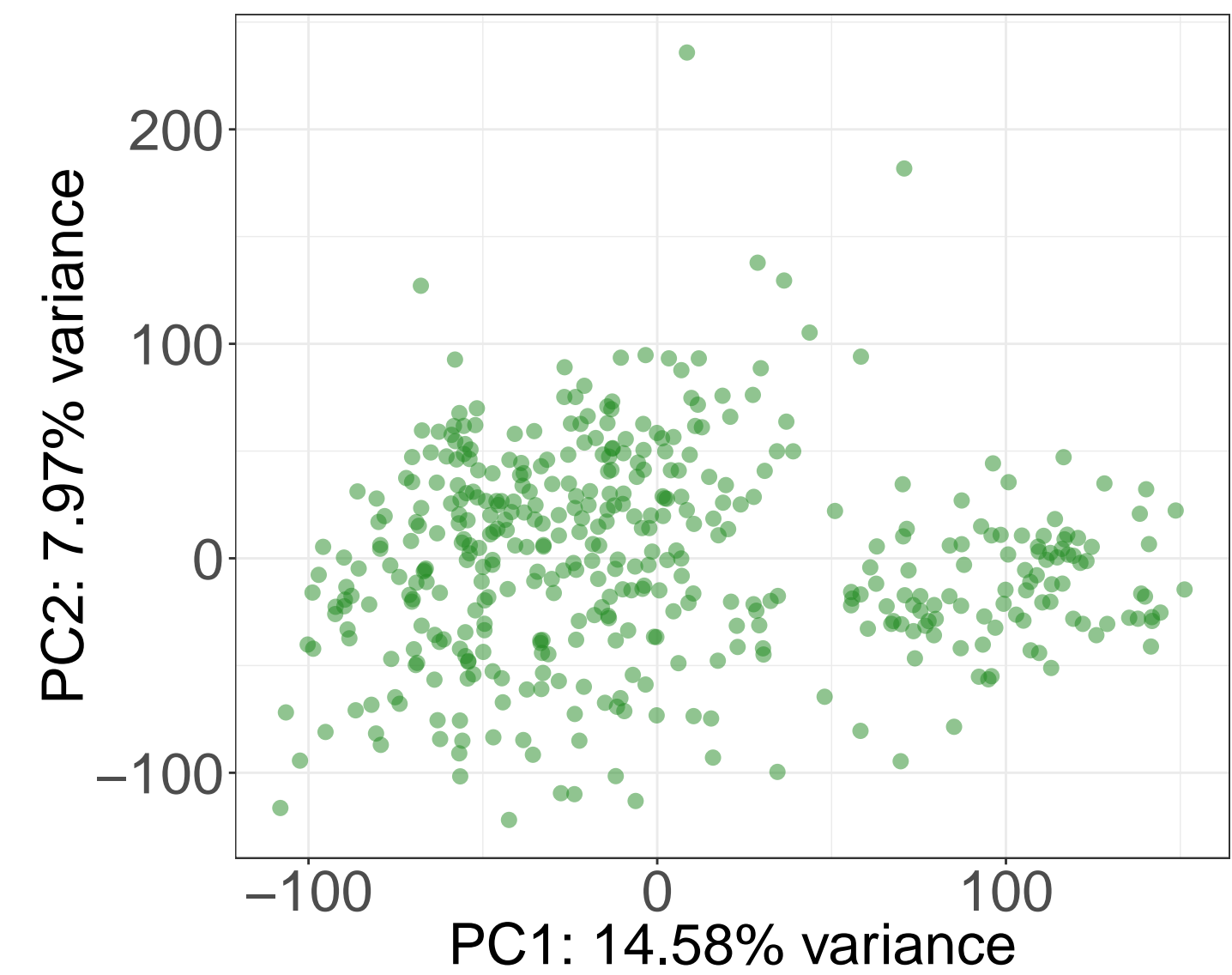
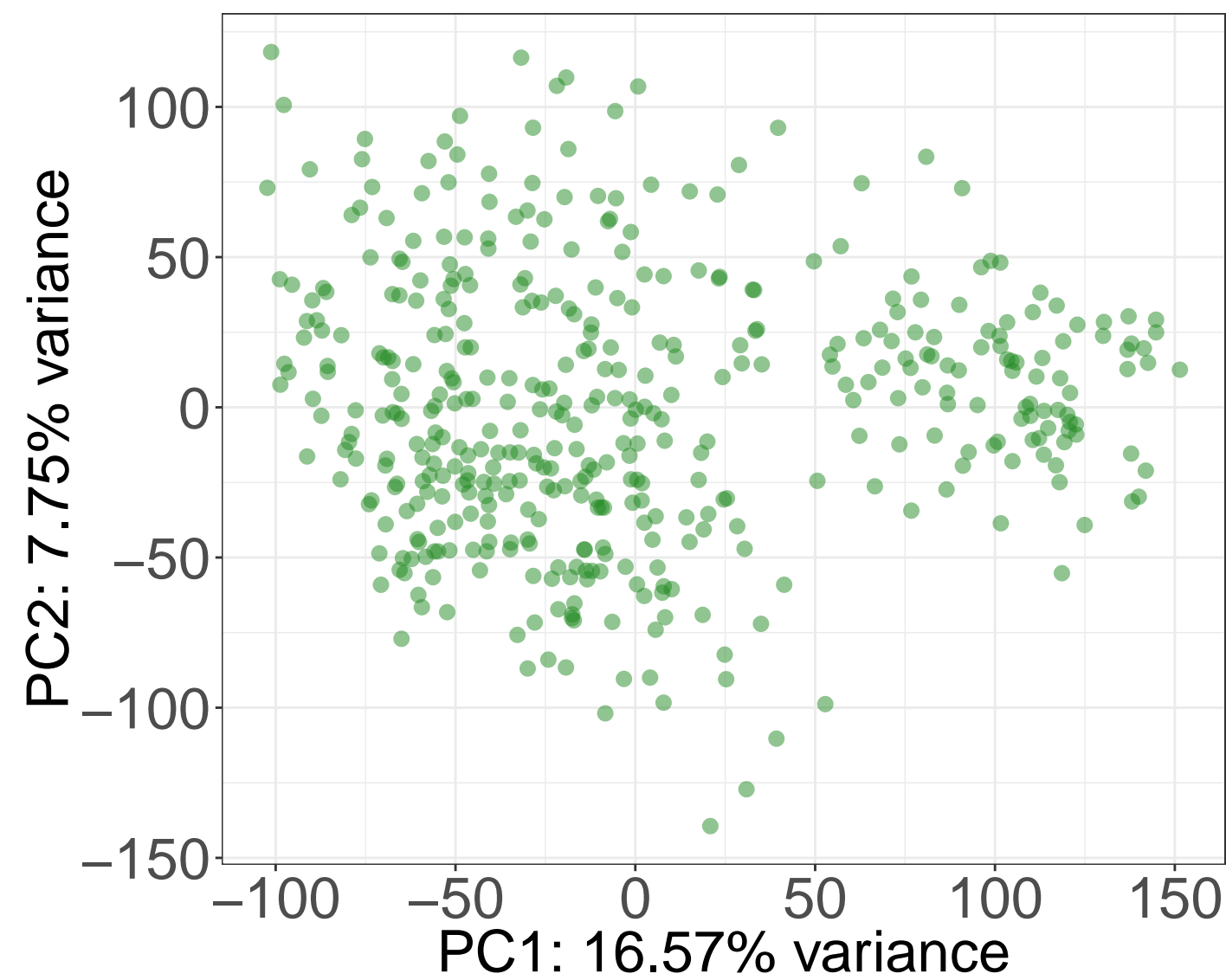
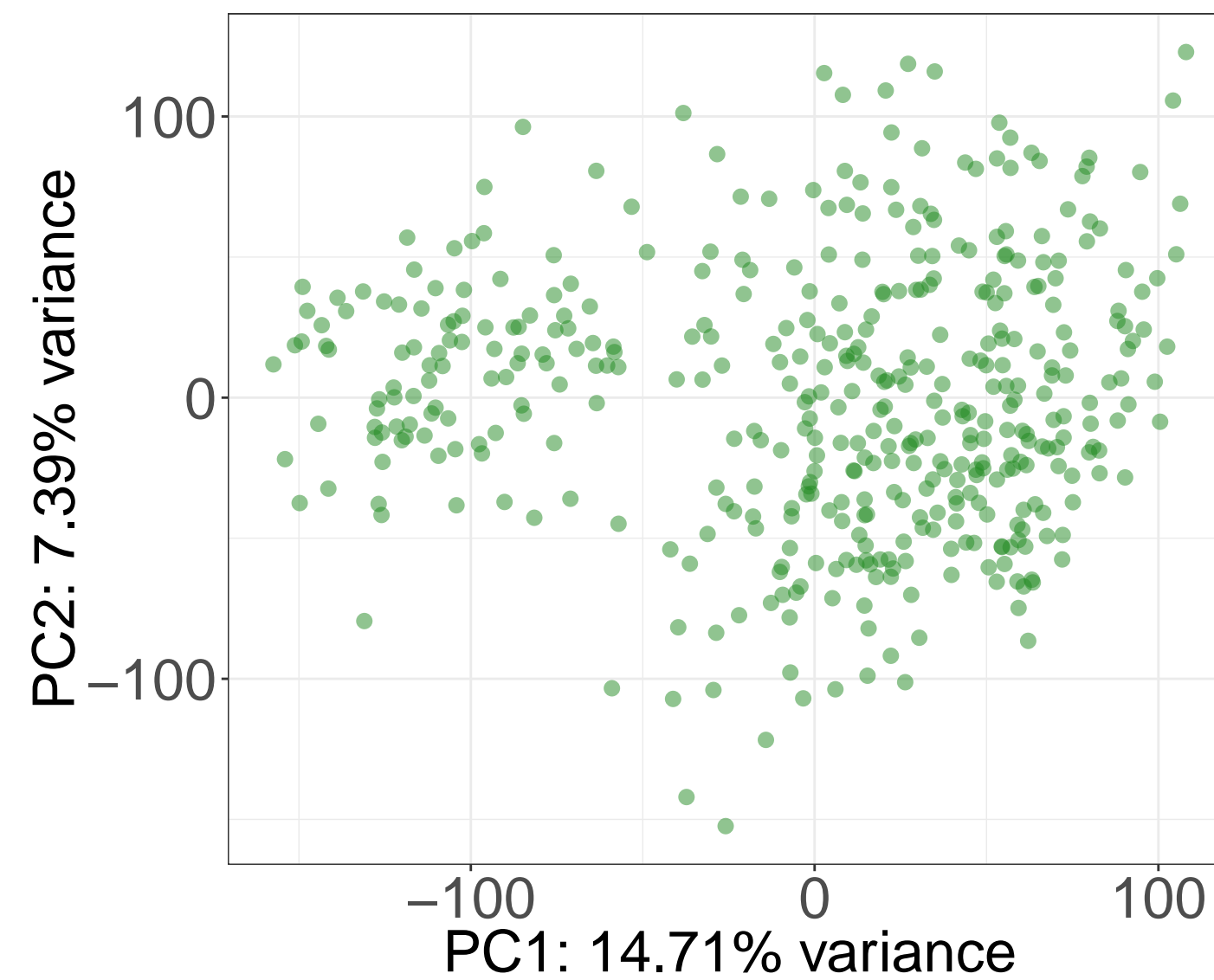
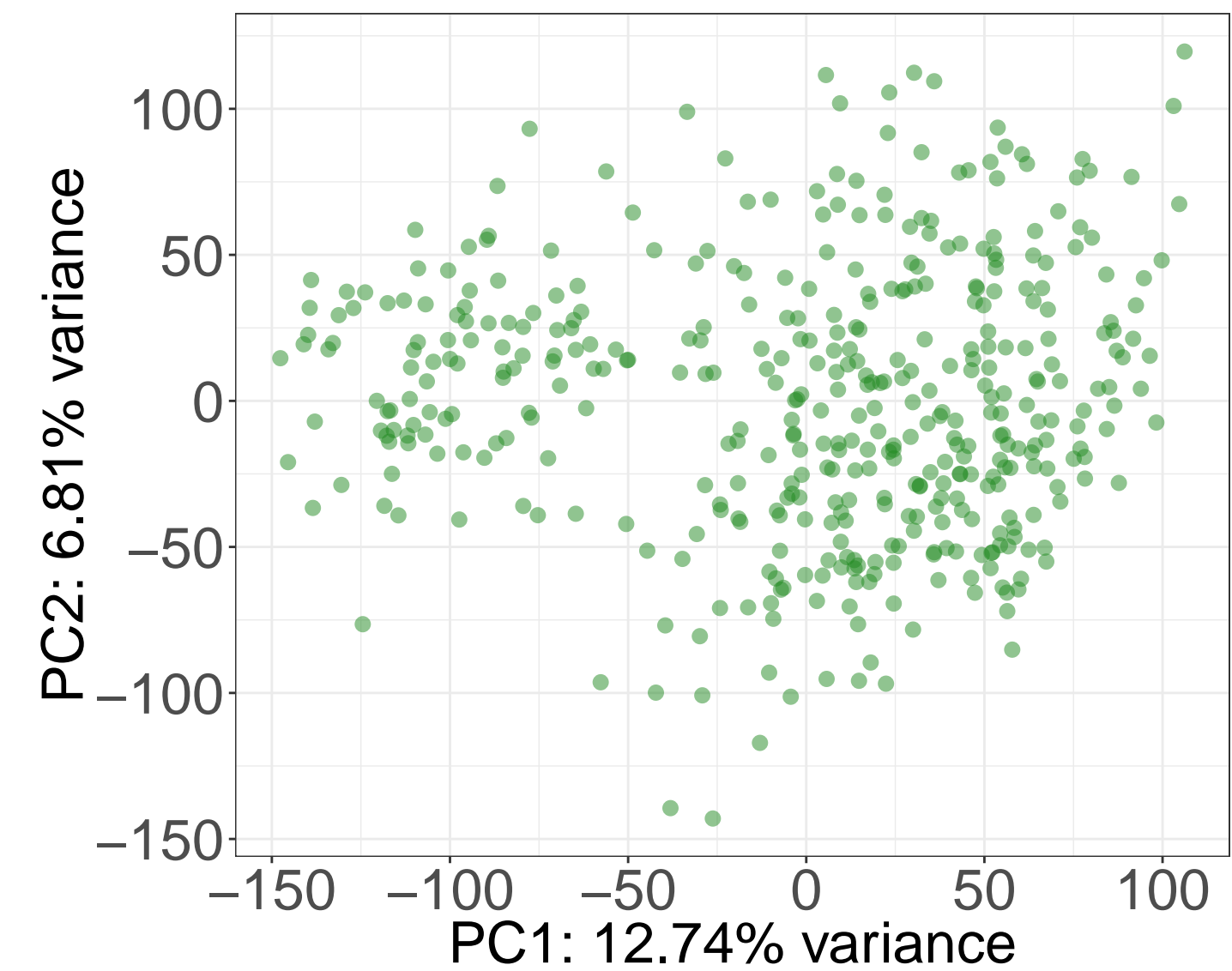
GDC**Xena****Piccolo****Recount2****MSKCC****MSKCC Batch**

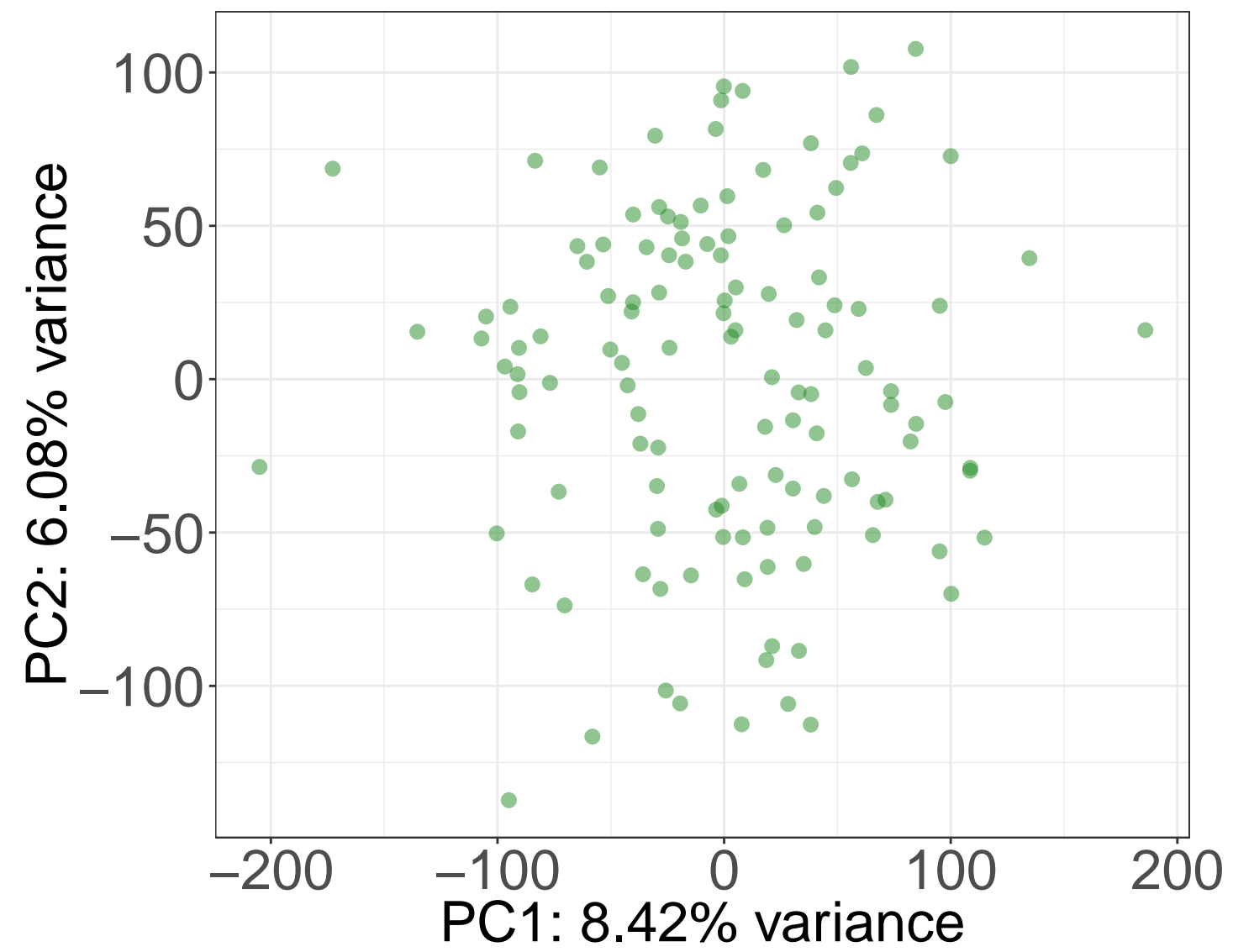
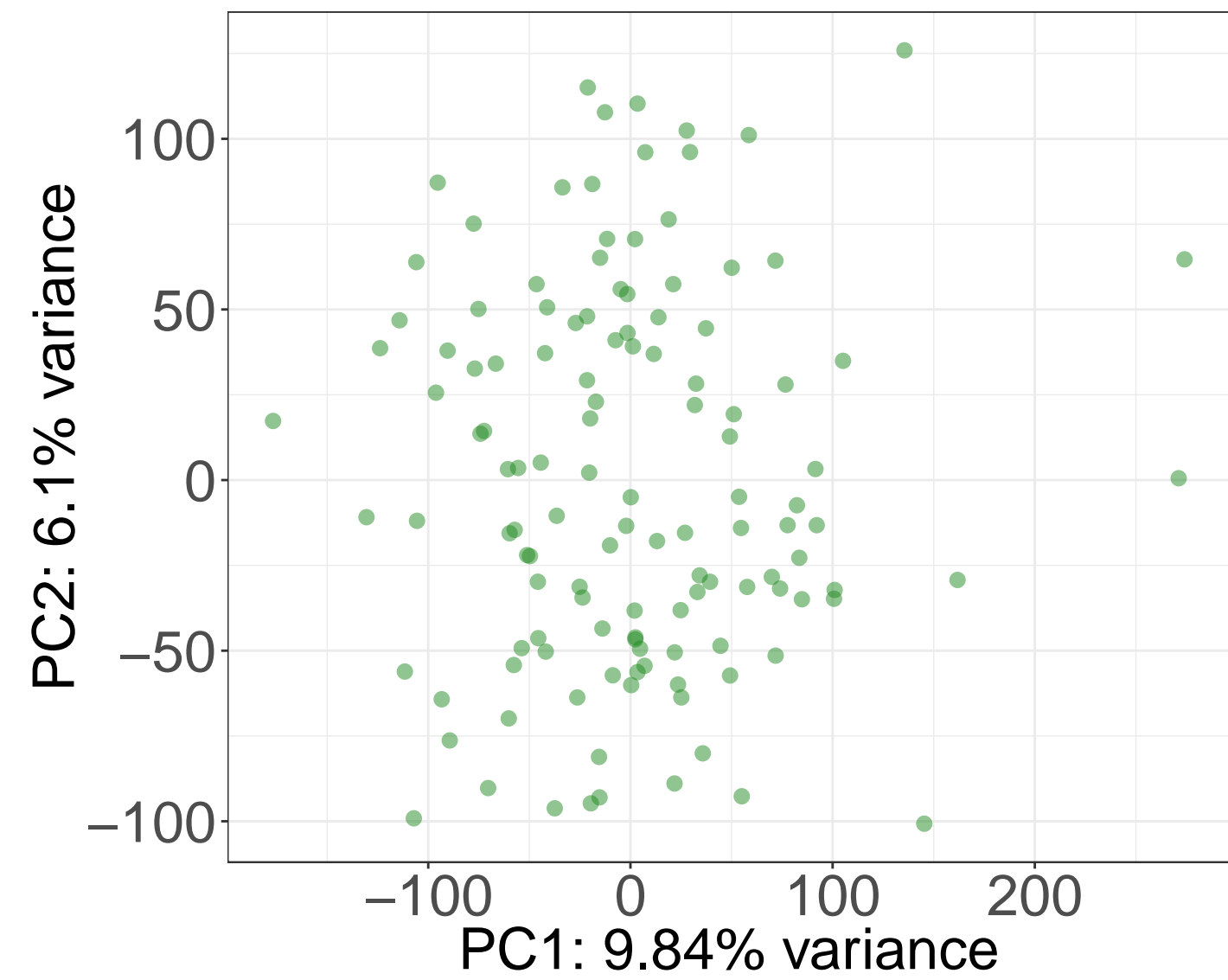
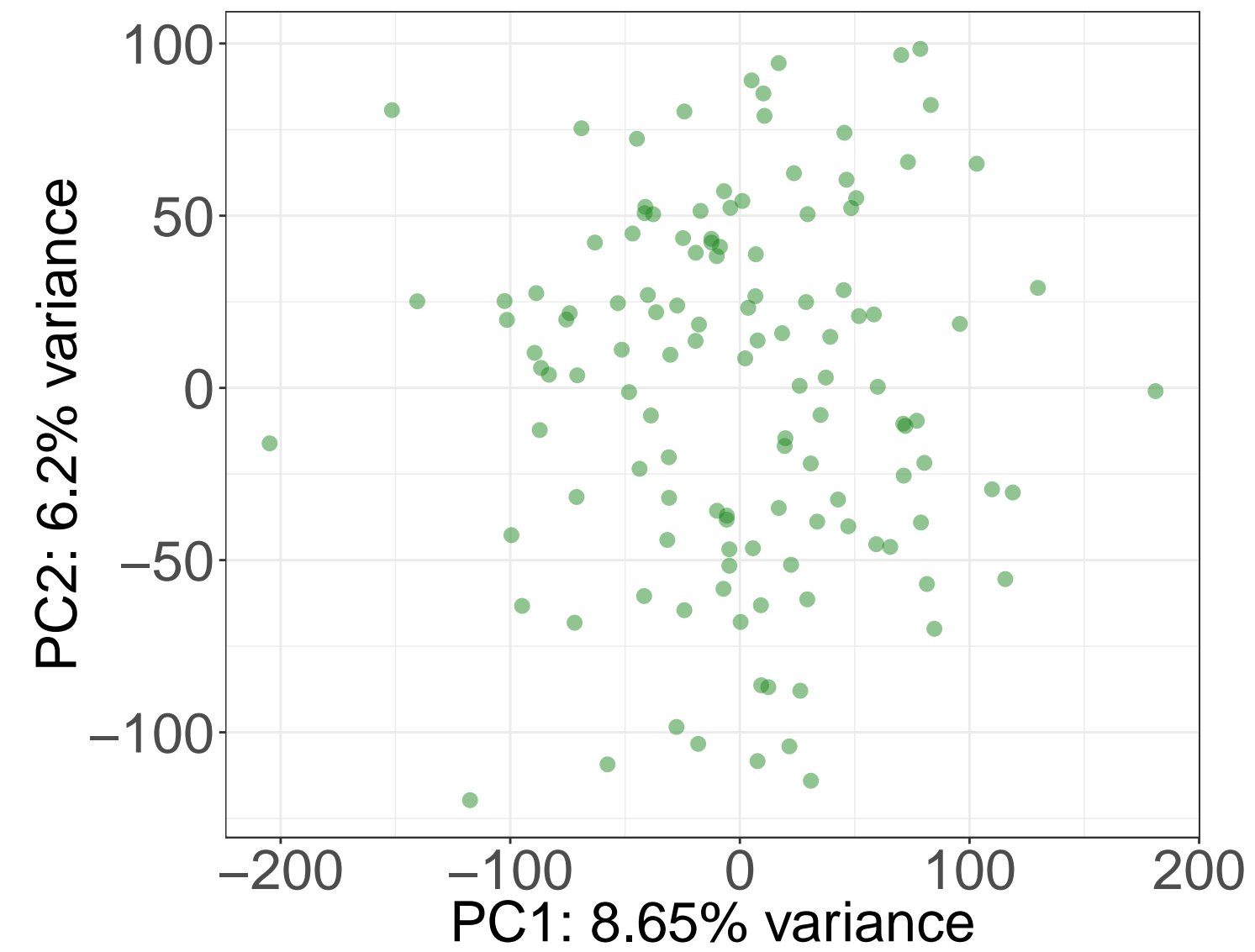
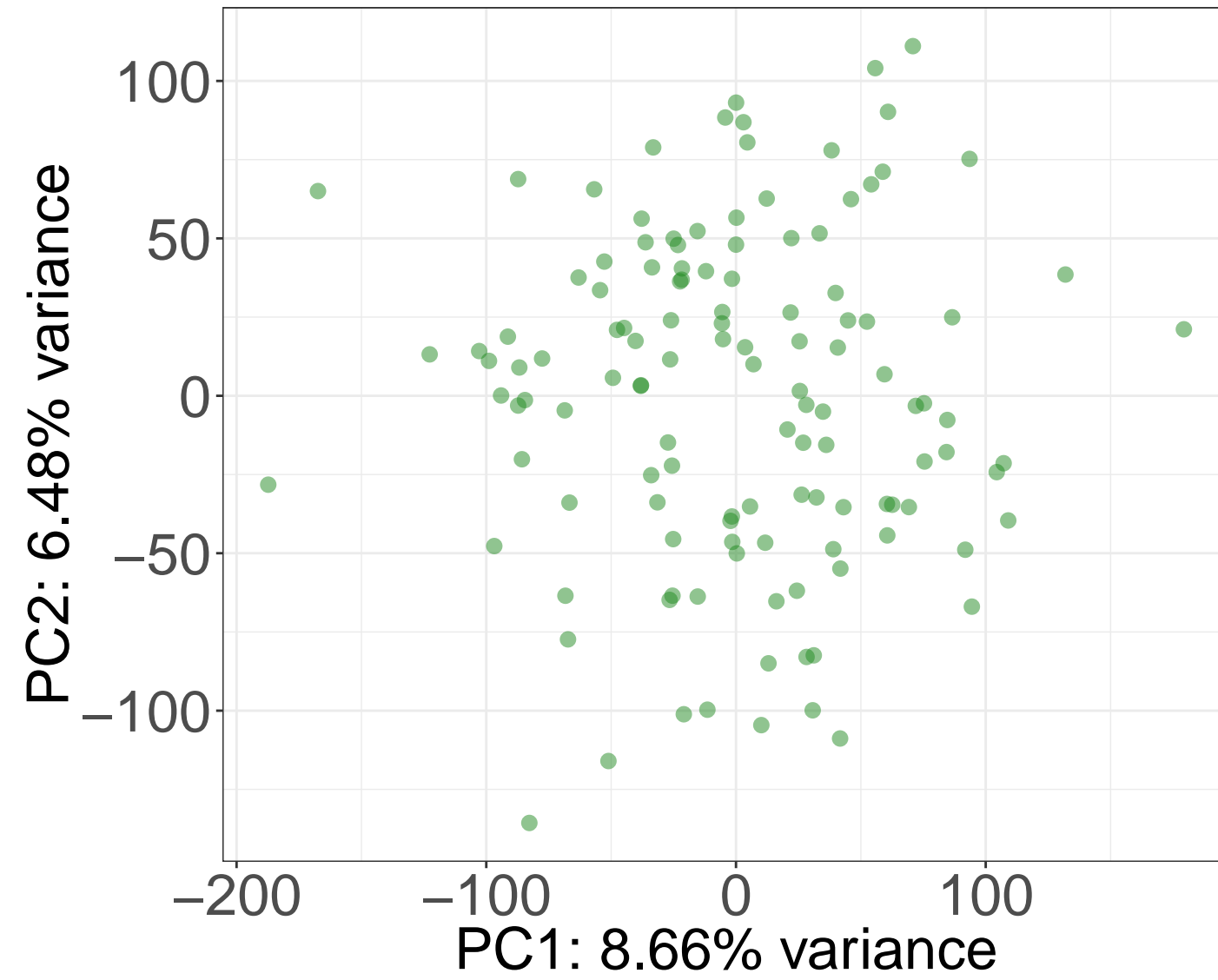
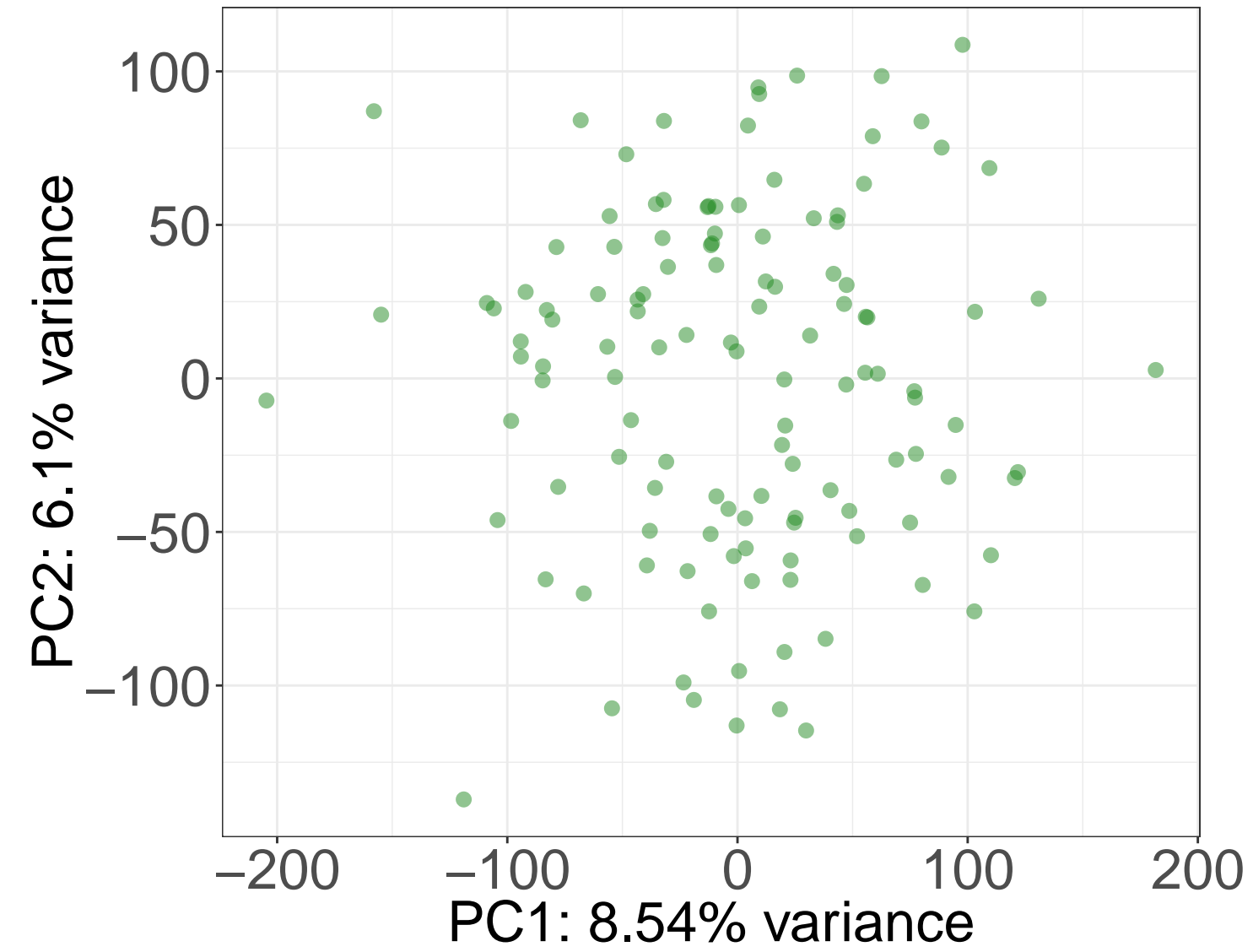
GDC**Xena****Piccolo****Recount2****MSKCC****MSKCC Batch**

GDC**Xena****Piccolo****Recount2****MSKCC****MSKCC Batch**

GDC**Xena****Piccolo****Recount2****MSKCC****MSKCC Batch**

● 13 ● 31

GDC**Xena****Piccolo****Recount2****MSKCC****MSKCC Batch**

GDC**Xena****Piccolo****Recount2****MSKCC****MSKCC Batch**