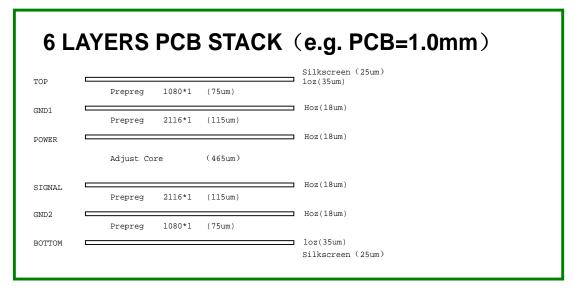
RK3399_BOX_REF_V1.0

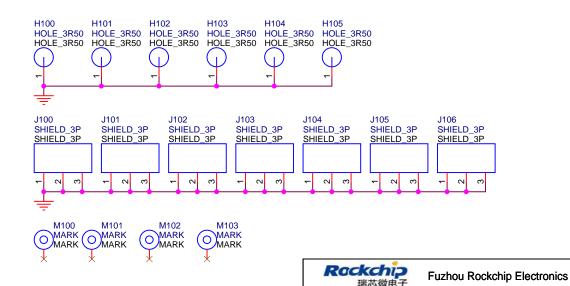
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Note:

- 1: The partment "value" identifies the "DNP" for no paste.
- 2: The partment "option" identifies the "DNP" for reserved.



Project:

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File:

RK3399 BOX REF

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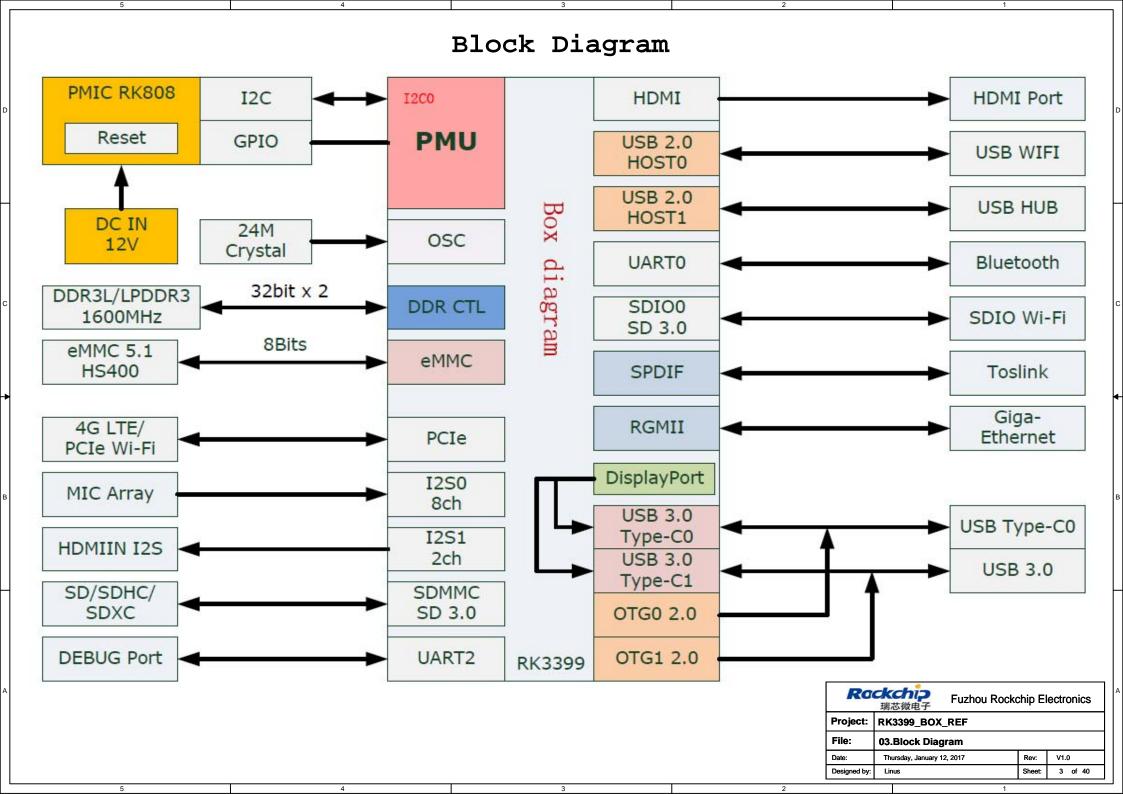
O1.Index
Tuesday, March 07, 2017

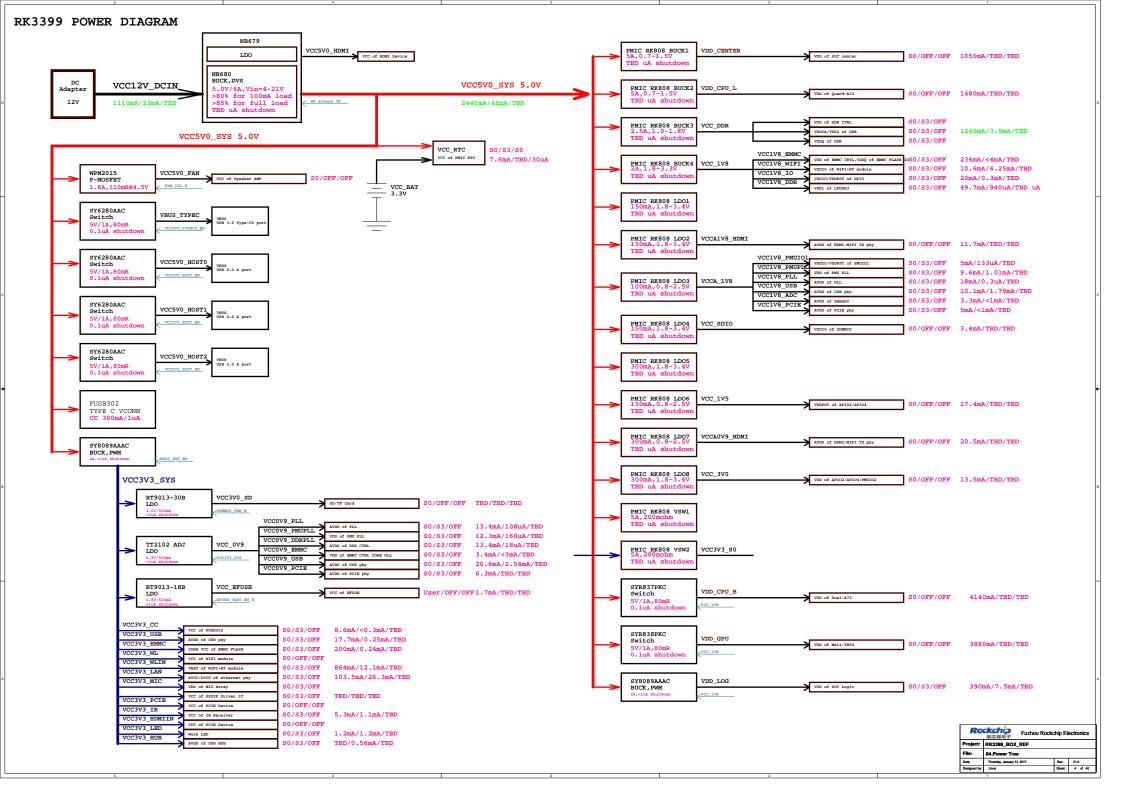
Linus

Change List

Version	Date	Author	Change Note	Approved
V1.0	20170112	Linus.Lin	First edition	

Ro	ckchip 瑞芯微电子	Fuzhou Rock	chip El	ectronics
Project:	RK3399_BOX	_REF		
File:	02.Change Li	st		
Date:	Thursday, January 1	2, 2017	Rev:	V1.0
Designed by:	Linus		Sheet:	2 of 40





I2C MAP

Port	Pin name	Domain	Bus name	Pull-up voltage	Slave Device	Slave Addr (MS 7Bits)	Note	Slave Bus Capability
					Rockchip RK808	0x1b	PMIC	100kHz,400KHz
I2C0	GPIO1_B7/SPI3_RXD/I2C0_SDA GPIO1_C0/SPI3_TXD/I2C0_SCL	PMUIO2	I2C_SDA_PMIC I2C_SCL_PMIC	VCC_1V8	SYR837	0x40	DC-DC BUCK	100kHz,400KHz,3.4MHz
					SYR838PKC	0x41	DC-DC BUCK	100kHz,400KHz,3.4MHz
I2C1	GPIO4_A1/I2C1_SDA GPIO4_A2/I2C1_SCL	APIO5	I2C_SDA_VIDEO I2C_SCL_VIDEO	VCC_1V8	TC358749XBG		HDMI Transmit	
I2C2	GPIO2_A0/VOP_D0/CIF_D0/I2C2_SDA GPIO2_A1/VOP_D1/CIF_D1/I2C2_SCL	APIO2	RESERVE					
I2C3	GPIO4_C0/I2C3_SDA/UART2B_RX GPIO4_C1/I2C3_SCL/UART2B_TX	APIO4	I2C_SDA_HDMI I2C_SCL_HDMI	VCC_3V0				
I2C4	GPIO1_B3/I2C4_SDA GPIO1_B4/I2C4_SCL	PMUIO2	I2C_SDA_MEMS I2C_SCL_MEMS	VCC_1V8	Fairchild FUSB302B	0x44,0x46	USB-TypeC Mux	100kHz,400KHz,1MHz
I2C5	GPIO3_B2/MAC_RXER/I2C5_SDA GPIO3_B3/MAC_CLK/I2C5_SCL	APIO1	Other pin function					
12C6	GPIO2_B1/SPI2_RXD/CIF_HREF/I2C6_SDA GPIO2_B2/SPI2_TXD/CIF_CLKIN/I2C6_SCL	APIO2	RESERVE					
I2C7	GPIO2_A7/VOP_D7/CIF_D7/I2C7_SDA GPIO2_B0/VOP_CLK/CIF_VSYNC/I2C7_SCL	APIO2	RESERVE					

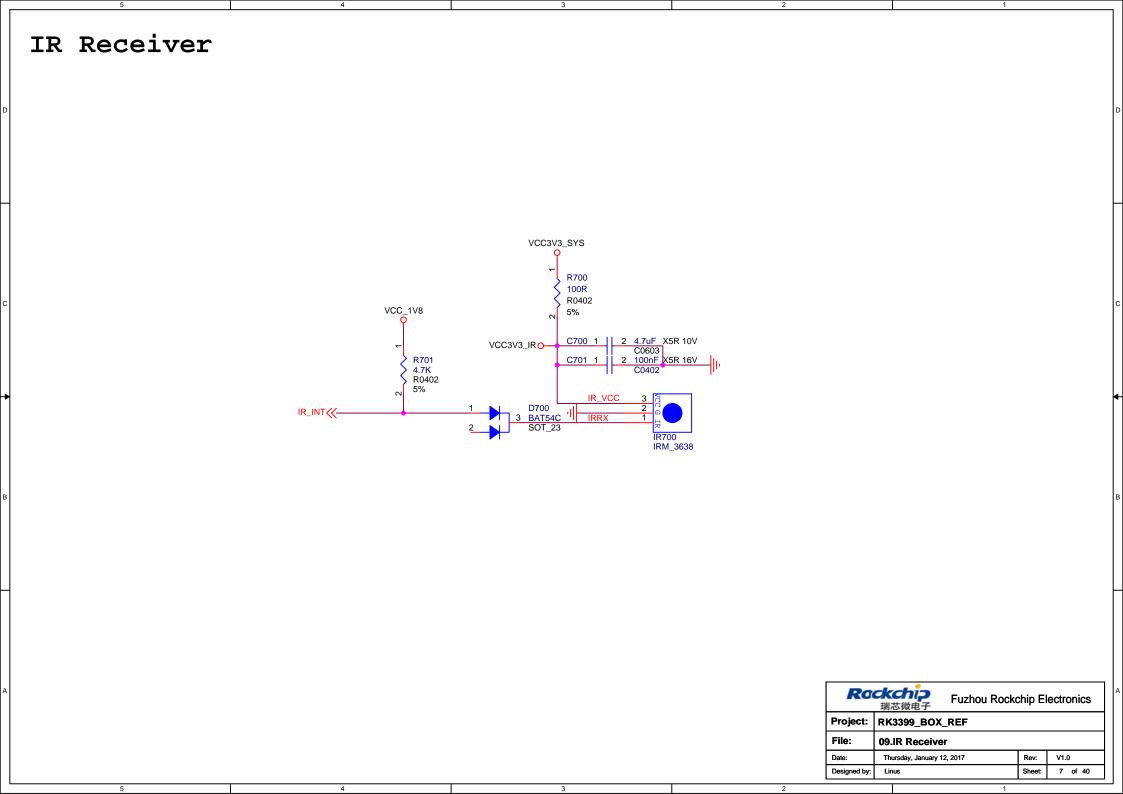
Rockchip Fuzhou Rockchip Electronics					
Project:	RK3399_BOX_REF				
File:	05.I2C Map				
Date:	Thursday, January 12, 2017	Rev:	V1.0		
Designed by:	Linus	Sheet:	5 of 40		

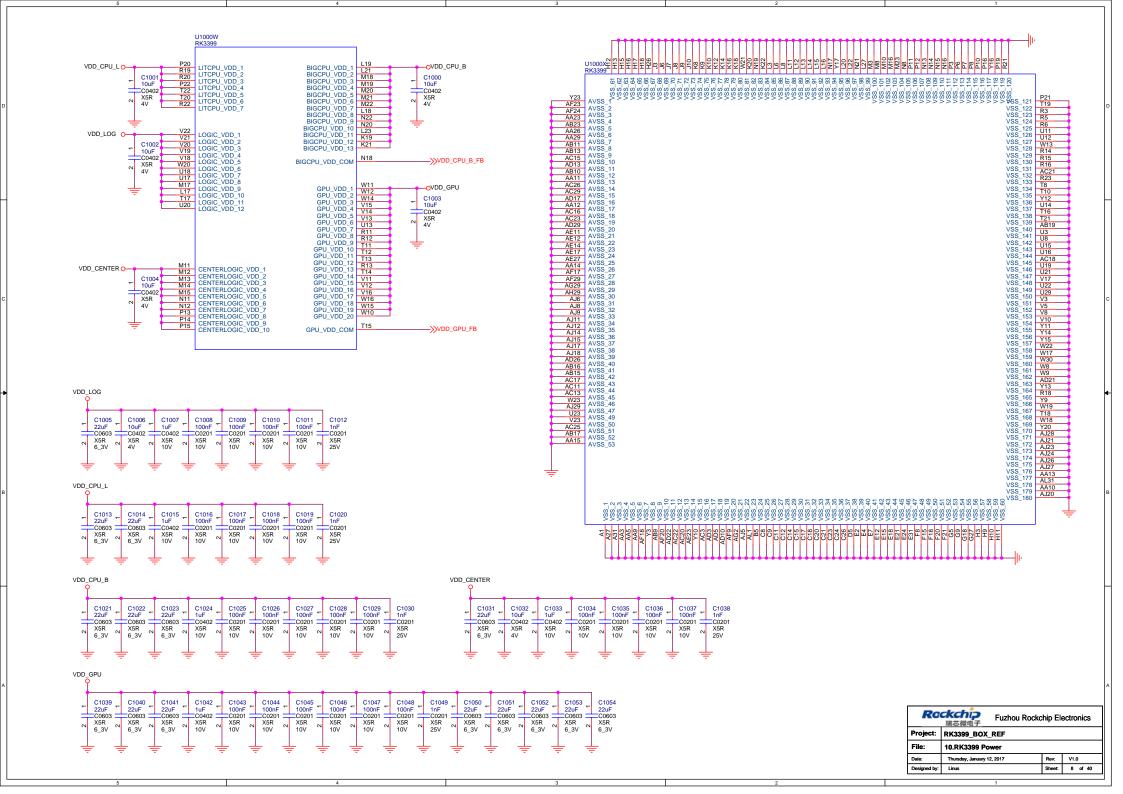
Power Domain Map

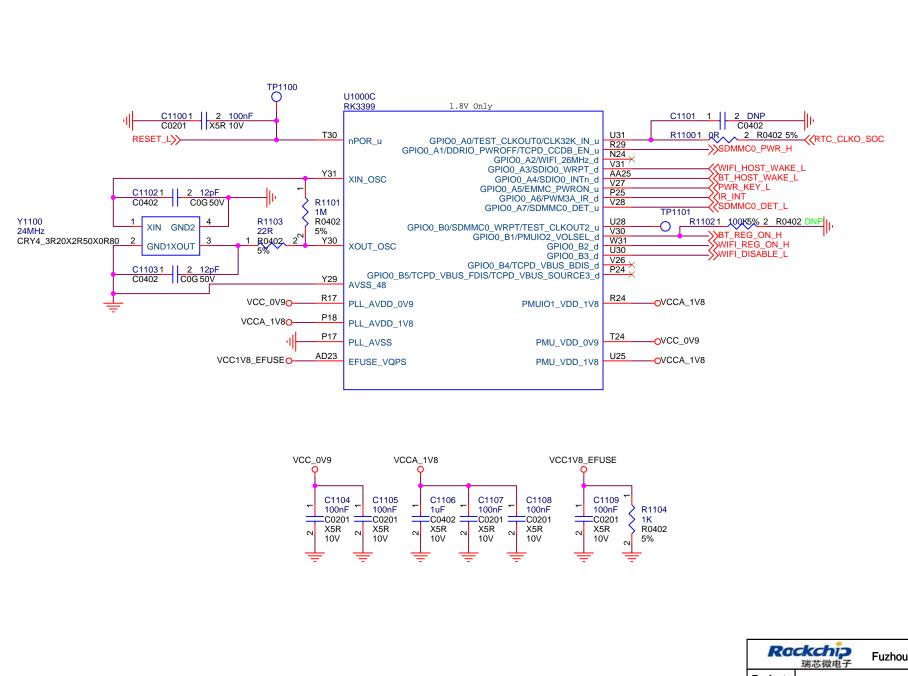
Part Port	Domain	Pin name in datasheet	I/O type	Power supply	Power source
Part C	PMUIO1	pmuio1_gpio0ab	1.8V only	VCCA_1V8	RK808-D VLDO3
Part E	PMUIO2	pmu1830_gpio1abcd	1.8V(Default) 3.0V	VCC_1V8	RK808-D Buck4
Part I	APIO1	gmac_gpio3abc	3.3V only	VCC_1V8 VCC3V3_LAN	RK808-D Buck4
Part L	APIO2	bt656_gpio2ab	1.8V(Default) 3.0V	VCC_1V8	RK808-D VLDO3
Part G	APIO3	wifi/bt_gpio2cd	1.8V only	VCC_1V8	RK808-D Buck4
Part K	APIO4	gpio1830_gpio4cd	1.8V 3.0V(Default)	VCC_1V5 VCC_3V0	RK808-D VLD06 RK808-D VLD08
Part J	APIO5	audio_gpio3d_gpio4a	1.8V(Default) 3.0V	VCC_1V8	RK808-D Buck4
Part F	SDMMC0	sdmmc_gpio4b	1.8V 3.0V(Default)	VCC_SDIO	RK808-D VLDO4

Rockchip Fuzhou Rockchip Electronics						
Project:	RK3399_BOX_REF					
File:	e: 06.Power Domain Map					
Date:	Thursday, January 12, 2017	Rev:	V1.0			
Designed by:	Linus	Sheet:	6 of 40			

5 4 3 2







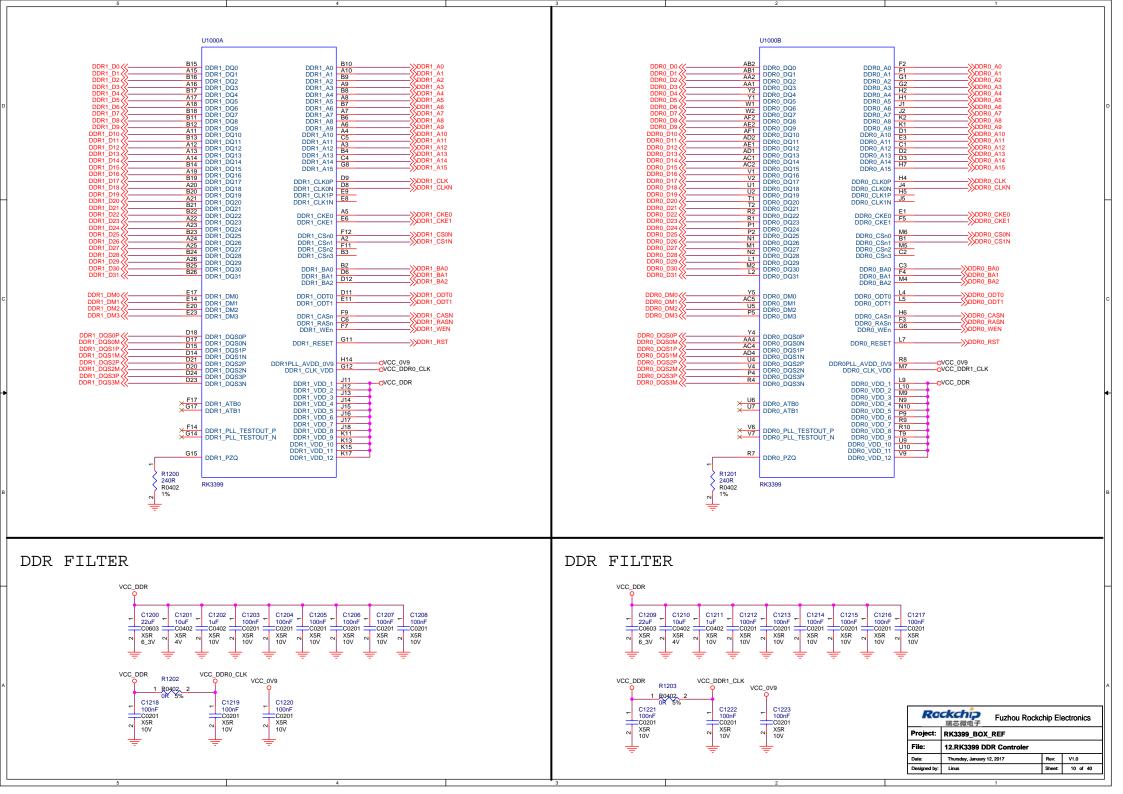
Fuzhou Rockchip Electronics

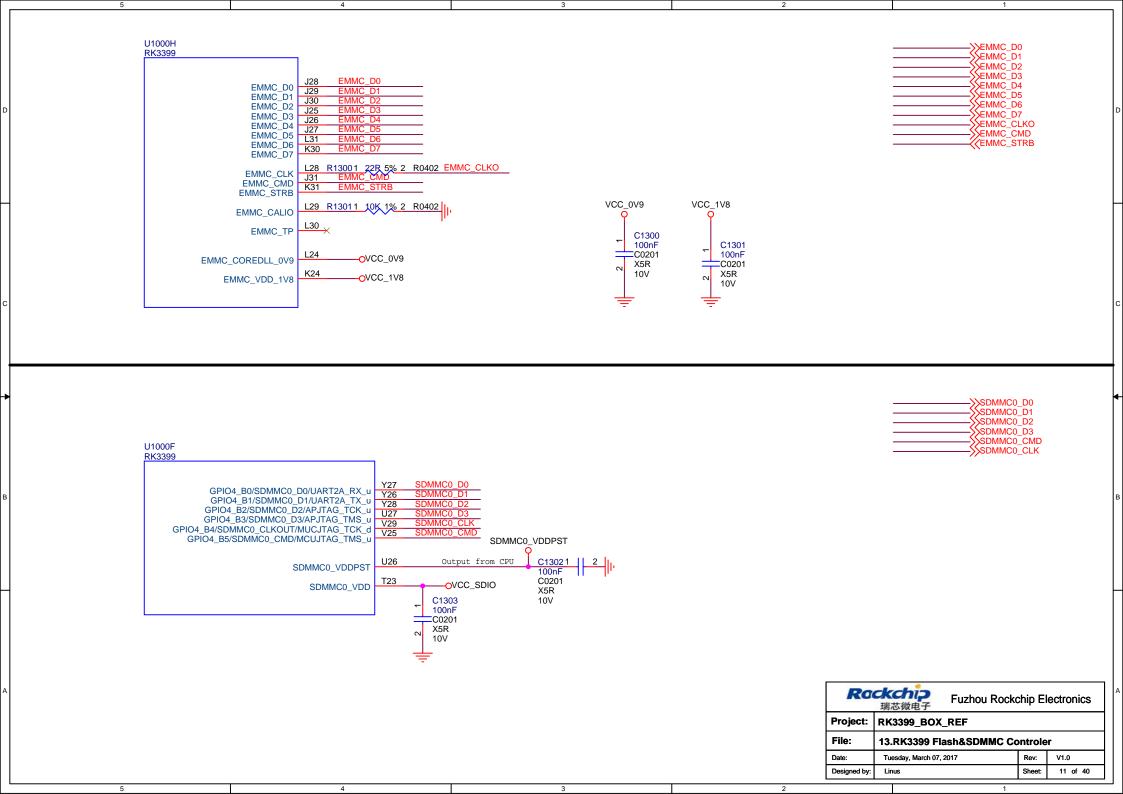
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File: 11.RK3399 PMU Controler

Date: Thursday, January 12, 2017 Rev: V1.0

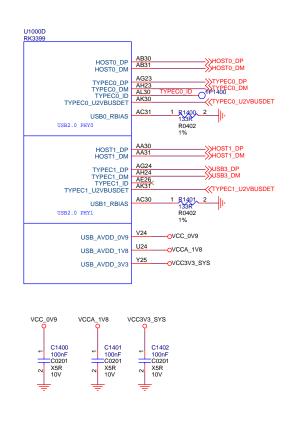
Designed by: Linus Sheet: 9 of 40



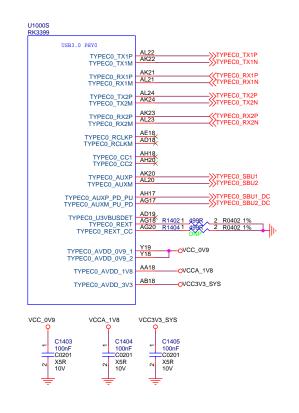


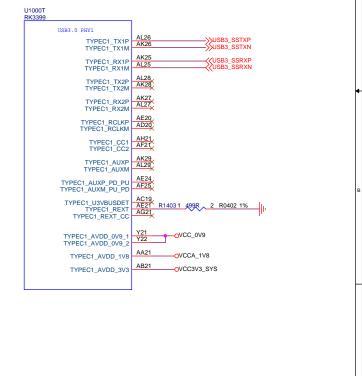


USB2.0

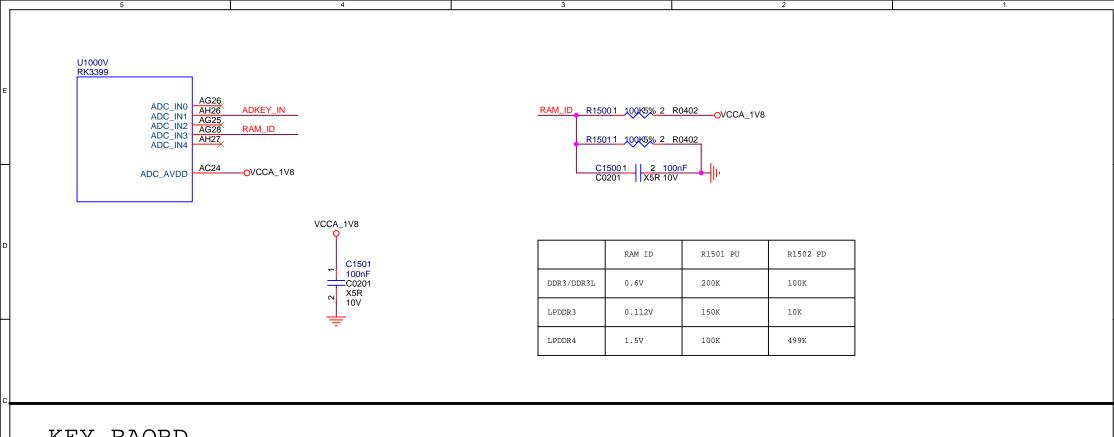


USB3.0



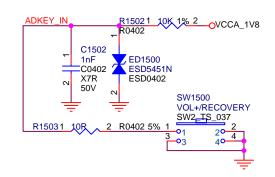


Ra	Kchip 瑞芯微电子 Fuzhou	u Rockchip Ele	ectronics
Project:	RK3399_BOX_REF		
File:	14.RK3399 USB/USIC	Controler	
Date:	Thursday, January 12, 2017	Rev:	V1.0
Designed by:	Linus	Sheet:	12 of 40

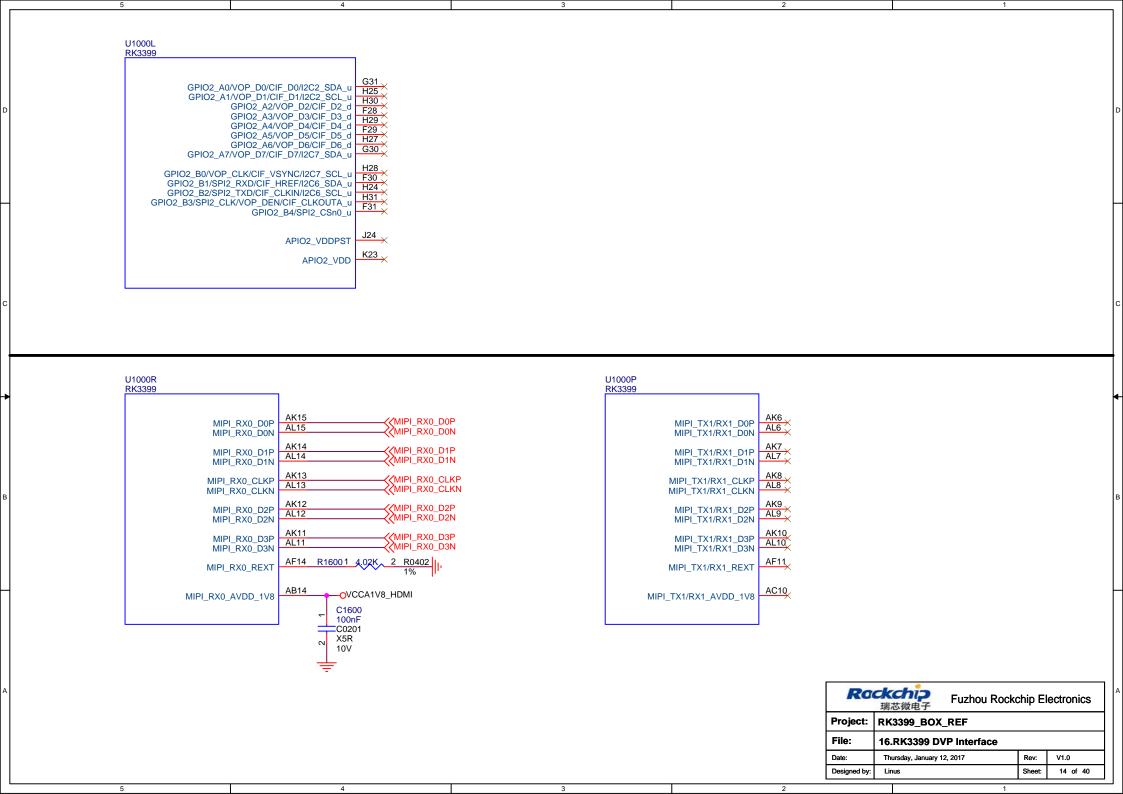


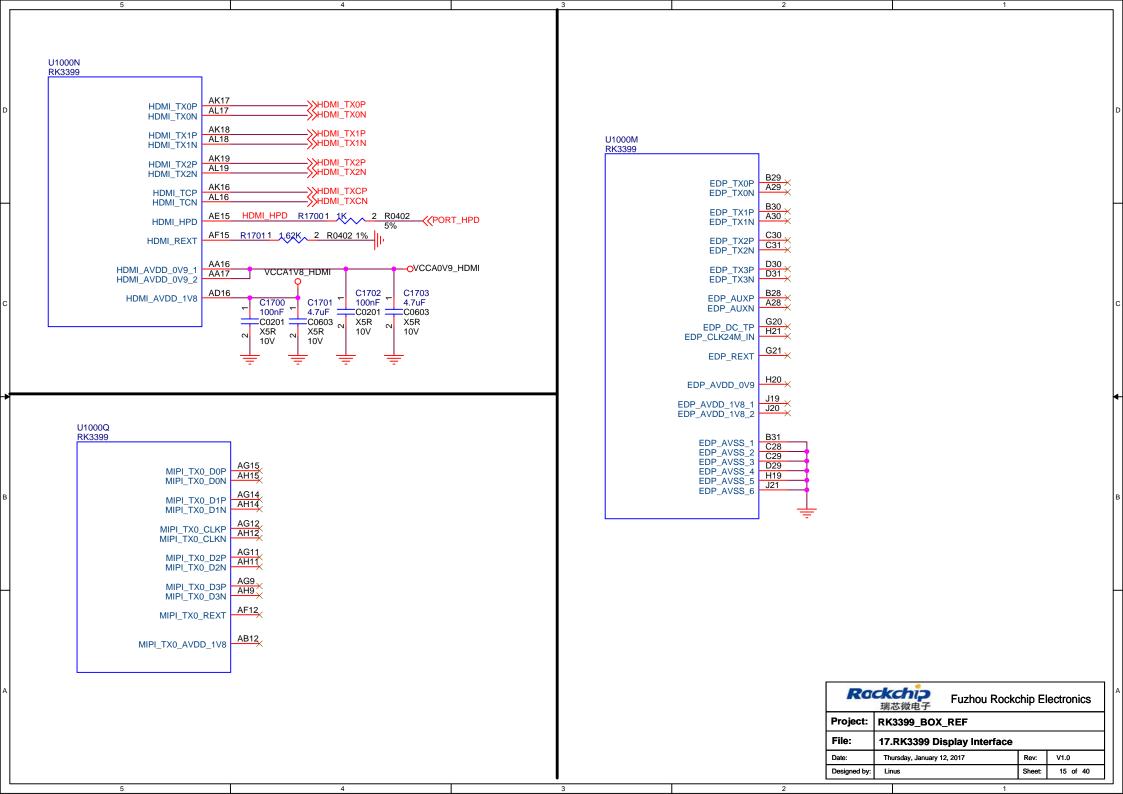
KEY BAORD

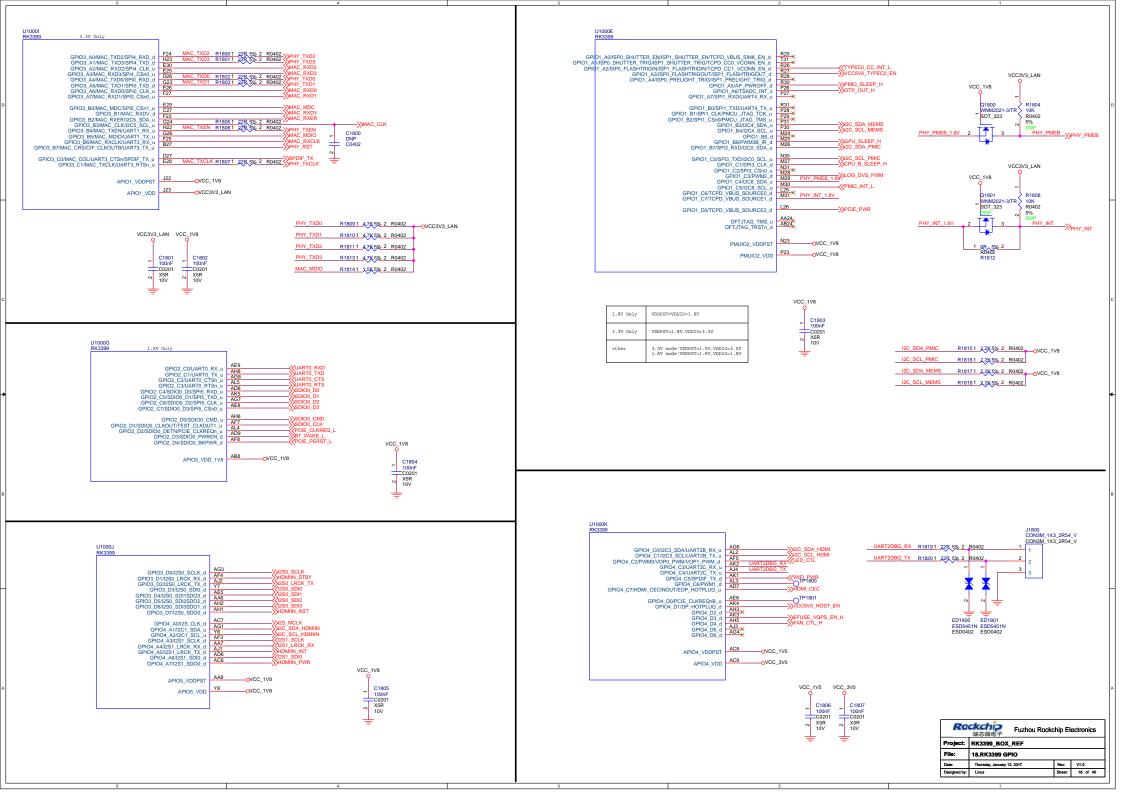
In debuging, ADKEY_IN value is 0V,Set RK3399 into "Recovery" mode; In producting,R1503,SW1500,ED1500 could not be pasted.

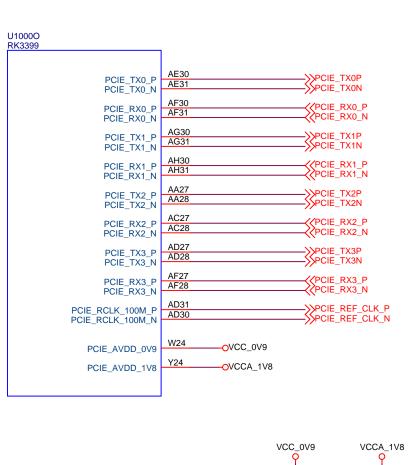


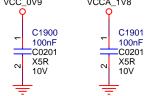
Ra	ckchip 瑞芯微电子	Fuzhou	Rockchip El	lectronics
Project:	RK3399_BOX	_REF		
File:	15.RK3399 S	ARADC/Key		
Date:	Tuesday, March 07,	, 2017	Rev:	V1.0
Designed by:	Linus		Sheet:	13 of 40



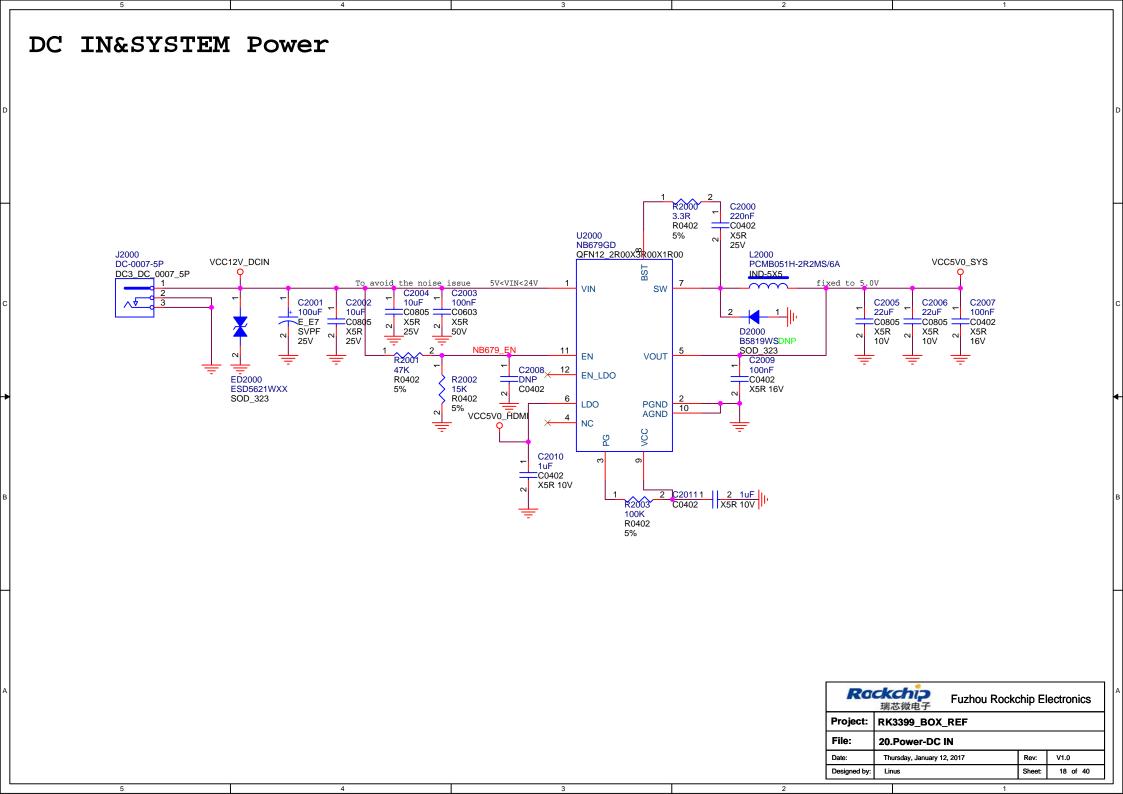


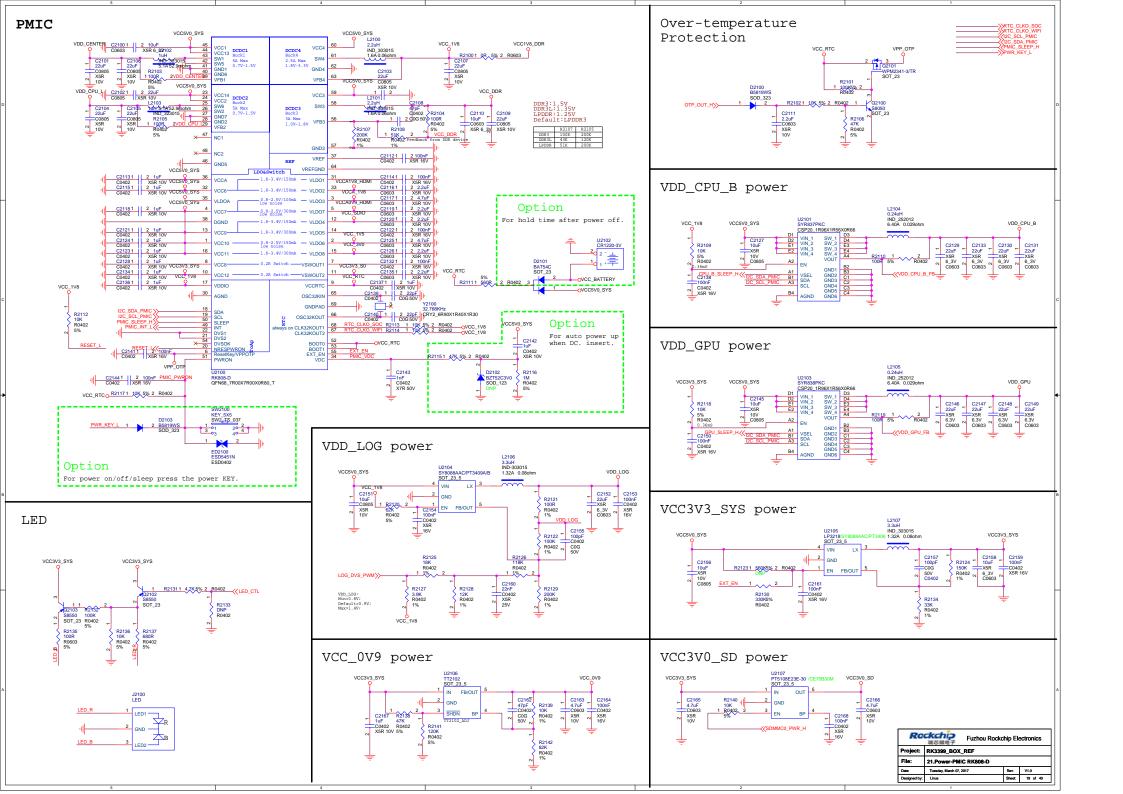


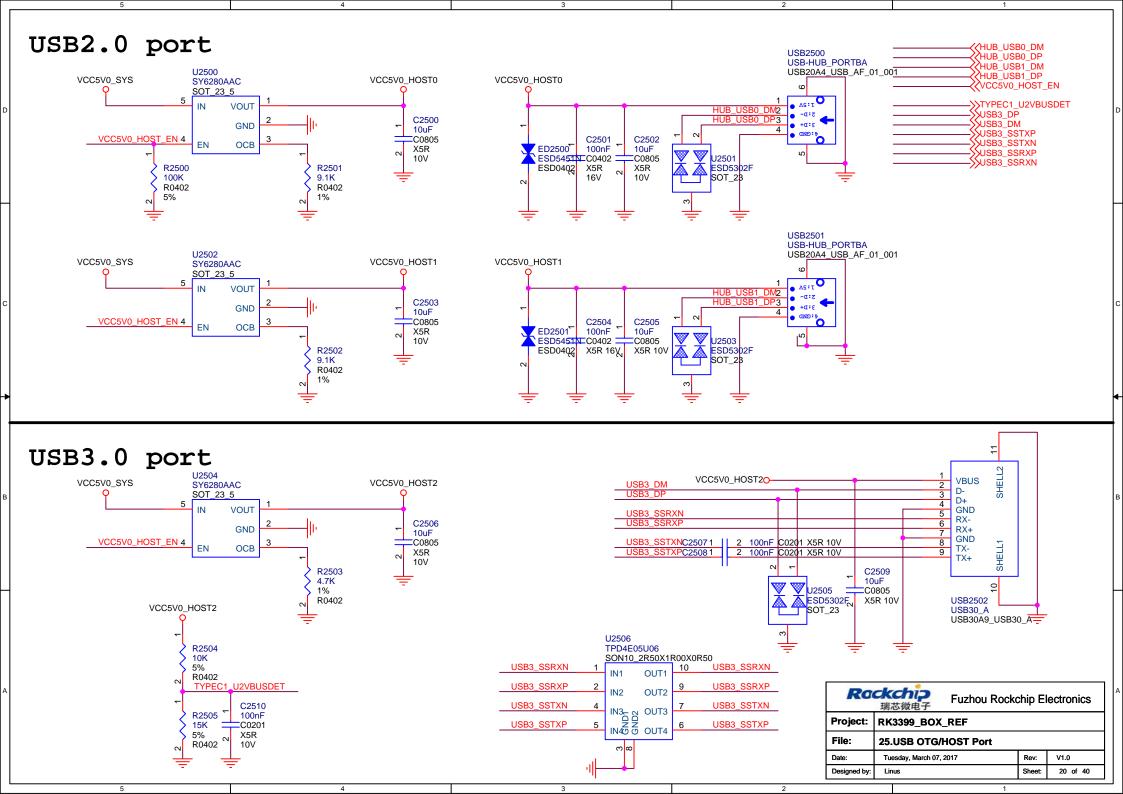


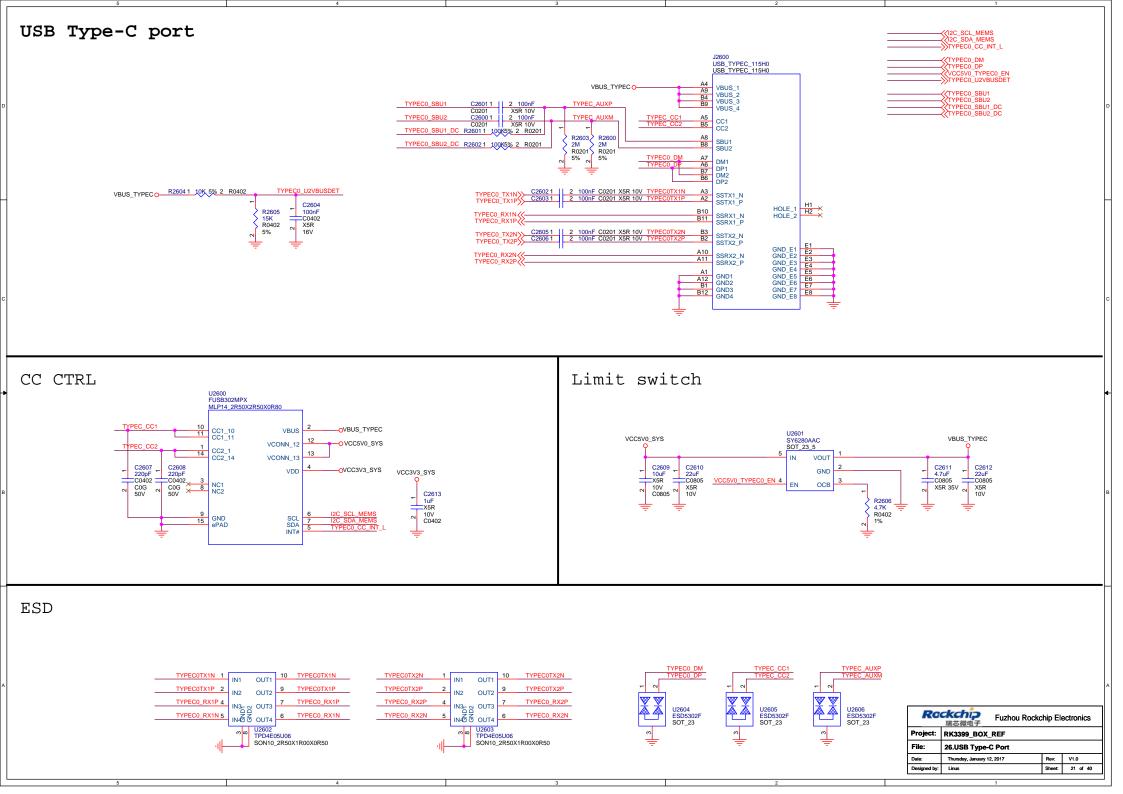


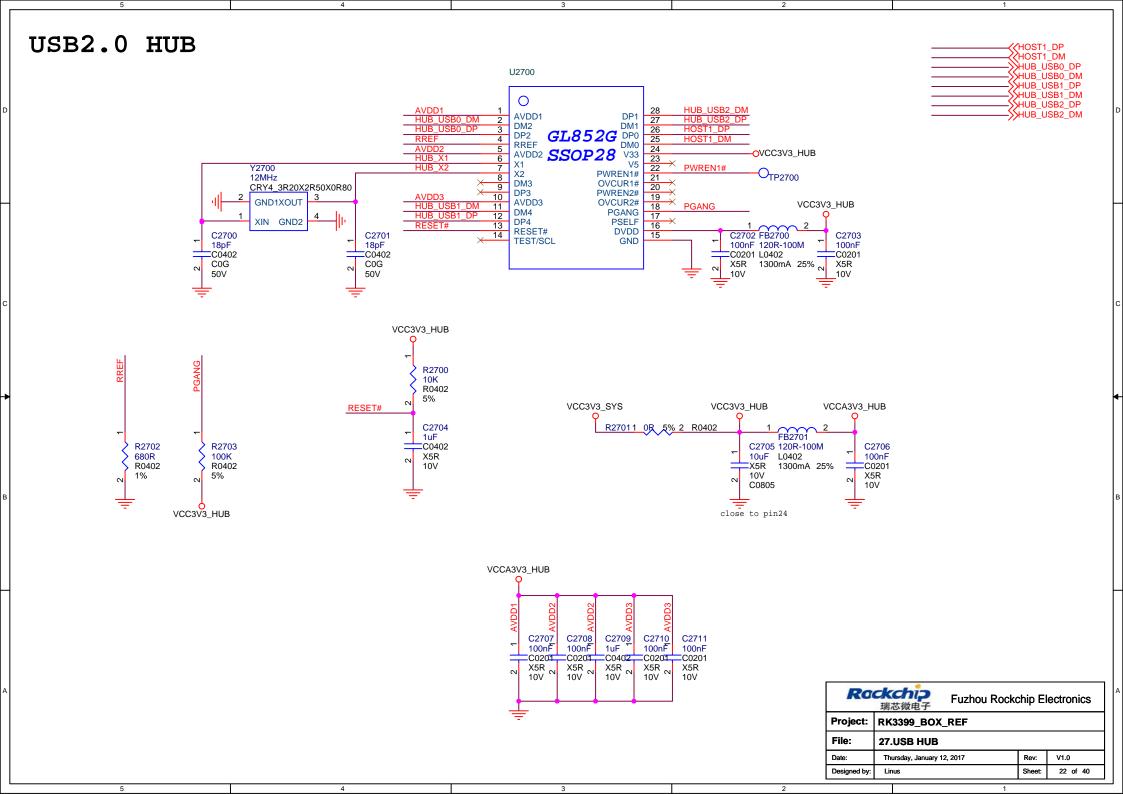
Ra	は は は は は は は は は は は は は に に に に に に に に に に に に に	Fuzhou Rocko	chip El	ectronics
Project:	RK3399_BOX	_REF		
File:	19.RK3399 P	CIE		
Date:	Thursday, January	12, 2017	Rev:	V1.0
Designed by:	Linus		Sheet:	17 of 40

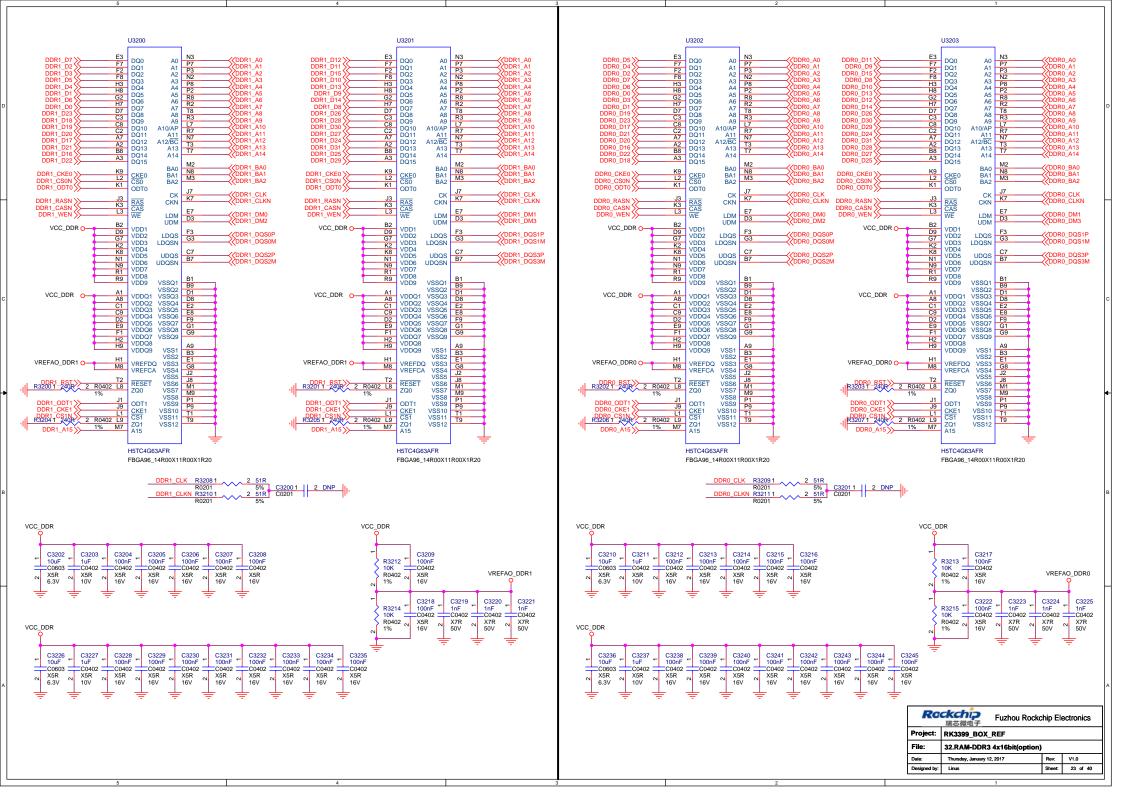


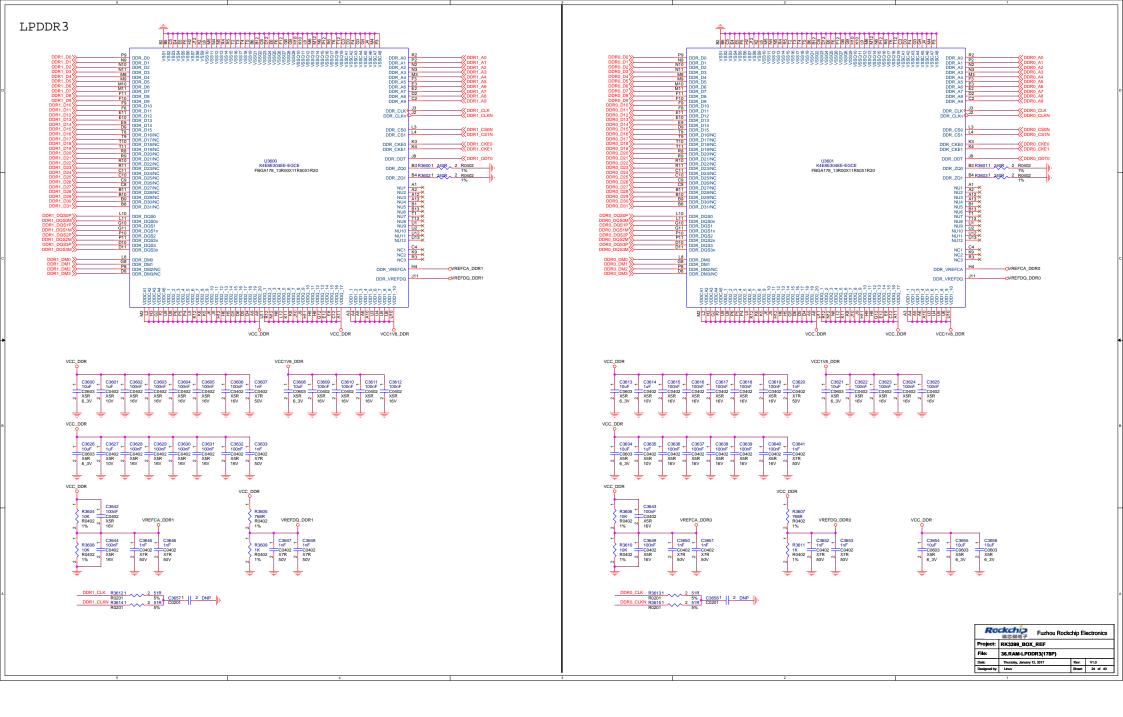


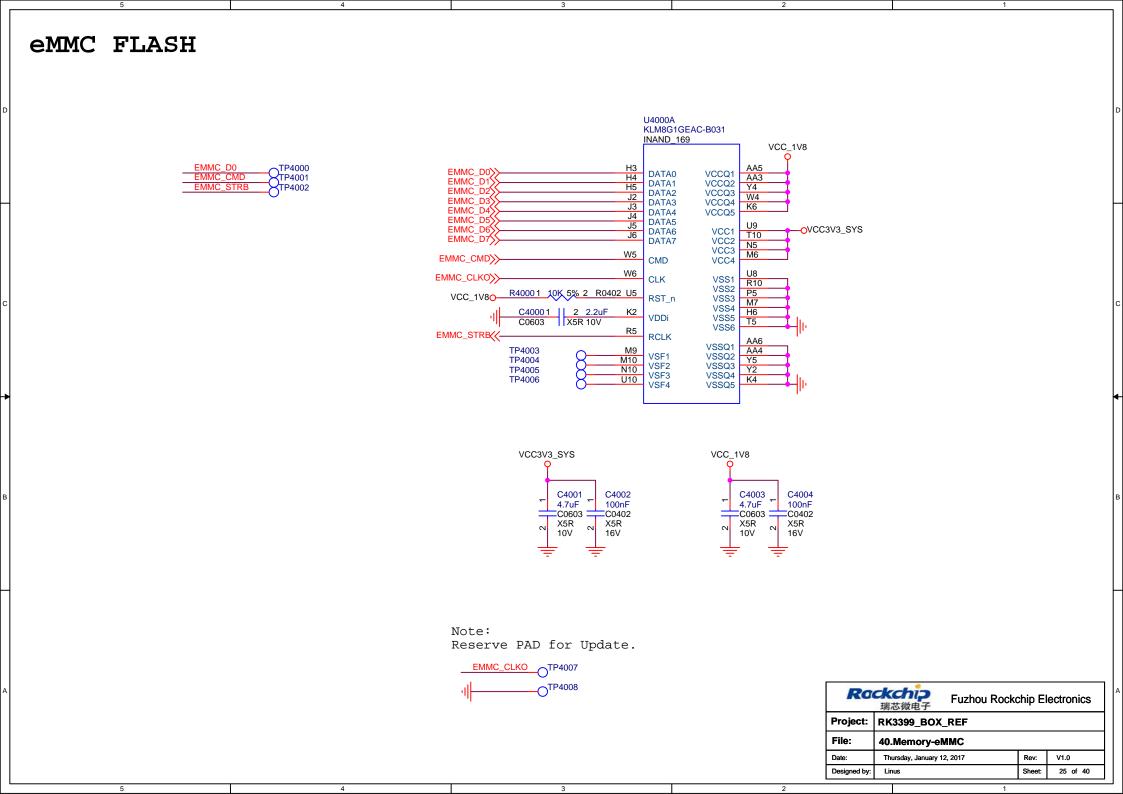






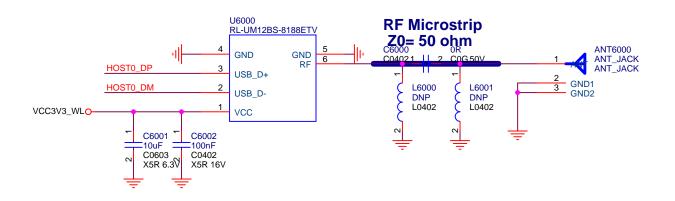


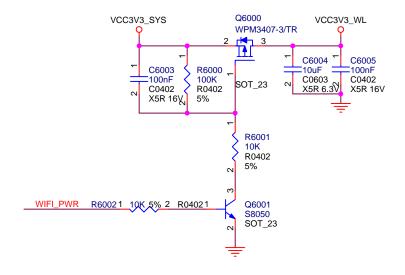




USB WIFI MODULE







Ro	KChip 瑞芯微电子 Fuzhou Ro	ckchip E	lectronics	
Project:	RK3399_BOX_REF			
File:	60.WIFI-USB (option)	3 (option)		
Date:	Thursday, January 12, 2017	Rev:	V1.0	
Designed by:	Linus	Sheet:	26 of 40	

