

CSE260 Lab Report

Experiment Name: Applications of Boolean algebra

Submitted by

Name: Shabab Abdullah

ID: 20301005

Section: 09

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1. Name of the experiment :

Application of Boolean Algebra.

2. Objective:-

- i) To investigate the rules of Boolean Algebra.
- ii) To gain experience working with partial circuits.
- iii) To simplify a complex function using Boolean Algebra.

3. Required Components and Equipments :

- i) Proteus 8 Professional.
- ii) NAND
- iii) LOGIC STATE (BIG)
- iv) LOGIC PROBE
- v) Ground
- vi) LED- Green

4. Experimental Setup :

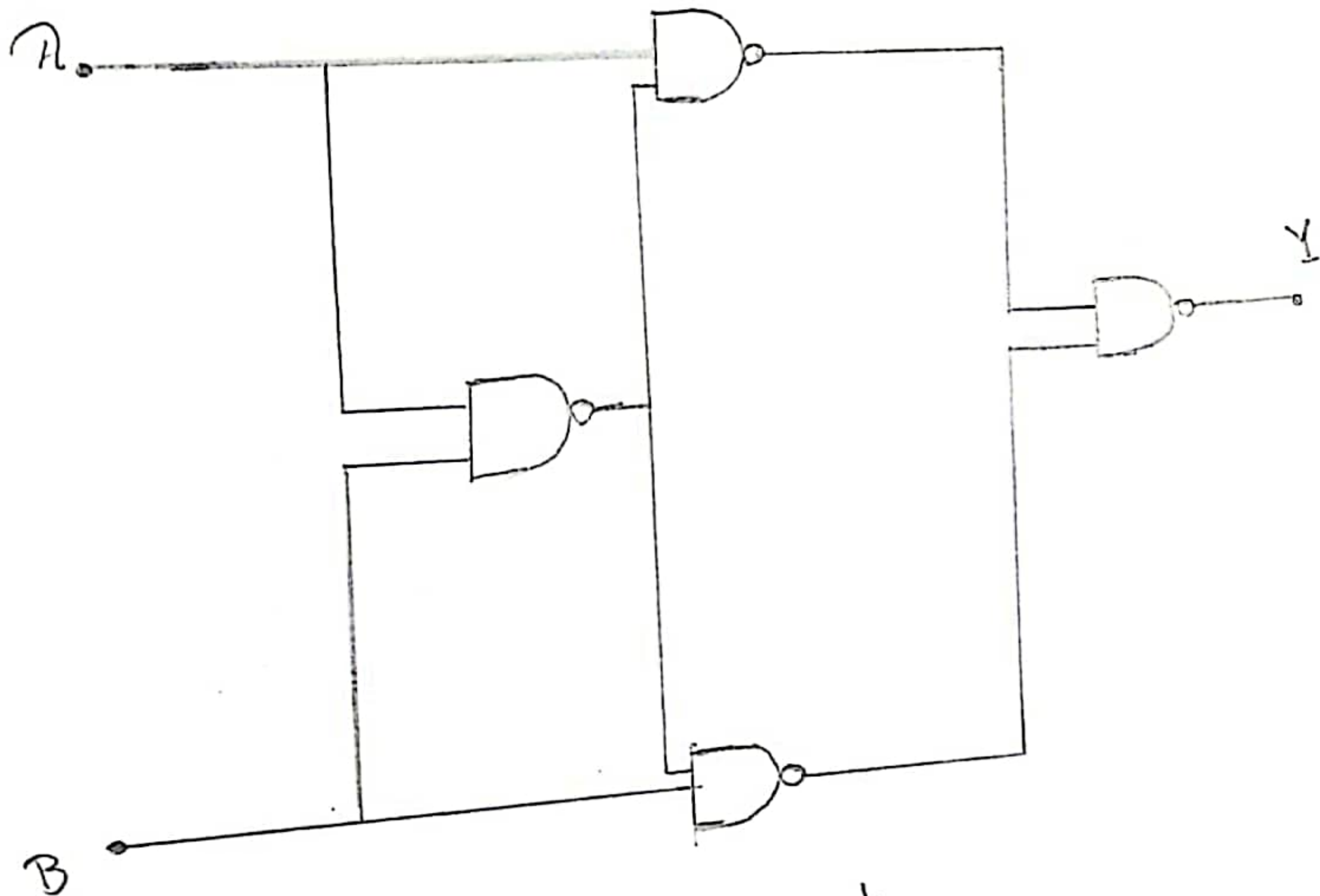


Figure: IC Configuration

P.T.O

5. Result: (Truth table)

A	B	\overline{AB}	$A \cdot \overline{AB}$	$\overline{A \cdot AB}$	$B \cdot \overline{AB}$	$\overline{B \cdot \overline{AB}}$	$\overline{(\overline{A + \overline{AB}}) \cdot (\overline{B \cdot \overline{AB}})}$	$A \oplus B$
0	0	1	0	1	0	1	0	0
0	1	1	0	1	1	0	1	1
1	0	1	1	0	0	1	1	1
1	1	0	0	1	0	1	0	0

Discussions:

The output is: $\overline{(\overline{A} \cdot \overline{AB}) \cdot (\overline{B} \cdot \overline{AB})}$

Here,

$$\begin{aligned} Y &= \overline{(\overline{A} \cdot \overline{AB}) \cdot (\overline{B} \cdot \overline{AB})} \\ &= \overline{(\overline{A} \cdot \overline{AB})} + \overline{(\overline{B} \cdot \overline{AB})} \\ &= (A \cdot \overline{AB}) + (B \cdot \overline{AB}) \\ &= \overline{AB} (A+B) \\ &= (\overline{A} + \overline{B}) (A+B) \\ &= A \cdot \overline{A} + B \cdot \overline{A} + A \cdot \overline{B} + B \cdot \overline{B} \\ &= 0 + \overline{A}B + 0 + A\overline{B} \\ &= \overline{A}B + A\overline{B} \\ &= A \oplus B \end{aligned}$$

Upon further examination, we can see that circuit function is equal to a single gate, which is referred to as the X-OR gate once it has been simplified.