This page is a KiCad schematic diagram titled "Jade Nano+".

USB-C

USB1
918-418K2023S40001

GND, GND, TP_VBUS1, VBUS, VBUS, VUSB, CC2, SBU1, DP2, DN1, D-, D+, DN2, DP1, SBU2, CC1, VBUS, VBUS, VUSB, GND, GND, TP_USH1

R9 5.1k, R10 5.1k

B1, B4, B5, B6, B7, B8, B9, B12, A12, A9, A8, A7, A6, A5, A4, A1, VUSB, D-, D+, VUSB

uC Decoupling

C6 4.7uF 16V, C7 100nF 16V, +5V, GND

Resonator 16MHz

Y1 CSTNE16M0V530000R0, XTAL1, XTAL2, GND

ATMEGA328PB

U1 ATMEGA328PB-MN

+5V, +5V, AVCC 18, VCC 4
D8 12 PB0, D9 13 PB1, D10 14 PB2, D11-MOSI 15 PB3, D12-MISO 16 PB4, D13-SCK 17 PB5
AREF 20 AREF, PE0/AC0 3, PE[1] 6, ADC6/PE2 19 A6, ADC7/PE3 22 A7
C4 100nF 16V, GND
XTAL1 7 XTAL1/PB6, XTAL2 8 XTAL2/PB7
A0 23 PC0, A1 24 PC1, A2 25 PC2, A3 26 PC3, A4 27 PC4, A5 28 PC5
RESET/PC6 29
uC_Reset
SW1 TS342A2P-WZ, R4 10k, +5V, GND
D0-uC_RX 30 PD0, D1-uC_TX 31 PD1, D2 1 PD2, D3 2 PD3, D4 9 PD4, D5 10 PD5, D6 11 PD6, D7 PD7
D13-SCK
R7 20k, TP_LL1 TestPoint
D3 GREEN, GND
GND 33, GND 21, GND 5, GND

HT42B534

IC1 HT42B534-2
R5 33, D+, R8 33, D-, UD+, UD-
C12 47pF 50V, C13 47pF 50V, C1 1uF 10V, +3V3, GND
D1 RED, TP1, R3 20k
D+ 1, D- 2, V330 3, GND 4, CTS 5, TX 6, UDET 7, LED 8
VDD 16, RI 15, DCD 14, RTS 13, DSR 12, RX 11, VDDIO 10, DTR 9
R1 1k D0-uC_RX, HOLTEK_TX
R2 1k D1-uC_TX, HOLTEK_RX
C2 100nF 16V, +5V, GND, HOLTEK_RX, D2 B0520LW, TP_DTR2, C3 100nF 16V, GND, C9 100nF 16V, uC_Reset, GND

PIN HEADERS

J1
D1-uC_TX 1, D0-uC_RX 2, uC_Reset 3, GND 4, D2 5, D3 6, D4 7, D5 8, D6 9, D7 10, D8 11, D9 12, D10 13, D11-MOSI 14, D12-MISO 15

J2
VIN 1, GND 2, uC_Reset 3, +5V 4, A7 5, A6 6, A5 7, A4 8, A3 9, A2 10, A1 11, A0 12, AREF 13, +3V3 14, D13-SCK 15

POWER REGULATION

U3 SSP7603P50MR
VIN, VI 3, VO 2, GND 1, +5V
C5 1uF 50V, C8 4.7uF 16V, GND
D4 B0520LW, VUSB
R6 20k, LED_POW1 RED, GND

USB DECOUPLING

VUSB, C10 4.7uF 16V, C11 100nF 16V, GND

ICSP

J3
D12-MISO 1, D13-SCK 3, uC_Reset 5, 2, 4 D11-MOSI, 6, +5V, GND

REV0.0.2:

—removed R11 and R12
—added revision history for schematic