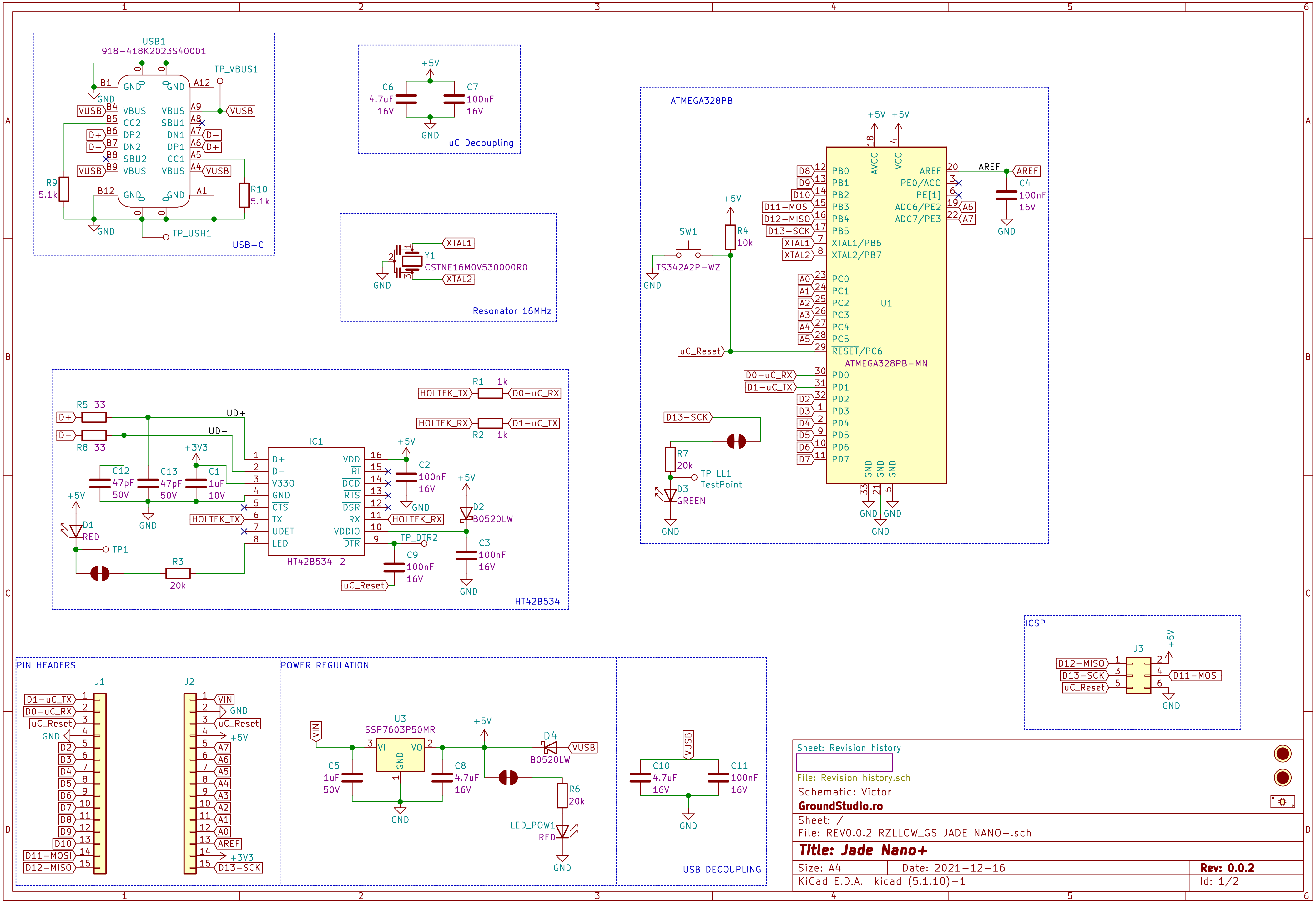
USB1
918-418K2023S40001
TP_VBUS1
GND
VBUS
CC2
DP2
DN2
SBU2
CC1
SBU1
DN1
DP1
VUSB
D+
D-
R9
5.1k
R10
5.1k
TP_USH1
USB-C
+5V
C6
4.7uF
16V
C7
100nF
16V
uC Decoupling
XTAL1
XTAL2
Y1
CSTNE16M0V530000R0
Resonator 16MHz
ATMEGA328PB
SW1
TS342A2P-WZ
R4
10k
uC_Reset
U1
ATMEGA328PB-MN
AVCC
VCC
AREF
PB0
PB1
PB2
PB3
PB4
PB5
XTAL1/PB6
XTAL2/PB7
PC0
PC1
PC2
PC3
PC4
PC5
RESET/PC6
PD0
PD1
PD2
PD3
PD4
PD5
PD6
PD7
PE0/ACO
PE[1]
ADC6/PE2
ADC7/PE3
D8
D9
D10
D11-MOSI
D12-MISO
D13-SCK
A0
A1
A2
A3
A4
A5
A6
A7
D0-uC_RX
D1-uC_TX
D2
D3
D4
D5
D6
D7
C4
100nF
16V
R7
20k
TP_LL1
TestPoint
D3
GREEN
R5 33
R8 33
UD+
UD-
C12
47pF
50V
C13
47pF
50V
+3V3
C1
1uF
10V
IC1
HT42B534-2
D+
D-
V33O
GND
CTS
TX
UDET
LED
VDD
RI
DCD
RTS
DSR
RX
VDDIO
DTR
HOLTEK_TX
HOLTEK_RX
R1 1k
R2 1k
C2
100nF
16V
D2
B0520LW
TP_DTR2
C9
100nF
16V
C3
100nF
16V
D1
RED
TP1
R3
20k
HT42B534
ICSP
J3
PIN HEADERS
J1
J2
VIN
AREF
POWER REGULATION
U3
SSP7603P50MR
VI
VO
C5
1uF
50V
C8
4.7uF
16V
D4
B0520LW
R6
20k
LED_POW1
RED
C10
4.7uF
16V
C11
100nF
16V
USB DECOUPLING

Sheet: Revision history
File: Revision history.sch

Schematic: Victor

GroundStudio.ro

Sheet: /
File: REV0.0.2 RZLLCW_GS JADE NANO+.sch

Title: Jade Nano+

Size: A4 Date: 2021-12-16 **Rev: 0.0.2**
KiCad E.D.A. kicad (5.1.10)-1 Id: 1/2

REV0.0.2:

-removed R11 and R12
-added revision history for schematic

Schematic: Victor
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Sheet: /Revision history/
File: Revision history.sch
Title: Jade Nano+
Size: A4
Date: 2021-12-16
Rev: 0.0.2
KiCad E.D.A. kicad (5.1.10)-1
Id: 2/2