

Spectre Circuit Simulator Reference

Spectre[®] Circuit Simulator Reference

Product Version 5.0

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Preface

This manual assumes that you are familiar with the development, design, and simulation of integrated circuits and that you have some familiarity with SPICE simulation. It contains information about the Spectre[®] circuit simulator.

Spectre is an advanced circuit simulator that simulates analog and digital circuits at the differential equation level. The simulator uses improved algorithms that offer increased simulation speed and greatly improved convergence characteristics over SPICE. Besides the basic capabilities, the Spectre circuit simulator provides significant additional capabilities over SPICE. SpectreHDL (Spectre High-Level Description Language) and Verilog[®]-A use functional description text files (modules) to model the behavior of electrical circuits and other systems. SpectreRF adds several new analyses that support the efficient calculation of the operating point, transfer function, noise, and distortion of common RF and communication circuits, such as mixers, oscillators, sample holds, and switched-capacitor filters.

This preface discusses the following topics:

- [Related Documents](#) on page Preface-9
- [Typographic and Syntax Conventions](#) on page Preface-10
- [References](#) on page Preface-11

Related Documents

The following can give you more information about the Spectre circuit simulator and related products:

- To learn more about the equations used in the Spectre circuit simulator, consult the [*Spectre Circuit Simulator Device Model Equations*](#) manual.
- The Spectre circuit simulator is often run within the Cadence[®] analog circuit design environment, under the Cadence[®] design framework II. To see how the Spectre circuit simulator is run under the analog circuit design environment, read the [Cadence Analog Design Environment User Guide](#).
- For more information about using the Spectre circuit simulator with SpectreHDL, see the [*SpectreHDL Reference*](#) manual.

- For more information about using the Spectre circuit simulator with Verilog-A, see the [Verilog-A Language Reference](#) manual.
- If you want to see how SpectreRF is run under the analog circuit design environment, read [SpectreRF Help](#).
- For more information about RF theory, see [SpectreRF Theory](#).
- For more information about how you work with the design framework II interface, see [Design Framework II Help](#).
- For more information about specific applications of Spectre analyses, see *The Designer's Guide to SPICE & Spectre*¹.

Typographic and Syntax Conventions

This list describes the syntax conventions used for the Spectre circuit simulator.

literal Nonitalic words indicate keywords that you must enter literally. These keywords represent command (function, routine) or option names, file names and paths, and any other sort of type-in commands.

argument Words in italics indicate user-defined arguments for which you must substitute a name or a value. (The characters before the underscore (`_`) in the word indicate the data types that this argument can take. Names are case sensitive.

|Vertical bars (OR-bars) separate possible choices for a single argument. They take precedence over any other character.

[] Brackets denote optional arguments. When used with OR-bars, they enclose a list of choices. You can choose one argument from the list.

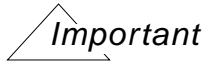
{ } Braces are used with OR-bars and enclose a list of choices. You must choose one argument from the list.

. . . Three dots (...) indicate that you can repeat the previous argument. If you use them with brackets, you can specify zero or more arguments. If they are used without brackets, you must specify at least one argument, but you can specify more.

1. Kundert, Kenneth S. *The Designer's Guide to SPICE & Spectre*. Boston: Kluwer Academic Publishers, 1995.

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The language requires many characters not included in the preceding list. You must enter required characters exactly as shown.

References

Text within brackets ([]) are references. See Appendix A, “References,” for more detailed information.

Introducing the Spectre Circuit Simulator

This chapter discusses the following:

- [Improvements over SPICE](#) on page 12
- [Analog HDLs](#) on page 16
- [RF Capabilities](#) on page 17
- [Mixed-Signal Simulation](#) on page 19
- [Environments](#) on page 19

The Spectre[®] circuit simulator is a modern circuit simulator that uses direct methods to simulate analog and digital circuits at the differential equation level. The basic capabilities of the Spectre circuit simulator are similar in function and application to SPICE, but the Spectre circuit simulator is not descended from SPICE. The Spectre and SPICE simulators use the same basic algorithms—such as implicit integration methods, Newton-Raphson, and direct matrix solution—but every algorithm is newly implemented. Spectre algorithms, the best currently available, give you an improved simulator that is faster, more accurate, more reliable, and more flexible than previous SPICE-like simulators.

Improvements over SPICE

The Spectre circuit simulator has many improvements over SPICE.

Improved Capacity

The Spectre circuit simulator can simulate larger circuits than other simulators because its convergence algorithms are effective with large circuits, because it is fast, and because it is frugal with memory and uses dynamic memory allocation. For large circuits, the Spectre circuit simulator typically uses less than half as much memory as SPICE.

Improved Accuracy

Improved component models and core simulator algorithms make the Spectre circuit simulator more accurate than other simulators. These features improve Spectre accuracy:

- Advanced metal oxide semiconductor (MOS) and bipolar models

- The Spectre BSIM 3v3 is a physics-based metal-oxide semiconductor field effect transistor (MOSFET) model for simulating analog circuits.
- The Spectre models include the MOS0 model, which is even simpler and faster than MOS1 for simulating noncritical MOS transistors in logic circuits and behavioral models, MOS 9, EKV, BTA-HVMOS, BTA-SOI, VBIC95, TOM2, and HBT.

- Charge-conserving models

The capacitance-based nonlinear MOS capacitor models used in many SPICE derivatives can create or destroy small amounts of charge on every time step. The Spectre circuit simulator avoids this problem because all Spectre models are charge-conserving.

- Improved Fourier analyzer

The Spectre circuit simulator includes a two-channel Fourier analyzer that is similar in application to the SPICE `.FOURIER` statement but is more accurate. The Spectre simulator's Fourier analyzer has greater resolution for measuring small distortion products on a large sinusoidal signal. Resolution is normally greater than 120 dB. Furthermore, the Spectre simulator's Fourier analyzer is not subject to aliasing, a common error in Fourier analysis. As a result, the Spectre simulator can accurately compute the Fourier coefficients of highly discontinuous waveforms.

- Better control of numerical error

Many algorithms in the Spectre circuit simulator are superior to their SPICE counterparts in avoiding known sources of numerical error. The Spectre circuit simulator improves the control of local truncation error in the transient analysis by controlling error in the voltage rather than the charge.

In addition, the Spectre circuit simulator directly checks Kirchhoff's Current Law (also known as Kirchhoff's Flow Law) at each time step, improves the charge-conservation accuracy of the Spectre circuit simulator, and eliminates the possibility of false convergence.

- Superior time-step control algorithm

The Spectre circuit simulator provides an adaptive time-step control algorithm that reliably follows rapid changes in the solution waveforms. It does so without limiting assumptions about the type of circuit or the magnitude of the signals.

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- More accurate simulation techniques

Techniques that reduce reliability or accuracy, such as device bypass, simplified models, or relaxation methods, are not used in the Spectre circuit simulator.

- User control of accuracy tolerances

For some simulations, you might want to sacrifice some degree of accuracy to improve the simulation speed. For other simulations, you might accept a slower simulation to achieve greater accuracy. With the Spectre circuit simulator, you can make such adjustments easily by setting a single parameter.

Improved Speed

The Spectre circuit simulator is designed to improve simulation speed. The Spectre circuit simulator improves speed by increasing the efficiency of the simulator rather than by sacrificing accuracy.

- Faster simulation of small circuits

The average Spectre simulation time for small circuits is typically two to three times faster than SPICE. The Spectre circuit simulator can be over 10 times faster than SPICE when SPICE is hampered by discontinuity in the models or problems in the code. Occasionally, the Spectre circuit simulator is slower when it finds ringing or oscillation that goes unnoticed by SPICE. This can be improved by setting the `macromodels` option to `yes`.

- Faster simulation for large circuits

The Spectre circuit simulator is generally two to five times faster than SPICE with large circuits because it has fewer convergence difficulties and because it rapidly factors and solves large sparse matrices.

Improved Reliability

The Spectre circuit simulator offers you the following improvements in reliability:

- Improved convergence

Spectre proprietary algorithms ensure convergence of the Newton-Raphson algorithm in the DC analysis. The Spectre circuit simulator virtually eliminates the convergence problems that earlier simulators had with transient simulation.

- Helpful error and warning messages

The Spectre circuit simulator detects and notifies you of many conditions that are likely to be errors. For example, the Spectre circuit simulator warns of models used in

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forbidden operating regions, of incorrectly wired circuits, and of erroneous component parameter values. By identifying such common errors, the Spectre circuit simulator saves you the time required to find these errors with other simulators.

The Spectre circuit simulator lets you define soft parameter limits and sends you warnings if parameters exceed these limits.

- **Thorough testing**

Automated tests, which include over 1,000 test circuits, are constantly run on all hardware platforms to ensure that the Spectre circuit simulator is consistently reliable and accurate.

- **Benchmark suite**

There is an independent collection of SPICE netlists that are difficult to simulate. You can obtain these circuits from the Microelectronics Center of North Carolina (MCNC) if you have File Transfer Protocol (FTP) access on the Internet. You can also get information about the performance of several simulators with these circuits.

The Spectre circuit simulator has successfully simulated all of these circuits. Sometimes the netlists required minor syntax corrections, such as inserting balance parentheses, but circuits were never altered, and options were never changed to affect convergence.

Improved Models

The Spectre circuit simulator has MOSFET Level 0–3, BSIM1, BSIM2, BSIM3, BSIM 3v3, EKV, MOS9, JFET, TOM2, GaAs MESFET, BJT, VBIC, HBT, diode, and many other models. It also includes the temperature effects, noise, and MOSFET intrinsic capacitance models.

The Spectre Compiled Model Interface (CMI) option lets you integrate new devices into the Spectre simulator using a very powerful, efficient, and flexible C language interface. This CMI option, the same one used by Spectre developers, lets you install proprietary models.

Spectre Usability Features and Customer Service

The following features and services help you use the Spectre circuit simulator easily and efficiently:

- You can use Spectre soft limits to catch errors created by typing mistakes.
- Spectre diagnosis mode, available as an options statement parameter, gives you information to help diagnose convergence problems.

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- You can run the Spectre circuit simulator standalone or run it under the Cadence analog design environment. To see how the Spectre circuit simulator is run under the analog design environment, read the [Cadence Analog Design Environment User Guide](#). You can also run the Spectre circuit simulator in the Composer-to-Spectre direct simulation environment. The environment provides a graphical user interface for running the simulation.
- The Spectre circuit simulator gives you an online help system. With this system, you can find information about any parameter associated with any Spectre component or analysis. You can also find articles on other topics that are important to use the Spectre circuit simulator effectively.
- The Spectre circuit simulator also includes a waveform display tool, Analog Waveform Display (AWD), to use to display simulation results. For more information about AWD, see the [Analog Waveform User Guide](#).
- If you experience a stubborn convergence or accuracy problem, you can send the circuit to Customer Support to get help with the simulation. For current phone numbers and e-mail addresses, see the following web site: <http://sourcelink.cadence.com/supportcontacts.html>.

Analog HDLs

The Spectre circuit simulator works with two analog high-level description languages (AHDLS): SpectreHDL and Verilog®-A. These languages are part of the Spectre Verilog-A Simulation option. SpectreHDL is proprietary to Cadence and is provided for backward compatibility. The Verilog-A language is an open standard, which was based upon SpectreHDL. The Verilog-A language is preferred because it is upward compatible with Verilog-AMS, a powerful and industry-standard mixed-signal language.

Both languages use functional description text files (modules) to model the behavior of electrical circuits and other systems. Each programming language allows you to create your own models by simply writing down the equations. The AHDLS lets you describe models in a simple and natural manner. This is a higher level modeling language than previous modeling languages, and you can use it without being concerned about the complexities of the simulator or the simulator algorithms. In addition, you can combine AHDLS components with Spectre built-in primitives.

Both languages let designers of analog systems and integrated circuits create and use modules that encapsulate high-level behavioral descriptions of systems and components. The behavior of each module is described mathematically in terms of its terminals and external parameters applied to the module. Designers can use these behavioral descriptions in many disciplines (electrical, mechanical, optical, and so on).

Both languages borrow many constructs from Verilog and the C programming language. These features are combined with a minimum number of special constructs for behavioral simulation. These high-level constructs make it easier for designers to use a high-level description language for the first time.

RF Capabilities

SpectreRF adds several new analyses that support the efficient calculation of the operating point, transfer function, noise, and distortion of common analog and RF communication circuits, such as mixers, oscillators, sample and holds, and switched-capacitor filters.

SpectreRF adds four types of analyses to the Spectre simulator. The first is periodic steady-state (PSS) analysis, a large-signal analysis that directly computes the periodic steady-state response of a circuit. With PSS, simulation times are independent of the time constants of the circuit, so PSS can quickly compute the steady-state response of circuits with long time constants, such as high-Q filters and oscillators.

You can also embed a PSS analysis in a sweep loop (referred to as an SPSS analysis in the Cadence analog design environment), which allows you to easily determine harmonic levels as a function of input level or frequency, making it easy to measure compression points, intercept points, and voltage-controlled oscillator (VCO) linearity.

The second new type of analysis is the periodic small-signal analysis. After completing a PSS analysis, SpectreRF can predict the small-signal transfer functions and noise of frequency translation circuits, such as mixers or periodically driven circuits such as oscillators or switched-capacitor or switched-current filters. The periodic small-signal analyses—periodic AC (PAC) analysis, periodic transfer function (PXF) analysis, and periodic noise (Pnoise) analysis—are similar to Spectre's AC, XF, and Noise analyses, but the traditional small-signal analyses are limited to circuits with DC operating points. The periodic small-signal analyses can be applied to circuits with periodic operating points, such as the following:

- Mixers
- VCOs
- Switched-current filters
- Phase/frequency detectors
- Frequency multipliers
- Chopper-stabilized amplifiers
- Oscillators

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- Switched-capacitor filters
- Sample and holds
- Frequency dividers
- Narrow-band active circuits

The third SpectreRF addition to Spectre functionality is periodic distortion (PDISTO) analysis. PDISTO analysis directly computes the steady-state response of a circuit driven with a large periodic signal, such as an LO (local oscillation) or a clock, and one or more tones with moderate level. With PDISTO, you can model periodic distortion and include harmonic effects. PDISTO computes both a large signal, the periodic steady-state response of the circuit, and also the distortion effects of a specified number of moderate signals, including the distortion effects of the number of harmonics that you choose. This is a common scenario when trying to predict the intermodulation distortion of a mixer, amplifier, or a narrow-band filter. In this analysis, the tones can be large enough to create significant distortion, but not so large as to cause the circuit to switch or clip. The frequencies of the tones need not be periodically related to each other or to the large signal LO or clock. Thus, you can make the tone frequencies very close to each other without penalty, which allows efficient computation of intermodulation distortion of even very narrow band circuits.

The fourth analysis that SpectreRF adds to the Spectre circuit simulator is the envelope-following analysis. This analysis computes the envelope response of a circuit. The simulator automatically determines the clock period by looking through all the sources with the specified name. Envelope-following analysis is most efficient for circuits where the modulation bandwidth is orders of magnitude lower than the clock frequency. This is typically the case, for example, in circuits where the clock is the only fast varying signal and other input signals have a spectrum whose frequency range is orders of magnitude lower than the clock frequency. For another example, the down conversion of two closely placed frequencies can also generate a slow-varying modulation envelope. The analysis generates two types of output files, a voltage versus time (td) file, and an amplitude/phase versus time (fd) file for each specified harmonic of the clock fundamental.

In summary, with periodic small-signal analyses, you apply a small signal at a frequency that might not be harmonically related (noncommensurate) to the periodic response of the undriven system, the clock. This small signal is assumed to be small enough so that the circuit is unaffected by its presence.

With PDISTO, you can apply one or two additional signals at frequencies not harmonically related to the large signal, and these signals can be large enough to drive the circuit to behave nonlinearly.

For complex nonlinear circuits, hand calculation of noise or transfer function is virtually impossible. Without SpectreRF, these circuits must be breadboarded to determine their

performances. The SpectreRF simulator eliminates unnecessary breadboarding, saving time.

Mixed-Signal Simulation

You can use the Spectre circuit simulator coupled with the Verilog®-XL simulator in the Cadence analog design environment to simulate mixed analog and digital circuits efficiently. This mixed-signal simulation solution can easily handle complex designs with tens of thousands of transistors and tens of thousands of gates. The digital Verilog data can come from the digital designer as either an RTL block or gates out of synthesis.

Environments

The Spectre circuit simulator is fully integrated into the Cadence® design framework II for the Cadence analog design environment and also into the Cadence analog workbench design system. You can also use the Spectre circuit simulator by itself with several different output format options.

Assura♠ interactive verification, Dracula® distributed multi-CPU option, and Assura hierarchical physical verification produce a netlist that can be read into the Spectre circuit simulator. However, only interactive verification when used with the Cadence analog design environment automatically attaches the stimulus file. All other situations require a stimulus file as well as device models.

Spectre Command Options

This chapter lists the options you can use with the `spectre` command and gives a brief description of each. It also discusses the following topics:

- [Default Values](#) on page 24
- [Default Parameter Values](#) on page 24

The `spectre` command takes the following syntax at the command line:

```
spectre <options> <inputfile>
```

Note: The Spectre[®] circuit simulator reads default values for all the command line arguments marked with a dagger (†) from the UNIX environment variable `%S_DEFAULTS`. If you do not

<code>-help</code>	Lists command options and available components and analyses. You can use <code>-h</code> as an abbreviation of <code>-help</code> .
<code>-help <name></code>	Gives a synopsis of the device or analysis <i>name</i> . If <i>name</i> is <code>all</code> , the synopses for all components and analyses are given. You can use <code>-h</code> as an abbreviation of <code>-help</code> .
<code>-helpsort <name></code>	Gives a synopsis of the device or analysis <i>name</i> and sorts all the parameters by name. You can use <code>-hs</code> as an abbreviation of <code>-helpsort</code> .
<code>-helpfull <name></code>	Gives a full synopsis of the component or analysis <i>name</i> , including parameter types and range limits. You can use <code>-hf</code> as an abbreviation of <code>-helpfull</code> .
<code>-helpsortfull <name></code>	Gives a full synopsis of component or analysis <i>name</i> , including parameter types and range limits. Sorts all parameters by name. You can use <code>-hsf</code> as an abbreviation of <code>-helpsortfull</code> .

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Spectre Command Options

<code>-param†</code>	Does not read the file containing the suggested parameter range limits. You can use <code>-p</code> as an abbreviation of <code>-param</code> .
<code>+param <file>†</code>	Reads <i>file</i> for the suggested parameter range limits. You can use <code>+p</code> as an abbreviation of <code>+param</code> .
<code>-log†</code>	Does not copy all messages to a file. You can use <code>-l</code> as an abbreviation of <code>-log</code> .
<code>+log <file>†</code>	Copies all messages to <i>file</i> . You can use <code>+l</code> as an abbreviation of <code>+log</code> .
<code>=log <file>†</code>	Sends all messages to <i>file</i> . You can use <code>=l</code> as an abbreviation of <code>=log</code> .
<code>-raw <raw>†</code>	Puts results in a file or directory named <i>raw</i> . In <i>raw</i> , <code>%C</code> is replaced by a circuit name. You can use <code>-r</code> as an abbreviation of <code>-raw</code> .
<code>-format <fmt>†</code>	Produces raw data in the format <i>fmt</i> . You can use <code>-f</code> as an abbreviation of <code>-format</code> . Possible values for <i>fmt</i> are <code>nutbin</code> , <code>nutascii</code> , <code>wsfbin</code> , <code>wsfascii</code> , <code>psfbin</code> , <code>psfascii</code> , or <code>awb</code> .
<code>+checkpoint†</code>	Turns on the checkpoint capability. You can use <code>+cp</code> as an abbreviation of <code>+checkpoint</code> .
<code>-checkpoint†</code>	Turns off the checkpoint capability. You can use <code>-cp</code> as an abbreviation of <code>-checkpoint</code> .
<code>-recover†</code>	Does not restart the simulation, even if a checkpoint file exists. You can use <code>-rec</code> as an abbreviation of <code>-recover</code> .
<code>+recover†</code>	Restarts the simulation from a checkpoint file, if it exists. You can use <code>+rec</code> as an abbreviation of <code>+recover</code> .
<code>-cols <N>†</code>	Sets screen width in characters to <i>N</i> . You can use <code>-c</code> as an abbreviation of <code>-cols</code> . If not set, the Spectre simulator determines the screen width automatically.

Spectre Circuit Simulator Reference

Spectre Command Options

<code>-env <env></code>	Calls the Spectre simulator from the <i>env</i> simulation environment. Possible values for <i>env</i> are <code>artist2</code> , <code>artist4</code> , <code>awb</code> , <code>edge</code> , <code>opus</code> , or <code>solo</code> .
<code>-%X</code>	In quoted strings within the netlist, replaces <code>%X</code> with nothing where <i>X</i> is any uppercase or lowercase letter.
<code>+%X <string>†</code>	In quoted strings within the netlist, replaces <code>%X</code> with <i>string</i> , where <i>X</i> is an uppercase or overcase letter. You can modify the string by using the <code>:x</code> operators.
<code>+error†</code>	Prints error messages.
<code>-error†</code>	Does not print error messages.
<code>+warn†</code>	Prints warning messages.
<code>-warn†</code>	Does not print warning messages.
<code>+note</code>	Prints notices.
<code>-note</code>	Does not print notices.
<code>+info†</code>	Prints informational messages.
<code>-info†</code>	Does not print informational messages.
<code>+debug†</code>	Prints debugging messages.
<code>-debug†</code>	Does not print debugging messages.
<code>-slave <cmd></code>	Starts the attached simulator using the command <i>cmd</i> .
<code>-slvhost <hostname></code>	Runs the attached simulator on machine <i>hostname</i> . Defaults to local machine.
<code>-V</code>	Prints version information.
<code>-W</code>	Prints subversion information.
<code>-alias <name>†</code>	Gives <i>name</i> to the license manager as the name of the simulator.
<code>-E†</code>	Runs the C preprocessor on an input file. In SPICE mode, the first line in the file must be a comment.
<code>-D<x>†</code>	Defines string <i>x</i> and runs the C preprocessor.

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Spectre Command Options

<code>-D<x=y>†</code>	Defines string <i>x</i> to be <i>y</i> and runs the C preprocessor.
<code>-U<x>†</code>	Undefines string <i>x</i> and runs the C preprocessor.
<code>-I<dir>†</code>	Runs the C preprocessor and searches the directory <i>dir</i> for include files.
<code>-sppt†</code>	Do not run the Spice netlist reader on the input file.
<code>+sppt†</code>	Run the Spice netlist reader on the input file. Use <code>+spp -sppbin</code> on the command line option to read other spp binaries.
<code>-sppbin file†</code>	Specify the path to nondefault spp binary. Default provided.
<code>+sensdata <file></code>	Sends the sensitivity analyses data to <i>file</i> .
<code>-interactive</code>	Run in the noninteractive mode, that is, process the input file and then return. You can use <code>-inter</code> as an abbreviation of <code>-interactive</code> .
<code>+interactive</code>	Run in the default interactive mode. You can use <code>+inter</code> as an abbreviation of <code>+interactive</code> .
<code>+interactive=type</code>	Run in the interactive mode of the type specified. You can use <code>+inter</code> as an abbreviation of <code>+interactive</code> . Possible values for <i>type</i> are <code>skill</code> or <code>mpsc</code> .
<code>+mpssession=sessionName</code>	The <i>sessionName</i> for an interactive session using multiprocess SKILL (MPS). This option is necessary for <code>+interactive=mpsc</code> and implies <code>+interactive=mpsc</code> .
<code>+mpshost=sessionHost</code>	The <i>sessionHost</i> for an interactive session using MPS.

specify an input file, the Spectre simulator reads from standard input. When +/- pairs of spectre command options are available, the default is the first value given in the previous list. For further information about the percent code options, +% and -%, see [Chapter 11, “Managing Files,”](#) in the *Spectre Circuit Simulator User Guide*.

Note: To remain consistent with the C preprocessor, there is no space between the preprocessor flags (D, U, I) and their arguments. The C preprocessor is available on UNIX

Spectre Circuit Simulator Reference

Spectre Command Options

systems only and requires that the first line of the file (the SPICE title line) begin with a comment character (* or //).

Default Values

The Spectre simulator reads default values for all the command line arguments marked with a dagger (†) from the UNIX environment variable %S_DEFAULTS. The name of the simulator as called replaces %S. Typically, this name is `spectre`, and the Spectre simulator looks for `spectre_DEFAULTS`. However, the name can be different if you move the executable to a file with a different name or if you call the Spectre simulator through a symbolic or hard link with a different name. This feature lets you set different default values for each name you use to call the Spectre simulator.

If the variable %S_DEFAULTS does not exist, SPECTRE_DEFAULTS is used instead. The command line arguments always override any specifications from the `options` statement in the circuit file. The `options` statement specifications, in turn, override any specifications in the environment variable.

Default Parameter Values

Many Spectre parameters have default values, and sometimes you will need to know them so you can determine whether they are acceptable for your simulation. You can find the default values for component, analysis, and control statement parameters by consulting the documentation for the statement in Spectre online help (`spectre -h`). Values given for parameters in the online help are the default values.

The following examples show you some defaults for different types of parameters from the Spectre online help:

`nf=1.0`Forward emission coefficient

`etchc=etch m`Narrowing due to etching for capacitances

`homotopy=all`Method used when there is no convergence on initial attempt of DC analysis;
possible values are `none`, `gmin`, `source'` `dptran`, `ptran`, or
`all`

`rawfile="%C:r.raw"`

Output raw data filename

Spectre Circuit Simulator Reference

Spectre Command Options

In this example, the default values for `nf`, `etchc`, `homotopy`, and `rawfile` are a real number (1.0), the value of a different parameter (`etch`), an enumerated type (`all`), and a character string with a percent code and a colon modifier that gives Spectre instructions for creating the output filename ("`%C:r.raw`").

For more information about percent codes and colon modifiers, see ["Description of Spectre Predefined Percent Codes," "Customizing Percent Codes," and "Creating Filenames from Parts of Input Filenames"](#) in the *Spectre Circuit Simulator User Guide*.

Component Statements Part 1

This chapter discusses the following topics:

- [Analog-to-Logic Converter \(a2d\)](#) on page 28
- [B3SOI-PD Transistor \(b3soipd\)](#) on page 28
- [Bipolar Junction Transistor \(bjt\)](#) on page 49
- [Lateral PNP Transistor \(bjt301\)](#) on page 60
- [Lateral PNP Transistor \(bjt500\)](#) on page 67
- [Vertical NPN/PNP Transistor \(bjt503\)](#) on page 77
- [Vertical NPN/PNP Transistor \(bjt504\)](#) on page 87
- [BSIM1 Field Effect Transistor \(bsim1\)](#) on page 104
- [BSIM2 Field Effect Transistor \(bsim2\)](#) on page 123
- [BSIM3 MOS Transistor \(bsim3\)](#) on page 147
- [BSIM3v3 MOS Transistor \(bsim3v3\)](#) on page 160
- [BSIM4 MOS Transistor \(bsim4\)](#) on page 178
- [BTA SOI Transistor \(btasoi\)](#) on page 202
- [Two Terminal Capacitor \(capacitor\)](#) on page 220
- [Linear Current Controlled Current Source \(cccs\)](#) on page 224
- [Linear Current Controlled Voltage Source \(ccvs\)](#) on page 225
- [Circuit Reduced Order Model \(cktrom\)](#) on page 226
- [Magnetic Core with Hysteresis \(core\)](#) on page 227
- [Logic-to-Analog Converter \(d2a\)](#) on page 231
- [Delay Line \(delay\)](#) on page 233

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Component Statements Part 1

- Diode Level 500 (dio500) on page 233
- Junction Diode (diode) on page 237
- EKV MOSFET Transistor (ekv) on page 243
- Ratiometric Fourier Analyzer (fourier) on page 256
- GaAs MESFET (gaas) on page 259

The rest of the component statements are in Chapter 4, “Component Statements Part 2.”

To examine the equations used for some of these components, consult the *Spectre Circuit Simulator Device Model Equations* manual.

Analog-to-Logic Converter (a2d)

Description

The analog-to-logic converter transfers analog waveforms to a logic simulator.

This device is not supported within altergroup.

To examine the equations used for this component, consult the [Spectre Circuit Simulator Device Model Equations](#) manual.

Sample Instance Statement

```
da99 (cmp_out 0) a2d dest="99991" vl=0 vh=5 timex=200u
// 99991 is a digital net in the verilog netlist.
```

Instance Definition

Name p n a2d parameter=value ...

Instance Parameters

- 1 `dest` The foreign simulator name for the destination of the signal.
- 2 `nestlev=0` Number of nesting levels to ignore in the hierarchical name. This should be used skip over extra levels that do not exist in the co-simulator.
- 3 `vl=0` Voltages below this will be logical 0.
- 4 `vh=5` Voltages above this will be logical 1.
- 5 `timex=1` Time signal can linger between `vl` and `vh` before the state becomes X.

B3SOI-PD Transistor (b3soipd)

Description

B3SOI is an SOI model developed by U.C. Berkeley based on bsim3v3. B3SOI devices require that you use a model statement. This is the B3SOI version-2.2 model

Spectre Circuit Simulator Reference

Component Statements Part 1

This device is not supported within altergroup.

Instance Definition

Name d g s e [p] [b] [t] ModelName parameter=value ...

Instance Parameters

- 1 w (m) Channel width.
- 2 l (m) Channel length.
- 3 as (m²) Area of source diffusion.
- 4 ad (m²) Area of drain diffusion.
- 5 ps (m) Perimeter of source diffusion.
- 6 pd (m) Perimeter of drain diffusion.
- 7 nrd (m/m) Number of squares of drain diffusion.
- 8 nrs (m/m) Number of squares of source diffusion.
- 9 nrb (m/m) Number of body squares.
- 10 m=1 Multiplicity factor (number of MOSFETs in parallel).
- 11 region=triode Estimated operating region.
Possible values are off, triode, sat, or subth.
- 12 rth0 (Ω) Thermal resistance.
- 13 cth0 (F) Thermal capacitance.
- 14 bjtoff=0 BJT off flag.
- 15 nbc=0 m/m Number of body contact isolation edge.
- 16 nseg=1 m/m Number of segments for channel width partitioning.
- 17 pdbcpr=0 m Perimeter length for body contact parasitic at drain.

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Component Statements Part 1

18 `psbcp=0` `m`Perimeter length for body contact parasitic at source.

19 `agbcp=0` `m`Gate to body overlap for body contact parasitic.

20 `aebcp=0` `m`Gate to body overlap for body contact parasitic.

21 `vbsusr=0.0` `v`Optional initial value of `Vbs` for transient.

22 `tnodeout=0` Temperature node flag associated with `T` node.

Model Definition

`model modelName b3soipd parameter=value ...`

Model Parameters

Device Type Parameters

1 `type=n` Transistor type.
Possible values are `n` or `p`.

2 `version=2.2` Model version selector.

Threshold Voltage Parameters

3 `vtho (V)` Threshold voltage at zero body bias for long-channel devices. For enhancement-mode devices, `vtho > 0` for n-channel and `vth < 0` for p-channel. Default value is calculated from other model parameters.

4 `k1=0.5` \sqrt{V} Body-effect coefficient.

5 `k1w1=0.0` `m`First body effect width dependent parameter.

6 `k1w2=0.0` `m`Second body effect width dependent parameter.

7 `k2=-0.0186` Charge-sharing parameter.

8 `k3=0` Narrow width coefficient.

9 `k3b=0` $1/V$ Narrow width coefficient.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 10 $w_0=2.5e-6$ mNarrow width coefficient.
- 11 $nlx=1.74e-7$ mLateral nonuniform doping coefficient.
- 12 γ_1 (\sqrt{V}) Body-effect coefficient near the surface.
- 13 γ_2 (\sqrt{V}) Body-effect coefficient in the bulk.
- 14 v_{bx} (V) Threshold voltage transition body voltage.
- 15 $v_{bm}=-3$ VMaximum applied body voltage.
- 16 $dvt_0=2.2$ First coefficient of short-channel effects.
- 17 $dvt_1=0.53$ Second coefficient of short-channel effects.
- 18 $dvt_2=-0.032$ $1/V$ Body-bias coefficient of short-channel effects.
- 19 $dvt_0w=0$ First coefficient of narrow-width effects.
- 20 $dvt_1w=5.3e6$ Second coefficient of narrow-width effects.
- 21 $dvt_2w=-0.032$ $1/V$ Body-bias coefficient of narrow-width effects.
- 22 $a_0=1$ Nonuniform depletion width effect coefficient.
- 23 $b_0=0$ mBulk charge coefficient due to narrow width effect.
- 24 $b_1=0$ mBulk charge coefficient due to narrow width effect.
- 25 $a_1=0$ No-saturation coefficient.
- 26 $a_2=1$ No-saturation coefficient.
- 27 $ags=0$ F/m^2 VGate-bias dependence of a_{bulk} .
- 28 $keta=-0.6$ $1/V$ Body-bias coefficient for non-uniform depletion width effect.
- 29 $ketas=0.0$ VSurface Potential adjustment for bulk charge effect.

Process Parameters

- 30 $n_{sub}=6e16$ cm^{-3} Substrate doping concentration.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 31 `nch=1.7e17 cm-3` Peak channel doping concentration.
- 32 `ngate (cm-3)` Poly-gate doping concentration.
- 33 `xj=0.15e-6 m` Source/drain junction depth.
- 34 `lint=0 m` Lateral diffusion for one side.
- 35 `wint=0 m` Width reduction for one side.
- 36 `ll=0 m` Length dependence of delta L.
- 37 `lln=1` Length exponent of delta L.
- 38 `lw=0 m` Width dependence of delta L.
- 39 `lwn=1` Width exponent of delta L.
- 40 `lwl=0 m2` Area dependence of delta L.
- 41 `wl=0 m` Length dependence of delta W.
- 42 `wln=1` Length exponent of delta W.
- 43 `ww=0 m` Width dependence of delta W.
- 44 `wwn=1` Width exponent of delta W.
- 45 `wwl=0 m2` Area dependence of delta W.
- 46 `dwg=0 m/v` Gate-bias dependence of channel width.
- 47 `dwb=0 m/√v` Body-bias dependence of channel width.
- 48 `dwbcb=0.0 m` Width offset for body contact isolation edge.
- 49 `tox=1e-8 m` Gate oxide thickness.
- 50 `tbox=3e-7 m` Buried oxide thickness.
- 51 `tsi=1e-7 m` Silicon film thickness.
- 52 `xt=1.55e-7 m` Doping depth.

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Component Statements Part 1

- 53 `rdsw=100 Ω μm` Width dependence of drain-source resistance.
- 54 `prwb=0 $1/\sqrt{v}$` Body-effect coefficient for R_{ds} .
- 55 `prwg=0 $1/v$` Gate-effect coefficient for R_{ds} .
- 56 `wr=1`Width offset for parasitic resistance.
- 57 `xl=0 m` Length variation due to masking and etching.
- 58 `xw=0 m` Width variation due to masking and etching.
- 59 `binunit=1`Bin parameter unit selector. 1 for microns and 2 for meters.

Mobility Parameters

- 60 `mobmod=1`Mobility model selector.
- 61 `u0=670 cm^2/V s` Low-field surface mobility at t_{nom} . Default is 250 for PMOS.
- 62 `vsat=8e4 m/s` Carrier saturation velocity at t_{nom} .
- 63 `ua=2.25e-9 m/v` First-order mobility reduction coefficient.
- 64 `ub=5.87e-19 m^2/v^2`
Second-order mobility reduction coefficient.
- 65 `uc=-4.65e-11 m/v^2`
Body-bias dependence of mobility. Default is -0.046 and unit is $1/\text{V}$ for `mobmod=3`.

Output Resistance Parameters

- 66 `drout=0.56`DIBL effect on output resistance coefficient.
- 67 `pclm=1.3`Channel length modulation coefficient.
- 68 `pdiblc1=0.39`First coefficient of drain-induced barrier lowering.
- 69 `pdiblc2=8.6e-3`Second coefficient of drain-induced barrier lowering.
- 70 `pdiblc b=0 $1/\text{v}$` Body-effect coefficient for DIBL.

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Component Statements Part 1

71 `pvag=0` Gate dependence of Early voltage.

72 `delta=0.01` v Effective drain voltage smoothing parameter.

Subthreshold Parameters

73 `cdsc=2.4e-4` F/m^2 Source/drain and channel coupling capacitance.

74 `cdscb=0` F/m^2 v Body-bias dependence of `cdsc`.

75 `cdscd=0` F/m^2 v Drain-bias dependence of `cdsc`.

76 `nfactor=1` Subthreshold swing coefficient.

77 `cit=0` F Interface trap parameter for subthreshold swing.

78 `voff=-0.08` v Threshold voltage offset.

79 `dsub=drout` DIBL effect in subthreshold region.

80 `eta0=0.08` DIBL coefficient subthreshold region.

81 `etab=-0.07` $1/v$ Body-bias dependence of `et0`.

Substrate Current Parameters

82 `alpha0=0` m/v Substrate current impact ionization coefficient.

83 `beta0=0` $1/v$ First V_{ds} dependent parameter of impact ionization current.

84 `fbjti=0.0` Fraction of bipolar current affecting the impact ionization.

85 `beta1=0` Second V_{ds} dependent parameter of impact ionization current.

86 `beta2=0` v Third V_{ds} dependent parameter of impact ionization current.

87 `vdsatii0=0.9` v Nominal drain saturation voltage at threshold for impact ionization current.

88 `tii=0` Temperature dependent parameter for impact ionization current.

89 `lii=0` Channel length dependent parameter at threshold for impact ionization current.

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Component Statements Part 1

- 90 `esatii=1e7 V/m` Saturation channel electric field for impact ionization current.
- 91 `sii0=0.5 1/V` First V_{gs} dependent parameter for impact ionization current.
- 92 `sii1=0.1 1/V` Second V_{gs} dependent parameter for impact ionization current.
- 93 `sii2=0.0 1/V` Third V_{gs} dependent parameter for impact ionization current.
- 94 `siid=0 1/V` V_{ds} dependent parameter of drain saturation voltage for impact ionization current.

Parasitic Resistance Parameters

- 95 `rbsh=0 Ω` Extrinsic body contact sheet resistance.
- 96 `rsh=0 Ω/sqr` Source/drain diffusion sheet resistance.
- 97 `rs=0 Ω` Source resistance.
- 98 `rd=0 Ω` Drain resistance.
- 99 `rbody=0 F` Body resistance.
- 100 `rsc=0 Ω` Source contact resistance.
- 101 `rdc=0 Ω` Drain contact resistance.
- 102 `rss=0 Ω m` Scalable source resistance.
- 103 `rdd=0 Ω m` Scalable drain resistance.
- 104 `hdif=0 m` Length of heavily doped diffusion.
- 105 `ldif=0 m` Lateral diffusion beyond the gate.
- 106 `minr=0.1 Ω` Minimum source/drain resistance.

Junction Diode Model Parameters

- 107 `dskip=yes` Use simple piece-wise linear model for diode currents below $0.1 \cdot i_{abstol}$.
Possible values are `no` or `yes`.

Spectre Circuit Simulator Reference

Component Statements Part 1

108 `imelt=`imaxA`` Explosion current.

Overlap Capacitance Parameters

109 `cgs0` (F/m) Gate-source overlap capacitance.

110 `cgd0` (F/m) Gate-drain overlap capacitance.

111 `cgeo=0.0` F/m Gate-substrate overlap capacitance.

112 `cgb0=2 Dwc Cox` F/m
Gate-bulk overlap capacitance. The default value is 0 if
version=3.0.

113 `meto=0 m` Metal overlap in fringing field.

114 `cgs1=0` F/m Gate-source overlap capacitance in LDD region.

115 `cgd1=0` F/m Gate-drain overlap capacitance in LDD region.

116 `ckappa=0.6` Overlap capacitance fitting parameter.

Junction Capacitance Model Parameters

117 `cjswg=cjsw` F/m Zero-bias gate-side junction capacitance density.

118 `mjswg=0.5` Gate-side junction grading coefficient.

119 `pbswg=0.7 v` Gate-side junction built-in potential.

120 `tt=1e-12 s` Transit time.

121 `ndif=1` Power coefficient of channel length dependency for diffusion capacitance.

122 `ldif0=1` Power coefficient of channel length dependency for diffusion capacitance.

Charge Model Selection Parameters

123 `capmod=2` Intrinsic charge model.

124 `dwc=wint m` Delta W for capacitance model.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 125 `delvt=0.0` v Threshold voltage adjustment for C-V.
- 126 `fbody=1.0` Scaling factor for body charge.
- 127 `dlc=lint` m Delta L for capacitance model.
- 128 `dlcb=lint` m Length offset fitting parameter for body charge.
- 129 `dlbg=0.0` m Length offset fitting parameter for backgate charge.
- 130 `clc=1e-8` m Intrinsic capacitance fitting parameter.
- 131 `cle=0.0` Intrinsic capacitance fitting parameter.
- 132 `cf` (F/m) Fringe capacitance parameter.
- 133 `vfbcv=-1` Flat-band voltage for capmod=0.
- 134 `xpart=0` Drain/source channel charge partition in saturation for BSIM charge model, use 0.0 for 40/60, 0.5 for 50/50, or 1.0 for 0/100.

Default Instance Parameters

- 135 `w=5e-6` m Default channel width.
- 136 `l=5e-6` m Default channel length.
- 137 `as=0` m^2 Default area of source diffusion.
- 138 `ad=0` m^2 Default area of drain diffusion.
- 139 `ps=0` m Default perimeter of source diffusion.
- 140 `pd=0` m Default perimeter of drain diffusion.
- 141 `nrd=0` m/m Default number of squares of drain diffusion.
- 142 `nrs=0` m/m Default number of squares of source diffusion.
- 143 `nrb=0` m/m Default body squares.

Spectre Circuit Simulator Reference

Component Statements Part 1

Temperature Effects Parameters

- 144 `tnom` (C) Parameters measurement temperature. Default set by `options`.
- 145 `tmax=500` cMaximum device temperature above ambient.
- 146 `shmod=0` Self-heating selector.
- 147 `tlev=0` DC temperature selector.
- 148 `tlevc=0` AC temperature selector.
- 149 `eg=1.12452` vEnergy band gap.
- 150 `gap1=7.02e-4` V/CBand gap temperature coefficient.
- 151 `gap2=1108` cBand gap temperature offset.
- 152 `kt1=-0.11` vTemperature coefficient for threshold voltage.
- 153 `kt1l=0` v mTemperature coefficient for threshold voltage.
- 154 `kt2=0.022`Temperature coefficient for threshold voltage.
- 155 `at=3.3e4` m/sTemperature coefficient for `vsat`.
- 156 `tcjswg=0` 1/KTemperature coefficient of `Cjswg`.
- 157 `tpbswg=0` V/KTemperature coefficient of `Pbswg`.
- 158 `ua1=4.31e-9` m/vTemperature coefficient for `ua`.
- 159 `ub1=-7.61e-18` m²/v²
Temperature coefficient for `ub`.
- 160 `uc1=-5.5e-11` m/v²
Temperature coefficient for `uc`. Default is -0.056 for `mobmod=3`.
- 161 `prt=0` ΩTemperature coefficient for `Rds`.
- 162 `trs=0` 1/CTemperature parameter for source resistance.
- 163 `trd=0` 1/CTemperature parameter for drain resistance.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 164 `ute=-1.5` Mobility temperature exponent.
- 165 `dt1=0` First temperature coefficient for tau.
- 166 `dt2=0` Second temperature coefficient for tau.
- 167 `cth0=0 F` Self-heating thermal capacitance.
- 168 `rth0=0 Ω` Self-heating thermal resistance.
- 169 `ntrecf=0` Temperature coefficient of Ntrecf.
- 170 `ntreocr=0` Temperature coefficient of Ntreocr.
- 171 `xbjt=2` BJT current temperature exponent.
- 172 `xdif=2` Diffusion current temperature exponent.
- 173 `xrec=20` Recombination current temperature exponent.
- 174 `xtun=0` Tunneling current temperature exponent.

Noise Model Parameters

- 175 `noimod=1` Noise model selector.
- 176 `kf=0` Flicker (1/f) noise coefficient.
- 177 `af=1` Flicker (1/f) noise exponent.
- 178 `ef=1` Flicker (1/f) noise frequency exponent.
- 179 `noia=1e20` Oxide trap density coefficient. Default is 9.9e18 for pmos.
- 180 `noib=5e4` Oxide trap density coefficient. Default is 2.4e3 for pmos.
- 181 `noic=-1.4e-12` Oxide trap density coefficient. Default is 1.4e-8 for pmos.
- 182 `em=4.1e7 V/m` Maximum electric field.

Spectre Circuit Simulator Reference

Component Statements Part 1

Auto Model Selector Parameters

183 `wmax=1` `m`Maximum channel width for which the model is valid.

184 `wmin=0` `m`Minimum channel width for which the model is valid.

185 `lmax=1` `m`Maximum channel length for which the model is valid.

186 `lmin=0` `m`Minimum channel length for which the model is valid.

Operating Region Warning Control Parameters

187 `alarm=none`Forbidden operating region.
Possible values are `none`, `off`, `triode`, `sat`, `subth`, or `rev`.

188 `imax=1` `A`Maximum allowable current.

189 `bvj= ∞` `v`Junction reverse breakdown voltage.

190 `vbox=1e9` `tox` `v`Oxide breakdown voltage.

191 `warn=on`Parameter to turn warnings on and off.
Possible values are `off` or `on`.

SOI Specific Parameters

192 `vbsa=0` `v``Vbs0t` offset voltage.

193 `delP=0.02`Offset constant for limiting `Vbseff` to `Phis`.

194 `kb1=1`Backgate coupling coefficient at strong inversion.

195 `kb3=1`Backgate coupling coefficient at subthreshold.

196 `dvbd0=0` `v`First coefficient of short-channel effect on `Vbs0t`.

197 `dvbd1=0`First coefficient of short-channel effect on `Vbs0t`.

198 `abp=1`Gate bias coefficient for `Xcsat` calculation.

199 `mxc=-0.9``A` smoothing parameter for `Xcsat` calculation.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 200 `agidl=0` GIDL constant.
- 201 `bgidl=0` V/m GIDL exponential coefficient.
- 202 `ngidl=1.2` V GIDL Vds enhancement coefficient.
- 203 `ntun=10` Reverse tunneling non-ideality factor.
- 204 `nrecf0=2.0` Recombination non-ideality factor at forward bias.
- 205 `nrecr0=10` Recombination non-ideality factor at reversed bias.
- 206 `vsdfb` (F/m) Source/Drain diffusion flatband voltage.
- 207 `vsdth` Source/Drain diffusion threshold voltage.
- 208 `csdmin` (F) Source/Drain diffusion bottom minimum capacitance.
- 209 `csdesw=0` Source/drain sidewall fringing constant.
- 210 `aii=0` First parameter for critical field.
- 211 `bii=0` Second parameter for critical field.
- 212 `cii=0` Gate dependence of critical field.
- 213 `dii=-1` Body dependence of critical field.
- 214 `ndiode=1` Diode non-ideality factor.
- 215 `asd=0.3` Source/Drain diffusion smoothing parameter.
- 216 `isbjt=1e-6` A BJT saturation current.
- 217 `isdif=0` A Diffusion saturation current.
- 218 `isrec=1e-5` A Recombination saturation current.
- 219 `istun=0` A Tunneling saturation current.
- 220 `ln=2e-6` m Electron diffusion length.
- 221 `vrec0=0` V Voltage dependent parameter for recombination current.

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Component Statements Part 1

222 `vtun0=0` v Voltage dependent parameter for tunneling current.

223 `nbt=1` Power coefficient of channel length dependency for bipolar current.

224 `lbt0=0.20e-6` m Reference channel length for bipolar current.

225 `vabt=10` v Early voltage for bipolar current.

226 `aely=0` v Channel length dependency of early voltage for bipolar current.

227 `ahli=0` High level injection parameter for bipolar current.

228 `kbt1=0` m Parasitic bipolar base width.

Gate Tunneling Parameters

229 `wth0=0.0` μm Minimum width for thermal resistance calculation..

230 `rhalo=1.0e15` Ω/sqr
Body halo sheet resistance.

231 `ntox=1.0` Body halo sheet resistance.

232 `toxref=2.5e-9` m Target oxide thickness.

233 `ebg=1.2` v Effective bandgap in gate current calculation.

234 `nevb=3.0` Valence-band electron non-ideality factor.

235 `alphagb1=0.35` First V_{ox} dependent parameter for gate current in inversion..

236 `betagb1=0.03` Second V_{ox} dependent parameter for gate current in inversion..

237 `vgb1=300` Third V_{ox} dependent parameter for gate current in inversion..

238 `alphagb2=0.43` First V_{ox} dependent parameter for gate current in accumulation..

239 `betagb2=0.05` Second V_{ox} dependent parameter for gate current in accumulation..

240 `necb=1.0` Conduction-band electron non-ideality factor.

241 `vgb2=17` Third V_{ox} dependent parameter for gate current in accumulation..

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Component Statements Part 1

242 `toxqm=Tox m`Effective oxide thickness considering quantum effects..

243 `voxh=5.0 v` Limit of Vox in gate current calculation..

244 `deltavox=0.005 v` Smoothing parameter in the Vox smoothing function..

245 `igmod=0`Gate current model selector.

Cross-Term Dependent Parameters

246 `paramchk=1`Model parameter checking selector.

247 `noif=1`Floating body excess noise ideality factor.

The `jmel`t parameter is used to aid convergence and prevent numerical overflow. The junction characteristics of the FET are accurately modeled for current (density) up to `jmel`t. For current density above `jmel`t, the junction is modeled as a linear resistor and a warning is printed.

Auto Model Selection

Many models need to be characterized for different geometries in order to obtain accurate results for model development. The model selector program automatically searches for a model with the length and width range specified in the instance statement and uses this model in the simulations.

For the auto model selector program to find a specific model, the models to be searched should be grouped together within braces. Such a group is called a model group. An opening brace is required at the end of the line defining each model group. Every model in the group is given a name followed by a colon and the list of parameters. Also, the four geometric parameters `lmax`, `lmin`, `wmax`, and `wmin` should be given. The selection criteria to choose a model is as follows:

```
lmin <= inst_length < lmax and wmin <= inst_width < wmax
```

Example

```
model ModelName ModelType {  
    1:    <model parameters> lmin=2 lmax=4 wmin=1 wmax=2  
    2:    <model parameters> lmin=1 lmax=2 wmin=2 wmax=4  
    3:    <model parameters> lmin=2 lmax=4 wmin=4 wmax=6  
}
```

Then for a given instance

Spectre Circuit Simulator Reference

Component Statements Part 1

M1 1 2 3 4 ModelName w=3 l=1.5

the program would search all the models in the model group with the name `ModelName` and then pick the first model whose geometric range satisfies the selection criteria. In the preceding example, the auto model selector program would choose `ModelName.2`.

The user must specify both length (*l*) and width (*w*) on the device instance line to enable automatic model selection.

Output Parameters

- 1 `weff` (m) Effective channel width.
- 2 `leff` (m) Effective channel length.
- 3 `rtheff` (Ω) Effective thermal resistance.
- 4 `ctheff` (F) Effective thermal capacitance.
- 5 `rseff` (Ω) Effective source resistance.
- 6 `rdeff` (Ω) Effective drain resistance.

Operating-Point Parameters

- 1 `type=n` Transistor type.
Possible values are `n` or `p`.
- 2 `region=triode` Estimated operating region.
Possible values are `off`, `triode`, `sat`, or `subth`.
- 3 `reversed` Reverse mode indicator.
Possible values are `no` or `yes`.
- 4 `vgs` (V) Gate-source voltage.
- 5 `vds` (V) Drain-source voltage.
- 6 `vbs` (V) Bulk-source voltage.
- 7 `vbgs` (V) Back-Gate-source voltage.

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Component Statements Part 1

- 8 `ids` (A) Resistive drain-to-source current.
- 9 `ic` (A) BJT collector current.
- 10 `isgidl` (A) Source GIDL current.
- 11 `idgidl` (A) Drain GIDL current.
- 12 `iii` (A) Impact ionization current.
- 13 `ibd` (A) Resistive bulk-to-drain junction current.
- 14 `igbt` (A) Gate-to-body tunneling current.
- 15 `ibs` (A) Resistive bulk-to-source junction current.
- 16 `vth` (V) Threshold voltage.
- 17 `vdsat` (V) Drain-source saturation voltage.
- 18 `gm` (S) Common-source transconductance.
- 19 `gds` (S) Common-source output conductance.
- 20 `gmb` (S) Body-transconductance.
- 21 `gmbg` (S) Back-gate-transconductance.
- 22 `ueff` ($\text{cm}^2/\text{V s}$) Effective mobility.
- 23 `betaeff` (A/V^2) Effective beta.
- 24 `qg` (Coul) Gate charge.
- 25 `qd` (Coul) Drain charge.
- 26 `qs` (Coul) Source charge.
- 27 `qb` (Coul) Body charge.
- 28 `qbg` (Coul) Back-Gate charge.
- 29 `cgg` (F) dQ_g/dV_g .

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Component Statements Part 1

- 30 `cgd (F)dQg_dVd.`
- 31 `cgs (F)dQg_dVs.`
- 32 `cgb (F)dQg_dVbk .`
- 33 `cdg (F)dQd_dVg.`
- 34 `cdd (F)dQd_dVd.`
- 35 `cds (F)dQd_dVs.`
- 36 `cdb (F)dQd_dVb.`
- 37 `csd (F)dQs_dVg.`
- 38 `csd (F)dQs_dVd.`
- 39 `css (F)dQs_dVs.`
- 40 `csb (F)dQs_dVb.`
- 41 `cbg (F)dQb_dVg.`
- 42 `cbd (F)dQb_dVd.`
- 43 `cbs (F)dQb_dVs.`
- 44 `cbb (F)dQb_dVb.`
- 45 `id (A)Total resistive drain current.`
- 46 `is (A)Total resistive source current.`
- 47 `ib (A)Total resistive bulk current.`
- 48 `pwr (W)Power at op point.`
- 49 `gmoverid (1/V)Gm/Ids.`
- 50 `tdev (C)Temperature rise from ambient.`

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Component Statements Part 1

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

Ctheff	O-4	dskip	M-107	lwl	M-40	sii2	M-93
a0	M-22	dsub	M-79	lwn	M-39	siid	M-94
a1	M-25	dt1	M-165	m	I-10	tbox	M-50
a2	M-26	dt2	M-166	meto	M-113	tcjswg	M-156
abp	M-198	dvbd0	M-196	minr	M-106	tdev	OP-50
ad	I-4	dvbd1	M-197	mjswg	M-118	tii	M-88
ad	M-138	dvt0	M-16	mobmod	M-60	tlev	M-147
aebcp	I-20	dvt0w	M-19	mxcl	M-199	tlevc	M-148
aely	M-226	dvt1	M-17	nbcl	I-15	tmax	M-145
af	M-177	dvt1w	M-20	nbjt	M-223	tnodeout	I-22
agbcp	I-19	dvt2	M-18	nch	M-31	tnom	M-144
agidl	M-200	dvt2w	M-21	ndif	M-121	tox	M-49
ags	M-27	dwb	M-47	ndiode	M-214	toxqm	M-242
ahli	M-227	dwbc	M-48	necb	M-240	toxref	M-232
aia	M-210	dwc	M-124	nevb	M-234	tpbswg	M-157
alarm	M-187	dwg	M-46	nfactor	M-76	trd	M-163
alpha0	M-82	ebg	M-233	ngate	M-32	trs	M-162
alphagb1	M-235	ef	M-178	ngidl	M-202	tsi	M-51
alphagb2	M-238	eg	M-149	nlx	M-11	tt	M-120
as	M-137	em	M-182	noia	M-179	type	M-1
as	I-3	esatii	M-90	noib	M-180	type	OP-1
asd	M-215	eta0	M-80	noic	M-181	u0	M-61
at	M-155	etab	M-81	noif	M-247	ua	M-63
b0	M-23	fbjtii	M-84	noimod	M-175	ual	M-158
b1	M-24	fbody	M-126	nrb	M-143	ub	M-64
beta0	M-83	gamma1	M-12	nrb	I-9	ub1	M-159
beta1	M-85	gamma2	M-13	nrd	M-141	uc	M-65
beta2	M-86	gap1	M-150	nrd	I-7	uc1	M-160
betaeff	OP-23	gap2	M-151	nrecf0	M-204	ueff	OP-22
betagb1	M-236	gds	OP-19	nrecr0	M-205	ute	M-164
betagb2	M-239	gm	OP-18	nrs	M-142	vabjt	M-225
bgidl	M-201	gmb	OP-20	nrs	I-8	vbgs	OP-7
bii	M-211	gmbg	OP-21	nseg	I-16	vbm	M-15
binunit	M-59	gmoverid	OP-49	nsub	M-30	vbox	M-190
bjtoff	I-14	hdif	M-104	ntox	M-231	vbs	OP-6
bvj	M-189	ib	OP-47	ntrecf	M-169	vbsa	M-192
capmod	M-123	ibd	OP-13	ntrecr	M-170	vbsusr	I-21

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Component Statements Part 1

cbb	OP-44	ibs	OP-15	ntun	M-203	vbx	M-14
cbd	OP-42	ic	OP-9	paramchk	M-246	vds	OP-5
cbg	OP-41	id	OP-45	pbswg	M-119	vdsat	OP-17
cbs	OP-43	idgidl	OP-11	pclm	M-67	vdsatii0	M-87
cdb	OP-36	ids	OP-8	pd	I-6	version	M-2
cdd	OP-34	igbt	OP-14	pd	M-140	vfbcv	M-133
cdg	OP-33	igmod	M-245	pdbcp	I-17	vgb1	M-237
cds	OP-35	iii	OP-12	pdiblc1	M-68	vgb2	M-241
cdsc	M-73	imax	M-188	pdiblc2	M-69	vgs	OP-4
cdscb	M-74	imelt	M-108	pdiblc3	M-70	vgs	OP-5
cdscd	M-75	is	OP-46	prt	M-161	voxh	M-243
cf	M-132	isbjt	M-216	prwb	M-54	vrec0	M-221
cgb	OP-32	isdif	M-217	prwg	M-55	vsat	M-62
cgbo	M-112	isgidl	OP-10	ps	M-139	vsdfb	M-206
cgd	OP-30	isrec	M-218	ps	I-5	vsdth	M-207
cgdl	M-115	istun	M-219	psbcp	I-18	vth	OP-16
cgdo	M-110	k1	M-4	pvag	M-71	vtho	M-3
cgeo	M-111	klw1	M-5	pwr	OP-48	vtun0	M-222
cgg	OP-29	klw2	M-6	qb	OP-27	w	M-135
cgs	OP-31	k2	M-7	qbg	OP-28	w	I-1
cgs1	M-114	k3	M-8	qd	OP-25	w0	M-10
cgso	M-109	k3b	M-9	qg	OP-24	warn	M-191
cii	M-212	kb1	M-194	qs	OP-26	weff	O-1
cit	M-77	kb3	M-195	rbody	M-99	wint	M-35
cjswg	M-117	kbjt1	M-228	rbsh	M-95	wl	M-41
ckappa	M-116	keta	M-28	rd	M-98	wln	M-42
clc	M-130	ketas	M-29	rdc	M-101	wmax	M-183
cle	M-131	kf	M-176	rdd	M-103	wmin	M-184
csb	OP-40	kt1	M-152	rdeff	O-6	wr	M-56
csd	OP-38	kt11	M-153	rdsw	M-53	wth0	M-229
csdesw	M-209	kt2	M-154	region	OP-2	ww	M-43
csdmin	M-208	l	I-2	region	I-11	wwl	M-45
csg	OP-37	l	M-136	reversed	OP-3	wnn	M-44
css	OP-39	lbjt0	M-224	rhalo	M-230	xbjt	M-171
cth0	I-13	ldif	M-105	rs	M-97	xdif	M-172
cth0	M-167	ldif0	M-122	rsc	M-100	xj	M-33
delp	M-193	leff	O-2	rseff	O-5	xl	M-57
delta	M-72	lii	M-89	rsh	M-96	xpart	M-134
deltavox	M-244	lint	M-34	rss	M-102	xrec	M-173
delvt	M-125	ll	M-36	rth0	M-168	xt	M-52
dii	M-213	lln	M-37	rth0	I-12	xtun	M-174
dlbg	M-129	lmax	M-185	rtheff	O-3	xw	M-58
dlc	M-127	lmin	M-186	shmod	M-146		
dlcb	M-128	ln	M-220	sii0	M-91		
drout	M-66	lw	M-38	sii1	M-92		

Bipolar Junction Transistor (bjt)

Description

The bipolar transistor model is adapted from the integral charge model of Gummel and Poon, and it includes several high bias-level effects. This model defaults to the simpler Ebers-Moll model if certain parameters are left unspecified. This model also includes a substrate junction that connects either to the collector or to the base to model vertical and lateral structures.

This model has the following enhancements over SPICE2G.6:

1. Two base resistance models are provided.
2. Nonlinear collector resistance is implemented.
3. The integral form of the Early voltage effect is available.
4. The substrate junction includes both the diode and the capacitor.

This device is supported within altergroups.

To examine the equations used for this component, consult the [Spectre Circuit Simulator Device Model Equations](#) manual.

Sample Instance Statement

```
q1 (vcc net3 minus) npn_mod region=fwd area=1 m=1
```

Sample Model Statement

```
model npn_mod bjt type=npn is=10e-13 bf=200 va=58.8 ikf=5.63e-3 rb=700 rbm=86  
re=3.2 cje=0.352e-12 pe=0.76 me=0.34 tf=249e-12 cjc=0.34e-12 pc=0.55
```

Instance Definition

```
Name c b e [s] ModelName parameter=value ...
```

You do not have to specify the substrate terminal. If you do not specify it, the substrate is connected to ground.

Instance Parameters

- 1 area=1 Transistor area factor.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 2 `m=1` Multiplicity factor.
- 3 `trise` Temperature rise from ambient.
- 4 `region=fwd` Estimated operating region.
Possible values are `off`, `fwd`, `rev`, `sat`, or `breakdown`.

Model Definition

`model modelName bjt parameter=value ...`

Model Parameters

Structural parameters

- 1 `type=npn` Transistor type.
Possible values are `npn` or `pnP`.
- 2 `struct=vertical` Transistor structure. For `pnP` default=`lateral`.
Possible values are `vertical` or `lateral`.

Saturation current parameters

- 3 `is=1e-16 A` Saturation current (*area).
- 4 `ise=0 A` B-E leakage saturation current. Set to `c2*is` if not given. (*area).
- 5 `isc=0 A` B-C leakage saturation current. Set to `c4*is` if not given. (*area).
- 6 `iss=0 A` Substrate leakage saturation current (*area).
- 7 `c2=0` Forward leakage saturation current coefficient.
- 8 `c4=0` Reverse leakage saturation current coefficient.

B-C leakage model parameters

- 9 `cbo=0 A` Extrapolated 0-volt B-C leakage current (*area).
- 10 `gbo=0 S` Slope of I_{cbo} vs. V_{bc} above V_{bo} (*area).

Spectre Circuit Simulator Reference

Component Statements Part 1

11 $v_{bo}=0$ v Slope of I_{cbo} vs. V_{bc} at $V_{bc}=0$.

12 $\tau_{cbo}=0$ $1/C$ Temperature coefficient for cbo .

13 $\tau_{gbo}=0$ $1/C$ Temperature coefficient for gbo .

Emission coefficient parameters

14 $n_f=1$ Forward emission coefficient.

15 $n_r=1$ Reverse emission coefficient.

16 $n_e=1.5$ B-E leakage emission coefficient.

17 $n_c=2$ B-C leakage emission coefficient.

18 $n_s=1$ Substrate junction emission coefficient.

Current gain parameters

19 $\beta_f=100$ A/A Forward current gain (β).

20 $\beta_r=1$ A/A Reverse current gain (β).

21 $i_{kf}=\infty$ A High current corner for forward β (*area).

22 $i_{kr}=\infty$ A High current corner for reverse β (*area).

Early voltage parameters

23 $v_{af}=\infty$ v Forward Early voltage.

24 $v_{ar}=\infty$ v Reverse Early voltage.

25 $k_e=0$ $1/v$ B-E space-charge integral multiplier.

26 $k_c=0$ $1/v$ B-C space-charge integral multiplier.

Parasitic resistance parameters

27 $r_b=0$ Ω Zero-bias base resistance (/area).

Spectre Circuit Simulator Reference

Component Statements Part 1

28 `rbm=rb` Ω Minimum base resistance for high currents (/area).

29 `irb= ∞` ACurrent at base resistance midpoint (*area).

30 `rbmod=spice`Nonlinear `Rb` model.
Possible values are `spectre` or `spice`.

31 `rc=0` Ω Collector resistance (/area).

32 `rcv=0` Ω Variable collector resistance (/area).

33 `rcm=0` Ω Minimum collector resistance (/area).

34 `dope=1e15` cm^{-3} Collector background doping concentration.

35 `cex=1`Current crowding exponent.

36 `cco=1` ACurrent crowding normalization constant (*area).

37 `re=0` Ω Emitter resistance (/area).

38 `minr=0.1` Ω Minimum parasitic resistance.

Junction capacitance parameters

39 `cje=0` FB-E zero-bias junction capacitance (*area).

40 `vje=0.75` VB-E built-in junction potential.

41 `mje=1/3`B-E junction exponent.

42 `cjc=0` FB-C zero-bias junction capacitance (*area).

43 `vjc=0.75` VB-C built-in junction potential.

44 `mjc=1/3`B-C junction exponent.

45 `xcjc=1`Fraction of B-C capacitance tied to internal base node.

46 `xcjc2=1`Fraction of B-C capacitance tied to collector and fraction of B-C tied to internal node.

47 `cjs=0` FB-S zero-bias junction capacitance (*area).

Spectre Circuit Simulator Reference

Component Statements Part 1

- 48 `vjs=0.75` `vB-S` built-in junction potential.
- 49 `mjs=0` `B-S` junction exponent.
- 50 `fc=0.5` Junction capacitor forward-bias threshold.
- 51 `cbcp=0` `FB-C` parasitic capacitance.
- 52 `cbep=0` `FB-E` parasitic capacitance.
- 53 `ccsp=0` `FC-S` parasitic capacitance.

Transit time and excess phase parameters

- 54 `tf=0` `s` Ideal forward transit time.
- 55 `td=0` `s` Intrinsic base delay time.
- 56 `xtf=0` Coefficient for bias dependence of `tf`.
- 57 `vtf= ∞` `v` Voltage describing `Vbc` dependence of `tf`.
- 58 `itf=0` `A` High current parameter for effect on `tf` (*area).
- 59 `tr=0` `s` Ideal reverse transit time.
- 60 `ptf=0` `°` Excess phase at $\text{freq} = 1.0/(tf * 2 \pi)$ Hz.

Temperature effects parameters

- 61 `tnom (C)` Parameters measurement temperature. Default set by `options`.
- 62 `trise=0` `C` Temperature rise from ambient.
- 63 `eg=1.11` `v` Band-gap.
- 64 `xtb=0` Beta temperature exponent.
- 65 `xti=3` Temperature exponent for effect on `is`.
- 66 `trb1=0` `1/C` Linear temperature coefficient for the base resistor.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 67 `trb2=0` C^{-2} Quadratic temperature coefficient for the base resistor.
- 68 `trm1=0` $1/C$ Linear temperature coefficient for the minimum base resistor.
- 69 `trm2=0` C^{-2} Quadratic temperature coefficient for the minimum base resistor.
- 70 `trc1=0` $1/C$ Linear temperature coefficient for the collector resistor.
- 71 `trc2=0` C^{-2} Quadratic temperature coefficient for the collector resistor.
- 72 `tre1=0` $1/C$ Linear temperature coefficient for the emitter resistor.
- 73 `tre2=0` C^{-2} Quadratic temperature coefficient for the emitter resistor.
- 74 `tlev=0`DC temperature selector.
- 75 `tlevc=0`AC temperature selector.
- 76 `gap1=7.02e-4` V/C Band-gap temperature coefficient.
- 77 `gap2=1108` C Band-gap temperature offset.
- 78 `tikf1=0` $1/C$ Linear temperature coefficient for `ikf`.
- 79 `tikf2=0` C^{-2} Quadratic temperature coefficient for `ikf`.
- 80 `tikr1=0` $1/C$ Linear temperature coefficient for `ikr`.
- 81 `tikr2=0` C^{-2} Quadratic temperature coefficient for `ikr`.
- 82 `tirb1=0` $1/C$ Linear temperature coefficient for `irb`.
- 83 `tirb2=0` C^{-2} Quadratic temperature coefficient for `irb`.
- 84 `tis1=0` $1/C$ Linear temperature coefficient for `is`.
- 85 `tis2=0` C^{-2} Quadratic temperature coefficient for `is`.
- 86 `tise1=0` $1/C$ Linear temperature coefficient for `ise`.
- 87 `tise2=0` C^{-2} Quadratic temperature coefficient for `ise`.
- 88 `tisc1=0` $1/C$ Linear temperature coefficient for `isc`.

Spectre Circuit Simulator Reference

Component Statements Part 1

89 `tisc2=0` C^{-2} Quadratic temperature coefficient for `isc`.

90 `tiss1=0` $1/C$ Linear temperature coefficient for `iss`.

91 `tiss2=0` C^{-2} Quadratic temperature coefficient for `iss`.

92 `tbf1=0` $1/C$ Linear temperature coefficient for `bf`.

93 `tbf2=0` C^{-2} Quadratic temperature coefficient for `bf`.

94 `tbr1=0` $1/C$ Linear temperature coefficient for `br`.

95 `tbr2=0` C^{-2} Quadratic temperature coefficient for `br`.

96 `tvaf1=0` $1/C$ Linear temperature coefficient for `vaf`.

97 `tvaf2=0` C^{-2} Quadratic temperature coefficient for `vaf`.

98 `tvar1=0` $1/C$ Linear temperature coefficient for `var`.

99 `tvar2=0` C^{-2} Quadratic temperature coefficient for `var`.

100 `titf1=0` $1/C$ Linear temperature coefficient for `itf`.

101 `titf2=0` C^{-2} Quadratic temperature coefficient for `itf`.

102 `ttf1=0` $1/C$ Linear temperature coefficient for `tf`.

103 `ttf2=0` C^{-2} Quadratic temperature coefficient for `tf`.

104 `ttr1=0` $1/C$ Linear temperature coefficient for `tr`.

105 `ttr2=0` C^{-2} Quadratic temperature coefficient for `tr`.

106 `tnf1=0` $1/C$ Linear temperature coefficient for `nf`.

107 `tnf2=0` C^{-2} Quadratic temperature coefficient for `nf`.

108 `tnr1=0` $1/C$ Linear temperature coefficient for `nr`.

109 `tnr2=0` C^{-2} Quadratic temperature coefficient for `nr`.

110 `tne1=0` $1/C$ Linear temperature coefficient for `ne`.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 111 $t_{ne2}=0$ C^{-2} Quadratic temperature coefficient for n_e .
- 112 $t_{nc1}=0$ $1/C$ Linear temperature coefficient for n_c .
- 113 $t_{nc2}=0$ C^{-2} Quadratic temperature coefficient for n_c .
- 114 $t_{ns1}=0$ $1/C$ Linear temperature coefficient for n_s .
- 115 $t_{ns2}=0$ C^{-2} Quadratic temperature coefficient for n_s .
- 116 $t_{mje1}=0$ $1/C$ Linear temperature coefficient for m_{je} .
- 117 $t_{mje2}=0$ C^{-2} Quadratic temperature coefficient for m_{je} .
- 118 $t_{mjc1}=0$ $1/C$ Linear temperature coefficient for m_{jc} .
- 119 $t_{mjc2}=0$ C^{-2} Quadratic temperature coefficient for m_{jc} .
- 120 $t_{mjs1}=0$ $1/C$ Linear temperature coefficient for m_{js} .
- 121 $t_{mjs2}=0$ C^{-2} Quadratic temperature coefficient for m_{js} .
- 122 $cte=0$ $1/C$ Temperature coefficient for c_{je} .
- 123 $ctc=0$ $1/C$ Temperature coefficient for c_{jc} .
- 124 $cts=0$ $1/C$ Temperature coefficient for c_{js} .
- 125 $tv_{je}=0$ V/C Temperature coefficient for v_{je} .
- 126 $tv_{jc}=0$ V/C Temperature coefficient for v_{jc} .
- 127 $tv_{js}=0$ V/C Temperature coefficient for v_{js} .
- 128 $tv_{tf1}=0$ $1/C$ Linear temperature coefficient for v_{tf} .
- 129 $tv_{tf2}=0$ C^{-2} Quadratic temperature coefficient for v_{tf} .
- 130 $tx_{tf1}=0$ $1/C$ Linear temperature coefficient for x_{tf} .
- 131 $tx_{tf2}=0$ C^{-2} Quadratic temperature coefficient for x_{tf} .

Spectre Circuit Simulator Reference

Component Statements Part 1

Junction diode model control parameters

132 `dskip=yes` Skip junction calculations if they are reverse-saturated.

Possible values are `no` or `yes`.

133 `imelt=imax` A Junction explosion current (*area).

Operating region warning control parameters

134 `bvbe= ∞` V_{B-E} breakdown voltage.

135 `bvbc= ∞` V_{B-C} breakdown voltage.

136 `bvce= ∞` V_{C-E} breakdown voltage.

137 `bvsub= ∞` V_{Substrate} junction breakdown voltage.

138 `vbefwd=0.2` V_{B-E} forward voltage.

139 `vbcfwd=0.2` V_{B-C} forward voltage.

140 `vsubfwd=0.2` V_{Substrate} junction forward voltage.

141 `imax=1e3` A Maximum allowable base current (*area).

142 `imax1=imax` A Maximum allowable collector current (*area).

143 `alarm=none` Forbidden operating region.

Possible values are `none`, `off`, `fwd`, `rev`, or `sat`.

Noise model parameters

144 `kf=0` Flicker (1/f) noise coefficient.

145 `af=1` Flicker (1/f) noise exponent.

146 `kb=0` Burst noise coefficient.

147 `bnoisefc=1` Burst noise cutoff frequency.

148 `rbnoi=rb` Ω Effective base noise resistance.

Spectre Circuit Simulator Reference

Component Statements Part 1

Imax and Imelt

The `imax` parameter aids convergence and prevents numerical overflow. The junction characteristics of the device are accurately modeled for current up to `imax`. If `imax` is exceeded during iterations, the linear model is substituted until the current drops below `imax` or until convergence is achieved. If convergence is achieved with the current exceeding `imax`, the results are inaccurate, and Spectre prints a warning.

A separate model parameter, `imelt`, is used as a limit warning for the junction current. This parameter can be set to the maximum current rating of the device. When any component of the junction current exceeds `imelt`, note that base and collector currents are composed of many exponential terms, Spectre issues a warning and the results become inaccurate. The junction current is linearized above the value of `imelt` to prevent arithmetic exception, with the exponential term replaced by a linear equation at `imelt`.

Operating-Point Parameters

- 1 `type=npn` Transistor type.
Possible values are `npn` or `pnnp`.
- 2 `struct=vertical` Transistor structure. For `pnnp` default=`lateral`.
Possible values are `vertical` or `lateral`.
- 3 `region=fwd` Estimated operating region.
Possible values are `off`, `fwd`, `rev`, `sat`, or `breakdown`.
- 4 `vbe (V)` Base-emitter voltage.
- 5 `vbc (V)` Base-collector voltage.
- 6 `vce (V)` Collector-emitter voltage.
- 7 `vsub (V)` Substrate junction voltage.
- 8 `ic (A)` Resistive collector current.
- 9 `ib (A)` Resistive base current.
- 10 `isub (A)` Resistive substrate current.
- 11 `pwr (W)` Power dissipation.
- 12 `betadc (A/A)` Ratio of resistive collector current to resistive base current.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 13 `betaac` (A/A) Small-signal common-emitter current gain.
- 14 `gm` (S) Common-emitter transconductance.
- 15 `rpi` (Ω) Common-emitter input resistance.
- 16 `ro` (Ω) Common-emitter output resistance.
- 17 `rb` (Ω) Parasitic base resistance.
- 18 `rc` (Ω) Parasitic collector resistance.
- 19 `cpi` (F) Common-emitter input capacitance.
- 20 `cmu` (F) Common-base output capacitance.
- 21 `cmux` (F) External common-base output capacitance.
- 22 `csub` (F) Substrate capacitance.
- 23 `ft` (Hz) Unity small-signal current-gain frequency.

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

<code>af</code> M-145	<code>imax1</code> M-142	<code>tbr2</code> M-95	<code>trc2</code> M-71
<code>alarm</code> M-143	<code>imelt</code> M-133	<code>tcbo</code> M-12	<code>tre1</code> M-72
<code>area</code> I-1	<code>irb</code> M-29	<code>td</code> M-55	<code>tre2</code> M-73
<code>betaac</code> OP-13	<code>is</code> M-3	<code>tf</code> M-54	<code>trise</code> I-3
<code>betadc</code> OP-12	<code>isc</code> M-5	<code>tgbo</code> M-13	<code>trise</code> M-62
<code>bf</code> M-19	<code>ise</code> M-4	<code>tikf1</code> M-78	<code>trml</code> M-68
<code>bnoiseFc</code> M-147	<code>iss</code> M-6	<code>tikf2</code> M-79	<code>trm2</code> M-69
<code>br</code> M-20	<code>isub</code> OP-10	<code>tikr1</code> M-80	<code>ttf1</code> M-102
<code>bvbc</code> M-135	<code>itf</code> M-58	<code>tikr2</code> M-81	<code>ttf2</code> M-103
<code>bvbe</code> M-134	<code>kb</code> M-146	<code>tirb1</code> M-82	<code>ttr1</code> M-104
<code>bvce</code> M-136	<code>kc</code> M-26	<code>tirb2</code> M-83	<code>ttr2</code> M-105
<code>bvsub</code> M-137	<code>ke</code> M-25	<code>tisl</code> M-84	<code>tvaf1</code> M-96

Spectre Circuit Simulator Reference

Component Statements Part 1

c2	M-7	kf	M-144	tis2	M-85	tvaf2	M-97
c4	M-8	m	I-2	tisc1	M-88	tvar1	M-98
cbcp	M-51	minr	M-38	tisc2	M-89	tvar2	M-99
cbep	M-52	mjc	M-44	tisel	M-86	tvjc	M-126
cbo	M-9	mje	M-41	tise2	M-87	tvje	M-125
cco	M-36	mjs	M-49	tiss1	M-90	tvjs	M-127
ccsp	M-53	nc	M-17	tiss2	M-91	tvtf1	M-128
cex	M-35	ne	M-16	titf1	M-100	tvtf2	M-129
cjc	M-42	nf	M-14	titf2	M-101	txtf1	M-130
cje	M-39	nr	M-15	tlev	M-74	txtf2	M-131
cjs	M-47	ns	M-18	tlevc	M-75	type	OP-1
cmu	OP-20	ptf	M-60	tmjc1	M-118	type	M-1
cmux	OP-21	pwr	OP-11	tmjc2	M-119	vaf	M-23
cpi	OP-19	rb	M-27	tmjel	M-116	var	M-24
csub	OP-22	rb	OP-17	tmje2	M-117	vbc	OP-5
ctc	M-123	rbm	M-28	tmjs1	M-120	vbcfwd	M-139
cte	M-122	rbmod	M-30	tmjs2	M-121	vbe	OP-4
cts	M-124	rbnoi	M-148	tncl	M-112	vbefwd	M-138
dope	M-34	rc	M-31	tnc2	M-113	vbo	M-11
dskip	M-132	rc	OP-18	tne1	M-110	vce	OP-6
eg	M-63	rcm	M-33	tne2	M-111	vjc	M-43
fc	M-50	rcv	M-32	tnf1	M-106	vje	M-40
ft	OP-23	re	M-37	tnf2	M-107	vjs	M-48
gap1	M-76	region	OP-3	tnom	M-61	vsub	OP-7
gap2	M-77	region	I-4	tnr1	M-108	vsubfwd	M-140
gbo	M-10	ro	OP-16	tnr2	M-109	vtf	M-57
gm	OP-14	rpi	OP-15	tns1	M-114	xcjc	M-45
ib	OP-9	struct	OP-2	tns2	M-115	xcjc2	M-46
ic	OP-8	struct	M-2	tr	M-59	xtb	M-64
ikf	M-21	tbf1	M-92	trb1	M-66	xtf	M-56
ikr	M-22	tbf2	M-93	trb2	M-67	xti	M-65
imax	M-141	tbr1	M-94	trcl	M-70		

Lateral PNP Transistor (bjt301)

Description

The bjt301 model provides an extensive description of a lateral integrated circuit junction-isolated PNP transistor. It is described in the Philips Bipolar Modelbook (Dec.93) as TPL level 301.

(c) Philips Electronics N.V. 1993

Spectre Circuit Simulator Reference

Component Statements Part 1

In extension to the modelbook description a minimum conductance `gmin` is inserted between the internal base and internal collector node, between the internal base and the internal emitter node, and between the external base and the substrate node to aid convergence. The value of `gmin` is set by an options statement, default = 1e-12 S.

The `imax` parameter is used to aid convergence and to prevent numerical overflow. The junction characteristics of the transistor are accurately modeled for currents up to `imax`. For currents above `imax`, the junction is modeled as a linear resistor and a warning is printed.

This device is supported within altergroups.

This device is dynamically loaded from the shared object `/vobs/spectre_dev/tools.sun4v/spectre/lib/cmi/2.0.doc/libphilips.so`

Sample Instance Statement

```
q2 (minus net3 vcc) pnp_mod region=fwd area=1 m=1
```

Sample Model Statement

```
model pnp_mod bjt301 type=pnp struct=lateral is=1e-14 bf=85 ilf=11e-9 ikf=95e-6 re=3.2  
cje=0.352e-12
```

Instance Definition

Name c b e [s] ModelName parameter=value ...

Instance Parameters

- 1 `area=1`Area factor.
- 2 `mult=1`Alias of area factor.
- 3 `m=1`Multiplicity factor.
- 4 `region=fwd`Estimated DC operating region, used as a convergence aid.
Possible values are `off`, `fwd`, `rev`, or `sat`.

Model Definition

model modelName bjt301 parameter=value ...

Model Parameters

Structural parameters

- 1 type=pin Transistor type.
Possible values are pin or pinl.
- 2 struct=lateral Transistor structure.
Possible values are lateral.

Current parameters

- 3 is=1.0e-15 A Saturation current.
- 4 imax=1.0 A Explosion current.
- 5 bf=100.0 A/A Ideal forward common-emitter current gain (beta).
- 6 ilf=10.0e-9 A Low-level knee-current of forward beta.
- 7 nlf=2.0 Emission coefficient of non-ideal forward base current.
- 8 ikf=100.0e-6 A High-injection knee-current of forward beta.
- 9 nhf=1.0 Basewidening exponent.
- 10 veaf=50.0 V Early voltage related to collector junction.
- 11 br=10.0 A/A Ideal reverse common-collector current gain (beta).
- 12 ilr=10.0e-9 A Low-level knee-current of reverse beta.
- 13 nlr=2.0 Emission coefficient of non-ideal reverse base current.
- 14 ikr=100.0e-6 A High-injection knee-current of reverse beta.
- 15 iks=100.0e-6 A High-injection current of substrate effect.

Spectre Circuit Simulator Reference

Component Statements Part 1

16 `xcs=1.0` Current fraction of c-b-s transistor.

17 `xes=0.01` Current fraction of e-b-s transistor.

Parasitic resistance parameters

18 `rc=1.0` Ω Collector resistance.

19 `rbc=10.0` Ω Constant part of base resistance.

20 `rbv=10.0` Ω Variable part of base resistance.

21 `re=1.0` Ω Emitter series resistance.

Junction capacitance parameters

22 `taub=25.0e-9` s Forward transit time related to neutral base.

23 `taune=1.0e-9` s Forward transit time related to neutral emitter in neutral e-b region.

24 `mtau=1.0` Coefficient of current dependence of `taune`.

25 `cje=100.0e-15` F Zero bias emitter-base depletion capacitance.

26 `vde=0.55` V Emitter-base diffusion voltage.

27 `pe=0.333` Emitter-base grading coefficient.

28 `taur=100.0e-9` s Ideal reverse transit time.

29 `cjc=200.0e-15` F Zero bias collector-base depletion capacitance.

30 `vdc=0.55` V Collector-base diffusion voltage.

31 `pc=0.333` Collector-base grading coefficient.

32 `cjs=1.0e-12` F Zero bias substrate junction depletion capacitance.

33 `vds=0.55` V Substrate junction diffusion voltage.

34 `ps=0.333` Substrate junction grading coefficient.

Spectre Circuit Simulator Reference

Component Statements Part 1

35 `exphi=0.3` Excess phase shift.

36 `fc=0.95` Coefficient for forward bias capacitance.

Temperature effects parameters

37 `tref (C)` Reference temperature. Default set by option `tnom`.

38 `tnom (C)` Alias of `tref`. Default set by option `tnom`.

39 `dta=0.0 K` Difference between device temperature and ambient temperature.

40 `trise=0.0 K` Alias of `dta`.

41 `ptbf=0.0` Power for temperature dependence of `bf`.

42 `ptbr=0.0` Power for temperature dependence of `br`.

43 `ptrc=0.0` Power for temperature dependence of `rc`.

44 `ptrb=0.0` Power for temperature dependence of `rbc` and `rbv`.

45 `vg=1.2 V` Band-gap voltage.

46 `pt=1.2` Power for temperature dependence of diffusion coefficient.

Noise model parameters

47 `kf=0.0` Flicker noise coefficient.

48 `af=1.0` Flicker noise exponent.

Output Parameters

1 `ist (A)` Saturation current.

2 `iole (A)` Non-ideal forward base saturation current.

3 `iolc (A)` Non-ideal reverse base saturation current.

4 `bft (A/A)` Ideal forward common-emitter current gain (beta).

Spectre Circuit Simulator Reference

Component Statements Part 1

- 5 `brt` (A/A) Ideal reverse common-collector current gain (beta).
- 6 `rct` (Ω) Collector resistance.
- 7 `rbct` (Ω) Constant part of base resistance.
- 8 `rbvt` (Ω) Variable part of base resistance.
- 9 `taubt` (s) Forward transit time related to neutral base.
- 10 `cjet` (F) Zero bias emitter-base depletion capacitance.
- 11 `vdet` (V) Emitter-base diffusion voltage.
- 12 `taurt` (s) Ideal reverse transit time.
- 13 `cjct` (F) Zero bias collector-base depletion capacitance.
- 14 `vdct` (V) Collector-base diffusion voltage.
- 15 `cjst` (F) Zero bias substrate junction depletion capacitance.
- 16 `vdst` (V) Substrate junction diffusion voltage.

Operating-Point Parameters

- 1 `ib` (A) Base current.
- 2 `ic` (A) Collector current.
- 3 `ie` (A) Emitter current.
- 4 `isub` (A) Substrate current.
- 5 `vbe` (V) Base-emitter voltage.
- 6 `vbc` (V) Base-collector voltage.
- 7 `vce` (V) Collector-emitter voltage.
- 8 `vsubj` (V) Substrate voltage.
- 9 `betadc` (A/A) Ratio of DC collector current to DC Base current.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 10 `rb` (Ω) Base resistance at operating point.
- 11 `rc` (Ω) Collector resistance at operating point.
- 12 `re` (Ω) Emitter resistance at operating point.
- 13 `icb` (A) Collector-Base current.
- 14 `ieb` (A) Emitter-Base current.
- 15 `icsub` (A) Collector-Substrate current.
- 16 `iesub` (A) Emitter-Substrate current.
- 17 `pwr` (W) Power.
- 18 `gpi` (S) Conductance emitter-base junction.
- 19 `gmucb` (S) Conductance collector-base junction.
- 20 `gf` (S) Forward transconductance.
- 21 `gr` (S) Reverse transconductance.
- 22 `gs` (S) Conductance substrate-base junction.
- 23 `g3` (S) Transconductance (parasitic PNP) c-b-s transistor.
- 24 `g4` (S) Transconductance (parasitic PNP) e-b-s transistor.
- 25 `ced` (F) Emitter diffusion capacitance.
- 26 `ccd` (F) Collector diffusion capacitance.
- 27 `cet` (F) Emitter junction depletion capacitance.
- 28 `cct` (F) Collector junction depletion capacitance.
- 29 `cst` (F) Substrate junction depletion capacitance.
- 30 `betaac` (A/A) Small-signal common-emitter current gain.
- 31 `ft` (Hz) Unity small-signal current-gain frequency.

Spectre Circuit Simulator Reference

Component Statements Part 1

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

af	M-48	gf	OP-20	mtau	M-24	struct	M-2
area	I-1	gmu	OP-19	mult	I-2	taub	M-22
betaac	OP-30	gpi	OP-18	nhf	M-9	taubt	O-9
betadc	OP-9	gr	OP-21	nlf	M-7	taune	M-23
bf	M-5	gs	OP-22	nlr	M-13	taur	M-28
bft	O-4	ib	OP-1	pc	M-31	taurt	O-12
br	M-11	ic	OP-2	pe	M-27	tnom	M-38
brt	O-5	icb	OP-13	ps	M-34	tref	M-37
ccd	OP-26	icsub	OP-15	pt	M-46	trise	M-40
cct	OP-28	ie	OP-3	ptbf	M-41	type	M-1
ced	OP-25	ieb	OP-14	ptbr	M-42	vbc	OP-6
cet	OP-27	iesub	OP-16	ptrb	M-44	vbe	OP-5
cjc	M-29	ikf	M-8	ptrc	M-43	vce	OP-7
cjct	O-13	ikr	M-14	pwr	OP-17	vdc	M-30
cje	M-25	iks	M-15	rb	OP-10	vdct	O-14
cjet	O-10	ilf	M-6	rbc	M-19	vde	M-26
cjs	M-32	ilr	M-12	rbct	O-7	vdet	O-11
cjst	O-15	imax	M-4	rbv	M-20	vds	M-33
cst	OP-29	iolc	O-3	rbvt	O-8	vdst	O-16
dta	M-39	iole	O-2	rc	M-18	veaf	M-10
exphi	M-35	is	M-3	rc	OP-11	vg	M-45
fc	M-36	ist	O-1	rct	O-6	vsubj	OP-8
ft	OP-31	isub	OP-4	re	OP-12	xcs	M-16
g3	OP-23	kf	M-47	re	M-21	xes	M-17
g4	OP-24	m	I-3	region	I-4		

Lateral PNP Transistor (bjt500)

Description

The **bjt500** model provides an extensive description of a lateral integrated circuit junction-isolated PNP transistor. It is described in the Philips Bipolar Modelbook (Dec.93) as TPL-level-500. Information on how to obtain this document can be found on Source Link by searching for Philips.

Spectre Circuit Simulator Reference

Component Statements Part 1

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In extension to the modelbook description a minimum conductance `gmin` is inserted between the internal base and internal collector node, between the internal base and the internal emitter node, and between the external base and the substrate node to aid convergence. The value of `gmin` is set by an options statement, default is `gmin = 1.0e-12 S`.

The `imax` parameter is used to aid convergence and to prevent numerical overflow. The junction characteristics of the transistor are accurately modeled for currents up to `imax`. For currents above `imax`, the junction is modeled as a linear resistor, and a warning is printed.

This device is supported within altergroups.

This device is dynamically loaded from the shared object `/vobs/spectre_dev/tools.sun4v/spectre/lib/cmi/2.0.doc/libphilips.so`

Sample Instance Statement

```
q3 (minus net3 vcc) pnp_mod region=fwd area=1 m=1
```

Sample Model Statement

```
model pnp_mod bjt500 type=pnp struct=lateral is=1e-14 bf=85 ik=95e-6 reex=3.2  
cje=0.352e-12
```

Instance Definition

Name `c b e [s]` ModelName parameter=value ...

Instance Parameters

- 1 `area=1`Area factor.
- 2 `mult=1`Alias of area factor.
- 3 `m=1`Multiplicity factor.
- 4 `region=fwd`Estimated DC operating region, used as a convergence aid.
Possible values are `off`, `fwd`, `rev`, or `sat`.

Spectre Circuit Simulator Reference

Component Statements Part 1

Model Definition

model modelName bjt500 parameter=value ...

Model Parameters

Structural parameters

- 1 type=pinp Transistor type.
Possible values are pinp or pinpl.
- 2 struct=lateral Transistor structure.
Possible values are lateral.

Current parameters

- 3 is=1.8e-16 A Collector-emitter saturation current.
- 4 imax=1.0 A Explosion current.
- 5 bf=131.0 A/A Ideal forward common-emitter current gain (beta).
- 6 ibf=2.6e-14 A Saturation current of non-ideal forward base current.
- 7 vlf=0.54 V Cross-over voltage of non-ideal forward base current.
- 8 ik=1.1e-4 A High injection knee current.
- 9 xifv=0.43 Vertical fraction of forward current.
- 10 eaf1=20.5 V Early voltage of the lateral forward current component.
- 11 eafv=75.0 V Early voltage of the vertical forward current component.
- 12 br=25.0 A/A Ideal reverse common-emitter current gain.
- 13 ibr=1.2e-13 A Saturation current of non-ideal reverse base current.
- 14 vlr=0.48 V Cross-over voltage of non-ideal reverse base current.
- 15 xirv=0.43 Vertical fraction of reverse current.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 16 earl=13.1 V Early voltage of the lateral reverse current component.
- 17 earv=104.0 V Early voltage of the vertical reverse current component.
- 18 xes=2.7e-3 Ratio between saturation current of e-b-s transistor and e-b-c transistor.
- 19 xhes=0.7 Fraction of substrate current of e-b-s transistor subject to high injection.
- 20 xcs=3.0 Ratio between saturation current of c-b-s transistor and c-b-e transistor.
- 21 xhcs=1.0 Fraction of substrate current of c-b-s transistor subject to high injection.
- 22 iss=4.0e-13 A Saturation current of substrate-base diode.

Parasitic resistance parameters

- 23 rcex=5.0 Ω External part of the collector resistance.
- 24 rcin=47.0 Ω Internal part of the collector resistance.
- 25 rbcc=10.0 Ω Constant part of the base resistance rbc.
- 26 rbcv=10.0 Ω Variable part of the base resistance rbc.
- 27 rbec=10.0 Ω Constant part of the base resistance rbe.
- 28 rbev=50.0 Ω Variable part of the base resistance rbe.
- 29 reex=27.0 Ω External part of the emitter resistance.
- 30 rein=66.0 Ω Internal part of the emitter resistance.
- 31 rsb=1.0e15 Ω Substrate-base leakage resistance.

Junction capacitance parameters

- 32 tlat=2.4e-9 s Low injection (forward and reverse) transit time of charge stored in the epilayer between emitter and collector.
- 33 tfvr=3.0e-8 s Low injection forward transit time due to charge stored in the epilayer under the emitter.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 34 `tfn=2.0e-10 s` Low injection forward transit time due to charge stored in the emitter and the buried layer under the emitter.
- 35 `cje=6.1e-14 F` Zero-bias emitter-base depletion capacitance.
- 36 `vde=0.52 v` Emitter-base diffusion voltage.
- 37 `pe=0.3` Emitter-base grading coefficient.
- 38 `trvr=1.0e-9 s` Low injection reverse transit time due to charge stored in the epilayer under the collector.
- 39 `trn=3.0e-9 s` Low injection reverse transit time due to charge stored in the collector and the buried layer under the collector.
- 40 `cjc=3.9e-13 F` Zero-bias collector-base depletion capacitance.
- 41 `vdc=0.57 v` Collector-base diffusion voltage.
- 42 `pc=0.36` Collector-base grading coefficient.
- 43 `cjs=1.3e-12 F` Zero-bias substrate-base depletion capacitance.
- 44 `vds=0.52 v` Substrate-base diffusion voltage.
- 45 `ps=0.35` Substrate-base grading coefficient.
- 46 `exphi` Not used in model bjt500.

Temperature effects parameters

- 47 `tref (C)` Reference temperature. Default set by option `tnom`.
- 48 `tnom (C)` Alias of `tref`.
- 49 `tr (C)` Alias of `tref`.
- 50 `dta=0.0 K` Difference between the device temperature and the ambient analysis temperature.
- 51 `trise=0.0 K` Alias of `dta`.
- 52 `vgeb=1.206 v` Bandgap voltage of the emitter-base depletion region.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 53 `vgcb=1.206` `vBandgap` voltage of the collector-base depletion region.
- 54 `vgsb=1.206` `vBandgap` voltage of the substrate-base depletion region.
- 55 `vgb=1.206` `vBandgap` voltage of the base between emitter and collector.
- 56 `vge=1.206` `vBandgap` voltage of the emitter.
- 57 `vgje=1.123` `vBandgap` voltage recombination emitter-base junction.
- 58 `ae=4.48` Temperature coefficient of `bf`.
- 59 `spb=2.853` Temperature coefficient of the epitaxial base hole mobility.
- 60 `snb=2.6` Temperature coefficient of the epitaxial base electron mobility.
- 61 `snbn=0.3` Temperature coefficient of buried layer electron mobility.
- 62 `spe=0.73` Temperature coefficient of emitter hole mobility.
- 63 `spc=0.73` Temperature coefficient of collector hole mobility.
- 64 `sx=1.0` Temperature coefficient of combined minority carrier mobility in emitter and buried layer.

Noise model parameters

- 65 `kf=0.0` Flicker noise coefficient.
- 66 `af=1.0` Flicker noise exponent.

Output Parameters

- 1 `ist (A)` Collector-emitter saturation current.
- 2 `bft (A/A)` Ideal forward common-emitter current gain (beta).
- 3 `ibft (A)` Saturation current of non-ideal forward base current.
- 4 `ikt (A)` High injection knee current.
- 5 `eaflt (V)` Early voltage of the lateral forward current component.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 6 `eafvt` (V) Early voltage of the vertical forward current component.
- 7 `brt` (A/A) Ideal reverse common-emitter current gain.
- 8 `ibr` (A) Saturation current of non-ideal reverse base current.
- 9 `earlt` (V) Early voltage of the lateral reverse current component.
- 10 `earvt` (V) Early voltage of the vertical reverse current component.
- 11 `isst` (A) Saturation current of substrate-base diode.
- 12 `rcint` (Ω) Internal part of the collector resistance.
- 13 `rbcct` (Ω) Constant part of the base resistance `rbc`.
- 14 `rbcvt` (Ω) Variable part of the base resistance `rbc`.
- 15 `rbe` (Ω) Constant part of the base resistance `rbe`.
- 16 `rbev` (Ω) Variable part of the base resistance `rbe`.
- 17 `reint` (Ω) Internal part of the emitter resistance.
- 18 `tlatt` (s) Low injection (forward and reverse) transit time of charge stored in the epilayer between emitter and collector.
- 19 `tfvrt` (s) Low injection forward transit time due to charge stored in the epilayer under the emitter.
- 20 `tfnt` (s) Low injection forward transit time due to charge stored in the emitter and the buried layer under the emitter.
- 21 `cjet` (F) Zero-bias emitter-base depletion capacitance.
- 22 `vdet` (V) Emitter-base diffusion voltage.
- 23 `trvrt` (s) Low injection reverse transit time due to charge stored in the epilayer under the collector.
- 24 `trnt` (s) Low injection reverse transit time due to charge stored in the collector and the buried layer under the collector.

Spectre Circuit Simulator Reference

Component Statements Part 1

25 `cjct (F)` Zero-bias collector-base depletion capacitance.

26 `vdct (V)` Collector-base diffusion voltage.

27 `cjst (F)` Zero-bias substrate-base depletion capacitance.

28 `vdst (V)` Substrate-base diffusion voltage.

Operating-Point Parameters

1 `ic (A)` Resistive collector current.

2 `ib (A)` Resistive base current.

3 `ie (A)` Resistive emitter current.

4 `isub (A)` Resistive substrate current.

5 `iflat (A)` Lateral forward current.

6 `irlat (A)` Lateral reverse current.

7 `ifver (A)` Vertical forward current.

8 `irver (A)` Vertical reverse current.

9 `ire (A)` Ideal forward base current.

10 `ile (A)` Non-ideal forward base current.

11 `ise (A)` Forward substrate current.

12 `irc (A)` Ideal reverse base current.

13 `ilc (A)` Non-ideal reverse base current.

14 `isc (A)` Reverse substrate current.

15 `isf (A)` Reverse leakage current of the substrate-base junction.

16 `ip (A)` Main current.

17 `betadc (A/A)` Ratio of DC collector current to DC base current.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 18 `vbc` (V) Base-collector voltage.
- 19 `vbe` (V) Base-emitter voltage.
- 20 `vce` (V) Collector-emitter voltage.
- 21 `vsb` (V) Substrate-base voltage.
- 22 `rcex` (Ω) External part of the collector resistance.
- 23 `rcint` (Ω) Internal part of the collector resistance.
- 24 `reex` (Ω) External part of the emitter resistance.
- 25 `reint` (Ω) Internal part of the emitter resistance.
- 26 `rbc` (Ω) Base resistance under the collector.
- 27 `rbe` (Ω) Base resistance under the emitter.
- 28 `rsb` (Ω) Ohmic leakage across the substrate-base junction.
- 29 `pwr` (W) Power.
- 30 `gfl` (S) Forward conductance, lateral path.
- 31 `grl` (S) Reverse conductance, lateral path.
- 32 `g11v` (S) Forward conductance, vertical path.
- 33 `g12v` (S) Collector Early-effect on I_{fver} .
- 34 `g21v` (S) Emitter Early-effect on I_{rver} .
- 35 `g22v` (S) Reverse conductance, vertical path.
- 36 `gpiv` (S) Conductance emitter-base junction.
- 37 `gmuv` (S) Conductance collector-base junction.
- 38 `gbe` (S) Emitter-side: base conductance B1-B.
- 39 `gibe` (S) Emitter Early-effect on I_{b1b} .

Spectre Circuit Simulator Reference

Component Statements Part 1

- 40 `gbc` (S) Collector-side: base conductance B2-B.
- 41 `gibc` (S) Collector Early-effect on Ib2b.
- 42 `gise` (S) Transconductance (parasitic PNP) e-b-s transistor.
- 43 `gisc` (S) Transconductance (parasitic PNP) c-b-s transistor.
- 44 `gsb` (S) Conductance substrate-base junction.
- 45 `cpil` (F) Forward diffusion capacitance, lateral path.
- 46 `cipil` (F) Collector Early-effect on Qflat.
- 47 `cpiv` (F) Forward total capacitance, vertical path.
- 48 `cmul` (F) Reverse diffusion capacitance, lateral path.
- 49 `cimul` (F) Emitter Early-effect on Qrlat.
- 50 `cmuv` (F) Reverse total capacitance, vertical path.
- 51 `csb` (F) Total capacitance substrate-base junction.
- 52 `irbe` (A) Ideal total forward base current.
- 53 `irbc` (A) Ideal total reverse base current.
- 54 `irsb` (A) Substrate base leakage resistance current.

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

<code>ae</code>	M-58	<code>gibc</code>	OP-41	<code>kf</code>	M-65	<code>tfnt</code>	O-20
<code>af</code>	M-66	<code>gibe</code>	OP-39	<code>m</code>	I-3	<code>tfvr</code>	M-33
<code>area</code>	I-1	<code>gisc</code>	OP-43	<code>mult</code>	I-2	<code>tfvrt</code>	O-19
<code>betadc</code>	OP-17	<code>gise</code>	OP-42	<code>pc</code>	M-42	<code>tlat</code>	M-32

Spectre Circuit Simulator Reference

Component Statements Part 1

bf	M-5	gmuv	OP-37	pe	M-37	tlatt	O-18
bft	O-2	gpiv	OP-36	ps	M-45	tnom	M-48
br	M-12	grl	OP-31	pwr	OP-29	tr	M-49
brt	O-7	gsb	OP-44	rbc	OP-26	tref	M-47
cimul	OP-49	ib	OP-2	rbcc	M-25	trise	M-51
cipil	OP-46	ibf	M-6	rbcct	O-13	trn	M-39
cjc	M-40	ibft	O-3	rbcv	M-26	trnt	O-24
cjct	O-25	ibr	M-13	rbcvt	O-14	trvr	M-38
cje	M-35	ibrtr	O-8	rbe	OP-27	trvrt	O-23
cjet	O-21	ic	OP-1	rbec	M-27	type	M-1
cjs	M-43	ie	OP-3	rbecl	O-15	vbc	OP-18
cjst	O-27	iflat	OP-5	rbev	M-28	vbe	OP-19
cmul	OP-48	ifver	OP-7	rbevt	O-16	vce	OP-20
cmuv	OP-50	ik	M-8	rcex	OP-22	vdc	M-41
cpil	OP-45	ikt	O-4	rcex	M-23	vdct	O-26
cpiv	OP-47	ilc	OP-13	rcin	M-24	vde	M-36
csb	OP-51	ile	OP-10	rcint	O-12	vdet	O-22
dta	M-50	imax	M-4	rcint	OP-23	vds	M-44
eafl	M-10	ip	OP-16	reex	OP-24	vdst	O-28
eaflt	O-5	irbc	OP-53	reex	M-29	vgb	M-55
eafov	M-11	irbe	OP-52	region	I-4	vgcb	M-53
eafov	O-6	irc	OP-12	rein	M-30	vge	M-56
earl	M-16	ire	OP-9	reint	OP-25	vgeb	M-52
earlt	O-9	irlat	OP-6	reint	O-17	vgje	M-57
earv	M-17	irsb	OP-54	rsb	OP-28	vgsb	M-54
earvt	O-10	irver	OP-8	rsb	M-31	vlf	M-7
exphi	M-46	is	M-3	snb	M-60	vlr	M-14
g1lv	OP-32	isc	OP-14	snbn	M-61	vsb	OP-21
g12v	OP-33	ise	OP-11	spb	M-59	xcs	M-20
g2lv	OP-34	isf	OP-15	spc	M-63	xes	M-18
g22v	OP-35	iss	M-22	spe	M-62	xhcs	M-21
gbc	OP-40	isst	O-11	struct	M-2	xhes	M-19
gbe	OP-38	ist	O-1	sx	M-64	xifv	M-9
gfl	OP-30	isub	OP-4	tfn	M-34	xirv	M-15

Vertical NPN/PNP Transistor (bjt503)

Description

The bjt503 model provides a detailed description of a vertical integrated NPN and PNP transistor. It is described in the Philips Bipolar Modelbook (Dec.95) as TN/TNS and TP/TPS level 503.

Spectre Circuit Simulator Reference

Component Statements Part 1

The NPN is also described in Nat.Lab. Unclassified Report Nr. 006/94 as Mextram Bipolar Transistor Model. Information on how to obtain this document can be found on Source Link by searching for Philips.

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In addition to the model description a `level` parameter is added. Via the `level` parameter the user can switch between Philips Bipolar Modelbook (Dec.95) and Philips Bipolar Modelbook (Dec.94).

The `imax` parameter is used to aid convergence and to prevent numerical overflow. The junction characteristics of the transistor are accurately modeled for currents up to `imax`. For currents above `imax`, the junction is modeled as a linear resistor and a warning is printed.

The descriptions of the operating point derivatives are given for the NPN type. For the PNP type the terminal voltage in the descriptions has to be exchanged. E.g.:

NPN: $g_x = dI_n/dV_{b2e1}$

PNP: $g_x = dI_n/dV_{e1b2}$

This device is supported within altergroups.

This device is dynamically loaded from the shared object `/vobs/spectre_dev/tools.sun4v/spectre/lib/cmi/2.0.doc/libphilips.so`

Sample Instance Statement

```
q4 (vcc net3 minus) npn_mod region=fwd m=1 mult=1
```

Sample Model Statement

```
model npn_mod bjt503 type=npn level=2 exmod=1 is=1e-14 bf=85 ik=95e-6 rbc=50  
cje=0.352e-12
```

Instance Definition

Name `c b e` [s] ModelName parameter=value ...

Instance Parameters

1 `area=1`Area factor.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 2 `mult=1` Alias of area factor.
- 3 `m=1` Multiplication factor.
- 4 `region=fwd` Estimated DC operating region, used as a convergence aid.
Possible values are `off`, `fwd`, `rev`, or `sat`.

Model Definition

`model modelName bjt503 parameter=value ...`

Model Parameters

- 1 `type=npn` Transistor type.
Possible values are `npn`, `npnv`, `pnv`, or `pnv`.
- 2 `level=2.0` Transistor Level. Possible values are 1 (Philips Bipolar Modelbook Dec.94) or 2 (Philips Bipolar Modelbook Dec.95).
- 3 `exmod=0` Flag for extended modeling of the reverse current gain.
- 4 `exphi=0` Flag for distributed high frequency effects.
- 5 `exavl=1` Flag for extended modeling of avalanche currents.
- 6 `is=5.0e-17 A` Collector-emitter saturation current.
- 7 `bf=140.0 A/A` Ideal forward current gain.
- 8 `xibi=0.0` Fraction of ideal base current that belongs to the sidewall.
- 9 `ibf=2.0e-14 A` Saturation current of the non-ideal forward base current.
- 10 `vlf=0.5 V` Cross-over voltage of the non-ideal forward base current.
- 11 `ik=15.0e-3 A` High-injection knee current.
- 12 `bri=16.0 A/A` Ideal reverse current gain.
- 13 `ibr=8.0e-15 A` Saturation current of the non-ideal reverse base current.
- 14 `vlr=0.5 V` Cross-over voltage of the non-ideal reverse base current.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 15 `xext=0.5` Part of I_{ex} , Q_{ex} , Q_{tex} and I_{sub} that depends on V_{bc1} .
- 16 `qbo=1.2e-12` Coul Base charge at zero bias.
- 17 `eta=4.0` Factor of the built-in field of the base.
- 18 `avl=50.0` Weak avalanche parameter.
- 19 `efi=0.7` Electric field intercept (with $ex_{avl}=1$).
- 20 `ihc=3.0e-3` A Critical current for hot carriers.
- 21 `rcc=25.0` Ω Constant part of the collector resistance.
- 22 `rcv=750.0` Ω Resistance of the unmodulated epilayer.
- 23 `scrcv=1000.0` Ω Space charge resistance of the epilayer.
- 24 `sfh=0.6` Current spreading factor epilayer.
- 25 `rbc=50.0` Ω Constant part of the base resistance.
- 26 `rbv=100.0` Ω Variable part of the base resistance at zero bias.
- 27 `re=2.0` Ω Emitter series resistance.
- 28 `taune=3.0e-10` s Minimum delay time of neutral and emitter charge.
- 29 `mtau=1.18` Non-ideality factor of the neutral and emitter charge.
- 30 `cje=2.5e-13` F Zero bias emitter-base depletion capacitance.
- 31 `vde=0.9` V Emitter-base diffusion voltage.
- 32 `pe=0.33` Emitter-base grading coefficient.
- 33 `xcje=0.5` Fraction of the e-b depletion cap. that belongs to the sidewall.
- 34 `cjc=1.3e-13` F Zero bias collector-base depletion capacitance.
- 35 `vdc=0.6` V Collector-base diffusion voltage.
- 36 `pc=0.4` Collector-base grading coefficient variable part.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 37 `xp=0.2` Constant part of `cjc`.
- 38 `mc=0.5` Collector current modulation coefficient.
- 39 `xcjc=0.1` Fraction of the collector-base depletion cap. under the emitter area.
- 40 `tref (C)` Reference temperature. Default set by option `tnom`.
- 41 `tnom (C)` Alias of `tref`. Default set by option `tnom`.
- 42 `tr (C)` Alias of `tref`. Default set by option `tnom`.
- 43 `dta=0.0 K` Difference of the device temperature to the ambient temperature.
- 44 `trise=0.0 K` Alias of `dta`.
- 45 `vge=1.01 V` Band-gap voltage of the emitter.
- 46 `vgb=1.18 V` Band-gap voltage of the base.
- 47 `vgc=1.205 V` Band-gap voltage of the collector.
- 48 `vgj=1.1 V` Band-gap voltage recombination emitter-base junction.
- 49 `vi=0.04 V` Ionization voltage base dope.
- 50 `na=3.0e17 cm-3` Maximum base dope concentration.
- 51 `er=2.0e-3` Temperature coefficient of `vlf` and `vlr`.
- 52 `ab=1.35` Temperature coefficient resistivity base.
- 53 `aepi=2.15` Temperature coefficient resistivity of the epilayer.
- 54 `aex=1.0` Temperature coefficient resistivity of the extrinsic base.
- 55 `ac=0.4` Temperature coefficient resistivity of the buried layer.
- 56 `kf=2.0e-16` Flicker noise coefficient ideal base current.
- 57 `kfn=2.0e-16` Flicker noise coefficient non-ideal base current.
- 58 `af=1.0` Flicker noise exponent.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 59 `iss=6.0e-16` ABase-substrate saturation current.
- 60 `iks=5.0e-6` AKnee current of the substrate.
- 61 `cjs=1.0e-12` FZero bias collector-substrate depletion capacitance.
- 62 `vds=0.5` VCollector-substrate diffusion voltage.
- 63 `ps=0.33`Collector-substrate grading coefficient.
- 64 `vgs=1.15` VBand-gap voltage of the substrate.
- 65 `as=2.15`For a closed buried layer: `as=ac`. For an open buried layer: `as=aepi`.
- 66 `imax=1.0` AExplosion current.

Output Parameters

- 1 `ist` (A)Collector-Emitter saturation current.
- 2 `bft` (A/A)Ideal forward current gain.
- 3 `ibft` (A)Saturation current of the non-ideal forward base current.
- 4 `vlft` (V)Cross-over voltage of the non-ideal forward base current.
- 5 `ikt` (A)High-injection knee current.
- 6 `ibr` (A)Saturation current of the non-ideal reverse base current.
- 7 `vlr` (V)Cross-over voltage of the non-ideal reverse base current.
- 8 `qbot` (Coul)Base charge at zero bias.
- 9 `avlt`Weak avalanche parameter.
- 10 `rcct` (Ω)Constant part of the collector resistance.
- 11 `rcvt` (Ω)Resistance of the unmodulated epilayer.
- 12 `rbct` (Ω)Constant part of the base resistance.
- 13 `rbvt` (Ω)Variable part of the base resistance at zero bias.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 14 `taunet (s)` Minimum delay time of neutral and emitter charge.
- 15 `mtaut` Non-ideality factor of the neutral and emitter charge.
- 16 `cjet (F)` Zero bias emitter-base depletion capacitance.
- 17 `vdet (V)` Emitter-base diffusion voltage.
- 18 `cjct (F)` Zero bias collector-base depletion capacitance.
- 19 `vdct (V)` Collector-base diffusion voltage.
- 20 `xpt` Constant part of `cjc`.
- 21 `isst (A)` Base-substrate saturation current.
- 22 `ikst (A)` Knee current of the substrate.
- 23 `cjst (F)` Zero bias collector-substrate depletion capacitance.
- 24 `vdst (V)` Collector-substrate diffusion voltage.

Operating-Point Parameters

- 1 `ib (A)` Base current.
- 2 `ic (A)` Collector current.
- 3 `ie (A)` Emitter current.
- 4 `is (A)` Substrate current.
- 5 `vbe (V)` Base-emitter voltage.
- 6 `vbc (V)` Base-collector voltage.
- 7 `vce (V)` Collector-emitter voltage.
- 8 `vsc (V)` Substrate voltage.
- 9 `re (Ω)` Constant emitter resistance.
- 10 `rcc (Ω)` Constant collector resistance.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 11 `rbc` (Ω) Constant part of base resistance.
- 12 `betadc` (A/A) DC current gain.
- 13 `pwr` (W) Power.
- 14 `vble1` (V) Internal voltage.
- 15 `vb2e1` (V) Internal voltage.
- 16 `vb2c1` (V) Internal voltage.
- 17 `vb2c2` (V) Internal voltage.
- 18 `vb1b2` (V) Internal voltage.
- 19 `vb1c1` (V) Internal voltage.
- 20 `vbc1` (V) Internal voltage.
- 21 `in` (A) Main current.
- 22 `ic1c2` (A) Variable collector resistance current.
- 23 `ib1` (A) Bulk component of ideal base current.
- 24 `ib1s` (A) Sidewall component of ideal base current.
- 25 `ib2` (A) Non-ideal base current.
- 26 `iav1` (A) Weak avalanche current.
- 27 `ib1b2` (A) Variable base resistance current.
- 28 `ib3` (A) Non-ideal reverse base current.
- 29 `isex` (A) Internal extrinsic base current.
- 30 `isub` (A) Internal base-substrate current.
- 31 `isf` (A) Substrate-collector current.
- 32 `xiex` (A) External extrinsic base current.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 33 `xisub (A)` External base-substrate current.
- 34 `gx (S)` dI_n/dV_{b2e1} .
- 35 `gy (S)` dI_n/dV_{b2c2} .
- 36 `gz (S)` dI_n/dV_{b2c1} .
- 37 `grcvy (S)` dI_{c1c2}/dV_{b2c2} .
- 38 `grcvz (S)` dI_{c1c2}/dV_{b2c1} .
- 39 `gpi (S)` Conductance floor base-emitter junction: $dI_{b1}/dV_{b2e1} + dI_{b2}/dV_{b2e1}$.
- 40 `sgpi (S)` Conductance sidewall base-emitter junction: dI_{b1S}/dV_{b1e1} .
- 41 `gmux (S)` Dependence avalanche multiplication on internal b-e junction: $-dI_{avl}/dV_{b2e1}$.
- 42 `gmu (S)` Dependence avalanche multiplication on internal b-c junction: $-dI_{avl}/dV_{b2c2}$.
- 43 `gmuz (S)` Dependence avalanche multiplication on external b-c junction: $-dI_{avl}/dV_{b2c1}$.
- 44 `grbv (S)` dI_{b1b2}/dV_{b1b2} .
- 45 `grbvz (S)` Emitter Early-effect on I_{b1b2} : dI_{b1b2}/dV_{b2e1} .
- 46 `grbvy (S)` Internal collector Early-effect on I_{b1b2} : dI_{b1b2}/dV_{b2c2} .
- 47 `grbvz (S)` External collector Early effect on I_{b1b2} : dI_{b1b2}/dV_{b2c1} .
- 48 `gmux (S)` Conductance floor extrinsic b-c junction: $dI_{lex}/dV_{b1c1} + dI_{sub}/dV_{b1c1} + dI_{b3}/dV_{b1c1}$.
- 49 `xgmux (S)` Conductance sidewall extrinsic b-c junction: $dX_{lex}/dV_{bc1} + dX_{Isub}/dV_{bc1}$.
- 50 `gsub (S)` Conductance s-c junction: dI_{sf}/dV_{sc1} .
- 51 `gpnnp (S)` Transconductance floor extrinsic PNP transistor: dI_{sub}/dV_{b1c1} .
- 52 `xgpnnp (S)` Transconductance sidewall extrinsic PNP transistor: dX_{Isub}/dV_{bc1} .
- 53 `cbex (F)` Capacitance floor b-e junction: $dQ_{te}/dV_{b2e1} + dQ_{be}/dV_{b2e1} + dQ_n/dV_{b2e1}$.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 54 `cbey` (F) Internal collector Early-effect on Q_{be} : dQ_{be}/dV_{b2c2} .
- 55 `cbez` (F) External collector Early-effect on Q_{be} : dQ_{be}/dV_{b2c1} .
- 56 `scte` (F) Dependence of Q_{teS} on internal b-e junction: dQ_{teS}/dV_{b2e1} .
- 57 `cbcx` (F) Emitter Early-effect on Q_{bc} : dQ_{bc}/dV_{b2e1} .
- 58 `cbcy` (F) Capacitance intrinsic b-c junction: $dQ_{tc}/dV_{b2c2} + dQ_{bc}/dV_{b2c2} + dQ_{epi}/dV_{b2c2}$.
- 59 `cbcz` (F) Collector Early-effect on Q_{tc} : $dQ_{tc}/dV_{b2c1} + dQ_{bc}/dV_{b2c1} + dQ_{epi}/dV_{b2c1}$.
- 60 `cb1b2` (F) Capacitance AC current crowding: $dQ_{b1b2}/dV_{b1b2} = C_b$.
- 61 `cb1b2x` (F) Dependence of Q_{b1b2} on internal b-e junction voltage: dQ_{b1b2}/dV_{b2e1} .
- 62 `cbcx` (F) Capacitance floor extrinsic b-c junction: $dQ_{tex}/dV_{b1c1} + dQ_{ex}/dV_{b1c1}$.
- 63 `xcbcex` (F) Capacitance sidewall extrinsic b-c junction: $dXQ_{tex}/dV_{bc1} + dXQ_{ex}/dV_{bc1}$.
- 64 `cts` (F) Capacitance s-c junction: $dQ_{tex}/dV_{b1c1} + dQ_{ex}/dV_{b1c1}$.

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

<code>Vb1b2</code>	OP-18	<code>eta</code>	M-17	<code>imax</code>	M-66	<code>sgpi</code>	OP-40
<code>Vb1c1</code>	OP-19	<code>exavl</code>	M-5	<code>in</code>	OP-21	<code>taune</code>	M-28
<code>Vb1e1</code>	OP-14	<code>exmod</code>	M-3	<code>is</code>	OP-4	<code>taunet</code>	O-14
<code>Vb2c1</code>	OP-16	<code>exphi</code>	M-4	<code>is</code>	M-6	<code>tnom</code>	M-41
<code>Vb2c2</code>	OP-17	<code>gmu</code>	OP-42	<code>isf</code>	OP-31	<code>tr</code>	M-42
<code>Vb2e1</code>	OP-15	<code>gmux</code>	OP-48	<code>iss</code>	M-59	<code>tref</code>	M-40
<code>Vbc1</code>	OP-20	<code>gmux</code>	OP-41	<code>isst</code>	O-21	<code>trise</code>	M-44
<code>Xisub</code>	OP-33	<code>gmuz</code>	OP-43	<code>ist</code>	O-1	<code>type</code>	M-1
<code>ab</code>	M-52	<code>gpi</code>	OP-39	<code>isub</code>	OP-30	<code>vbc</code>	OP-6
<code>ac</code>	M-55	<code>gpn</code>	OP-51	<code>kf</code>	M-56	<code>vbe</code>	OP-5
<code>aepi</code>	M-53	<code>grbv</code>	OP-44	<code>kfn</code>	M-57	<code>vce</code>	OP-7

Spectre Circuit Simulator Reference

Component Statements Part 1

aex	M-54	grbvz	OP-47	level	M-2	vdc	M-35
af	M-58	grbvy	OP-46	m	I-3	vdct	O-19
area	I-1	grbvz	OP-47	mc	M-38	vde	M-31
as	M-65	grcvy	OP-37	mtau	M-29	vdet	O-17
avl	M-18	grcvz	OP-38	mtaut	O-15	vds	M-62
avlt	O-9	gsub	OP-50	mult	I-2	vdst	O-24
betadc	OP-12	gx	OP-34	na	M-50	vgb	M-46
bf	M-7	gy	OP-35	pc	M-36	vgc	M-47
bft	O-2	gz	OP-36	pe	M-32	vge	M-45
bri	M-12	iavl	OP-26	ps	M-63	vgj	M-48
cb1b2	OP-60	ib	OP-1	pwr	OP-13	vgs	M-64
cb1b2x	OP-61	ib1	OP-23	qbo	M-16	vi	M-49
cbcex	OP-62	ib1b2	OP-27	qbot	O-8	vlf	M-10
cbcx	OP-57	ib1s	OP-24	rbc	OP-11	vlft	O-4
cbcy	OP-58	ib2	OP-25	rbc	M-25	vlr	M-14
cbcz	OP-59	ib3	OP-28	rbct	O-12	vlrt	O-7
cbex	OP-53	ibf	M-9	rbv	M-26	vsc	OP-8
cbey	OP-54	ibft	O-3	rbvt	O-13	xcbcex	OP-63
cbez	OP-55	ibr	M-13	rcc	OP-10	xcjc	M-39
cjc	M-34	ibrt	O-6	rcc	M-21	xcje	M-33
cjct	O-18	ic	OP-2	rcct	O-10	xext	M-15
cje	M-30	ic1c2	OP-22	rcv	M-22	xgmux	OP-49
cjet	O-16	ie	OP-3	rcvt	O-11	xgpnnp	OP-52
cjs	M-61	iex	OP-29	re	M-27	xibi	M-8
cjst	O-23	ihc	M-20	re	OP-9	xiex	OP-32
cts	OP-64	ik	M-11	region	I-4	xp	M-37
dta	M-43	iks	M-60	scrcv	M-23	xpt	O-20
efi	M-19	ikst	O-22	scte	OP-56		
er	M-51	ikt	O-5	sfh	M-24		

Vertical NPN/PNP Transistor (bjt504)

Description

The bjt504 model provides a detailed description of a vertical integrated NPN and PNP transistor. It is described in the Philips Bipolar Modelbook (Dec.95) as TN/TNS and TP/TPS level 504.

The NPN is also described in Nat.Lab. Unclassified Report Nr. 006/94 as Mextram Bipolar Transistor Model. Information on how to obtain this document can be found on Source Link by searching for Philips.

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Spectre Circuit Simulator Reference

Component Statements Part 1

In addition to the model description a `level` parameter is added. Via the `level` parameter the user can switch between Philips Bipolar Modelbook (Dec.95) and Philips Bipolar Modelbook (Dec.94).

The `imax` parameter is used to aid convergence and to prevent numerical overflow. The junction characteristics of the transistor are accurately modeled for currents up to `imax`. For currents above `imax`, the junction is modeled as a linear resistor and a warning is printed.

The descriptions of the operating point derivatives are given for the NPN type. For the PNP type the terminal voltage in the descriptions has to be exchanged. E.g.:

NPN: $g_x = dI_n/dV_{b2e1}$

PNP: $g_x = dI_n/dV_{e1b2}$

This device is supported within altergroups.

This device is dynamically loaded from the shared object `/vobs/spectre_dev/tools.sun4v/spectre/lib/cmi/2.0.doc/libphilips.so`

Instance Definition

Name C B E [S] ModelName parameter=value ...

Instance Parameters

1 `level=XLEVEL.`

2 `mult=XMULT.`

3 `tref=XTREF.`

4 `dta=XDTA.`

5 `exmod=XEXMOD.`

6 `exphi=XEXPHI.`

7 `exavl=XEXAVL.`

8 `is=XIS.`

9 `ik=XIK.`

Spectre Circuit Simulator Reference

Component Statements Part 1

10 ver=XVER.
11 vef=XVEF.
12 bf=XBF.
13 ibf=XIBF.
14 mlf=XMLF.
15 xibi=XXIBI.
16 bri=XBRI.
17 ibr=XIBR.
18 vlr=XVLR.
19 xext=XXEXT.
20 wavl=XWAVL.
21 vavl=XVAVL.
22 sfh=XSFH.
23 re=XRE.
24 rbc=XRBC.
25 rbv=XRBV.
26 rcc=XRCC.
27 rcv=XRCV.
28 scrcv=XSCRCV.
29 ihc=XIHC.
30 axi=XAXI.
31 cje=XCJE.

Spectre Circuit Simulator Reference

Component Statements Part 1

32 vde=XVDE.

33 pe=XPE.

34 xcje=XXCJE.

35 cbeo=XCBEQ.

36 cjc=XCJC.

37 vdc=XVDC.

38 pc=XPC.

39 xp=XXP.

40 mc=XMC.

41 xcjc=XXCJC.

42 cbco=XCBCO.

43 mtau=XMTAU.

44 taue=XTAUE.

45 taub=XTAUB.

46 tepi=XTEPI.

47 taur=XTAUR.

48 deg=XDEG.

49 xrec=XXREC.

50 aqbo=XAQBO.

51 ae=XAE.

52 ab=XAB.

53 aepe=XAEPI.

Spectre Circuit Simulator Reference

Component Statements Part 1

54 `aex=XAEX.`

55 `ac=XAC.`

56 `dvgbf=XDVGBF.`

57 `dvgbr=XDVGBR.`

58 `vgb=XVGB.`

59 `vgc=XVGC.`

60 `vgj=XVGJ.`

61 `dvgte=XDVGTE.`

62 `af=XAF.`

63 `kf=XKF.`

64 `kfn=XKFN.`

65 `iss=XISS.`

66 `iks=XIKS.`

67 `cjs=XCJS.`

68 `vds=XVDS.`

69 `ps=XPS.`

70 `vgs=XVGS.`

71 `as=XAS.`

Model Definition

`model modelName bjt504 parameter=value ...`

Spectre Circuit Simulator Reference

Component Statements Part 1

Model Parameters

- 1 `type=npn` Transistor type.
Possible values are npn or pnp.
- 2 `level=NULLLEVEL.`
- 3 `mult=NULLMULT.`
- 4 `tref=NULLTREF.`
- 5 `dta=NULLDTA.`
- 6 `exmod=NULLEXMOD.`
- 7 `exphi=NULLEXPHI.`
- 8 `exavl=NULLEXAVL.`
- 9 `is=NULLIS.`
- 10 `ik=NULLIK.`
- 11 `ver=NULLVER.`
- 12 `vef=NULLVEF.`
- 13 `bf=NULLBF.`
- 14 `ibf=NULLIBF.`
- 15 `mlf=NULLMLF.`
- 16 `xibi=NULLXIBI.`
- 17 `bri=NULLBRI.`
- 18 `ibr=NULLIBR.`
- 19 `vlr=NULLVLR.`
- 20 `xext=NULLXEXT.`

Spectre Circuit Simulator Reference

Component Statements Part 1

21 wavl=NULLWAVL.
22 vavl=NULLVAVL.
23 sfh=NULLSFH.
24 re=NULLRE.
25 rbc=NULLRBC.
26 rbv=NULLRBV.
27 rcc=NULLRCC.
28 rcv=NULLRCV.
29 srcrv=NULLSCRCV.
30 ihc=NULLIHC.
31 axi=NULLAXI.
32 cje=NULLCJE.
33 vde=NULLVDE.
34 pe=NULLPE.
35 xcje=NULLXCJE.
36 cbeo=NULLCBEO.
37 cjc=NULLCJC.
38 vdc=NULLVDC.
39 pc=NULLPC.
40 xp=NULLXP.
41 mc=NULLMC.
42 xcjc=NULLXCJC.

Spectre Circuit Simulator Reference

Component Statements Part 1

43 cbco=NULLCBCO.

44 mtau=NULLMTAU.

45 taue=NULLTAUE.

46 taub=NULLTAUB.

47 tepi=NULLTEPI.

48 taur=NULLTAUR.

49 deg=NULLDEG.

50 xrec=NULLXREC.

51 aqbo=NULLAQBO.

52 ae=NULLAE.

53 ab=NULLAB.

54 aepi=NULLAEPI.

55 aex=NULLAEX.

56 ac=NULLAC.

57 dvgbf=NULLDVGBF.

58 dvgbr=NULLDVGBR.

59 vgb=NULLVGB.

60 vgc=NULLVGC.

61 vgj=NULLVGJ.

62 dvgte=NULLDVGTE.

63 af=NULLAF.

64 kf=NULLKF.

Spectre Circuit Simulator Reference

Component Statements Part 1

65 kfn=NULLKFN.

66 iss=NULLISS.

67 iks=NULLIKS.

68 cjs=NULLCJS.

69 vds=NULLVDS.

70 ps=NULLPS.

71 vgs=NULLVGS.

72 as=NULLAS.

Operating-Point Parameters

1 level=XLEVEL.

2 mult=XMULT.

3 tref=XTREF.

4 dta=XDTA.

5 exmod=XEXMOD.

6 exphi=XEXPHI.

7 exavl=XEXAVL.

8 is=XIS.

9 ik=XIK.

10 ver=XVER.

11 vef=XVEF.

12 bf=XBF.

13 ibf=XIBF.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 14 mlf=XMLF.
- 15 xibi=XXIBI.
- 16 bri=XBRI.
- 17 ibr=XIBR.
- 18 vlr=XVLR.
- 19 xext=XXEXT.
- 20 wavl=XWAVL.
- 21 vavl=XVAVL.
- 22 sfh=XSFH.
- 23 re=XRE.
- 24 rbc=XRBC.
- 25 rbv=XRBV.
- 26 rcc=XRCC.
- 27 rcv=XRCV.
- 28 scrcv=XSCRCV.
- 29 ihc=XIHC.
- 30 axi=XAXI.
- 31 cje=XCJE.
- 32 vde=XVDE.
- 33 pe=XPE.
- 34 xcje=XXCJE.
- 35 cbeo=XCBEO.

Spectre Circuit Simulator Reference

Component Statements Part 1

36 cjc=XCJC.
37 vdc=XVDC.
38 pc=XPC.
39 xp=XXP.
40 mc=XMC.
41 xcjc=XXCJC.
42 cbco=XCBCO.
43 mtau=XMTAU.
44 taue=XTAUE.
45 taub=XTAUB.
46 tepi=XTEPI.
47 taur=XTAUR.
48 deg=XDEG.
49 xrec=XXREC.
50 aqbo=XAQBO.
51 ae=XAE.
52 ab=XAB.
53 aepe=XAEPI.
54 aex=XAEX.
55 ac=XAC.
56 dvgbf=XDVGBF.
57 dvgbr=XDVGBR.

Spectre Circuit Simulator Reference

Component Statements Part 1

58 vgb=XVGB.

59 vgc=XVGC.

60 vgj=XVGJ.

61 dvgte=XDVGTE.

62 af=XAF.

63 kf=XKF.

64 kfn=XKFN.

65 iss=XISS.

66 iks=XIKS.

67 cjs=XCJS.

68 vds=XVDS.

69 ps=XPS.

70 vgs=XVGS.

71 as=XAS.

72 vb2e1Vb2e1.

73 vb2c2Vb2c2.

74 vb2c1Vb2c1.

75 vb1c1Vb1c1.

76 ve1eVe1e.

77 InIn.

78 Ic1c2Ic1c2.

79 Ib1b2Ib1b2.

Spectre Circuit Simulator Reference

Component Statements Part 1

80 Ib1Ib1.

81 SIb1SIb1.

82 Ib2Ib2.

83 Ib3Ib3.

84 IexIex.

85 XIexXIex.

86 IavlIavl.

87 IREIRE.

88 IRBCIRBC.

89 IRCCIRCC.

90 QeQe.

91 QteQte.

92 SQteSQte.

93 QbeQbe.

94 QbcQbc.

95 QtcQtc.

96 QepiQepi.

97 Qb1b2Qb1b2.

98 QtexQtex.

99 XQtexXQtex.

100 QexQex.

101 XQexXQex.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 102 gxgx.
- 103 gygy.
- 104 gzgz.
- 105 SgpiSgpi.
- 106 gpixgpix.
- 107 gpiygpiy.
- 108 gpizgpiz.
- 109 gmuxgmux.
- 110 gmuygmuy.
- 111 gmuzgmuz.
- 112 gmuexgmuex.
- 113 XgmueXgmueX.
- 114 grcvygrcvy.
- 115 grcvzgrcvz.
- 116 rbvrbv.
- 117 grbvXgrbvX.
- 118 grbvygrbvy.
- 119 grbvzgrbvz.
- 120 RERE.
- 121 RBCRBC.
- 122 RCCRCC.
- 123 SCbeSCbe.

Spectre Circuit Simulator Reference

Component Statements Part 1

124 CbexCbex.

125 CbeyCbey.

126 CbezCbez.

127 CbcxCbcx.

128 CbcyCbcy.

129 CbczCbcz.

130 CbcexCbcex.

131 XCbcexXCbcex.

132 Cb1b2Cb1b2.

133 Cb1b2xCb1b2x.

134 Cb1b2yCb1b2y.

135 Cb1b2zCb1b2z.

136 gmgm.

137 betabeta.

138 goutgout.

139 gmugmu.

140 RBRB.

141 CbeCbe.

142 CbcCbc.

143 fTfT.

144 Iqslqs.

145 XiWepiXiWepi.

Spectre Circuit Simulator Reference

Component Statements Part 1

146 `Vb2c2starVb2c2star`.

147 `PdissPdiss`.

148 `TKTK`.

149 `IsubIsub`.

150 `XIsubXIsub`.

151 `IsfIsf`.

152 `QtsQts`.

153 `gSgS`.

154 `XgSXgS`.

155 `gSfgSf`.

156 `CtsCts`.

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

<code>Cb1b2</code>	OP-132	<code>aex</code>	OP-54	<code>grbvz</code>	OP-119	<code>scrcv</code>	OP-28
<code>Cb1b2x</code>	OP-133	<code>aex</code>	M-55	<code>grcvy</code>	OP-114	<code>sfh</code>	OP-22
<code>Cb1b2y</code>	OP-134	<code>af</code>	I-62	<code>grcvz</code>	OP-115	<code>sfh</code>	I-22
<code>Cb1b2z</code>	OP-135	<code>af</code>	M-63	<code>gx</code>	OP-102	<code>sfh</code>	M-23
<code>Cbc</code>	OP-142	<code>af</code>	OP-62	<code>gy</code>	OP-103	<code>taub</code>	I-45
<code>Cbcex</code>	OP-130	<code>aqbo</code>	M-51	<code>gz</code>	OP-104	<code>taub</code>	M-46
<code>Cbcx</code>	OP-127	<code>aqbo</code>	OP-50	<code>ibf</code>	I-13	<code>taub</code>	OP-45
<code>Cbcy</code>	OP-128	<code>aqbo</code>	I-50	<code>ibf</code>	OP-13	<code>taue</code>	OP-44
<code>Cbcz</code>	OP-129	<code>as</code>	I-71	<code>ibf</code>	M-14	<code>taue</code>	I-44
<code>Cbe</code>	OP-141	<code>as</code>	M-72	<code>ibr</code>	I-17	<code>taue</code>	M-45
<code>Cbex</code>	OP-124	<code>as</code>	OP-71	<code>ibr</code>	OP-17	<code>taur</code>	OP-47
<code>Cbey</code>	OP-125	<code>axi</code>	I-30	<code>ibr</code>	M-18	<code>taur</code>	M-48

Spectre Circuit Simulator Reference

Component Statements Part 1

Cbez	OP-126	axi	OP-30	ihc	M-30	taur	I-47
Cts	OP-156	axi	M-31	ihc	OP-29	tepi	M-47
IRBC	OP-88	beta	OP-137	ihc	I-29	tepi	I-46
IRCC	OP-89	bf	OP-12	ik	OP-9	tepi	OP-46
IRE	OP-87	bf	I-12	ik	I-9	tref	OP-3
Iavl	OP-86	bf	M-13	ik	M-10	tref	I-3
Ibl	OP-80	bri	M-17	iks	M-67	tref	M-4
Iblb2	OP-79	bri	I-16	iks	OP-66	type	M-1
Ib2	OP-82	bri	OP-16	iks	I-66	vavl	M-22
Ib3	OP-83	cbco	M-43	is	I-8	vavl	I-21
Ic1c2	OP-78	cbco	I-42	is	OP-8	vavl	OP-21
Iex	OP-84	cbco	OP-42	is	M-9	vdc	M-38
In	OP-77	cbeo	I-35	iss	I-65	vdc	I-37
Iqs	OP-144	cbeo	OP-35	iss	M-66	vdc	OP-37
Isf	OP-151	cbeo	M-36	iss	OP-65	vde	OP-32
Isub	OP-149	cjc	I-36	kf	OP-63	vde	M-33
Pdiss	OP-147	cjc	M-37	kf	M-64	vde	I-32
Qblb2	OP-97	cjc	OP-36	kf	I-63	vds	OP-68
Qbc	OP-94	cje	M-32	kfn	I-64	vds	I-68
Qbe	OP-93	cje	I-31	kfn	OP-64	vds	M-69
Qe	OP-90	cje	OP-31	kfn	M-65	vef	I-11
Qepi	OP-96	cjs	OP-67	level	OP-1	vef	M-12
Qex	OP-100	cjs	I-67	level	I-1	vef	OP-11
Qtc	OP-95	cjs	M-68	level	M-2	ver	I-10
Qte	OP-91	deg	I-48	mc	OP-40	ver	OP-10
Qtex	OP-98	deg	OP-48	mc	I-40	ver	M-11
Qts	OP-152	deg	M-49	mc	M-41	vgb	OP-58
RB	OP-140	dta	OP-4	mlf	M-15	vgb	I-58
RBC	OP-121	dta	M-5	mlf	OP-14	vgb	M-59
RCC	OP-122	dta	I-4	mlf	I-14	vgc	I-59
RE	OP-120	dvgbf	I-56	mtau	OP-43	vgc	M-60
SCbe	OP-123	dvgbf	OP-56	mtau	M-44	vgc	OP-59
SIbl	OP-81	dvgbf	M-57	mtau	I-43	vgj	I-60
SQte	OP-92	dvgbr	M-58	mult	M-3	vgj	M-61
Sgpi	OP-105	dvgbr	I-57	mult	OP-2	vgj	OP-60
TK	OP-148	dvgbr	OP-57	mult	I-2	vgs	I-70
Vblc1	OP-75	dvgte	M-62	pc	OP-38	vgs	OP-70
Vb2c1	OP-74	dvgte	OP-61	pc	M-39	vgs	M-71
Vb2c2	OP-73	dvgte	I-61	pc	I-38	vlr	OP-18
Vb2c2star	OP-146	exavl	M-8	pe	M-34	vlr	I-18
Vb2e1	OP-72	exavl	I-7	pe	OP-33	vlr	M-19
Ve1e	OP-76	exavl	OP-7	pe	I-33	wavl	I-20
XCbcex	OP-131	exmod	M-6	ps	M-70	wavl	M-21
XIex	OP-85	exmod	I-5	ps	OP-69	wavl	OP-20
XIsub	OP-150	exmod	OP-5	ps	I-69	xcjc	M-42
XQex	OP-101	exphi	I-6	rbc	I-24	xcjc	OP-41
XQtex	OP-99	exphi	M-7	rbc	M-25	xcjc	I-41

Spectre Circuit Simulator Reference

Component Statements Part 1

XgS	OP-154	exphi	OP-6	rbc	OP-24	xcje	OP-34
Xgmux	OP-113	fT	OP-143	rbv	M-26	xcje	M-35
XiWepi	OP-145	gS	OP-153	rbv	I-25	xcje	I-34
ab	OP-52	gSf	OP-155	rbv	OP-116	xext	M-20
ab	M-53	gm	OP-136	rbv	OP-25	xext	I-19
ab	I-52	gmu	OP-139	rcc	I-26	xext	OP-19
ac	OP-55	gmux	OP-112	rcc	M-27	xibi	M-16
ac	M-56	gmux	OP-109	rcc	OP-26	xibi	OP-15
ac	I-55	gmuy	OP-110	rcv	I-27	xibi	I-15
ae	M-52	gmuz	OP-111	rcv	M-28	xp	OP-39
ae	I-51	gout	OP-138	rcv	OP-27	xp	I-39
ae	OP-51	gpix	OP-106	re	I-23	xp	M-40
aepi	OP-53	gpiy	OP-107	re	M-24	xrec	M-50
aepi	M-54	gpiz	OP-108	re	OP-23	xrec	OP-49
aepi	I-53	grbv	OP-117	scrcv	M-29	xrec	I-49
aex	I-54	grbvy	OP-118	scrcv	I-28		

BSIM1 Field Effect Transistor (bsim1)

Description

BSIM1 is a semiempirical MOSFET model developed at the University of California, Berkeley. All the model parameters are extracted directly from physical devices. Three charge models are available. In SPICE mode, you can refer to BSIM1 as MOS level 4 or BSIM level 1. BSIM1 transistors require that you use a model statement.

This device is supported within altergroups.

To examine the equations used for this component, consult the [Spectre Circuit Simulator Device Model Equations](#) manual.

Sample Instance Statement

```
m1 (1 2 0 0) nchmod l=5u w=10u as=40u ad=40u pd=28u ps=28u m=1
```

Sample Model Statement

```
model nchmod bsim1 vfb0=-0.5 lvfb=0.5 wvfb=0.3 phi0=0.8 eta0=0.056 k1=0.5 muz=454  
eg=0.99 gap1=5.5e-04 trs=1e-3 trd=1e-3 xpart=0.5 rs=10 rd=10
```

Instance Definition

```
Name d g s b ModelName parameter=value ...
```


Spectre Circuit Simulator Reference

Component Statements Part 1

Instance Parameters

- 1 `w` (m) Channel width.
- 2 `l` (m) Channel length.
- 3 `as` (m²) Area of source diffusion.
- 4 `ad` (m²) Area of drain diffusion.
- 5 `ps` (m) Perimeter of source diffusion.
- 6 `pd` (m) Perimeter of drain diffusion.
- 7 `nrd` (m/m) Number of squares of drain diffusion.
- 8 `nrs` (m/m) Number of squares of source diffusion.
- 9 `ld` (m) Drain diffusion length.
- 10 `ls` (m) Source diffusion length.
- 11 `m=1` Multiplicity factor (number of MOSFETs in parallel).
- 12 `region=triode` Estimated operating region.
Possible values are `off`, `triode`, `sat`, `subth`, or `breakdown`.
- 13 `trise` (C) Temperature rise from ambient.
- 14 `degradation=no` Hot-electron degradation flag.
Possible values are `no` or `yes`.

Model Definition

`model modelName bsim1 parameter=value ...`

Model Parameters

Device Type Parameters

- 1 `type=n` Transistor type.
Possible values are `n` or `p`.

Spectre Circuit Simulator Reference

Component Statements Part 1

Threshold Voltage parameters

- 2 $v_{fb0} = -0.8$ V Flat-band voltage.
- 3 $lv_{fb} = 0$ V μm Length dependence of v_{fb} .
- 4 $wv_{fb} = 0$ V μm Width dependence of v_{fb} .
- 5 $pv_{fb} = 0$ V μm Width-length dependence of v_{fb} .
- 6 $\phi_{i0} = 0.75$ V Surface potential.
- 7 $l\phi_i = 0$ V μm Length dependence of ϕ_i .
- 8 $w\phi_i = 0$ V μm Width dependence of ϕ_i .
- 9 $p\phi_i = 0$ V μm Width-length dependence of ϕ_i .
- 10 $k_1 = 0.7$ $\sqrt{\text{V}}$ Body-effect coefficient.
- 11 $lk_1 = 0$ $\sqrt{\text{V}}$ μm Length dependence of k_1 .
- 12 $wk_1 = 0$ $\sqrt{\text{V}}$ μm Width dependence of k_1 .
- 13 $pk_1 = 0$ $\sqrt{\text{V}}$ μm Width-length dependence of k_1 .
- 14 $k_2 = 0$ Charge-sharing parameter.
- 15 $lk_2 = 0$ μm Length dependence of k_2 .
- 16 $wk_2 = 0$ μm Width dependence of k_2 .
- 17 $pk_2 = 0$ μm Width-length dependence of k_2 .
- 18 $\eta_{a0} = 0$ Drain-induced barrier-lowering coefficient.
- 19 $l\eta_a = 0$ μm Length dependence of η_a .
- 20 $w\eta_a = 0$ μm Width dependence of η_a .
- 21 $p\eta_a = 0$ μm Width-length dependence of η_a .
- 22 $x_{2e} = 0$ $1/\text{V}$ Body-bias dependence of η_a .

Spectre Circuit Simulator Reference

Component Statements Part 1

- 23 $lx2e=0$ $\mu\text{m}/\text{V}$ Length dependence of $x2e$.
- 24 $wx2e=0$ $\mu\text{m}/\text{V}$ Width dependence of $x2e$.
- 25 $px2e=0$ $\mu\text{m}/\text{V}$ Width-length dependence of $x2e$.
- 26 $x3e=0$ $1/\text{V}$ Drain-bias dependence of η .
- 27 $lx3e=0$ $\mu\text{m}/\text{V}$ Length dependence of $x3e$.
- 28 $wx3e=0$ $\mu\text{m}/\text{V}$ Width dependence of $x3e$.
- 29 $px3e=0$ $\mu\text{m}/\text{V}$ Width-length dependence of $x3e$.

Mobility Parameters

- 30 $\text{muz}=400$ $\text{cm}^2/\text{V s}$ Low-field mobility.
- 31 $lmuz=0$ $\text{cm}^2/\text{V s}$ Length dependence of muz .
- 32 $wmuz=0$ $\text{cm}^2/\text{V s}$ Width dependence of muz .
- 33 $pmuz=0$ $\text{cm}^2/\text{V s}$ Width-length dependence of muz .
- 34 $x2mz=0$ cm^2/V^2 s Body-bias dependence of muz .
- 35 $lx2mz=0$ $\text{cm}^2 \mu\text{m}/\text{V}^2$ s
Length dependence of $x2mz$.
- 36 $wx2mz=0$ $\text{cm}^2 \mu\text{m}/\text{V}^2$ s
Width dependence of $x2mz$.
- 37 $px2mz=0$ $\text{cm}^2 \mu\text{m}/\text{V}^2$ s
Width-length dependence of $x2mz$.
- 38 $\text{mus}=450$ $\text{cm}^2/\text{V s}$ Mobility in the saturation region.
- 39 $lmus=0$ $\text{cm}^2 \mu\text{m}/\text{V s}$
Length dependence of mus .
- 40 $wmus=0$ $\text{cm}^2 \mu\text{m}/\text{V s}$
Width dependence of mus .

Spectre Circuit Simulator Reference

Component Statements Part 1

- 41 $\text{pmus}=0 \text{ cm}^2 \mu\text{m}/\text{V s}$
Width-length dependence of μ_{us} .
- 42 $\text{x2ms}=0 \text{ cm}^2/\text{V}^2 \text{ s}$ Body-bias dependence of μ_{us} .
- 43 $\text{lx2ms}=0 \text{ cm}^2 \mu\text{m}/\text{V}^2 \text{ s}$
Length dependence of x2ms .
- 44 $\text{wx2ms}=0 \text{ cm}^2 \mu\text{m}/\text{V}^2 \text{ s}$
Width dependence of x2ms .
- 45 $\text{px2ms}=0 \text{ cm}^2 \mu\text{m}/\text{V}^2 \text{ s}$
Width-length dependence of x2ms .
- 46 $\text{x3ms}=0 \text{ cm}^2/\text{V}^2 \text{ s}$ Drain-bias dependence of μ_{us} .
- 47 $\text{lx3ms}=0 \text{ cm}^2 \mu\text{m}/\text{V}^2 \text{ s}$
Length dependence of x3ms .
- 48 $\text{wx3ms}=0 \text{ cm}^2 \mu\text{m}/\text{V}^2 \text{ s}$
Width dependence of x3ms .
- 49 $\text{px3ms}=0 \text{ cm}^2 \mu\text{m}/\text{V}^2 \text{ s}$
Width-length dependence of x3ms .

Mobility Modulation Parameters

- 50 $\text{u00}=0 \text{ 1/V}$ Gate voltage dependence of mobility.
- 51 $\text{lu0}=0 \mu\text{m}/\text{V}$ Length dependence of u0 .
- 52 $\text{wu0}=0 \mu\text{m}/\text{V}$ Width dependence of u0 .
- 53 $\text{pu0}=0 \mu\text{m}/\text{V}$ Width-length dependence of u0 .
- 54 $\text{x2u0}=0 \text{ 1/V}^2$ Body-bias dependence of u0 .
- 55 $\text{lx2u0}=0 \mu\text{m}/\text{V}^2$ Length dependence of x2u0 .
- 56 $\text{wx2u0}=0 \mu\text{m}/\text{V}^2$ Width dependence of x2u0 .
- 57 $\text{px2u0}=0 \mu\text{m}/\text{V}^2$ Width-length dependence of x2u0 .

Spectre Circuit Simulator Reference

Component Statements Part 1

Velocity Saturation Parameters

- 58 $u10=0$ $1/V$ Velocity saturation coefficient.
- 59 $lu1=0$ $\mu\text{m}/V$ Length dependence of $u1$.
- 60 $wu1=0$ $\mu\text{m}/V$ Width dependence of $u1$.
- 61 $pu1=0$ $\mu\text{m}/V$ Width-length dependence of $u1$.
- 62 $x2u1=0$ $1/V^2$ Body-bias dependence of $u1$.
- 63 $lx2u1=0$ $\mu\text{m}/V^2$ Length dependence of $x2u1$.
- 64 $wx2u1=0$ $\mu\text{m}/V^2$ Width dependence of $x2u1$.
- 65 $px2u1=0$ $\mu\text{m}/V^2$ Width-length dependence of $x2u1$.
- 66 $x3u1=0$ $1/V^2$ Drain-bias dependence of $u1$.
- 67 $lx3u1=0$ $\mu\text{m}/V^2$ Length dependence of $x3u1$.
- 68 $wx3u1=0$ $\mu\text{m}/V^2$ Width dependence of $x3u1$.
- 69 $px3u1=0$ $\mu\text{m}/V^2$ Width-length dependence of $x3u1$.

Subthreshold Parameters

- 70 $n0=0$ Subthreshold swing parameter.
- 71 $ln0=0$ μm Length dependence of subthreshold swing parameter.
- 72 $wn0=0$ μm Width dependence of subthreshold swing parameter.
- 73 $pn0=0$ μm Width-length dependence of subthreshold swing parameter.
- 74 $nb=0$ \sqrt{V} Body-bias dependence of $n0$.
- 75 $lnb=0$ \sqrt{V} μm Length dependence of nb .
- 76 $wnb=0$ \sqrt{V} μm Width dependence of nb .
- 77 $pnb=0$ \sqrt{V} μm Width-length dependence of nb .

Spectre Circuit Simulator Reference

Component Statements Part 1

- 78 `nd=0` $1/V$ Drain-bias dependence of `n0`.
- 79 `lnd=0` $\mu\text{m}/V$ Length dependence of `nd`.
- 80 `wnd=0` $\mu\text{m}/V$ Width dependence of `nd`.
- 81 `pnd=0` $\mu\text{m}/V$ Width-length dependence of `nd`.
- 82 `subthmod=2` Subthreshold model selector.

Impact Ionization Parameters

- 83 `ai0=0` $1/V$ Hot-electron effect on `Rout` parameter.
- 84 `lai0=0` $\mu\text{m}/V$ Length dependence of `ai0`.
- 85 `wai0=0` $\mu\text{m}/V$ Width dependence of `ai0`.
- 86 `pai0=0` $\mu\text{m}/V$ Width-length dependence of `ai0`.
- 87 `bi0=0` V Hot-electron effect on `Rout` exponent.
- 88 `lbi0=0` V μm Length dependence of `bi0`.
- 89 `wbi0=0` V μm Width dependence of `bi0`.
- 90 `pbi0=0` V μm Width-length dependence of `bi0`.

Length and Width Modulation Parameters

- 91 `dl0=0` μm Lateral diffusion.
- 92 `dw0=0` μm Field oxide encroachment.
- 93 `lref= ∞` `m` Reference channel length.
- 94 `wref= ∞` `m` Reference channel width.
- 95 `xw=0` `m` Width variation due to masking and etching.
- 96 `xl=0` `m` Length variation due to masking and etching.

Spectre Circuit Simulator Reference

Component Statements Part 1

Temperature Effects Parameters

97 `temp` (C) Parameters measurement temperature. Default set by options.

98 `trise=0` cTemperature rise from ambient.

99 `tempmod=432` Temperature model selector.

100 `version=432` Version selector.

101 `uto=0` cMobility temperature offset.

102 `ute=-1.5` Mobility temperature exponent.

103 `tlev=0` DC temperature selector.

104 `tlevc=0` AC temperature selector.

105 `eg=1.12452` vEnergy band gap.

106 `gap1=7.02e-4` V/C^2
Band gap temperature coefficient.

107 `gap2=1108` kBand gap temperature offset.

108 `trs=0` $1/\text{C}$ Temperature coefficient for source resistance.

109 `trd=0` $1/\text{C}$ Temperature coefficient for drain resistance.

110 `xti=3` Saturation current temperature exponent.

Overlap Capacitance Parameters

111 `cgso=0` F/mGate-source overlap capacitance.

112 `cgdo=0` F/mGate-drain overlap capacitance.

113 `cgbo=0` F/mGate-bulk overlap capacitance.

114 `meto=0` mMetal overlap in fringing field.

Spectre Circuit Simulator Reference

Component Statements Part 1

Charge Model Selection Parameters

115 `capmod=bsim` Intrinsic charge model.

Possible values are `none`, `meyer`, `yang`, or `bsim`.

116 `xpart=1` Drain/source channel charge partition in saturation for BSIM charge model, use 0.0 for 40/60, 0.5 for 50/50, or 1.0 for 0/100.

117 `xqc=0` Drain/source channel charge partition in saturation for charge models, e.g. use 0.4 for 40/60, 0.5 for 50/50, or 0 for 0/100.

Parasitic Resistance Parameters

118 `rs=0` Ω Source resistance.

119 `rd=0` Ω Drain resistance.

120 `rsh=0` Ω/sqr Source/drain diffusion sheet resistance.

121 `rsc=0` Ω Source contact resistance.

122 `rdc=0` Ω Drain contact resistance.

123 `rss=0` Ω mScalable source resistance.

124 `rdd=0` Ω mScalable drain resistance.

125 `minr=0.1` Ω Minimum source/drain resistance.

126 `hdif=0` mLength of heavily doped diffusion.

127 `ldif=0` mLateral diffusion beyond the gate.

128 `lgcs=0` mGate-to-contact length of source side.

129 `lgcd=0` mGate-to-contact length of drain side.

130 `sc= ∞` mSpacing between contacts.

Junction Diode Parameters

131 `js` (A/m^2) Bulk junction reverse saturation current density.

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Component Statements Part 1

132 `is=1e-14` `A` Bulk junction reverse saturation current.

133 `n=1` Junction emission coefficient.

134 `imelt='imaxA'` Explosion current, diode is linearized beyond this current to aid convergence.

135 `jmelt='jmaxA/m'2`
Explosion current density, diode is linearized beyond this current to aid convergence.

136 `dskip=yes` Use simple piece-wise linear model for diode currents below $0.1 \cdot i_{abstol}$. Possible values are `no` or `yes`.

Junction Capacitance Model Parameters

137 `cbs=0` `F` Bulk-source zero-bias junction capacitance.

138 `cbd=0` `F` Bulk-drain zero-bias junction capacitance.

139 `cj=0` `F/m2` Zero-bias junction bottom capacitance density.

140 `mj=1/2` Bulk junction bottom grading coefficient.

141 `pb=0.8` `V` Bulk junction potential.

142 `fc=0.5` Forward-bias depletion capacitance threshold.

143 `cjsw=0` `F/m` Zero-bias junction sidewall capacitance density.

144 `mjsw=1/3` Bulk junction sidewall grading coefficient.

145 `pbsw=0.8` `V` Side-wall junction potential.

146 `fcsw=0.5` Side-wall forward-bias depletion capacitance threshold.

Operating Region Warning Control Parameters

147 `alarm=none` Forbidden operating region.
Possible values are `none`, `off`, `triode`, `sat`, `subth`, or `rev`.

148 `imax=1` `A` Maximum current, currents above this limit generate a warning.

Spectre Circuit Simulator Reference

Component Statements Part 1

149 `jmax=1e8 A/m2` Maximum current density, currents above this limit generate a warning.

150 `bvj= ∞ V` Junction reverse breakdown voltage.

151 `vbox=1e9 tox V` Oxide breakdown voltage.

Process and Power Supply Parameters

152 `tox=4e-8 m` Gate oxide thickness.

153 `vdd=5 V` Drain voltage at which parameters are extracted.

Default Device Parameters

154 `w=3e-6 m` Channel width.

155 `l=3e-6 m` Channel length.

156 `as=0 m2` Area of source diffusion.

157 `ad=0 m2` Area of drain diffusion.

158 `ps=0 m` Perimeter of source diffusion.

159 `pd=0 m` Perimeter of drain diffusion.

160 `nrd=0 m/m` Number of squares of drain diffusion.

161 `nrs=0 m/m` Number of squares of source diffusion.

162 `ldd=0 m` Drain diffusion length.

163 `lds=0 m` Source diffusion length.

Noise Model Parameters

164 `noisemod=1` Noise model selector.

165 `kf=0` Flicker (1/f) noise coefficient.

166 `af=1` Flicker (1/f) noise exponent.

Spectre Circuit Simulator Reference

Component Statements Part 1

167 `ef=1` Flicker (1/f) noise frequency exponent.

168 `wnoi=1e-5` mChannel width at which noise parameters were extracted.

Auto Model Selector Parameters

169 `wmax=1.0` mMaximum channel width for which the model is valid.

170 `wmin=0.0` mMinimum channel width for which the model is valid.

171 `lmax=1.0` mMaximum channel length for which the model is valid.

172 `lmin=0.0` mMinimum channel length for which the model is valid.

Degradation Parameters

173 `degramod=spectre` Degradation model selector.
Possible values are `spectre` or `bert`.

174 `degradation=no` Hot-electron degradation flag.
Possible values are `no` or `yes`.

175 `dvthc=1` vDegradation coefficient for threshold voltage.

176 `dvthe=1` Degradation exponent for threshold voltage.

177 `duoc=1` sDegradation coefficient for transconductance.

178 `duoe=1` Degradation exponent for transconductance.

179 `crivth=0.1` vMaximum allowable threshold voltage shift.

180 `criuo=10%` Maximum allowable normalized mobility change.

181 `crigm=10%` Maximum allowable normalized transconductance change.

182 `criids=10%` Maximum allowable normalized drain current change.

183 `wnom=5e-6` mNominal device width in degradation calculation.

184 `lnom=1e-6` mNominal device length in degradation calculation.

Spectre Circuit Simulator Reference

Component Statements Part 1

185 `vbsn=0` `v`Substrate voltage in degradation calculation.

186 `vdsni=0.1` `v`Drain voltage in `Ids` degradation calculation.

187 `vgzni=5` `v`Gate voltage in `Ids` degradation calculation.

188 `vdsng=0.1` `v`Drain voltage in `Gm` degradation calculation.

189 `vgzng=5` `v`Gate voltage in `Gm` degradation calculation.

Spectre Stress Parameters

190 `esat=1.1e7` `V/m`Critical field in `Vdsat` calculation.

191 `esatg=2.5e6` `1/m`Gate voltage dependence of `esat`.

192 `vpg=-0.25`Gate voltage modifier.

193 `vpb=-0.13`Gate voltage modifier.

194 `subc1=2.24e-5`Substrate current coefficient.

195 `subc2=-0.1e-5` `1/V`Substrate current coefficient.

196 `sube=6.4`Substrate current exponent.

197 `strc=1`Stress coefficient.

198 `stre=1`Stress exponent.

BERT Stress Parameters

199 `h0=1`Aging coefficient.

200 `hgd=0` `1/V`Bias dependence of `h0`.

201 `m0=1`Aging exponent.

202 `mgd=0` `1/V`Bias dependence of `m0`.

203 `ecrit0=1.1e5` `V/cm`Critical electric field.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 204 `lecrit0=0` $\mu\text{m V/cm}$ Length dependence of `ecrit0`.
- 205 `wecrit0=0` $\mu\text{m V/cm}$ Width dependence of `ecrit0`.
- 206 `ecritg=0` $1/\text{cm}$ Gate voltage dependence of `ecrit0`.
- 207 `lecritg=0` $\mu\text{m/cm}$ Length dependence of `ecritg`.
- 208 `wecritg=0` $\mu\text{m/cm}$ Width dependence of `ecritg`.
- 209 `ecritb=0` $1/\text{cm}$ Substrate voltage dependence of `ecrit0`.
- 210 `lecritb=0` $\mu\text{m/cm}$ Length dependence of `ecritb`.
- 211 `wecritb=0` $\mu\text{m/cm}$ Width dependence of `ecritb`.
- 212 `lc0=1` Substrate current coefficient.
- 213 `llc0=0` μm Length dependence of `lc0`.
- 214 `wlc0=0` μm Width dependence of `lc0`.
- 215 `lc1=1` Substrate current coefficient.
- 216 `llc1=0` μm Length dependence of `lc1`.
- 217 `wlc1=0` μm Width dependence of `lc1`.
- 218 `lc2=1` Substrate current coefficient.
- 219 `llc2=0` μm Length dependence of `lc2`.
- 220 `wlc2=0` μm Width dependence of `lc2`.
- 221 `lc3=1` Substrate current coefficient.
- 222 `llc3=0` μm Length dependence of `lc3`.
- 223 `wlc3=0` μm Width dependence of `lc3`.
- 224 `lc4=1` Substrate current coefficient.
- 225 `llc4=0` μm Length dependence of `lc4`.

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Component Statements Part 1

226 `wlc4=0` μm Width dependence of `lc4`.

227 `lc5=1` Substrate current coefficient.

228 `llc5=0` μm Length dependence of `lc5`.

229 `wlc5=0` μm Width dependence of `lc5`.

230 `lc6=1` Substrate current coefficient.

231 `llc6=0` μm Length dependence of `lc6`.

232 `wlc6=0` μm Width dependence of `lc6`.

233 `lc7=1` Substrate current coefficient.

234 `llc7=0` μm Length dependence of `lc7`.

235 `wlc7=0` μm Width dependence of `lc7`.

Imax and Imelt

The `imax` parameter aids convergence and prevents numerical overflow. The junction characteristics of the device are accurately modeled for current up to `imax`. If `imax` is exceeded during iterations, the linear model is substituted until the current drops below `imax` or until convergence is achieved. If convergence is achieved with the current exceeding `imax`, the results are inaccurate, and Spectre prints a warning.

A separate model parameter, `imelt`, is used as a limit warning for the junction current. This parameter can be set to the maximum current rating of the device. When any component of the junction current exceeds `imelt`, note that base and collector currents are composed of many exponential terms, Spectre issues a warning and the results become inaccurate. The junction current is linearized above the value of `imelt` to prevent arithmetic exception, with the exponential term replaced by a linear equation at `imelt`.

Both of these parameters have current density counterparts, `jmax` and `jmelt`, that you can specify if you want the absolute current values to depend on the device area.

Auto Model Selection

Many models need to be characterized for different geometries in order to obtain accurate results for model development. The model selector program automatically searches for a

Spectre Circuit Simulator Reference

Component Statements Part 1

model with the length and width range specified in the instance statement and uses this model in the simulations.

For the auto model selector program to find a specific model, the models to be searched should be grouped together within braces. Such a group is called a model group. An opening brace is required at the end of the line defining each model group. Every model in the group is given a name followed by a colon and the list of parameters. Also, the four geometric parameters `lmax`, `lmin`, `wmax`, and `wmin` should be given. The selection criteria to choose a model is as follows:

```
lmin <= inst_length < lmax  and  wmin <= inst_width  < wmax
```

Example

```
model ModelName ModelType {  
    1:      <model parameters> lmin=2 lmax=4 wmin=1 wmax=2  
    2:      <model parameters> lmin=1 lmax=2 wmin=2 wmax=4  
    3:      <model parameters> lmin=2 lmax=4 wmin=4 wmax=6  
}
```

Then for a given instance

```
M1 1 2 3 4 ModelName w=3 l=1.5
```

the program would search all the models in the model group with the name `ModelName` and then pick the first model whose geometric range satisfies the selection criteria. In the preceding example, the auto model selector program would choose `ModelName.2`.

The user must specify both length (`l`) and width (`w`) on the device instance line to enable automatic model selection.

Output Parameters

- 1 `weff` (m) Effective channel width.
- 2 `leff` (m) Effective channel length.
- 3 `rseff` (Ω) Effective source resistance.
- 4 `rdeff` (Ω) Effective drain resistance.
- 5 `aseff` (m^2) Effective area of source diffusion.
- 6 `adeff` (m^2) Effective area of drain diffusion.
- 7 `pseff` (m) Effective perimeter of source diffusion.

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Component Statements Part 1

- 8 `pdeff (m)` Effective perimeter of source diffusion.
- 9 `isseff (A)` Effective source-bulk junction reverse saturation current.
- 10 `isdeff (A)` Effective drain-bulk junction reverse saturation current.
- 11 `cbseff (F)` Effective zero-bias source-bulk junction capacitance.
- 12 `cbdeff (F)` Effective zero-bias drain-bulk junction capacitance.
- 13 `vto (V)` Effective zero-bias threshold voltage.
- 14 `vfb (V)` Effective flat-band voltage.
- 15 `phi (V)` Effective surface potential.
- 16 `k1 (\sqrt{V})` Effective body-effect coefficient.
- 17 `k2` Effective charge-sharing parameter.
- 18 `eta` Effective DIBL coefficient.

Operating-Point Parameters

- 1 `type=n` Transistor type.
Possible values are `n` or `p`.
- 2 `region=triode` Estimated operating region.
Possible values are `off`, `triode`, `sat`, `subth`, or `breakdown`.
- 3 `degradation=no` Hot-electron degradation flag.
Possible values are `no` or `yes`.
- 4 `reversed` Reverse mode indicator.
Possible values are `no` or `yes`.
- 5 `ids (A)` Resistive drain-to-source current.
- 6 `vgs (V)` Gate-source voltage.
- 7 `vds (V)` Drain-source voltage.
- 8 `vbs (V)` Bulk-source voltage.

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Component Statements Part 1

- 9 `vth` (V) Threshold voltage.
- 10 `vdsat` (V) Drain-source saturation voltage.
- 11 `betaeff` (A/V²) Effective beta.
- 12 `gm` (S) Common-source transconductance.
- 13 `gds` (S) Common-source output conductance.
- 14 `gmbs` (S) Body-transconductance.
- 15 `cbd` (F) Drain-bulk junction capacitance.
- 16 `cbs` (F) Source-bulk junction capacitance.
- 17 `cgs` (F) Gate-source capacitance.
- 18 `cgd` (F) Gate-drain capacitance.
- 19 `cgb` (F) Gate-bulk capacitance.
- 20 `ron` (Ω) ON-resistance.
- 21 `id` (A) Resistive drain current.
- 22 `ibulk` (A) Resistive bulk current.
- 23 `pwr` (W) Power at op point.
- 24 `gmoverid` (1/V) Gm/Ids.
- 25 `isub` (A) Substrate current.
- 26 `stress` Hot-electron stress.
- 27 `age` (s) Device age.
- 28 `he_vdsat` (V) hot electron vdsat.

Spectre Circuit Simulator Reference

Component Statements Part 1

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

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adeff	O-6	k1	O-16	nrd	M-160	vbs	OP-8
af	M-166	k1	M-10	nrd	I-7	vbsn	M-185
age	OP-27	k2	M-14	nrs	M-161	vdd	M-153
ai0	M-83	k2	O-17	nrs	I-8	vds	OP-7
alarm	M-147	kf	M-165	pai0	M-86	vdsat	OP-10
as	I-3	l	M-155	pb	M-141	vdsng	M-188
as	M-156	l	I-2	pbi0	M-90	vdsni	M-186
aseff	O-5	lai0	M-84	pbsw	M-145	version	M-100
betaeff	OP-11	lbi0	M-88	pd	M-159	vfb	O-14
bi0	M-87	lc0	M-212	pd	I-6	vfb0	M-2
bvj	M-150	lc1	M-215	pdeff	O-8	vgs	OP-6
capmod	M-115	lc2	M-218	peta	M-21	vgsng	M-189
cbd	OP-15	lc3	M-221	phi	O-15	vgsni	M-187
cbd	M-138	lc4	M-224	phi0	M-6	vpb	M-193
cbdeff	O-12	lc5	M-227	pk1	M-13	vpg	M-192
cbs	M-137	lc6	M-230	pk2	M-17	vth	OP-9
cbs	OP-16	lc7	M-233	pmus	M-41	vto	O-13
cbseff	O-11	ld	I-9	pmuz	M-33	w	I-1
cgb	OP-19	ldd	M-162	pn0	M-73	w	M-154
cgbo	M-113	ldif	M-127	pnb	M-77	wai0	M-85
cgd	OP-18	lds	M-163	pnd	M-81	wbi0	M-89
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cgso	M-111	lecritg	M-207	ps	I-5	wecritg	M-208
cj	M-139	leff	O-2	pseff	O-7	weff	O-1
cjsw	M-143	leta	M-19	pu0	M-53	weta	M-20
crigm	M-181	lgcd	M-129	pul	M-61	wk1	M-12
criids	M-182	lgcs	M-128	pvfb	M-5	wk2	M-16
criuo	M-180	lk1	M-11	pwr	OP-23	wlc0	M-214
crivth	M-179	lk2	M-15	px2e	M-25	wlc1	M-217
degradation	OP-3	llc0	M-213	px2ms	M-45	wlc2	M-220
degradation	I-14	llc1	M-216	px2mz	M-37	wlc3	M-223
degradation	M-174	llc2	M-219	px2u0	M-57	wlc4	M-226
degramod	M-173	llc3	M-222	px2u1	M-65	wlc5	M-229
dl0	M-91	llc4	M-225	px3e	M-29	wlc6	M-232

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Component Statements Part 1

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duoc	M-177	llc6	M-231	px3ul	M-69	wmax	M-169
duoe	M-178	llc7	M-234	rd	M-119	wmin	M-170
dvthc	M-175	lmax	M-171	rdc	M-122	wmus	M-40
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dw0	M-92	lmus	M-39	rdeff	O-4	wn0	M-72
ecrit0	M-203	lmuz	M-31	region	OP-2	wnb	M-76
ecritb	M-209	ln0	M-71	region	I-12	wnd	M-80
ecritg	M-206	lnb	M-75	reversed	OP-4	wnoi	M-168
ef	M-167	lnd	M-79	ron	OP-20	wnom	M-183
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eta	O-18	ls	I-10	rsh	M-120	wul	M-60
eta0	M-18	lu0	M-51	rss	M-123	wvfb	M-4
fc	M-142	lu1	M-59	sc	M-130	wx2e	M-24
fcs	M-146	lvfb	M-3	strc	M-197	wx2ms	M-44
gap1	M-106	lx2e	M-23	stre	M-198	wx2mz	M-36
gap2	M-107	lx2ms	M-43	stress	OP-26	wx2u0	M-56
gds	OP-13	lx2mz	M-35	subc1	M-194	wx2u1	M-64
gm	OP-12	lx2u0	M-55	subc2	M-195	wx3e	M-28
gmbs	OP-14	lx2u1	M-63	sube	M-196	wx3ms	M-48
gmoverid	OP-24	lx3e	M-27	subthmod	M-82	wx3u1	M-68
h0	M-199	lx3ms	M-47	temp	M-97	x2e	M-22
hdif	M-126	lx3u1	M-67	tempmod	M-99	x2ms	M-42
he_vdsat	OP-28	m	I-11	tlev	M-103	x2mz	M-34
hgd	M-200	m0	M-201	tlevc	M-104	x2u0	M-54
ibulk	OP-22	meto	M-114	tox	M-152	x2u1	M-62
id	OP-21	mgd	M-202	trd	M-109	x3e	M-26
ids	OP-5	minr	M-125	trise	M-98	x3ms	M-46
imax	M-148	mj	M-140	trise	I-13	x3u1	M-66
imelt	M-134	mjs	M-144	trs	M-108	x1	M-96
is	M-132	mus	M-38	type	OP-1	xpart	M-116
isdeff	O-10	muz	M-30	type	M-1	xqc	M-117
isseff	O-9	n	M-133	u00	M-50	xti	M-110
isub	OP-25	n0	M-70	u10	M-58	xw	M-95
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BSIM2 Field Effect Transistor (bsim2)

Description

BSIM2 is a semiempirical MOSFET model developed at the University of California, Berkeley. All the model parameters are extracted directly from physical devices. Both the drain current

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Component Statements Part 1

and output resistance are accurately modeled. Three charge models are available. In SPICE mode, you can refer to BSIM2 as MOS level 5 or BSIM level 2. BSIM2 transistors require that you use a model statement.

This device is supported within altergroups.

To examine the equations used for this component, consult the [Spectre Circuit Simulator Device Model Equations](#) manual.

Sample Instance Statement

```
m2 (0 2 1 1) pchmod l=5u w=10u as=40u ad=40u pd=28u ps=28u m=1
```

Sample Model Statement

```
model pchmod bsim2 type=p vfb0=-0.5 lvfb=0.5 wvfb=0.3 phi0=0.8 eta0=0.056 k1=0.5  
eg=0.99 gap1=5.5e-04 trs=1e-3 trd=1e-3 xpart=0.5 rs=10 rd=10
```

Instance Definition

```
Name d g s b ModelName parameter=value ...
```

Instance Parameters

- 1 w (m) Channel width.
- 2 l (m) Channel length.
- 3 as (m²) Area of source diffusion.
- 4 ad (m²) Area of drain diffusion.
- 5 ps (m) Perimeter of source diffusion.
- 6 pd (m) Perimeter of drain diffusion.
- 7 nrd (m/m) Number of squares of drain diffusion.
- 8 nrs (m/m) Number of squares of source diffusion.
- 9 ld (m) Drain diffusion length.
- 10 ls (m) Source diffusion length.

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Component Statements Part 1

- 11 `m=1` Multiplicity factor (number of MOSFETs in parallel).
- 12 `region=triode` Estimated operating region.
Possible values are `off`, `triode`, `sat`, `subth`, or `breakdown`.
- 13 `trise (C)` Temperature rise from ambient.
- 14 `degradation=no` Hot-electron degradation flag.
Possible values are `no` or `yes`.

Model Definition

`model modelName bsim2 parameter=value ...`

Model Parameters

Device Type Parameters

- 1 `type=n` Transistor type.
Possible values are `n` or `p`.

Threshold Voltage Parameters

- 2 `vfb0=-0.8 V` Flat-band voltage.
- 3 `lvfb=0 V` μm Length dependence of `vfb`.
- 4 `wvfb=0 V` μm Width dependence of `vfb`.
- 5 `pvfb=0 V` μm Width-length dependence of `vfb`.
- 6 `phi0=0.75 V` Surface potential.
- 7 `lphi=0 V` μm Length dependence of `phi`.
- 8 `wphi=0 V` μm Width dependence of `phi`.
- 9 `pphi=0 V` μm Width-length dependence of `phi`.
- 10 `k1=0.7 $\sqrt{\text{V}}$` Body-effect coefficient.
- 11 `lk1=0 $\sqrt{\text{V}}$` μm Length dependence of `k1`.

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- 12 $wk1=0 \sqrt{V}$ μm Width dependence of $k1$.
- 13 $pk1=0 \sqrt{V}$ μm Width-length dependence of $k1$.
- 14 $k2=0$ Charge-sharing parameter.
- 15 $lk2=0$ μm Length dependence of $k2$.
- 16 $wk2=0$ μm Width dependence of $k2$.
- 17 $pk2=0$ μm Width-length dependence of $k2$.
- 18 $\text{eta}0=0$ Drain-induced barrier-lowering coefficient.
- 19 $l\text{eta}0=0$ μm Length dependence of $\text{eta}0$.
- 20 $w\text{eta}0=0$ μm Width dependence of $\text{eta}0$.
- 21 $p\text{eta}0=0$ μm Width-length dependence of $\text{eta}0$.
- 22 $\text{etab}=0$ $1/V$ Body-bias dependence of $\text{eta}0$.
- 23 $l\text{etab}=0$ $\mu\text{m}/V$ Length dependence of etab .
- 24 $w\text{etab}=0$ $\mu\text{m}/V$ Width dependence of etab .
- 25 $p\text{etab}=0$ $\mu\text{m}/V$ Width-length dependence of etab .

Mobility Parameters

- 26 $\mu0=400 \text{ cm}^2/V \text{ s}$ Low-field mobility.
- 27 $l\mu0=0 \text{ cm}^2/V \text{ s}$ Length dependence of $\mu0$.
- 28 $w\mu0=0 \text{ cm}^2/V \text{ s}$ Width dependence of $\mu0$.
- 29 $p\mu0=0 \text{ cm}^2/V \text{ s}$ Width-length dependence of $\mu0$.
- 30 $\mu0b=0 \text{ cm}^2/V^2 \text{ s}$ Body-bias dependence of $\mu0$.
- 31 $l\mu0b=0 \text{ cm}^2 \mu\text{m}/V^2 \text{ s}$
Length dependence of $\mu0$.

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- 32 $w\mu_{0b}=0 \text{ cm}^2 \mu\text{m}/\text{V}^2 \text{ s}$
Width dependence of x_{2mz} .
- 33 $p\mu_{0b}=0 \text{ cm}^2 \mu\text{m}/\text{V}^2 \text{ s}$
Width-length dependence of x_{2mz} .
- 34 $\mu_{s0}=450 \text{ cm}^2/\text{V} \text{ s}$ Mobility in the saturation region.
- 35 $l\mu_{s0}=0 \text{ cm}^2 \mu\text{m}/\text{V} \text{ s}$
Length dependence of μ_{s0} .
- 36 $w\mu_{s0}=0 \text{ cm}^2 \mu\text{m}/\text{V} \text{ s}$
Width dependence of μ_{s0} .
- 37 $p\mu_{s0}=0 \text{ cm}^2 \mu\text{m}/\text{V} \text{ s}$
Width-length dependence of μ_{s0} .
- 38 $\mu_{sb}=0 \text{ cm}^2/\text{V}^2 \text{ s}$ Body-bias dependence of μ_{s0} .
- 39 $l\mu_{sb}=0 \text{ cm}^2 \mu\text{m}/\text{V}^2 \text{ s}$
Length dependence of μ_{s0} .
- 40 $w\mu_{sb}=0 \text{ cm}^2 \mu\text{m}/\text{V}^2 \text{ s}$
Length dependence of μ_{s0} .
- 41 $p\mu_{sb}=0 \text{ cm}^2 \mu\text{m}/\text{V}^2 \text{ s}$
Length dependence of μ_{s0} .
- 42 $\mu_{20}=1$ Empirical channel length modulation parameter.
- 43 $l\mu_{20}=0 \mu\text{m}$ Length dependence of μ_{20} .
- 44 $w\mu_{20}=0 \mu\text{m}$ Width dependence of μ_{20} .
- 45 $p\mu_{20}=0 \mu\text{m}$ Width-length dependence of μ_{20} .
- 46 $\mu_{2b}=0 \text{ 1/V}$ Body-bias dependence of μ_{20} .
- 47 $l\mu_{2b}=0 \mu\text{m}/\text{V}$ Length dependence of μ_{2b} .
- 48 $w\mu_{2b}=0 \mu\text{m}/\text{V}$ Width dependence of μ_{2b} .
- 49 $p\mu_{2b}=0 \mu\text{m}/\text{V}$ Width-length dependence of μ_{2b} .

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Component Statements Part 1

- 50 $\text{mu2g}=0$ $1/\text{V}$ Gate-bias dependence of mu20 .
- 51 $\text{lmu2g}=0$ $\mu\text{m}/\text{V}$ Length dependence of mu2g .
- 52 $\text{wmu2g}=0$ $\mu\text{m}/\text{V}$ Width dependence of mu2g .
- 53 $\text{pmu2g}=0$ $\mu\text{m}/\text{V}$ Width-length dependence of mu2g .
- 54 $\text{mu30}=5$ cm^2/V^2 s Empirical output resistance parameter.
- 55 $\text{lmu30}=0$ cm^2 $\mu\text{m}/\text{V}^2$ s
Length dependence of mu30 .
- 56 $\text{wmu30}=0$ cm^2 $\mu\text{m}/\text{V}^2$ s
Width dependence of mu30 .
- 57 $\text{pmu30}=0$ cm^2 $\mu\text{m}/\text{V}^2$ s
Width-length dependence of mu30 .
- 58 $\text{mu3b}=0$ cm^2/V^3 s Body-bias dependence of mu30 .
- 59 $\text{lmu3b}=0$ cm^2 $\mu\text{m}/\text{V}^3$ s
Length dependence of mu3b .
- 60 $\text{wmu3b}=0$ cm^2 $\mu\text{m}/\text{V}^3$ s
Width dependence of mu3b .
- 61 $\text{pmu3b}=0$ cm^2 $\mu\text{m}/\text{V}^3$ s
Width-length dependence of mu3b .
- 62 $\text{mu3g}=0$ cm^2/V^3 s Gate-bias dependence of mu30 .
- 63 $\text{lmu3g}=0$ cm^2 $\mu\text{m}/\text{V}^3$ s
Length dependence of mu3g .
- 64 $\text{wmu3g}=0$ cm^2 $\mu\text{m}/\text{V}^3$ s
Width dependence of mu3g .
- 65 $\text{pmu3g}=0$ cm^2 $\mu\text{m}/\text{V}^3$ s
Width-length dependence of mu3g .
- 66 $\text{mu40}=0$ cm^2/V^3 s Empirical output resistance parameter.

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Component Statements Part 1

- 67 $l\mu 40=0 \text{ cm}^2 \mu\text{m}/\text{V}^3 \text{ s}$
Length dependence of $\mu 40$.
- 68 $w\mu 40=0 \text{ cm}^2 \mu\text{m}/\text{V}^3 \text{ s}$
Width dependence of $\mu 40$.
- 69 $p\mu 40=0 \text{ cm}^2 \mu\text{m}/\text{V}^3 \text{ s}$
Width-length dependence of $\mu 40$.
- 70 $\mu 4b=0 \text{ cm}^2/\text{V}^3 \text{ s}$ Empirical output resistance parameter.
- 71 $l\mu 4b=0 \text{ cm}^2 \mu\text{m}/\text{V}^3 \text{ s}$
Length dependence of $\mu 4b$.
- 72 $w\mu 4b=0 \text{ cm}^2 \mu\text{m}/\text{V}^3 \text{ s}$
Width dependence of $\mu 4b$.
- 73 $p\mu 4b=0 \text{ cm}^2 \mu\text{m}/\text{V}^3 \text{ s}$
Width-length dependence of $\mu 4b$.
- 74 $\mu 4g=0 \text{ cm}^2/\text{V}^3 \text{ s}$ Gate-bias dependence of $\mu 4g$.
- 75 $l\mu 4g=0 \text{ cm}^2 \mu\text{m}/\text{V}^3 \text{ s}$
Length dependence of $\mu 4g$.
- 76 $w\mu 4g=0 \text{ cm}^2 \mu\text{m}/\text{V}^3 \text{ s}$
Width dependence of $\mu 4g$.
- 77 $p\mu 4g=0 \text{ cm}^2 \mu\text{m}/\text{V}^3 \text{ s}$
Width-length dependence of $\mu 4g$.

Mobility Modulation Parameters

- 78 $u a 0=0 \text{ 1/V}$ Gate voltage dependence of mobility.
- 79 $l u a 0=0 \mu\text{m}/\text{V}$ Length dependence of $u a 0$.
- 80 $w u a 0=0 \mu\text{m}/\text{V}$ Width dependence of $u a 0$.
- 81 $p u a 0=0 \mu\text{m}/\text{V}$ Width-length dependence of $u a 0$.
- 82 $u a b=0 \text{ 1/V}^2$ Body-bias dependence of $u a$.

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Component Statements Part 1

- 83 $luab=0$ $\mu m/V^2$ Length dependence of uab .
- 84 $wuab=0$ $\mu m/V^2$ Width dependence of uab .
- 85 $puab=0$ $\mu m/V^2$ Width-length dependence of uab .
- 86 $ub0=0$ $1/V^2$ Second-order effect of gate voltage dependence of mobility.
- 87 $lub0=0$ $\mu m/V^2$ Length dependence of $ub0$.
- 88 $wub0=0$ $\mu m/V^2$ Width dependence of $ub0$.
- 89 $pub0=0$ $\mu m/V^2$ Width-length dependence of $ub0$.
- 90 $ubb=0$ $1/V^3$ Body-bias dependence of ub .
- 91 $lubb=0$ $\mu m/V^3$ Length dependence of ubb .
- 92 $wubb=0$ $\mu m/V^3$ Width dependence of ubb .
- 93 $pubb=0$ $\mu m/V^3$ Width-length dependence of ubb .

Velocity Saturation Parameters

- 94 $u10=0$ $1/V$ Velocity saturation coefficient.
- 95 $lu10=0$ $\mu m/V$ Length dependence of $u1$.
- 96 $wu10=0$ $\mu m/V$ Width dependence of $u1$.
- 97 $pu10=0$ $\mu m/V$ Width-length dependence of $u1$.
- 98 $u1b=0$ $1/V^2$ Body-bias dependence of $u1$.
- 99 $lu1b=0$ $\mu m/V^2$ Length dependence of $u1b$.
- 100 $wu1b=0$ $\mu m/V^2$ Width dependence of $u1b$.
- 101 $pu1b=0$ $\mu m/V^2$ Width-length dependence of $u1b$.
- 102 $u1d=0$ $1/V^2$ Drain-bias dependence of $u1$.
- 103 $lu1d=0$ $\mu m/V^2$ Length dependence of $u1d$.

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Component Statements Part 1

104 $w_{uld}=0$ $\mu\text{m}/\text{V}^2$ Width dependence of u_{ld} .

105 $p_{uld}=0$ $\mu\text{m}/\text{V}^2$ Width-length dependence of u_{ld} .

Subthreshold Parameters

106 $n_0=0$ Subthreshold swing parameter.

107 $l_{n_0}=0$ μm Length dependence of subthreshold swing parameter.

108 $w_{n_0}=0$ μm Width dependence of subthreshold swing parameter.

109 $p_{n_0}=0$ μm Width-length dependence of subthreshold swing parameter.

110 $n_b=0$ $\sqrt{\text{V}}$ Body-bias dependence of n_0 .

111 $l_{n_b}=0$ $\sqrt{\text{V}}$ μm Length dependence of n_b .

112 $w_{n_b}=0$ $\sqrt{\text{V}}$ μm Width dependence of n_b .

113 $p_{n_b}=0$ $\sqrt{\text{V}}$ μm Width-length dependence of n_b .

114 $n_d=0$ $1/\text{V}$ Drain-bias dependence of n_0 .

115 $l_{n_d}=0$ $\mu\text{m}/\text{V}$ Length dependence of n_d .

116 $w_{n_d}=0$ $\mu\text{m}/\text{V}$ Width dependence of n_d .

117 $p_{n_d}=0$ $\mu\text{m}/\text{V}$ Width-length dependence of n_d .

118 $v_{of0}=1$ V Threshold voltage offset in the subthreshold region.

119 $l_{v_{of0}}=0$ V μm Length dependence of v_{of} .

120 $w_{v_{of0}}=0$ V μm Width dependence of v_{of} .

121 $p_{v_{of0}}=0$ V μm Width-length dependence of v_{of} .

122 $v_{ofb}=0$ Body-bias dependence of v_{of0} .

123 $l_{v_{ofb}}=0$ μm Length dependence of v_{ofb} .

124 $w_{v_{ofb}}=0$ μm Width dependence of v_{ofb} .

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Component Statements Part 1

125 `pvofb=0` μm Width-length dependence of `vofb`.

126 `vofd=0` Drain-bias dependence of `vof0`.

127 `lvofd=0` μm Length dependence of `vofd`.

128 `wvofd=0` μm Width dependence of `vofd`.

129 `pvofd=0` μm Width-length dependence of `vofd`.

130 `subthmod=2` Subthreshold model selector.

Impact Ionization Parameters

131 `ai0=0` $1/V$ Hot-electron effect on `Rout` parameter.

132 `lai0=0` $\mu\text{m}/V$ Length dependence of `ai0`.

133 `wai0=0` $\mu\text{m}/V$ Width dependence of `ai0`.

134 `pai0=0` $\mu\text{m}/V$ Width-length dependence of `ai0`.

135 `aib=0` $1/V^2$ Body-bias dependence of `ai0`.

136 `laib=0` $\mu\text{m}/V^2$ Length dependence of `aib`.

137 `waib=0` $\mu\text{m}/V^2$ Width dependence of `aib`.

138 `paib=0` $\mu\text{m}/V^2$ Width-length dependence of `aib`.

139 `bi0=0` V Hot-electron effect on `Rout` exponent.

140 `lbi0=0` V μm Length dependence of `bi0`.

141 `wbi0=0` V μm Width dependence of `bi0`.

142 `pbi0=0` V μm Width-length dependence of `bi0`.

143 `bib=0` Body-bias dependence of `bi0`.

144 `lbib=0` μm Length dependence of `bib`.

145 `wbib=0` μm Width dependence of `bib`.

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Component Statements Part 1

146 `pbib=0` μm Width-length dependence of `bib`.

Transition Region Bound Parameters

147 `vghigh=0.2` V Upper bound of the transition region.

148 `lvghigh=0` V μm Length dependence of `vghigh`.

149 `wvghigh=0` V μm Width dependence of `vghigh`.

150 `pvghigh=0` V μm Width-length dependence of `vghigh`.

151 `vglow=-0.15` V Lower bound of the transition region.

152 `lvglow=0` V μm Length dependence of `vglow`.

153 `wvglow=0` V μm Width dependence of `vglow`.

154 `pvglow=0` V μm Width-length dependence of `vglow`.

Length and Width Modulation Parameters

155 `dl0=0` μm Lateral diffusion.

156 `dw0=0` μm Field oxide encroachment.

157 `lref= ∞` m Reference channel length.

158 `wref= ∞` m Reference channel width.

159 `xw=0` m Width variation due to masking and etching.

160 `xl=0` m Length variation due to masking and etching.

Temperature Effects Parameters

161 `temp` (C)Parameters measurement temperature. Default set by options.

162 `trise=0` CTemperature rise from ambient.

163 `tempmod=432`Temperature model selector.

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164 `version=432` Version selector.

165 `uto=0` `c` Mobility temperature offset.

166 `ute=-1.5` Mobility temperature exponent.

167 `tlev=0` DC temperature selector.

168 `tlevc=0` AC temperature selector.

169 `ptc=0` `V/C` Surface potential temperature coefficient.

170 `eg=1.12452` `V` Energy band gap.

171 `gap1=7.02e-4` `V/C2`
Band gap temperature coefficient.

172 `gap2=1108` `K` Band gap temperature offset.

173 `trs=0` `1/C` Temperature coefficient for source resistance.

174 `trd=0` `1/C` Temperature coefficient for drain resistance.

175 `xti=3` Saturation current temperature exponent.

Overlap Capacitance Parameters

176 `cgso=0` `F/m` Gate-source overlap capacitance.

177 `cgdo=0` `F/m` Gate-drain overlap capacitance.

178 `cgbo=0` `F/m` Gate-bulk overlap capacitance.

179 `meto=0` `m` Metal overlap in fringing field.

Charge Model Selection Parameters

180 `capmod=bsim` Intrinsic charge model.
Possible values are `none`, `meyer`, `yang`, or `bsim`.

181 `xpart=1` Drain/source channel charge partition in saturation for BSIM charge model, use 0.0 for 40/60, 0.5 for 50/50, or 1.0 for 0/100.

Spectre Circuit Simulator Reference

Component Statements Part 1

182 `xqc=0` Drain/source channel charge partition in saturation for charge models, e.g. use 0.4 for 40/60, 0.5 for 50/50, or 0 for 0/100.

Parasitic Resistance Parameters

183 `rs=0` Ω Source resistance.

184 `rd=0` Ω Drain resistance.

185 `rsh=0` Ω/sqr Source/drain diffusion sheet resistance.

186 `rsc=0` Ω Source contact resistance.

187 `rdc=0` Ω Drain contact resistance.

188 `rss=0` Ω mScalable source resistance.

189 `rdd=0` Ω mScalable drain resistance.

190 `minr=0.1` Ω Minimum source/drain resistance.

191 `hdif=0` mLength of heavily doped diffusion.

192 `ldif=0` mLateral diffusion beyond the gate.

193 `lgcs=0` mGate-to-contact length of source side.

194 `lgcd=0` mGate-to-contact length of drain side.

195 `sc= ∞` mSpacing between contacts.

Junction Diode Parameters

196 `js` (A/m^2) Bulk junction reverse saturation current density.

197 `is=1e-14` A Bulk junction reverse saturation current.

198 `n=1` Junction emission coefficient.

199 `dskip=yes` Use simple piece-wise linear model for diode currents below $0.1 \cdot i_{abstol}$. Possible values are `no` or `yes`.

Spectre Circuit Simulator Reference

Component Statements Part 1

200 `imelt=imaxA` Explosion current, diode is linearized beyond this current to aid convergence.

201 `jmelt=jmaxA/m'2`
Explosion current density, diode is linearized beyond this current to aid convergence.

Operating Region Warning Control Parameters

202 `alarm=none` Forbidden operating region.
Possible values are `none`, `off`, `triode`, `sat`, `subth`, or `rev`.

203 `imax=1 A` Maximum current, currents above this limit generate a warning.

204 `jmax=1e8 A/m'2` Maximum current density, currents above this limit generate a warning.

205 `bvj= ∞ V` Junction reverse breakdown voltage.

206 `vbox=1e9 tox V` Oxide breakdown voltage.

Junction Capacitance Model Parameters

207 `cbs=0 F` Bulk-source zero-bias junction capacitance.

208 `cbd=0 F` Bulk-drain zero-bias junction capacitance.

209 `cj=0 F/m'2` Zero-bias junction bottom capacitance density.

210 `mj=1/2` Bulk junction bottom grading coefficient.

211 `pb=0.8 V` Bulk junction potential.

212 `fc=0.5` Forward-bias depletion capacitance threshold.

213 `cjsw=0 F/m` Zero-bias junction sidewall capacitance density.

214 `mjsw=1/3` Bulk junction sidewall grading coefficient.

215 `pbsw=0.8 V` Side-wall junction potential.

216 `fcsw=0.5` Side-wall forward-bias depletion capacitance threshold.

Spectre Circuit Simulator Reference

Component Statements Part 1

Process and Power Supply Parameters

217 $t_{ox}=4e-8$ mGate oxide thickness.

218 $v_{dd}=5$ vDrain voltage at which parameters are extracted.

219 $v_{gg}=5$ vGate voltage at which parameters are extracted.

220 $v_{bb}=-5$ vBody voltage at which parameters are extracted.

Default Device Parameters

221 $w=3e-6$ mChannel width.

222 $l=3e-6$ mChannel length.

223 $a_s=0$ m²Area of source diffusion.

224 $a_d=0$ m²Area of drain diffusion.

225 $p_s=0$ mPerimeter of source diffusion.

226 $p_d=0$ mPerimeter of drain diffusion.

227 $n_{rd}=0$ m/mNumber of squares of drain diffusion.

228 $n_{rs}=0$ m/mNumber of squares of source diffusion.

229 $l_{dd}=0$ mDrain diffusion length.

230 $l_{ds}=0$ mSource diffusion length.

Noise Model Parameters

231 $noisemod=1$ Noise model selector.

232 $k_f=0$ Flicker (1/f) noise coefficient.

233 $a_f=1$ Flicker (1/f) noise exponent.

234 $e_f=1$ Flicker (1/f) noise frequency exponent.

Spectre Circuit Simulator Reference

Component Statements Part 1

235 `wnoi=1e-5` `m`Channel width at which noise parameters were extracted.

Auto Model Selector Parameters

236 `wmax=1.0` `m`Maximum channel width for which the model is valid.

237 `wmin=0.0` `m`Minimum channel width for which the model is valid.

238 `lmax=1.0` `m`Maximum channel length for which the model is valid.

239 `lmin=0.0` `m`Minimum channel length for which the model is valid.

Degradation Parameters

240 `degramod=spectre` Degradation model selector.
Possible values are `spectre` or `bert`.

241 `degradation=no` Hot-electron degradation flag.
Possible values are `no` or `yes`.

242 `dvthc=1` `v`Degradation coefficient for threshold voltage.

243 `dvthe=1` Degradation exponent for threshold voltage.

244 `duoc=1` `s`Degradation coefficient for transconductance.

245 `duoe=1` Degradation exponent for transconductance.

246 `crivth=0.1` `v`Maximum allowable threshold voltage shift.

247 `criuo=10%` Maximum allowable normalized mobility change.

248 `crigm=10%` Maximum allowable normalized transconductance change.

249 `criids=10%` Maximum allowable normalized drain current change.

250 `wnom=5e-6` `m`Nominal device width in degradation calculation.

251 `lnom=1e-6` `m`Nominal device length in degradation calculation.

252 `vbsn=0` `v`Substrate voltage in degradation calculation.

Spectre Circuit Simulator Reference

Component Statements Part 1

253 `vdsni=0.1` `V`Drain voltage in `Ids` degradation calculation.

254 `vgsni=5` `V`Gate voltage in `Ids` degradation calculation.

255 `vdsng=0.1` `V`Drain voltage in `Gm` degradation calculation.

256 `vgsng=5` `V`Gate voltage in `Gm` degradation calculation.

Spectre Stress Parameters

257 `esat=1.1e7` `V/m`Critical field in `Vdsat` calculation.

258 `esatg=2.5e6` `1/m`Gate voltage dependence of `esat`.

259 `vpg=-0.25`Gate voltage modifier.

260 `vpb=-0.13`Gate voltage modifier.

261 `subc1=2.24e-5`Substrate current coefficient.

262 `subc2=-0.1e-5` `1/V`Substrate current coefficient.

263 `sube=6.4`Substrate current exponent.

264 `strc=1`Stress coefficient.

265 `stre=1`Stress exponent.

BERT Stress Parameters

266 `h0=1`Aging coefficient.

267 `hgd=0` `1/V`Bias dependence of `h0`.

268 `m0=1`Aging exponent.

269 `mgd=0` `1/V`Bias dependence of `m0`.

270 `ecrit0=1.1e5` `V/cm`Critical electric field.

271 `lecrit0=0` `μm` `V/cm`Length dependence of `ecrit0`.

Spectre Circuit Simulator Reference

Component Statements Part 1

272 `wecrit0=0` μm V/cm Width dependence of `ecrit0`.

273 `ecritg=0` 1/cm Gate voltage dependence of `ecrit0`.

274 `lecritg=0` $\mu\text{m}/\text{cm}$ Length dependence of `ecritg`.

275 `wecritg=0` $\mu\text{m}/\text{cm}$ Width dependence of `ecritg`.

276 `ecritb=0` 1/cm Substrate voltage dependence of `ecrit0`.

277 `lecritb=0` $\mu\text{m}/\text{cm}$ Length dependence of `ecritb`.

278 `wecritb=0` $\mu\text{m}/\text{cm}$ Width dependence of `ecritb`.

279 `lc0=1` Substrate current coefficient.

280 `llc0=0` μm Length dependence of `lc0`.

281 `wlc0=0` μm Width dependence of `lc0`.

282 `lc1=1` Substrate current coefficient.

283 `llc1=0` μm Length dependence of `lc1`.

284 `wlc1=0` μm Width dependence of `lc1`.

285 `lc2=1` Substrate current coefficient.

286 `llc2=0` μm Length dependence of `lc2`.

287 `wlc2=0` μm Width dependence of `lc2`.

288 `lc3=1` Substrate current coefficient.

289 `llc3=0` μm Length dependence of `lc3`.

290 `wlc3=0` μm Width dependence of `lc3`.

291 `lc4=1` Substrate current coefficient.

292 `llc4=0` μm Length dependence of `lc4`.

293 `wlc4=0` μm Width dependence of `lc4`.

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Component Statements Part 1

294 `lc5=1` Substrate current coefficient.

295 `llc5=0` μm Length dependence of `lc5`.

296 `wlc5=0` μm Width dependence of `lc5`.

297 `lc6=1` Substrate current coefficient.

298 `llc6=0` μm Length dependence of `lc6`.

299 `wlc6=0` μm Width dependence of `lc6`.

300 `lc7=1` Substrate current coefficient.

301 `llc7=0` μm Length dependence of `lc7`.

302 `wlc7=0` μm Width dependence of `lc7`.

Imax and Imelt

The `imax` parameter aids convergence and prevents numerical overflow. The junction characteristics of the device are accurately modeled for current up to `imax`. If `imax` is exceeded during iterations, the linear model is substituted until the current drops below `imax` or until convergence is achieved. If convergence is achieved with the current exceeding `imax`, the results are inaccurate, and Spectre prints a warning.

A separate model parameter, `imelt`, is used as a limit warning for the junction current. This parameter can be set to the maximum current rating of the device. When any component of the junction current exceeds `imelt`, note that base and collector currents are composed of many exponential terms, Spectre issues a warning and the results become inaccurate. The junction current is linearized above the value of `imelt` to prevent arithmetic exception, with the exponential term replaced by a linear equation at `imelt`.

Both of these parameters have current density counterparts, `jmax` and `jmelt`, that you can specify if you want the absolute current values to depend on the device area.

Auto Model Selection

Many models need to be characterized for different geometries in order to obtain accurate results for model development. The model selector program automatically searches for a model with the length and width range specified in the instance statement and uses this model in the simulations.

Spectre Circuit Simulator Reference

Component Statements Part 1

For the auto model selector program to find a specific model, the models to be searched should be grouped together within braces. Such a group is called a model group. An opening brace is required at the end of the line defining each model group. Every model in the group is given a name followed by a colon and the list of parameters. Also, the four geometric parameters `lmax`, `lmin`, `wmax`, and `wmin` should be given. The selection criteria to choose a model is as follows:

`lmin <= inst_length < lmax and wmin <= inst_width < wmax`

Example

```
model ModelName ModelType {  
    1:      <model parameters> lmin=2 lmax=4 wmin=1 wmax=2  
    2:      <model parameters> lmin=1 lmax=2 wmin=2 wmax=4  
    3:      <model parameters> lmin=2 lmax=4 wmin=4 wmax=6  
}
```

Then for a given instance

`M1 1 2 3 4 ModelName w=3 l=1.5`

the program would search all the models in the model group with the name `ModelName` and then pick the first model whose geometric range satisfies the selection criteria. In the preceding example, the auto model selector program would choose `ModelName.2`.

The user must specify both length (`l`) and width (`w`) on the device instance line to enable automatic model selection.

Output Parameters

- 1 `weff` (m) Effective channel width.
- 2 `leff` (m) Effective channel length.
- 3 `rseff` (Ω) Effective source resistance.
- 4 `rdeff` (Ω) Effective drain resistance.
- 5 `aseff` (m^2) Effective area of source diffusion.
- 6 `adeff` (m^2) Effective area of drain diffusion.
- 7 `pseff` (m) Effective perimeter of source diffusion.
- 8 `pdeff` (m) Effective perimeter of source diffusion.

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Component Statements Part 1

- 9 `isseff` (A) Effective source-bulk junction reverse saturation current.
- 10 `isdeff` (A) Effective drain-bulk junction reverse saturation current.
- 11 `cbseff` (F) Effective zero-bias source-bulk junction capacitance.
- 12 `cbdeff` (F) Effective zero-bias drain-bulk junction capacitance.
- 13 `vto` (V) Effective zero-bias threshold voltage.
- 14 `vfb` (V) Effective flat-band voltage.
- 15 `phi` (V) Effective surface potential.
- 16 `k1` (\sqrt{V}) Effective body-effect coefficient.
- 17 `k2` Effective charge-sharing parameter.
- 18 `eta` Effective DIBL coefficient.

Operating-Point Parameters

- 1 `type=n` Transistor type.
Possible values are `n` or `p`.
- 2 `region=triode` Estimated operating region.
Possible values are `off`, `triode`, `sat`, `subth`, or `breakdown`.
- 3 `degradation=no` Hot-electron degradation flag.
Possible values are `no` or `yes`.
- 4 `reversed` Reverse mode indicator.
Possible values are `no` or `yes`.
- 5 `ids` (A) Resistive drain-to-source current.
- 6 `vgs` (V) Gate-source voltage.
- 7 `vds` (V) Drain-source voltage.
- 8 `vbs` (V) Bulk-source voltage.
- 9 `vth` (V) Threshold voltage.

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Component Statements Part 1

- 10 `vdsat` (V) Drain-source saturation voltage.
- 11 `betaeff` (A/V²) Effective beta.
- 12 `gm` (S) Common-source transconductance.
- 13 `gds` (S) Common-source output conductance.
- 14 `gmbs` (S) Body-transconductance.
- 15 `cbd` (F) Drain-bulk junction capacitance.
- 16 `cbs` (F) Source-bulk junction capacitance.
- 17 `cgs` (F) Gate-source capacitance.
- 18 `cgd` (F) Gate-drain capacitance.
- 19 `cgb` (F) Gate-bulk capacitance.
- 20 `ron` (Ω) ON-resistance.
- 21 `id` (A) Resistive drain current.
- 22 `ibulk` (A) Resistive bulk current.
- 23 `pwr` (W) Power at op point.
- 24 `gmoverid` (1/V) Gm/Ids.
- 25 `isub` (A) Substrate current.
- 26 `stress` Hot-electron stress.
- 27 `age` (s) Device age.
- 28 `he_vdsat` (V) hot electron vdsat.

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the

Spectre Circuit Simulator Reference

Component Statements Part 1

description for that parameter. For example, a reference of M-35 means the 35th model parameter.

ad	M-224	lc1	M-282	nrs	I-8	ute	M-166
ad	I-4	lc2	M-285	nrs	M-228	uto	M-165
adeff	O-6	lc3	M-288	pai0	M-134	vbb	M-220
af	M-233	lc4	M-291	paib	M-138	vbox	M-206
age	OP-27	lc5	M-294	pb	M-211	vbs	OP-8
ai0	M-131	lc6	M-297	pbi0	M-142	vbsn	M-252
aib	M-135	lc7	M-300	pbib	M-146	vdd	M-218
alarm	M-202	ld	I-9	pbsw	M-215	vds	OP-7
as	I-3	ldd	M-229	pd	I-6	vdsat	OP-10
as	M-223	ldif	M-192	pd	M-226	vdsng	M-255
aseff	O-5	lds	M-230	pdeff	O-8	vdsni	M-253
betaeff	OP-11	lecrit0	M-271	peta0	M-21	version	M-164
bi0	M-139	lecritb	M-277	petab	M-25	vfb	O-14
bib	M-143	lecritg	M-274	phi	O-15	vfb0	M-2
bvj	M-205	leff	O-2	phi0	M-6	vgg	M-219
capmod	M-180	leta0	M-19	pk1	M-13	vghigh	M-147
cbd	OP-15	letab	M-23	pk2	M-17	vglow	M-151
cbd	M-208	lgcd	M-194	pmu0	M-29	vgs	OP-6
cbdeff	O-12	lgcs	M-193	pmu0b	M-33	vgsng	M-256
cbs	OP-16	lk1	M-11	pmu20	M-45	vgsni	M-254
cbs	M-207	lk2	M-15	pmu2b	M-49	vof0	M-118
cbseff	O-11	llc0	M-280	pmu2g	M-53	vofb	M-122
cgb	OP-19	llc1	M-283	pmu30	M-57	vofd	M-126
cgbo	M-178	llc2	M-286	pmu3b	M-61	vpb	M-260
cgd	OP-18	llc3	M-289	pmu3g	M-65	vpg	M-259
cgdo	M-177	llc4	M-292	pmu40	M-69	vth	OP-9
cgs	OP-17	llc5	M-295	pmu4b	M-73	vto	O-13
cgso	M-176	llc6	M-298	pmu4g	M-77	w	M-221
cj	M-209	llc7	M-301	pmus0	M-37	w	I-1
cjsw	M-213	lmax	M-238	pmusb	M-41	wai0	M-133
crigm	M-248	lmin	M-239	pn0	M-109	waib	M-137
criids	M-249	lmu0	M-27	pnb	M-113	wbi0	M-141
criuo	M-247	lmu0b	M-31	pnd	M-117	wbib	M-145
crivth	M-246	lmu20	M-43	pphi	M-9	wecrit0	M-272
degradation	OP-3	lmu2b	M-47	ps	I-5	wecritb	M-278
degradation	M-241	lmu2g	M-51	ps	M-225	wecritg	M-275
degradation	I-14	lmu30	M-55	pseff	O-7	weff	O-1
degramod	M-240	lmu3b	M-59	ptc	M-169	weta0	M-20
d10	M-155	lmu3g	M-63	pul0	M-97	wetab	M-24
dskip	M-199	lmu40	M-67	pulb	M-101	wk1	M-12
duoc	M-244	lmu4b	M-71	puld	M-105	wk2	M-16
duoe	M-245	lmu4g	M-75	pua0	M-81	wlc0	M-281

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Component Statements Part 1

dvthc	M-242	lmus0	M-35	puab	M-85	wlc1	M-284
dvthe	M-243	lmusb	M-39	pub0	M-89	wlc2	M-287
dw0	M-156	ln0	M-107	pubb	M-93	wlc3	M-290
ecrit0	M-270	lnb	M-111	pvsb	M-5	wlc4	M-293
ecritb	M-276	lnd	M-115	pvghigh	M-150		
ecritg	M-273	lnom	M-251	pvglow	M-154	wlc6	M-299
ef	M-234	lphi	M-7	pvofo	M-121	wlc7	M-302
eg	M-170	lref	M-157	pvofo	M-125	wmax	M-236
esat	M-257	ls	I-10	pvofo	M-129	wmin	M-237
esatg	M-258	lu0	M-95	pwr	OP-23	wmu0	M-28
eta	O-18	lulb	M-99	rd	M-184	wmu0b	M-32
eta0	M-18	luld	M-103	rdc	M-187	wmu20	M-44
etab	M-22	lua0	M-79	rdd	M-189	wmu2b	M-48
fc	M-212	luab	M-83	rdeff	O-4	wmu2g	M-52
fcs	M-216	lub0	M-87	region	OP-2	wmu30	M-56
gap1	M-171	lubb	M-91	region	I-12	wmu3b	M-60
gap2	M-172	lvfb	M-3	reversed	OP-4	wmu3g	M-64
gds	OP-13	lvghigh	M-148	ron	OP-20	wmu40	M-68
gm	OP-12	lvglow	M-152	rs	M-183	wmu4b	M-72
gmbs	OP-14	lvof0	M-119	rsc	M-186	wmu4g	M-76
gmoverid	OP-24	lvofb	M-123	rseff	O-3	wmus0	M-36
h0	M-266	lvofd	M-127	rsh	M-185	wmusb	M-40
hdif	M-191	m	I-11	rss	M-188	wn0	M-108
he_vdsat	OP-28	m0	M-268	sc	M-195	wnb	M-112
hgd	M-267	meto	M-179	strc	M-264	wnd	M-116
ibulk	OP-22	mgd	M-269	stre	M-265	wnoi	M-235
id	OP-21	minr	M-190	stress	OP-26	wnom	M-250
ids	OP-5	mj	M-210	subc1	M-261	wphi	M-8
imax	M-203	mjs	M-214	subc2	M-262	wref	M-158
imelt	M-200	mu0	M-26	sube	M-263	wul0	M-96
is	M-197	mu0b	M-30	subthmod	M-130	wulb	M-100
isdeff	O-10	mu20	M-42	temp	M-161	wuld	M-104
isseff	O-9	mu2b	M-46	tempmod	M-163	wua0	M-80
isub	OP-25	mu2g	M-50	tlev	M-167	wuab	M-84
jmax	M-204	mu30	M-54	tlevc	M-168	wub0	M-88
jmelt	M-201	mu3b	M-58	tox	M-217	wubb	M-92
js	M-196	mu3g	M-62	trd	M-174	wvfb	M-4
k1	M-10	mu40	M-66	trise	M-162	wvghigh	M-149
k1	O-16	mu4b	M-70	trise	I-13	wvglow	M-153
k2	M-14	mu4g	M-74	trs	M-173	wvof0	M-120
k2	O-17	mus0	M-34	type	M-1	wvofb	M-124
kf	M-232	musb	M-38	type	OP-1	wvofd	M-128
l	M-222	n	M-198	ul0	M-94	xl	M-160
l	I-2	n0	M-106	ulb	M-98	xpart	M-181
lai0	M-132	nb	M-110	uld	M-102	xqc	M-182
laib	M-136	nd	M-114	ua0	M-78	xti	M-175
lbi0	M-140	noisemod	M-231	uab	M-82	xw	M-159

Spectre Circuit Simulator Reference

Component Statements Part 1

lbib	M-144	nrd	I-7	ub0	M-86
lc0	M-279	nrd	M-227	ubb	M-90

BSIM3 MOS Transistor (bsim3)

Description

This is the BSIM3 version-2 (BSIM3v2) model. The BSIM3v2 model is a physically-based, predictive, and computationally efficient model developed at the University of California, Berkeley. It is suitable for both digital and analog applications. In SPICE mode, refer to BSIM3 as MOS level 10. BSIM3 transistors require that you use a model statement.

This device is supported within altergroups.

To examine the equations used for this component, consult the [Spectre Circuit Simulator Device Model Equations](#) manual.

Sample Instance Statement

```
m3 (1 2 0 0) nchmod l=1.5u w=100u as=450p ad=450p pd=209u ps=209u nrd=207m nrs=207m m=1
```

Sample Model Statement

```
model nchmod bsim3 vtho=5.94e-01 phi=0.69 k1=0.72 k2=0 w0=1.3e-07 tox=5.9e-09  
rdsw=80 uo=499 xj=2e-07 vsat=600e+04 at=3.4e+04 a0=0.8 cdsc=1.4e-03 nfactor=1.03
```

Instance Definition

Name d g s b ModelName parameter=value ...

Instance Parameters

- 1 w (m) Channel width.
- 2 l (m) Channel length.
- 3 as (m²) Area of source diffusion.
- 4 ad (m²) Area of drain diffusion.
- 5 ps (m) Perimeter of source diffusion.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 6 `pd (m)` Perimeter of drain diffusion.
- 7 `nrd (m/m)` Number of squares of drain diffusion.
- 8 `nrs (m/m)` Number of squares of source diffusion.
- 9 `m=1` Multiplicity factor (number of MOSFETs in parallel).
- 10 `region=triode` Estimated operating region.
Possible values are `off`, `triode`, `sat`, `subth`, or `breakdown`.
- 11 `trise` Temperature rise from ambient.

Model Definition

`model modelName bsim3 parameter=value ...`

Model Parameters

Device Type Parameters

- 1 `type=n` Transistor type.
Possible values are `n` or `p`.

Threshold Voltage Parameters

- 2 `vtho=0 V` Threshold voltage at zero body bias.
- 3 `phi=0.7 V` Surface potential at strong inversion.
- 4 `k1=0.53 \sqrt{V}` Body-effect coefficient.
- 5 `k2=-0.0186` Charge-sharing parameter.
- 6 `k3=80` Narrow width coefficient.
- 7 `k3b=0 1/V` Narrow width coefficient.
- 8 `w0=2.5e-6 m` Narrow width coefficient.
- 9 `n1x=1.74e-7 m` Lateral nonuniform doping coefficient.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 10 `gamma1=0` \sqrt{V} Body-effect coefficient near the surface.
- 11 `gamma2=0` \sqrt{V} Body-effect coefficient in the bulk.
- 12 `theta=0.02` $1/V$ Drain-induced barrier lowering coefficient.
- 13 `eta=0.3` $1/V$ Effective drain voltage coefficient.
- 14 `littl (m)` Depth of current path.
- 15 `vfb (V)` Flat-band voltage.
- 16 `vbv (V)` Threshold voltage transition body voltage.
- 17 `vbi (V)` Substrate junction built-in potential.
- 18 `vbm=-5` V Maximum applied body voltage.
- 19 `dvt0=2.2` First coefficient of short-channel effects.
- 20 `dvt1=0.53` Second coefficient of short-channel effects.
- 21 `dvt2=-0.032` $1/V$ Body-bias coefficient of short-channel effects.
- 22 `a0=1` for nmos and 4.4 for pmos
Nonuniform depletion width effect coefficient.
- 23 `a1=0` for nmos, 0.23 for pmos
No-saturation coefficient.
- 24 `a2=1` for nmos, 0.08 for pmos
No-saturation coefficient.
- 25 `keta=-0.047` $1/V$ Body-bias coefficient for non-uniform depletion width effect.

Process Parameters

- 26 `nsub=2e15` cm^{-3} Substrate doping concentration.
- 27 `npeak=1.7e17` cm^{-3}
Peak channel doping concentration.
- 28 `ngate (cm-3)` Poly-gate doping concentration.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 29 $x_j=0.15e-6$ mSource/drain junction depth.
- 30 $d_l=0$ mLateral diffusion for one side.
- 31 $d_w=0$ mWidth reduction for one side.
- 32 $t_{ox}=1.5e-8$ mGate oxide thickness.
- 33 $v_{dd}=5$ vMaximum drain voltage.
- 34 $x_t=1.55e-7$ mDoping depth.
- 35 $l_{dd}=0$ mTotal length of lightly doped drain region.
- 36 $r_{ds0}=0$ Ω Total drain-source resistance.
- 37 $r_{dsw}=0$ Ω μ mWidth dependence of drain-source resistance.

Mobility Parameters

- 38 $u_o=670$ cm^2/V sLow-field surface mobility at t_{nom} . Default is 250 for PMOS.
- 39 $v_{sat}=9.58e4$ m/sCarrier saturation velocity at t_{nom} .
- 40 $u_a=2.25e-9$ m/vFirst-order mobility reduction coefficient.
- 41 $u_b=5.87e-19$ m^2/v^2
Second-order mobility reduction coefficient.
- 42 $u_c=0.0465$ 1/vBody-bias dependence of mobility.
- 43 $u_c0=0$ Mobility coefficient.

Output Resistance Parameters

- 44 $satmod=2$ Saturation model selector.
- 45 $bulkmod=1$ Bulk-charge effect model selector.
- 46 $drout=0.56$ DIBL effect on output resistance coefficient.
- 47 $alpha=1.9$ Reference voltage multiplication factor.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 48 `em=4.1e7 V/m`Maximum electric field.
- 49 `pclm=1.3`Channel length modulation coefficient.
- 50 `pdibl1=0.39`First coefficient of drain-induced barrier lowering.
- 51 `pdibl2=8.6e-3`Second coefficient of drain-induced barrier lowering.
- 52 `pscbe1=4.24e8 V/m`First coefficient of substrate current body effect.
- 53 `pscbe2=1e-5 m/v`Second coefficient of substrate current body effect.
- 54 `pvag=0`Gate dependence of Early voltage.

Subthreshold Parameters

- 55 `subthmod=2`Subthreshold model selector.
- 56 `vghigh=0.12 V`Upper bound of transition region.
- 57 `vglow=-0.12 V`Lower bound of transition region.
- 58 `cdsc=2.4e-4 F/m2`Source/drain and channel coupling capacitance.
- 59 `cdscb=0 F/m2 V`Body-bias dependence of `cdsc`.
- 60 `nfactor=1`Subthreshold swing coefficient.
- 61 `cit=0 F`Interface trap parameter for subthreshold swing.
- 62 `voff=-0.11 V`Threshold voltage offset.
- 63 `dsub=drout`DIBL effect in subthreshold region.
- 64 `eta0=0.08`DIBL coefficient subthreshold region.
- 65 `etab=-0.07 1/V`Body-bias dependence of `et0`.

Parasitic Resistance Parameters

- 66 `rsh=0 Ω/sqr`Source/drain diffusion sheet resistance.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 67 `rs=0` Ω Source resistance.
- 68 `rd=0` Ω Drain resistance.
- 69 `lgcs=0` mGate-to-contact length of source side.
- 70 `lgcd=0` mGate-to-contact length of drain side.
- 71 `rsc=0` Ω Source contact resistance.
- 72 `rdc=0` Ω Drain contact resistance.
- 73 `rss=0` Ω mScalable source resistance.
- 74 `rdd=0` Ω mScalable drain resistance.
- 75 `sc= ∞` mSpacing between contacts.
- 76 `ldif=0` mLateral diffusion beyond the gate.
- 77 `hdif=0` mLength of heavily doped diffusion.
- 78 `minr=0.1` Ω Minimum source/drain resistance.

Junction Diode Model Parameters

- 79 `js` (A/m^2) Bulk junction reverse saturation current density.
- 80 `is=1e-14` ABulk junction reverse saturation current.
- 81 `n=1` Junction emission coefficient.
- 82 `dskip=yes` Use simple piece-wise linear model for diode currents below $0.1 \cdot i_{abstol}$.
Possible values are `no` or `yes`.
- 83 `imelt=`imaxA`` Explosion current.
- 84 `jmelt=`jmaxA/m`2`
Explosion current density.

Spectre Circuit Simulator Reference

Component Statements Part 1

Overlap Capacitance Parameters

85 `cgs0` (F/m) Gate-source overlap capacitance.

86 `cgd0` (F/m) Gate-drain overlap capacitance.

87 `cgbo` (F/m) Gate-bulk overlap capacitance.

88 `meto=0` mMetal overlap in fringing field.

Junction Capacitance Model Parameters

89 `cbs=0` F Bulk-source zero-bias junction capacitance.

90 `cbd=0` F Bulk-drain zero-bias junction capacitance.

91 `cj=5e-4` F/m² Zero-bias junction bottom capacitance density.

92 `mj=1/2` Bulk junction bottom grading coefficient.

93 `pb=0.8` V Bulk junction built-in potential.

94 `fc=0.5` Forward-bias depletion capacitance threshold.

95 `cjsw=5e-10` F/m Zero-bias junction sidewall capacitance density.

96 `mjsw=1/3` Bulk junction sidewall grading coefficient.

97 `pbsw=0.8` V Side-wall junction built-in potential.

98 `fcsw=0.5` Side-wall forward-bias depletion capacitance threshold.

Charge Model Selection Parameters

99 `capmod=yang` Intrinsic charge model.

Possible values are `none`, `meyer`, `yang`, or `bsim`.

100 `xpart=1` Drain/source channel charge partition in saturation for BSIM charge model, use 0.0 for 40/60, 0.5 for 50/50, or 1.0 for 0/100.

101 `xqc=0` Drain/source channel charge partition in saturation for charge models, e.g. use 0.4 for 40/60, 0.5 for 50/50, 0 for 0/100.

Spectre Circuit Simulator Reference

Component Statements Part 1

Default Instance Parameters

- 102 $w=5e-6$ m Default channel width.
- 103 $l=5e-6$ m Default channel length.
- 104 $as=0$ m² Default area of source diffusion.
- 105 $ad=0$ m² Default area of drain diffusion.
- 106 $ps=0$ m Default perimeter of source diffusion.
- 107 $pd=0$ m Default perimeter of drain diffusion.
- 108 $nrd=0$ m/m Default number of squares of drain diffusion.
- 109 $nrs=0$ m/m Default number of squares of source diffusion.

Temperature Effects Parameters

- 110 t_{nom} (C) Parameters measurement temperature. Default set by `options`.
- 111 $trise=0$ C Temperature rise from ambient.
- 112 $tlev=0$ DC temperature selector.
- 113 $tlevc=0$ AC temperature selector.
- 114 $eg=1.12452$ V Energy band gap.
- 115 $gap1=7.02e-4$ V/C Band gap temperature coefficient.
- 116 $gap2=1108$ C Band gap temperature offset.
- 117 $kt1=-0.11$ V Temperature coefficient for threshold voltage.
- 118 $kt1l=-1.86e-7$ V m Temperature coefficient for threshold voltage.
- 119 $kt2=0.022$ Temperature coefficient for threshold voltage.
- 120 $at=3.3e4$ m/s Temperature coefficient for v_{sat} .
- 121 $ua1=4.31e-9$ m/V Temperature coefficient for u_a .

Spectre Circuit Simulator Reference

Component Statements Part 1

122 `ub1=-7.61e-18 m2/v2`

Temperature coefficient for `ub`.

123 `uc1=-0.056 1/V`Temperature coefficient for `uc`.

124 `trs=0 1/C`Temperature parameter for source resistance.

125 `trd=0 1/C`Temperature parameter for drain resistance.

126 `ute=-1.5`Mobility temperature exponent.

127 `xti=3`Saturation current temperature exponent.

128 `ptc=0 V/C`Surface potential temperature coefficient.

129 `tcv=0 V/C`Threshold voltage temperature coefficient.

130 `pta=0 V/C`Junction potential temperature coefficient.

131 `ptp=0 V/C`Sidewall junction potential temperature coefficient.

132 `cta=0 1/C`Junction capacitance temperature coefficient.

133 `ctp=0 1/C`Sidewall junction capacitance temperature coefficient.

Noise Model Parameters

134 `noisemod=1`Noise model selector.

135 `kf=0`Flicker (1/f) noise coefficient.

136 `af=1`Flicker (1/f) noise exponent.

137 `ef=1`Flicker (1/f) noise frequency exponent.

138 `wnoi=1e-5 m`Channel width at which noise parameters were extracted.

139 `a=1e16 for nmos and 9.9e14 for pmos`
Oxide trap density coefficient.

140 `b=5e4 for nmos and 2.4e3 for pmos`
Oxide trap density coefficient.

Spectre Circuit Simulator Reference

Component Statements Part 1

141 `c=-1.4e-8` for `nmos` and `1.4e-8` for `pmos`
Oxide trap density coefficient.

Operating Region Warning Control Parameters

142 `alarm=none` Forbidden operating region.
Possible values are `none`, `off`, `triode`, `sat`, `subth`, or `rev`.

143 `imax=1` A Maximum allowable current.

144 `jmax=1e8` A/m² Maximum allowable current density.

145 `bvj=∞` V Junction reverse breakdown voltage.

146 `vbox=1e9` `tox` V Oxide breakdown voltage.

147 `maxvp=1.12` V Maximum allowable voltage across the gate poly layer.

Auto Model Selector Parameters

148 `wmax=1.0` m Maximum channel width for which the model is valid.

149 `wmin=0.0` m Minimum channel width for which the model is valid.

150 `lmax=1.0` m Maximum channel length for which the model is valid.

151 `lmin=0.0` m Minimum channel length for which the model is valid.

`Imax` and `Imelt`

The `imax` parameter aids convergence and prevents numerical overflow. The junction characteristics of the device are accurately modeled for current up to `imax`. If `imax` is exceeded during iterations, the linear model is substituted until the current drops below `imax` or until convergence is achieved. If convergence is achieved with the current exceeding `imax`, the results are inaccurate, and Spectre prints a warning.

A separate model parameter, `imelt`, is used as a limit warning for the junction current. This parameter can be set to the maximum current rating of the device. When any component of the junction current exceeds `imelt`, note that base and collector currents are composed of many exponential terms, Spectre issues a warning and the results become inaccurate. The junction current is linearized above the value of `imelt` to prevent arithmetic exception, with the exponential term replaced by a linear equation at `imelt`.

Spectre Circuit Simulator Reference

Component Statements Part 1

Both of these parameters have current density counterparts, `jmax` and `jmelt`, that you can specify if you want the absolute current values to depend on the device area.

Auto Model Selection

Many models need to be characterized for different geometries in order to obtain accurate results for model development. The model selector program automatically searches for a model with the length and width range specified in the instance statement and uses this model in the simulations.

For the auto model selector program to find a specific model, the models to be searched should be grouped together within braces. Such a group is called a model group. An opening brace is required at the end of the line defining each model group. Every model in the group is given a name followed by a colon and the list of parameters. Also, the four geometric parameters `lmax`, `lmin`, `wmax`, and `wmin` should be given. The selection criteria to choose a model is as follows:

```
lmin <= inst_length < lmax  and  wmin <= inst_width  < wmax
```

Example

```
model ModelName ModelType {  
    1:    <model parameters> lmin=2 lmax=4 wmin=1 wmax=2  
    2:    <model parameters> lmin=1 lmax=2 wmin=2 wmax=4  
    3:    <model parameters> lmin=2 lmax=4 wmin=4 wmax=6  
}
```

Then for a given instance

```
M1 1 2 3 4 ModelName w=3 l=1.5
```

the program would search all the models in the model group with the name `ModelName` and then pick the first model whose geometric range satisfies the selection criteria. In the preceding example, the auto model selector program would choose `ModelName.2`.

The user must specify both length (`l`) and width (`w`) on the device instance line to enable automatic model selection.

Output Parameters

- 1 `weff` (m) Effective channel width.
- 2 `leff` (m) Effective channel length.
- 3 `rseff` (Ω) Effective source resistance.

Spectre Circuit Simulator Reference

Component Statements Part 1

4 `rdef` (Ω) Effective drain resistance.

Operating-Point Parameters

1 `type`=n Transistor type.

Possible values are n or p.

2 `region`=triode Estimated operating region.

Possible values are off, triode, sat, subth, or breakdown.

3 `reversed` Reverse mode indicator.

Possible values are no or yes.

4 `ids` (A) Resistive drain-to-source current.

5 `vgs` (V) Gate-source voltage.

6 `vds` (V) Drain-source voltage.

7 `vbs` (V) Bulk-source voltage.

8 `vth` (V) Threshold voltage.

9 `vdsat` (V) Drain-source saturation voltage.

10 `gm` (S) Common-source transconductance.

11 `gds` (S) Common-source output conductance.

12 `gmbs` (S) Body-transconductance.

13 `betaeff` (A/V^2) Effective beta.

14 `cbd` (F) Drain-bulk junction capacitance.

15 `cbs` (F) Source-bulk junction capacitance.

16 `cgs` (F) Gate-source capacitance.

17 `cgd` (F) Gate-drain capacitance.

18 `cgb` (F) Gate-bulk capacitance.

Spectre Circuit Simulator Reference

Component Statements Part 1

19 `ron` (Ω) On-resistance.

20 `id` (A) Resistive drain current.

21 `ibulk` (A) Resistive bulk current.

22 `pwr` (W) Power at op point.

23 `gmoverid` (1/V) Gm/Ids.

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

<code>a</code> M-139	<code>etab</code> M-65	<code>ngate</code> M-28	<code>tnom</code> M-110
<code>a0</code> M-22	<code>fc</code> M-94	<code>nlx</code> M-9	<code>tox</code> M-32
<code>a1</code> M-23	<code>fcs</code> M-98	<code>noisemod</code> M-134	<code>trd</code> M-125
<code>a2</code> M-24	<code>gamma1</code> M-10	<code>npeak</code> M-27	<code>trise</code> I-11
<code>ad</code> I-4	<code>gamma2</code> M-11	<code>nrd</code> I-7	<code>trise</code> M-111
<code>ad</code> M-105	<code>gap1</code> M-115	<code>nrd</code> M-108	<code>trs</code> M-124
<code>af</code> M-136	<code>gap2</code> M-116	<code>nrs</code> M-109	<code>type</code> M-1
<code>alarm</code> M-142	<code>gds</code> OP-11	<code>nrs</code> I-8	<code>type</code> OP-1
<code>alpha</code> M-47	<code>gm</code> OP-10	<code>nsub</code> M-26	<code>ua</code> M-40
<code>as</code> I-3	<code>gmbs</code> OP-12	<code>pb</code> M-93	<code>ua1</code> M-121
<code>as</code> M-104	<code>gmoverid</code> OP-23	<code>pbsw</code> M-97	<code>ub</code> M-41
<code>at</code> M-120	<code>hdif</code> M-77	<code>pclm</code> M-49	<code>ub1</code> M-122
<code>b</code> M-140	<code>ibulk</code> OP-21	<code>pd</code> M-107	<code>uc</code> M-42
<code>betaeff</code> OP-13	<code>id</code> OP-20	<code>pd</code> I-6	<code>uc0</code> M-43
<code>bulkmod</code> M-45	<code>ids</code> OP-4	<code>pdibl1</code> M-50	<code>uc1</code> M-123
<code>bvj</code> M-145	<code>imax</code> M-143	<code>pdibl2</code> M-51	<code>uo</code> M-38
<code>c</code> M-141	<code>imelt</code> M-83	<code>phi</code> M-3	<code>ute</code> M-126
<code>capmod</code> M-99	<code>is</code> M-80	<code>ps</code> I-5	<code>vbi</code> M-17
<code>cbd</code> OP-14	<code>jmax</code> M-144	<code>ps</code> M-106	<code>vbm</code> M-18
<code>cbd</code> M-90	<code>jmelt</code> M-84	<code>pscbel</code> M-52	<code>vbox</code> M-146
<code>cbs</code> OP-15	<code>js</code> M-79	<code>pscbe2</code> M-53	<code>vbs</code> OP-7
<code>cbs</code> M-89	<code>k1</code> M-4	<code>pta</code> M-130	<code>vbx</code> M-16
<code>cdsc</code> M-58	<code>k2</code> M-5	<code>ptc</code> M-128	<code>vdd</code> M-33
<code>cdscb</code> M-59	<code>k3</code> M-6	<code>ptp</code> M-131	<code>vds</code> OP-6
<code>cgb</code> OP-18	<code>k3b</code> M-7	<code>pvag</code> M-54	<code>vdsat</code> OP-9

Spectre Circuit Simulator Reference

Component Statements Part 1

cgbo	M-87	keta	M-25	pwr	OP-22	vfb	M-15
cgd	OP-17	kf	M-135	rd	M-68	vghigh	M-56
cgdo	M-86	kt1	M-117	rdc	M-72	vglow	M-57
cgs	OP-16	kt11	M-118	rdd	M-74	vgs	OP-5
cgso	M-85	kt2	M-119	rdeff	O-4	voff	M-62
cit	M-61	l	M-103	rds0	M-36	vsat	M-39
cj	M-91	l	I-2	rdsw	M-37	vth	OP-8
cjsw	M-95	ldd	M-35	region	I-10	vtho	M-2
cta	M-132	ldif	M-76	region	OP-2	w	I-1
ctp	M-133	leff	O-2	reversed	OP-3	w	M-102
dl	M-30	lgcd	M-70	ron	OP-19	w0	M-8
drout	M-46	lgcs	M-69	rs	M-67	weff	O-1
dskip	M-82	lit1	M-14	rsc	M-71	wmax	M-148
dsub	M-63	lmax	M-150	rseff	O-3	wmin	M-149
dvt0	M-19	lmin	M-151	rsh	M-66	wnoi	M-138
dvt1	M-20	m	I-9	rss	M-73	xj	M-29
dvt2	M-21	maxvp	M-147	satmod	M-44	xpart	M-100
dw	M-31	meto	M-88	sc	M-75	xqc	M-101
ef	M-137	minr	M-78	subthmod	M-55	xt	M-34
eg	M-114	mj	M-92	tcv	M-129	xti	M-127
em	M-48	mjsw	M-96	theta	M-12		
eta	M-13	n	M-81	tlev	M-112		
eta0	M-64	nfactor	M-60	tlevc	M-113		

BSIM3v3 MOS Transistor (bsim3v3)

Description

BSIM3v3 is the version-3 of bsim3 model. The versions supported are 3.1, 3.2, 3.21, 3.22, 3.23 and 3.24. It uses single-piece equations for all regions to improve the smoothness of the model characteristics. BSIM3v3 also allows the binning option like the approach used in bsim1 and bsim2. This option is provided for people who want to achieve the highest accuracy of the model. The binning equation is given by

$$P = P_0 + P_I / Leff + P_w / Weff + P_p / (Leff * Weff)$$

Only the P0 parameters are listed. PI, Pw, and Pp are not shown but can be recognized. The names of PI, Pw, and Pp are identical to that of P0 but with a prefix of I, w, and p, respectively. BSIM3v3 transistors require that you use a model statement.

For more information on this model, please consult the University of California at Berkeley BSIM3 home page at

<http://www-device.eecs.berkeley.edu/~bsim3/index.html>

Spectre Circuit Simulator Reference

Component Statements Part 1

This device is supported within altergroups.

To examine the equations used for this component, consult the [Spectre Circuit Simulator Device Model Equations](#) manual.

Sample Instance Statement

```
m4 (0 2 1 1) pchmod w=2u l=0.8u as=250p ad=250p pd=168p ps=168p m=1
```

Sample Model Statement

```
model pchmod bsim3v3 type=p mobmod=1 capmod=2 version=3.1 tox=9e-5 cdsc=1e-3  
cdscb=-4.36889e-4 cdsd=0 cit=0 nfactor=1.79 xj=1.5e-7 vsat=1.5737e5 at=1e5  
a0=1.2522809 ags=0.2912413 al=1.01222e-4 a2=0.996841 keta=0 nch=4.06263e17  
ngate=7.6e19 kl=0.823562
```

Instance Definition

```
Name d g s b ModelName parameter=value ...
```

Instance Parameters

- 1 w (m) Channel width.
- 2 l (m) Channel length.
- 3 as (m²) Area of source diffusion.
- 4 ad (m²) Area of drain diffusion.
- 5 ps (m) Perimeter of source diffusion.
- 6 pd (m) Perimeter of drain diffusion.
- 7 nrd (m/m) Number of squares of drain diffusion.
- 8 nrs (m/m) Number of squares of source diffusion.
- 9 m=1 Multiplicity factor (number of MOSFETs in parallel).
- 10 region=triode Estimated operating region.
Possible values are off, triode, sat, subth, or breakdown.
- 11 nqsmod NQS flag.

12 `trise` Temperature rise from ambient.

Model Definition

`model modelName bsim3v3 parameter=value ...`

Model Parameters

Device Type Parameters

1 `type=n` Transistor type.
Possible values are `n` or `p`.

Threshold Voltage Parameters

- 2 `vtho` (V) Threshold voltage at zero body bias for long-channel devices. For enhancement-mode devices, `vtho` > 0 for n-channel and `vth` < 0 for p-channel. Default value is calculated from other model parameters.
- 3 `vfb=-1` Flat-band voltage.
- 4 `k1=0.5` \sqrt{V} Body-effect coefficient.
- 5 `k2=-0.0186` Charge-sharing parameter.
- 6 `k3=80` Narrow width coefficient.
- 7 `k3b=0` $1/V$ Narrow width coefficient.
- 8 `w0=2.5e-6` m Narrow width coefficient.
- 9 `nlx=1.74e-7` m Lateral nonuniform doping coefficient.
- 10 `gamma1` (\sqrt{V}) Body-effect coefficient near the surface.
- 11 `gamma2` (\sqrt{V}) Body-effect coefficient in the bulk.
- 12 `vbx` (V) Threshold voltage transition body voltage.
- 13 `vbm=-3` V Maximum applied body voltage.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 14 $\text{dvt0}=2.2$ First coefficient of short-channel effects.
- 15 $\text{dvt1}=0.53$ Second coefficient of short-channel effects.
- 16 $\text{dvt2}=-0.032$ $1/V$ Body-bias coefficient of short-channel effects.
- 17 $\text{dvt0w}=0$ First coefficient of narrow-width effects.
- 18 $\text{dvt1w}=5.3e6$ Second coefficient of narrow-width effects.
- 19 $\text{dvt2w}=-0.032$ $1/V$ Body-bias coefficient of narrow-width effects.
- 20 $\text{a0}=1$ Nonuniform depletion width effect coefficient.
- 21 $\text{b0}=0$ m Bulk charge coefficient due to narrow width effect.
- 22 $\text{b1}=0$ m Bulk charge coefficient due to narrow width effect.
- 23 $\text{a1}=0$ No-saturation coefficient.
- 24 $\text{a2}=1$ No-saturation coefficient.
- 25 $\text{ags}=0$ F/m^2 V Gate-bias dependence of A_{bulk} .
- 26 $\text{keta}=-0.047$ $1/V$ Body-bias coefficient for non-uniform depletion width effect.

Process Parameters

- 27 $\text{nsub}=6e16$ cm^{-3} Substrate doping concentration.
- 28 $\text{nch}=1.7e17$ cm^{-3} Peak channel doping concentration.
- 29 $\text{ngate}=\infty$ cm^{-3}
Poly-gate doping concentration.
- 30 $\text{xj}=0.15e-6$ m Source/drain junction depth.
- 31 $\text{lnt}=0$ m Lateral diffusion for one side.
- 32 $\text{wnt}=0$ m Width reduction for one side.
- 33 $\text{ll}=0$ m Length dependence of δL .

Spectre Circuit Simulator Reference

Component Statements Part 1

- 34 `lln=1` Length exponent of delta L.
- 35 `lw=0` mWidth dependence of delta L.
- 36 `lwn=1` Width exponent of delta L.
- 37 `lwl=0` m²Area dependence of delta L.
- 38 `wl=0` mLength dependence of delta W.
- 39 `wln=1` Length exponent of delta W.
- 40 `ww=0` mWidth dependence of delta W.
- 41 `wwn=1` Width exponent of delta W.
- 42 `wwl=0` m²Area dependence of delta W.
- 43 `dwg=0` m/ \sqrt{v} Gate-bias dependence of channel width.
- 44 `dwb=0` m/ \sqrt{v} Body-bias dependence of channel width.
- 45 `tox=1.5e-8` mGate oxide thickness.
- 46 `toxm=tox` mTox at which parameters were extracted.
- 47 `xt=1.55e-7` mDoping depth.
- 48 `rdsw=0` Ω μm Width dependence of drain-source resistance.
- 49 `prwb=0` $1/\sqrt{v}$ Body-effect coefficient for Rds.
- 50 `prwg=0` $1/v$ Gate-effect coefficient for Rds.
- 51 `wr=1` Width offset for parasitic resistance.
- 52 `binunit=1` Bin parameter unit selector. 1 for microns and 2 for meters.

Mobility Parameters

- 53 `mobmod=1` Mobility model selector.

Spectre Circuit Simulator Reference

Component Statements Part 1

54 $u0=670 \text{ cm}^2/\text{V s}$ Low-field surface mobility at t_{nom} . Default is 250 for PMOS Mobility can also be specified in $M2/Vs$.

55 $vsat=8e4 \text{ m/s}$ Carrier saturation velocity at t_{nom} .

56 $ua=2.25e-9 \text{ m/v}$ First-order mobility reduction coefficient.

57 $ub=5.87e-19 \text{ m}^2/\text{V}^2$
Second-order mobility reduction coefficient.

58 $uc=-4.65e-11 \text{ m/v}^2$
Body-bias dependence of mobility. Default is -0.046 and unit is 1/V for $mobmod=3$.

Output Resistance Parameters

59 $drout=0.56$ DIBL effect on output resistance coefficient.

60 $pclm=1.3$ Channel length modulation coefficient.

61 $pdiblc1=0.39$ First coefficient of drain-induced barrier lowering.

62 $pdiblc2=8.6e-3$ Second coefficient of drain-induced barrier lowering.

63 $pdiblc b=0 \text{ 1/v}$ Body-effect coefficient for DIBL.

64 $pscbe1=4.24e8 \text{ V/m}$ First coefficient of substrate current body effect.

65 $pscbe2=1e-5 \text{ m/v}$ Second coefficient of substrate current body effect.

66 $pvag=0$ Gate dependence of Early voltage.

67 $\delta=0.01 \text{ v}$ Effective drain voltage smoothing parameter.

Subthreshold Parameters

68 $cdsc=2.4e-4 \text{ F/m}^2$ Source/drain and channel coupling capacitance.

69 $cdsc b=0 \text{ F/m}^2 \text{ v}$ Body-bias dependence of $cdsc$.

70 $cdsc d=0 \text{ F/m}^2 \text{ v}$ Drain-bias dependence of $cdsc$.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 71 `nfactor=1` Subthreshold swing coefficient.
- 72 `cit=0` `F` Interface trap parameter for subthreshold swing.
- 73 `voff=-0.08` `V` Threshold voltage offset.
- 74 `dsub=drout` DIBL effect in subthreshold region.
- 75 `eta0=0.08` DIBL coefficient subthreshold region.
- 76 `etab=-0.07` $1/V$ Body-bias dependence of `et0`.

Substrate Current Parameters

- 77 `alpha0=0` `m/v` Substrate current impact ionization coefficient.
- 78 `alpha1=0` $1/V$ Substrate current impact ionization coefficient.
- 79 `beta0=30` $1/V$ Substrate current impact ionization exponent.

Parasitic Resistance Parameters

- 80 `rsh=0` Ω/sqr Source/drain diffusion sheet resistance.
- 81 `rs=0` Ω Source resistance.
- 82 `rd=0` Ω Drain resistance.
- 83 `lgcs=0` `m` Gate-to-contact length of source side.
- 84 `lgcd=0` `m` Gate-to-contact length of drain side.
- 85 `rsc=0` Ω Source contact resistance.
- 86 `rdc=0` Ω Drain contact resistance.
- 87 `rss=0` Ω `m` Scalable source resistance.
- 88 `rdd=0` Ω `m` Scalable drain resistance.
- 89 `sc= ∞` `m` Spacing between contacts.

Spectre Circuit Simulator Reference

Component Statements Part 1

90 `ldif=0` `m`Lateral diffusion beyond the gate.

91 `hdif=0` `m`Length of heavily doped diffusion.

92 `minr=0.1` Ω Minimum source/drain resistance.

Junction Diode Model Parameters

93 `js` (A/m^2) Bulk junction reverse saturation current density.

94 `jsw=0` A/m Sidewall junction reverse saturation current density.

95 `is=1e-14` A Bulk junction reverse saturation current.

96 `n=1` Junction emission coefficient.

97 `dskip=yes` Use simple piece-wise linear model for diode currents below $0.1 \cdot i_{abstol}$.
Possible values are `no` or `yes`.

98 `imelt='imaxA'` Explosion current.

99 `jmelt='jmaxA/m'^2`
Explosion current density.

Overlap Capacitance Parameters

100 `cgso` (F/m) Gate-source overlap capacitance.

101 `cgdo` (F/m) Gate-drain overlap capacitance.

102 `cgbo=2 Dwc Cox F/m`
Gate-bulk overlap capacitance. The default value is 0 if
version=3.0.

103 `meto=0` `m` Metal overlap in fringing field.

104 `cgs1=0` F/m Gate-source overlap capacitance in LDD region.

105 `cgd1=0` F/m Gate-drain overlap capacitance in LDD region.

106 `ckappa=0.6` Overlap capacitance fitting parameter.

Spectre Circuit Simulator Reference

Component Statements Part 1

Junction Capacitance Model Parameters

- 107 `cbs=0` F Bulk-source zero-bias junction capacitance.
- 108 `cbd=0` F Bulk-drain zero-bias junction capacitance.
- 109 `cj=5e-4` F/m^2 Zero-bias junction bottom capacitance density.
- 110 `mj=1/2` Bulk junction bottom grading coefficient.
- 111 `pb=1` V Bulk junction built-in potential.
- 112 `fc=0.5` Forward-bias depletion capacitance threshold.
- 113 `cjsw=5e-10` F/m Zero-bias junction sidewall capacitance density.
- 114 `mjsw=0.33` Bulk junction sidewall grading coefficient.
- 115 `pbsw=1` V Side-wall junction built-in potential.
- 116 `cjswg=cjsw` F/m Zero-bias gate-side junction capacitance density.
- 117 `mjswg=mjsw` Gate-side junction grading coefficient.
- 118 `pbswg=pbsw` V Gate-side junction built-in potential.
- 119 `fcsw=0.5` Side-wall forward-bias depletion capacitance threshold.

Charge Model Selection Parameters

- 120 `capmod=2` Intrinsic charge model.
- 121 `nqsmod=0` Non-quasi static model selector. Set to 1 to turn on nqs.
- 122 `dwc=wint` m Delta W for capacitance model.
- 123 `dlc=lint` m Delta L for capacitance model.
- 124 `clc=1e-7` m Intrinsic capacitance fitting parameter.
- 125 `cle=0.6` Intrinsic capacitance fitting parameter.
- 126 `cf` (F/m) Fringe capacitance parameter.

Spectre Circuit Simulator Reference

Component Statements Part 1

127 `elm=5` Elmore constant of the channel.

128 `vfbcv=-1` Flat-band voltage for `capmod=0`.

129 `acde=1` $1/V_{CV}$ parameter.

130 `moIn=15` $1/V_{CV}$ parameter.

131 `noff=1` Transition parameter.

132 `voffcv=0` Transition parameter.

133 `xpart=0` Drain/source channel charge partition in saturation for BSIM charge model, use 0.0 for 40/60, 0.5 for 50/50, or 1.0 for 0/100.

134 `llc=ll` m Length dependence of delta L for CV.

135 `lwc=lw` m Width dependence of delta L for CV.

136 `lwlc=lwl` m^2 Area dependence of delta L for CV.

137 `wlc=w` m Length dependence of delta W for CV.

138 `wwc=ww` m Width dependence of delta W for CV.

139 `wwlc=wwl` m^2 Area dependence of delta W for CV.

Default for Instance Parameters

140 `w=5e-6` m Default channel width.

141 `l=5e-6` m Default channel length.

142 `as=0` m^2 Default area of source diffusion.

143 `ad=0` m^2 Default area of drain diffusion.

144 `ps=0` m Default perimeter of source diffusion.

145 `pd=0` m Default perimeter of drain diffusion.

146 `nrd=0` m/m Default number of squares of drain diffusion.

Spectre Circuit Simulator Reference

Component Statements Part 1

147 `nrs=0 m/m` Default number of squares of source diffusion.

148 `version=3.1` Model version selector. The available versions are 3.1, 3.2, 3.21, 3.22, 3.23 and 3.24.

149 `paramchk=1` Model parameter checking selector.

150 `fullreinit=0` Model parameter full reinit selector.

Temperature Effects Parameters

151 `tnom (C)` Parameters measurement temperature. Default set by `options`.

152 `trise=0 c` Temperature rise from ambient.

153 `tlev=0` DC temperature selector.

154 `tlevc=0` AC temperature selector.

155 `eg=1.12452 v` Energy band gap.

156 `gap1=7.02e-4 V/C` Band gap temperature coefficient.

157 `gap2=1108 c` Band gap temperature offset.

158 `diomod=1` Backward compatibility diode flag.

159 `kt1=-0.11 v` Temperature coefficient for threshold voltage.

160 `kt1l=0 v m` Temperature coefficient for threshold voltage.

161 `kt2=0.022` Temperature coefficient for threshold voltage.

162 `at=3.3e4 m/s` Temperature coefficient for `vsat`.

163 `ua1=4.31e-9 m/v` Temperature coefficient for `ua`.

164 `ub1=-7.61e-18 m2/v2`
Temperature coefficient for `ub`.

165 `uc1=-5.5e-11 m/v2`
Temperature coefficient for `uc`. Default is -0.056 for `mobmod=3`.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 166 `prt=0` Ω Temperature coefficient for `Rds`.
- 167 `trs=0` $1/C$ Temperature parameter for source resistance.
- 168 `trd=0` $1/C$ Temperature parameter for drain resistance.
- 169 `ute=-1.5`Mobility temperature exponent.
- 170 `xti=3`Saturation current temperature exponent.
- 171 `pta=0` V/C Junction potential temperature coefficient.
- 172 `tpb=0` V/C Temperature coefficient for `pb`.
- 173 `ptp=0` V/C Sidewall junction potential temperature coefficient.
- 174 `tpbsw=0` V/C Temperature coefficient for `pbsw`.
- 175 `tpbswg=0` V/C Temperature coefficient for `pbswg`.
- 176 `cta=0` $1/C$ Junction capacitance temperature coefficient.
- 177 `tcj=0` $1/C$ Temperature coefficient for `cj`.
- 178 `ctp=0` $1/C$ Sidewall junction capacitance temperature coefficient.
- 179 `tcjsw=0` $1/C$ Temperature coefficient for `cjsw`.
- 180 `tcjswg=0` $1/C$ Temperature coefficient for `cjswg`.

Noise Model Parameters

- 181 `noimod=1`Noise model selector.
- 182 `kf=0`Flicker (1/f) noise coefficient.
- 183 `af=1`Flicker (1/f) noise exponent.
- 184 `ef=1`Flicker (1/f) noise frequency exponent.
- 185 `noia=1e20`Oxide trap density coefficient. Default is $9.9e18$ for pmos.
- 186 `noib=5e4`Oxide trap density coefficient. Default is $2.4e3$ for pmos.

Spectre Circuit Simulator Reference

Component Statements Part 1

187 `noic=-1.4e-12` Oxide trap density coefficient. Default is 1.4e-8 for pmos.
188 `noid=2e14` flicker noise subthreshold-above threshold transition coefficient.
189 `wnoi=1e-5` `m`Channel width at which noise parameters were extracted.
190 `em=4.1e7` `V/m`Maximum electric field.
191 `flkmod=0` Flicker noise model (0 for `Ids` based model, 1 for `gm` based model).
192 `gamma=2.0/3.0` Thermal noise coefficient.

Auto Model Selector Parameters

193 `wmax=1` `m`Maximum channel width for which the model is valid.
194 `wmin=0` `m`Minimum channel width for which the model is valid.
195 `lmax=1` `m`Maximum channel length for which the model is valid.
196 `lmin=0` `m`Minimum channel length for which the model is valid.

Operating Region Warning Control Parameters

197 `alarm=none` Forbidden operating region.
Possible values are `none`, `off`, `triode`, `sat`, `subth`, or `rev`.
198 `imax=1` `A`Maximum allowable current.
199 `jmax=1e8` `A/m2`Maximum allowable current density.
200 `bvj=∞` `V`Junction reverse breakdown voltage.
201 `vbox=1e9` `tox` `V`Oxide breakdown voltage.
202 `warn=on` Parameter to turn warnings on and off.
Possible values are `off` or `on`.

Length Dependent Parameters

203 `x1=0` `m`Length variation due to masking and etching.

Spectre Circuit Simulator Reference

Component Statements Part 1

Width dependent parameters

204 `xw=0` `m`Width variation due to masking and etching.

DC-Mismatch Dependent Parameters

205 `mvtwl=0.0` `v` `m`Threshold mismatch area dependence.

206 `mvtwl2=0.0` `v` `m`^{1.5}
Threshold mismatch area square dependence.

207 `mvt0=0.0` `v`Threshold mismatch intercept.

208 `mbewl=0.0` `m`Beta mismatch area dependence.

209 `mbe0=0.0` `B`Beta mismatch intercept.

`Imax` and `Imelt`

The `imax` parameter aids convergence and prevents numerical overflow. The junction characteristics of the device are accurately modeled for current up to `imax`. If `imax` is exceeded during iterations, the linear model is substituted until the current drops below `imax` or until convergence is achieved. If convergence is achieved with the current exceeding `imax`, the results are inaccurate, and Spectre prints a warning.

A separate model parameter, `imelt`, is used as a limit warning for the junction current. This parameter can be set to the maximum current rating of the device. When any component of the junction current exceeds `imelt`, note that base and collector currents are composed of many exponential terms, Spectre issues a warning and the results become inaccurate. The junction current is linearized above the value of `imelt` to prevent arithmetic exception, with the exponential term replaced by a linear equation at `imelt`.

Both of these parameters have current density counterparts, `jmax` and `jmelt`, that you can specify if you want the absolute current values to depend on the device area.

Auto Model Selection

Many models need to be characterized for different geometries in order to obtain accurate results for model development. The model selector program automatically searches for a model with the length and width range specified in the instance statement and uses this model in the simulations.

Spectre Circuit Simulator Reference

Component Statements Part 1

For the auto model selector program to find a specific model, the models to be searched should be grouped together within braces. Such a group is called a model group. An opening brace is required at the end of the line defining each model group. Every model in the group is given a name followed by a colon and the list of parameters. Also, the four geometric parameters `lmax`, `lmin`, `wmax`, and `wmin` should be given. The selection criteria to choose a model is as follows:

`lmin <= inst_length < lmax and wmin <= inst_width < wmax`

Example

```
model ModelName ModelType {  
    1:      <model parameters> lmin=2 lmax=4 wmin=1 wmax=2  
    2:      <model parameters> lmin=1 lmax=2 wmin=2 wmax=4  
    3:      <model parameters> lmin=2 lmax=4 wmin=4 wmax=6  
}
```

Then for a given instance

`M1 1 2 3 4 ModelName w=3 l=1.5`

the program would search all the models in the model group with the name `ModelName` and then pick the first model whose geometric range satisfies the selection criteria. In the preceding example, the auto model selector program would choose `ModelName.2`.

The user must specify both length (`l`) and width (`w`) on the device instance line to enable automatic model selection.

Output Parameters

- 1 `weff` (m) Effective channel width.
- 2 `leff` (m) Effective channel length.
- 3 `rseff` (Ω) Effective source resistance.
- 4 `rdeff` (Ω) Effective drain resistance.

Operating-Point Parameters

- 1 `type=n` Transistor type.
Possible values are `n` or `p`.
- 2 `region=triode` Estimated operating region.
Possible values are `off`, `triode`, `sat`, `subth`, or `breakdown`.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 3 `reversed` Reverse mode indicator.
Possible values are `no` or `yes`.
- 4 `ids` (A) Resistive drain-to-source current.
- 5 `vgs` (V) Gate-source voltage.
- 6 `vds` (V) Drain-source voltage.
- 7 `vbs` (V) Bulk-source voltage.
- 8 `vth` (V) Threshold voltage.
- 9 `vdsat` (V) Drain-source saturation voltage.
- 10 `gm` (S) Common-source transconductance.
- 11 `gds` (S) Common-source output conductance.
- 12 `gmbs` (S) Body-transconductance.
- 13 `betaeff` (A/V²) Effective `beta`.
- 14 `cjd` (F) Drain-bulk junction capacitance.
- 15 `cjs` (F) Source-bulk junction capacitance.
- 16 `cgg` (F) dQ_g/dV_g .
- 17 `cgd` (F) dQ_g/dV_d .
- 18 `cgs` (F) dQ_g/dV_s .
- 19 `cgb` (F) dQ_g/dV_{bk} .
- 20 `cdg` (F) dQ_d/dV_g .
- 21 `cdd` (F) dQ_d/dV_d .
- 22 `cds` (F) dQ_d/dV_s .
- 23 `cdb` (F) dQ_d/dV_b .

Spectre Circuit Simulator Reference

Component Statements Part 1

- 24 `csg` (F) dQ_s/dV_g .
- 25 `csd` (F) dQ_s/dV_d .
- 26 `css` (F) dQ_s/dV_s .
- 27 `csb` (F) dQ_s/dV_b .
- 28 `cbg` (F) dQ_b/dV_g .
- 29 `cbd` (F) dQ_b/dV_d .
- 30 `cbs` (F) dQ_b/dV_s .
- 31 `cbb` (F) dQ_b/dV_b .
- 32 `ron` (Ω) On-resistance.
- 33 `id` (A) Resistive drain current.
- 34 `ibulk` (A) Resistive bulk current.
- 35 `pwr` (W) Power at op point.
- 36 `gmoverid` (1/V) G_m/I_{ds} .

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

<code>a0</code>	M-20	<code>dvt0w</code>	M-17	<code>mbewl</code>	M-208	<code>tcjsw</code>	M-179
<code>a1</code>	M-23	<code>dvt1</code>	M-15	<code>meto</code>	M-103	<code>tcjswg</code>	M-180
<code>a2</code>	M-24	<code>dvt1w</code>	M-18	<code>minr</code>	M-92	<code>tlev</code>	M-153
<code>acde</code>	M-129	<code>dvt2</code>	M-16	<code>mj</code>	M-110	<code>tlevc</code>	M-154
<code>ad</code>	I-4	<code>dvt2w</code>	M-19	<code>mjsw</code>	M-114	<code>tnom</code>	M-151
<code>ad</code>	M-143	<code>dwb</code>	M-44	<code>mjswg</code>	M-117	<code>tox</code>	M-45
<code>af</code>	M-183	<code>dwc</code>	M-122	<code>mobmod</code>	M-53	<code>toxm</code>	M-46
<code>ags</code>	M-25	<code>dwg</code>	M-43	<code>moin</code>	M-130	<code>tpb</code>	M-172

Spectre Circuit Simulator Reference

Component Statements Part 1

alarm	M-197	ef	M-184	mvt0	M-207	tpbsw	M-174
alpha0	M-77	eg	M-155	mvtwl	M-205	tpbswg	M-175
alpha1	M-78	elm	M-127	mvtwl2	M-206	trd	M-168
as	I-3	em	M-190	n	M-96	trise	M-152
as	M-142	eta0	M-75	nch	M-28	trise	I-12
at	M-162	etab	M-76	nfactor	M-71	trs	M-167
b0	M-21	fc	M-112	ngate	M-29	type	M-1
b1	M-22	fcs	M-119	nlx	M-9	type	OP-1
beta0	M-79	flkmod	M-191	noff	M-131	u0	M-54
betaeff	OP-13	fullreinit	M-150	noia	M-185	ua	M-56
binunit	M-52	gamma	M-192	noib	M-186	ual	M-163
bvj	M-200	gamma1	M-10	noic	M-187	ub	M-57
capmod	M-120	gamma2	M-11	noid	M-188	ubl	M-164
cbb	OP-31	gap1	M-156	noimod	M-181	uc	M-58
cbd	M-108	gap2	M-157	ngsmod	I-11	uc1	M-165
cbd	OP-29	gds	OP-11	ngsmod	M-121	ute	M-169
cbg	OP-28	gm	OP-10	nrd	I-7	vbm	M-13
cbs	OP-30	gmbs	OP-12	nrd	M-146	vbox	M-201
cbs	M-107	gmoverid	OP-36	nrs	M-147	vbs	OP-7
cdb	OP-23	hdif	M-91	nrs	I-8	vbx	M-12
cdd	OP-21	ibulk	OP-34	nsub	M-27	vds	OP-6
cdg	OP-20	id	OP-33	paramchk	M-149	vdsat	OP-9
cds	OP-22	ids	OP-4	pb	M-111	version	M-148
cdsc	M-68	imax	M-198	pbsw	M-115	vfb	M-3
cdscb	M-69	imelt	M-98	pbswg	M-118	vfbcv	M-128
cdscd	M-70	is	M-95	pclm	M-60	vgs	OP-5
cf	M-126	jmax	M-199	pd	M-145	voff	M-73
cgb	OP-19	jmelt	M-99	pd	I-6	voffcv	M-132
cgbo	M-102	js	M-93	pdiblc1	M-61	vsat	M-55
cgd	OP-17	jsw	M-94	pdiblc2	M-62	vth	OP-8
cgdl	M-105	k1	M-4	pdiblc3	M-63	vtho	M-2
cgdo	M-101	k2	M-5	prt	M-166	w	M-140
cgg	OP-16	k3	M-6	prwb	M-49	w	I-1
cgs	OP-18	k3b	M-7	prwg	M-50	w0	M-8
cgs1	M-104	keta	M-26	ps	M-144	warn	M-202
cgso	M-100	kf	M-182	ps	I-5	weff	O-1
cit	M-72	kt1	M-159	pscbel	M-64	wint	M-32
cj	M-109	kt11	M-160	pscbe2	M-65	wl	M-38
cjd	OP-14	kt2	M-161	pta	M-171	wlc	M-137
cjs	OP-15	l	M-141	ptp	M-173	wln	M-39
cjsw	M-113	l	I-2	pvag	M-66	wmax	M-193
cjswg	M-116	ldif	M-90	pwr	OP-35	wmin	M-194
ckappa	M-106	leff	O-2	rd	M-82	wnoi	M-189
clc	M-124	lgcd	M-84	rdc	M-86	wr	M-51
cle	M-125	lgcs	M-83	rdd	M-88	ww	M-40
csb	OP-27	lint	M-31	rdeff	O-4	wwc	M-138
csd	OP-25	ll	M-33	rdsw	M-48	wwl	M-42

Spectre Circuit Simulator Reference

Component Statements Part 1

csg	OP-24	llc	M-134	region	OP-2	wwlc	M-139
css	OP-26	lln	M-34	region	I-10	wwn	M-41
cta	M-176	lmax	M-195	reversed	OP-3	xj	M-30
ctp	M-178	lmin	M-196	ron	OP-32	xl	M-203
delta	M-67	lw	M-35	rs	M-81	xpart	M-133
diomod	M-158	lwc	M-135	rsc	M-85	xt	M-47
dlc	M-123	lwl	M-37	rseff	O-3	xti	M-170
drout	M-59	lwlc	M-136	rsh	M-80	xw	M-204
dskip	M-97	lwn	M-36	rss	M-87		
dsub	M-74	m	I-9	sc	M-89		
dvt0	M-14	mbe0	M-209	tcj	M-177		

BSIM4 MOS Transistor (bsim4)

Description

BSIM4 is the version-4.20 of bsim model. It uses single-piece equations for all regions to improve the smoothness of the model characteristics. BSIM4 also allows the binning option like the approach used in bsim3. This option is provided for people who want to achieve the highest accuracy of the model. The binning equation is given by

$$P = P_0 + P_I / Leff + P_w / Weff + P_p / (Leff * Weff)$$

Only the P0 parameters are listed. PI, Pw, and Pp are not shown but can be recognized. The names of PI, Pw, and Pp are identical to that of P0 but with a prefix of I, w, and p, respectively. BSIM4 transistors require that you use a model statement.

For more information on this model, please consult the University of California at Berkeley BSIM4 home page at

<http://www-device.eecs.berkeley.edu/~bsim3/bsim4.html>

This device is supported within altergroups.

Sample Instance Statement

```
m4 (0 2 1 1) pchmod w=2u l=0.8u as=250p ad=250p pd=168p ps=168p m=1
```

Sample Model Statement

```
model pchmod bsim4 type=p mobmod=0 capmod=2 version=4.20 tox=3e-9 cdsch=2.58e-4  
cdscb=0 cdsch=6.1e-8 cit=0 nfactor=1.1 xj=9e-8 vfb=0.76vsat=9.2e4 at=3.3e4 a0=1.1  
ags=1.0e-20 al=0 ngate=9e19 vth0=-0.42a1=0 a2=1 delta=0.014 pvag=1e-20 pclm=6.28e-  
4 pdits=0.2 pditsl=2.3e6pditsd=0.23 fprout=0.2 pdiblc=3.4e-8 pdiblc1=0.81
```

Spectre Circuit Simulator Reference

Component Statements Part 1

```
drout=0.56pdiblc2=9.84e-6 pscbe1=8.14e8 pscbe2=9.58e-07 lint=5e-9 wint=5e-
9dmcg=5e-6 dmci=5e-6 dmdg=5e-6 dmcgt=6e-7 dwj=4.5e-8 rsh=6cgso=7.43e-10
cgdo=7.43e-10 cgbo=2.56e-11 cgsl=1e-14 cgd1=1e-14ckappas=0.5 ckappad=0.5 noff=0.9
voffcv=0.02 acde=1 moin=15 xpart=0kt1l=0 kt2=2.2e-2 lpe0=5.75e-8 lpeb=2.3e-10
dvt0=2.89 dvt1=0.53dvt2=-3.2e-2 dvt0w=0 dvt1w=0 dvt2w=0 dvtp0=7.32e-7
dvtp1=0.12dsub=0.058 eta0=0.001 u0=4.19e-2 ua=8.7e-16 ub=3.06e-18 kl=0.33uc=4.6e-
13 ute=-1.5 ua1=4.31e-9 ub1=7.61e-18 uc1=-5.6e-11 k2=-1.87e-2rds=369.4 rdw=184.7
rsw=184.7 prwg=3.22e-8 prwb=6.8e-11 wr=1rdsmin=0 rdwmin=0 rswmin=0 prt=0 b0=-1e-
20 k3=80 k3b=0 w0=2.5e-6b1=0 keta=-0.047 alpha0=7.4e-2 alpha1=0.005 beta0=30
```

Instance Definition

Name d g s b ModelName parameter=value ...

Instance Parameters

- 1 w (m) Channel width.
- 2 l (m) Channel length.
- 3 as (m²) Area of source diffusion.
- 4 ad (m²) Area of drain diffusion.
- 5 ps (m) Perimeter of source diffusion.
- 6 pd (m) Perimeter of drain diffusion.
- 7 nrd (m/m) Number of squares of drain diffusion.
- 8 nrs (m/m) Number of squares of source diffusion.
- 9 m=1 Multiplicity factor (number of MOSFETs in parallel).
- 10 region=triode Estimated operating region.
Possible values are off, triode, sat, subth, or breakdown.
- 11 trnqsmo Transient NQS flag.
- 12 acnqsmo AC NQS flag.
- 13 trise Temperature rise from ambient.
- 14 rgatemod Rgate flag.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 15 `rbodymod`Rbody flag.
- 16 `geomod`Geometry flag.
- 17 `rgeomod`Diffusion resistance and contact model flag.
- 18 `rbpb` (Ω) Resistance connected between bNode and bNode .
- 19 `rbpd` (Ω) Resistance connected between bNode and dbNode.
- 20 `rbps` (Ω) Resistance connected between bNode and sbNode.
- 21 `rbdb` (Ω) Resistance connected between dbNode and bNode.
- 22 `rsb` (Ω) Resistance connected between sbNode and bNode.
- 23 `nf`Number of device fingers.
- 24 `min`Minimum number of device fingers.

Model Definition

`model modelName bsim4 parameter=value ...`

Model Parameters

Device Type Parameters

- 1 `type=n`Transistor type.
Possible values are n or p.

Threshold Voltage Parameters

- 2 `vtho` (V) Threshold voltage at zero body bias for long-channel devices. For enhancement-mode devices, $vtho > 0$ for n-channel and $vth < 0$ for p-channel. Default value is calculated from other model parameters.
- 3 `vfb=-1` VFlat-band voltage.
- 4 `phin=0` VNon-uniform vertical doping effect on surface potential.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 5 $k1=0.53 \sqrt{V}$ Body-effect coefficient.
- 6 $k2=-0.0186$ Charge-sharing parameter.
- 7 $k3=80$ Narrow width coefficient.
- 8 $k3b=0 \ 1/V$ Narrow width coefficient.
- 9 $w0=2.5e-6 \ m$ Narrow width coefficient.
- 10 $lpe0=1.74e-7 \ m$ Lateral non-uniform doping at $V_{bs}=0$.
- 11 $lpeb=0$ Lateral non-uniform doping effect on $K1$.
- 12 $\gamma1 \ (\sqrt{V})$ Body-effect coefficient near the surface.
- 13 $\gamma2 \ (\sqrt{V})$ Body-effect coefficient in the bulk.
- 14 $vbx \ (V)$ Threshold voltage transition body voltage.
- 15 $vbm=-3 \ V$ Maximum applied body voltage.
- 16 $dvt0=2.2$ First coefficient of short-channel effects.
- 17 $dvt1=0.53$ Second coefficient of short-channel effects.
- 18 $dvt2=-0.032 \ 1/V$ Body-bias coefficient of short-channel effects.
- 19 $dvtp0=0 \ m$ First Coef. of drain-induced V_{th} shift for long-channel pocket devices.
- 20 $dvtp1=0 \ 1/V$ Second Coef. of drain-induced V_{th} shift for long-channel pocket devices.
- 21 $dvt0w=0$ First coefficient of narrow-width effects.
- 22 $dvt1w=5.3e6 \ 1/m$ Second coefficient of narrow-width effects.
- 23 $dvt2w=-0.032 \ 1/V$ Body-bias coefficient of narrow-width effects.
- 24 $a0=1$ Nonuniform depletion width effect coefficient.
- 25 $b0=0 \ m$ Bulk charge coefficient due to narrow width effect.
- 26 $b1=0 \ m$ Bulk charge coefficient due to narrow width effect.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 27 $a1=0$ No-saturation coefficient.
- 28 $a2=1$ No-saturation coefficient.
- 29 $ags=0 \text{ F/m}^2 \text{ V}$ Gate-bias dependence of A_{bulk} .
- 30 $keta=-0.047 \text{ 1/V}$ Body-bias coefficient for non-uniform depletion width effect.

Process Parameters

- 31 $epsrox=3.9$ Gate dielectric constant.
- 32 $toxe=3.0e-9 \text{ m}$ Electrical gate oxide thickness.
- 33 $toxp=toxe \text{ m}$ Electrical gate oxide thickness.
- 34 $dtox=0.0 \text{ m}$ Difference between electrical and physical gate oxide thickness.
- 35 $ndep=1.7e17 \text{ cm}^{-3}$ Channel doping concentration.
- 36 $nsd=1.0e20 \text{ cm}^{-3}$ Source-drain doping concentration.
- 37 $nsub=6e16 \text{ cm}^{-3}$ Substrate doping concentration.
- 38 $ngate=0 \text{ cm}^{-3}$ Poly-gate doping concentration.
- 39 $xj=0.15e-6 \text{ m}$ Source/drain junction depth.
- 40 $lint=0 \text{ m}$ Lateral diffusion for one side.
- 41 $wint=0 \text{ m}$ Width reduction for one side.
- 42 $ll=0$ Length dependence of δL .
- 43 $lln=1$ Length exponent of δL .
- 44 $lw=0$ Width dependence of δL .
- 45 $lwn=1$ Width exponent of δL .
- 46 $lw1=0$ Area dependence of δL .
- 47 $w1=0$ Length dependence of δW .

Spectre Circuit Simulator Reference

Component Statements Part 1

- 48 `wln=1` Length exponent of delta W.
- 49 `ww=0` Width dependence of delta W.
- 50 `wn=1` Width exponent of delta W.
- 51 `wwl=0` Area dependence of delta W.
- 52 `dwg=0` m/\sqrt{v} Gate-bias dependence of channel width.
- 53 `dwb=0` m/\sqrt{v} Body-bias dependence of channel width.
- 54 `tox=toxex` m Toxe at which parameters were extracted.
- 55 `xt=1.55e-7` m Doping depth.
- 56 `binunit=1` Bin parameter unit selector. 1 for microns and 2 for meters.

Bias-Dependent RDS Parameters

- 57 `rdsm=1` bias-dependent D/S model selector.
- 58 `rdsw=200` Zero bias LDD resistance per unit width for RDSMOD=0..
- 59 `rdswmin=0` LDD resistance per unit width at high Vgs and zero Vbs for RDSMOD=0.
- 60 `rdw=100` Zero bias LDD resistance per unit width for RDSMOD=1..
- 61 `rdwmin=0` LDD resistance per unit width at high Vgs and zero Vbs for RDSMOD=1.
- 62 `rsw=100` Zero bias LDD resistance per unit width for RDSMOD=1..
- 63 `rswmin=0` LDD resistance per unit width at high Vgs and zero Vbs for RDSMOD=1.
- 64 `prwb=0` $1/\sqrt{v}$ Body-effect coefficient for Rds.
- 65 `prwg=1` $1/v$ Gate-effect coefficient for Rds.
- 66 `wr=1` Width offset for parasitic resistance.

Spectre Circuit Simulator Reference

Component Statements Part 1

Mobility Parameters

67 `mobmod=0` Mobility model selector.

68 `u0=670 cm2/V s` Low-field surface mobility at `tnom`. Default is 250 for PMOS.

69 `vsat=8e4 m/s` Carrier saturation velocity at `tnom`.

70 `ua=1.0e-9 m/v` First-order mobility reduction coefficient. Default is 1.0e-15 if `Mobmod = 2`.

71 `ub=1.0e-19 m2/v2`
Second-order mobility reduction coefficient.

72 `uc=-4.65e-11 m/v2`
Body-bias dependence of mobility. Default is -0.0465 and unit is 1/V for `mobmod=1`.

73 `eu=1.67` Exponent for mobility degradation of `mobmod=2`. Default is 1.0 for Pmos.

Output Resistance Parameters

74 `drout=0.56` DIBL effect on output resistance coefficient.

75 `fprout=0.0 V/√m`
Effect of pocket implant on `Rout` degradation.

76 `pclm=1.3` Channel length modulation coefficient.

77 `pdiblc1=0.39` First coefficient of drain-induced barrier lowering.

78 `pdiblc2=8.6e-3` Second coefficient of drain-induced barrier lowering.

79 `pdiblc b=0 1/v` Body-effect coefficient for DIBL.

80 `pscbe1=4.24e8 V/m` First coefficient of substrate current body effect.

81 `pscbe2=1e-5 m/v` Second coefficient of substrate current body effect.

82 `pvag=0` Gate dependence of Early voltage.

83 `delta=0.01 v` Effective drain voltage smoothing parameter.

Spectre Circuit Simulator Reference

Component Statements Part 1

84 `pdits=0.0 1/v`Effect of pocket implant on Rout degradation.

85 `pditsl=0.0 1/m`Channel-length of drain-induced Vth shift on Rout.

86 `pditsd=0.0 1/v`Channel-length of drain-induced Vth shift on Rout.

Subthreshold Parameters

87 `cdsc=2.4e-4 F/m2`Source/drain and channel coupling capacitance.

88 `cdscb=0 F/m2 v`Body-bias dependence of `cdsc`.

89 `cdscd=0 F/m2 v`Drain-bias dependence of `cdsc`.

90 `nfactor=1`Subthreshold swing coefficient.

91 `cit=0 F/m2`Interface trap parameter for subthreshold swing.

92 `voff=-0.08 v`Threshold voltage offset.

93 `voffl=0.0 v`Channel-length dependence of Voff..

94 `minv=0Vgsteff` fitting parameter for moderate inversion condition..

95 `dsub=drout`DIBL effect in subthreshold region.

96 `eta0=0.08`DIBL coefficient subthreshold region.

97 `etab=-0.07 1/v`Body-bias dependence of `et0`.

Substrate Current Parameters

98 `alpha0=0 m/v`Substrate current impact ionization coefficient.

99 `alpha1=0 1/v`Substrate current impact ionization coefficient.

100 `beta0=30 1/v`Substrate current impact ionization exponent.

Parasitic Resistance Parameters

101 `rgatemod`Rgate flag.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 102 `rsh=0` Ω/sqr Source/drain diffusion sheet resistance.
- 103 `rshg=0.1` Ω/sqr Gate electrode diffusion sheet resistance.
- 104 `dmcg=0` m Distance from S/D contact center to the gate edge.
- 105 `dmci=dmcg` m Distance from S/D contact center to the isolation edge in the channel-length direction.
- 106 `dmdg=0` m Distance from S/D contact center to the gate edge.
- 107 `dmcgt=0` m DMCG of test structures.
- 108 `dwj=Dwc` Offset of the S/D junction width.
- 109 `xgw=0` m Distance from the gate contact to the channel edge.
- 110 `xgl=0` m Offset of the gate length due to variations in patterning.
- 111 `ngcon=1` Number of gate contacts.
- 112 `nf=1` Number of device fingers.
- 113 `min` Minimum number of device fingers.
- 114 `permod=1` Perimeter model selector.
- 115 `geomod` Geometry flag.
- 116 `rgeomod` Diffusion resistance and contact model flag.
- 117 `xw=0` m Width variation due to masking and etching.
- 118 `xl=0` m Length variation due to masking and etching.
- 119 `minr=0.001` Ω Minimum source/drain resistance.

Gate-Induced Drain Leakage Parameters

- 120 `agidl=0` $1/\Omega$ Pre-exponential coefficient for GIDL.
- 121 `bgidl=2.3e9` v Exponential coefficient for GIDL.

Spectre Circuit Simulator Reference

Component Statements Part 1

122 `cgidl=0.5` v^3 Exponential coefficient for GIDL.

123 `egidl=0.8` v Fitting parameter for band bending for GIDL.

Gate Tunneling Parameters

124 `igcmod=0` Gate-to-channel tunneling model selector.

125 `igbmod=1` Gate-to-substrate tunneling model selector.

126 `aigbacc=0.43` $\sqrt{F/g}$ s/m
Parameter for I_{gb} in accumulation.

127 `bigbacc=0.054` $\sqrt{F/g}$ /sm
Parameter for I_{gb} in accumulation.

128 `cigbacc=0.075` $1/v$ Parameter for I_{gb} in accumulation.

129 `nigbacc=1` Parameter for I_{gb} in accumulation.

130 `aigbinv=0.35` $\sqrt{F/g}$ s/m
Parameter for I_{gb} in inversion.

131 `bigbinv=0.03` $\sqrt{F/g}$ /sm
Parameter for I_{gb} in inversion.

132 `cigbinv=0.006` $1/v$ Parameter for I_{gb} in inversion.

133 `eigbinv=1.1` v Parameter for I_{gb} in inversion.

134 `nigbinv=3` Parameter for I_{gb} in inversion.

135 `aigc=0.054` $\sqrt{F/g}$ s/m
Parameter for I_{gcs} and I_{gcd} . Default value for Pmos is 0.31.

136 `bigc=0.054` $\sqrt{F/g}$ /sm
Parameter for I_{gcs} and I_{gcd} . Default value for Pmos is 0.024.

137 `cigc=0.075` $1/v$ Parameter for I_{gcs} and I_{gcd} . Default value for Pmos is 0.03.

138 `aigsd=0.43` $\sqrt{F/g}$ s/m
Parameter for I_{gs} and I_{gd} . Default value for Pmos is 0.31.

Spectre Circuit Simulator Reference

Component Statements Part 1

139 `bigsd=0.054` $\sqrt{F/g}$ /sm

Parameter for I_{gs} and I_{gd} . Default value for Pmos is 0.024.

140 `cigsd=0.075` 1/vParameter for I_{gs} and I_{gd} . Default value for Pmos is 0.03.

141 `dlcig=Lint` mSource/drain overlap length for I_{gs} and I_{gd} .

142 `nigc=1.0`Source/drain overlap length for I_{gs} and I_{gd} .

143 `poxedge=1.0`Factor for the gate oxide thickness in source/drain overlap regions.

144 `pigcd=1.0`Vds dependence of I_{gs} and I_{gd} .

145 `ntox=1.0`Exponent for the gate oxide ratio.

146 `toxref=3.0e-9` mNominal gate oxide thickness for gate dielectric tunneling current model only.

Junction Diode Model Parameters

147 `diomod=1`Diode model selector.

148 `js=1.0e-4` A/m²Bottom junction reverse saturation current density..

149 `jss=1.0e-4` A/m²Bottom junction reverse saturation current density..

150 `jsd=Jss` A/m²Bottom junction reverse saturation current density..

151 `jsws=0` A/mIsolation-edge sidewall reverse saturation current density..

152 `jswd=Jsws` A/mIsolation-edge sidewall reverse saturation current density..

153 `jswgs=0` A/mGate-edge sidewall reverse saturation current density..

154 `jswgd=Jswgs` A/mGate-edge sidewall reverse saturation current density.

155 `is=1e-14` ABulk junction reverse saturation current.

156 `n=1`Junction emission coefficient.

157 `njs=1`Bulk-Source junction emission coefficient.

158 `njd=Njs`Bulk-Source junction emission coefficient.

Spectre Circuit Simulator Reference

Component Statements Part 1

159 `dskip=yes` Use simple piece-wise linear model for diode currents below $0.1 \cdot i_{abstol}$.
Possible values are `no` or `yes`.

160 `imelt=imaxA` Explosion current.

161 `jmelt=jmaxA/m2`
Explosion current density.

162 `ijthsrev=0.1 A` Limiting current in reverse bias region.

163 `ijthdrev=Ijthsrev A`
Limiting current in reverse bias region.

164 `ijthsfwd=0.1 A` Limiting current in forward bias region.

165 `ijthdfwd=Ijthsfwd A`
Limiting current in forward bias region.

166 `xjbvs=1.0` Limiting current in forward bias region.

167 `xjbvd=xjbvs` Limiting current in forward bias region.

Overlap Capacitance Parameters

168 `cgso (F/m)` Non LDD region source-gate overlap capacitance per unit channel width.

169 `cgdo (F/m)` Non LDD region drain-gate overlap capacitance per unit channel width.

170 `cgbo=2 Dwc Coxe F/m`
Non LDD region drain-gate overlap capacitance per unit channel width..

171 `meto=0 m` Metal overlap in fringing field.

172 `cgs1=0 F/m` Overlap capacitance between gate and lightly-doped source region.

173 `cgd1=Cgd1 F/m` Overlap capacitance between gate and lightly-doped drain region.

174 `ckappas=0.6 v` Coefficient of bias-dependent overlap capacitance for the source side.

175 `ckappad=Ckappas v` Coefficient of bias-dependent overlap capacitance for the source side.

Spectre Circuit Simulator Reference

Component Statements Part 1

Junction Capacitance Model Parameters

- 176 $c_j = 5e-4 \text{ F/m}^2$ Zero-bias junction bottom capacitance density.
- 177 $c_{js} = 5e-4 \text{ F/m}^2$ Zero bias bottom junction capacitance per unit area..
- 178 $c_{jd} = 5e-4 \text{ F/m}^2$ Zero bias bottom junction capacitance per unit area..
- 179 $m_j = 1/2$ Bulk junction bottom grading coefficient.
- 180 $m_{js} = 1/2$ Bulk junction bottom grading coefficient.
- 181 $m_{jd} = M_{js}$ Bulk junction bottom grading coefficient.
- 182 $p_b = 1 \text{ V}$ Bottom junction built-in potential.
- 183 $p_{bs} = 1 \text{ V}$ Bottom junction built-in potential.
- 184 $p_{bd} = p_{bs} \text{ V}$ Bottom junction built-in potential.
- 185 $f_c = 0.5$ Forward-bias depletion capacitance threshold.
- 186 $c_{jsw} = 5e-10 \text{ F/m}$ Zero-bias junction sidewall capacitance density.
- 187 $c_{jsws} = 5e-10 \text{ F/m}$ Zero-bias junction sidewall capacitance density.
- 188 $c_{jswd} = C_{jsws} \text{ F/m}$ Zero-bias junction sidewall capacitance density.
- 189 $m_{jsw} = 0.33$ Isolation-edge sidewall junction capacitance grading coefficient..
- 190 $m_{jsws} = 0.33$ Isolation-edge sidewall junction capacitance grading coefficient..
- 191 $m_{jswd} = M_{jsws}$ Isolation-edge sidewall junction capacitance grading coefficient..
- 192 $p_{bsw} = 1 \text{ V}$ Isolation-edge sidewall junction built-in potential.
- 193 $p_{bsws} = 1 \text{ V}$ Isolation-edge sidewall junction built-in potential.
- 194 $p_{bswd} = p_{bsws} \text{ V}$ Isolation-edge sidewall junction built-in potential.
- 195 $c_{jswg} = c_{jswg} \text{ F/m}$ Zero-bias gate-side junction capacitance density.
- 196 $c_{jswgs} = c_{jsws} \text{ F/m}$ Zero-bias gate-side junction capacitance density.

Spectre Circuit Simulator Reference

Component Statements Part 1

197 `cjswgd=cjswgs` F/m Zero-bias gate-side junction capacitance density.

198 `mjswg=mjsw` Gate-edge sidewall junction grading coefficient.

199 `mjswgs=mjsws` Gate-edge sidewall junction grading coefficient.

200 `mjswgd=mjsws` Gate-edge sidewall junction grading coefficient.

201 `pbswg=pbsw` V Gate-edge sidewall junction built-in potential.

202 `pbswgs=pbsws` V Gate-edge sidewall junction built-in potential.

203 `pbswgd=pbsws` V Gate-edge sidewall junction built-in potential.

204 `fcsw=0.5` Side-wall forward-bias depletion capacitance threshold.

205 `bvs=10.0` V Breakdown voltage.

206 `bvd=Bvs` V Breakdown voltage.

Charge Model Selection Parameters

207 `capmod=2` Intrinsic charge model.

208 `trnqsmode=0` Transient Non-quasi static model selector. Set to 1 to turn on nqs.

209 `acnqsmode=0` Ac Non-quasi static model selector. Set to 1 to turn on nqs.

210 `dwc=wint` m Delta W for capacitance model.

211 `dlc=Lint` m Delta L for capacitance model.

212 `clc=1e-7` m Constant term for the short channel model..

213 `cle=0.6` Intrinsic capacitance fitting parameter.

214 `cf` (F/m) Coefficient of bias-dependent overlap capacitance for the source side.

215 `vfbcv=-1` Flat-band voltage for `capmod=0`.

216 `acde=1` m/v Exponential coefficient for charge thickness in CAPMOD=2 for accumulation and depletion regions.

Spectre Circuit Simulator Reference

Component Statements Part 1

217 `moin=15 1/v` Exponential coefficient for charge thickness for accumulation and depletion regions.

218 `noff=1` Transition parameter.

219 `voffcv=0 v` CV parameter in `VgsteffCV` for weak to strong inversion..

220 `xpart=0` Charge partition number. Use 0.0 for 40/60, 0.5 for 50/50, or 1.0 for 0/100.

221 `llc=1l` Length dependence of delta L for CV.

222 `lwc=1w` Width dependence of delta L for CV.

223 `lwlc=1wl` Area dependence of delta L for CV.

224 `wlc=w1` Length dependence of delta W for CV.

225 `wwc=ww` Width dependence of delta W for CV.

226 `wwlc=wwl` Area dependence of delta W for CV.

Default for Instance Parameters

227 `w=5e-6 m` Default channel width.

228 `l=5e-6 m` Default channel length.

229 `as=0 m2` Default area of source diffusion.

230 `ad=0 m2` Default area of drain diffusion.

231 `ps=0 m` Default perimeter of source diffusion.

232 `pd=0 m` Default perimeter of drain diffusion.

233 `nrd=0 m/m` Default number of squares of drain diffusion.

234 `nrs=0 m/m` Default number of squares of source diffusion.

235 `version=4.20` Model version selector.

236 `level=14` Model level selector for spice compatibility.

Spectre Circuit Simulator Reference

Component Statements Part 1

237 `paramchk=1` Model parameter checking selector.

238 `fullreinit=0` Model parameter full reinit selector.

Temperature Effects Parameters

239 `tnom (C)` Parameters measurement temperature. Default set by `options`.

240 `trise=0 C` Temperature rise from ambient.

241 `tlev=0` DC temperature selector.

242 `tlevc=0` AC temperature selector.

243 `eg=1.12452 V` Energy band gap.

244 `gap1=7.02e-4 V/C` Band gap temperature coefficient.

245 `gap2=1108 C` Band gap temperature offset.

246 `kt1=-0.11 V` Temperature coefficient for threshold voltage.

247 `kt1l=0 V m` Temperature coefficient for threshold voltage.

248 `kt2=0.022` Temperature coefficient for threshold voltage.

249 `at=3.3e4 m/s` Temperature coefficient for `vsat`.

250 `ua1=4.31e-9 m/V` Temperature coefficient for `ua`.

251 `ub1=-7.61e-18 m2/V2`
Temperature coefficient for `ub`.

252 `uc1=-5.5e-11 m/V2`
Temperature coefficient for `uc`. Default is -0.056 for `mobmod=3`.

253 `prt=0 Ω m` Temperature coefficient for `Rds`.

254 `ute=-1.5` Mobility temperature exponent.

255 `xti=3` Saturation current temperature exponent.

256 `xtis=3` Bulk-Source junction saturation current temperature exponent.

Spectre Circuit Simulator Reference

Component Statements Part 1

257 `xtid=3` Bulk-Source junction saturation current temperature exponent.

258 `pta=0` V/C Temperature coefficient for `pb`.

259 `tpb=0` V/C Temperature coefficient for `pb`.

260 `ptp=0` V/C Temperature coefficient for `pbsw`.

261 `tpbsw=0` V/C Temperature coefficient for `pbsw`.

262 `tpbswg=0` V/C Temperature coefficient for `pbswg`.

263 `cta=0` 1/C Temperature coefficient for `cj`.

264 `tcj=0` 1/C Temperature coefficient for `cj`.

265 `ctp=0` 1/C Temperature coefficient for `cjsw`.

266 `tcjsw=0` 1/C Temperature coefficient for `cjsw`.

267 `tcjswg=0` 1/C Temperature coefficient for `cjswg`.

Noise Model Parameters

268 `fnoimod=1` Flicker noise model selector.

269 `tnoimod=0` Thermal noise model selector.

270 `kf=0` Flicker noise exponent.

271 `af=1` Flicker noise exponent.

272 `ef=1` Flicker noise frequency exponent.

273 `noia=6.25e41` Flicker noise parameter B. Default is 6.188e40 for pmos.

274 `noib=3.125e26` Flicker noise parameter C. Default is 1.5e25 for pmos.

275 `noic=8.75e9` Flicker noise parameter C.

276 `wnoi=1e-5` mChannel width at which noise parameters were extracted.

277 `em=4.1e7` V/m Saturation field.

Spectre Circuit Simulator Reference

Component Statements Part 1

278 `flkmod` Flicker Noise Model.

279 `ntnoi=1` Noise factor for short-channel devices for TNOIMOD=0 only.

280 `tnoia=1.5` Coefficient of channel-length dependence of total channel thermal noise.

281 `tnoib=3.5` Coefficient of channel-length dependence of total channel thermal noise.

Substrate Network Parameters

282 `rbodymod` Rbody flag.

283 `xrcrg1=12` Parameter for distributed channel-resistance effect for both intrinsic-input resistance and charge-deficit NQS models.

284 `xrcrg2=1` Parameter to account for the excess channel diffusion resistance for both intrinsic-input resistance and charge-deficit NQS models.

285 `rbpb` (Ω) Resistance connected between bNode and bNode .

286 `rbpd` (Ω) Resistance connected between bNode and dbNode.

287 `rbps` (Ω) Resistance connected between bNode and sbNode.

288 `rbdb` (Ω) Resistance connected between dbNode and bNode.

289 `rb sb` (Ω) Resistance connected between sbNode and bNode.

290 `gbmin=1.0e-12` $1/\Omega$

Conductance in parallel with each of the five substrate resistances to avoid potential numerical instability due to unreasonably too large a substrate resistance.

Auto Model Selector Parameters

291 `wmax=1` `m` Maximum channel width for which the model is valid.

292 `wmin=0` `m` Minimum channel width for which the model is valid.

293 `lmax=1` `m` Maximum channel length for which the model is valid.

294 `lmin=0` `m` Minimum channel length for which the model is valid.

Spectre Circuit Simulator Reference

Component Statements Part 1

Operating Region Warning Control Parameters

- 295 `alarm=none` Forbidden operating region.
Possible values are `none`, `off`, `triode`, `sat`, `subth`, or `rev`.
- 296 `imax=1 A` Maximum allowable current.
- 297 `jmax=1e8 A/m2` Maximum allowable current density.
- 298 `bvj= ∞ V` Junction reverse breakdown voltage.
- 299 `vbox=1e9 tox V` Oxide breakdown voltage.
- 300 `warn=on` Parameter to turn warnings on and off.
Possible values are `off` or `on`.

DC-Mismatch Dependent Parameters

- 301 `mvtwl=0.0 V m` Threshold mismatch area dependence.
- 302 `mvtwl2=0.0 V m1.5`
Threshold mismatch area square dependence.
- 303 `mvt0=0.0 V` Threshold mismatch intercept.
- 304 `mbewl=0.0 m` Beta mismatch area dependence.
- 305 `mbe0=0.0` Beta mismatch intercept.

`Imax` and `Imelt`

The `imax` parameter aids convergence and prevents numerical overflow. The junction characteristics of the device are accurately modeled for current up to `imax`. If `imax` is exceeded during iterations, the linear model is substituted until the current drops below `imax` or until convergence is achieved. If convergence is achieved with the current exceeding `imax`, the results are inaccurate, and Spectre prints a warning.

A separate model parameter, `imelt`, is used as a limit warning for the junction current. This parameter can be set to the maximum current rating of the device. When any component of the junction current exceeds `imelt`, note that base and collector currents are composed of many exponential terms, Spectre issues a warning and the results become inaccurate. The junction current is linearized above the value of `imelt` to prevent arithmetic exception, with the exponential term replaced by a linear equation at `imelt`.

Spectre Circuit Simulator Reference

Component Statements Part 1

Both of these parameters have current density counterparts, `jmax` and `jmelt`, that you can specify if you want the absolute current values to depend on the device area.

Auto Model Selection

Many models need to be characterized for different geometries in order to obtain accurate results for model development. The model selector program automatically searches for a model with the length and width range specified in the instance statement and uses this model in the simulations.

For the auto model selector program to find a specific model, the models to be searched should be grouped together within braces. Such a group is called a model group. An opening brace is required at the end of the line defining each model group. Every model in the group is given a name followed by a colon and the list of parameters. Also, the four geometric parameters `lmax`, `lmin`, `wmax`, and `wmin` should be given. The selection criteria to choose a model is as follows:

```
lmin <= inst_length < lmax  and  wmin <= inst_width  < wmax
```

Example

```
model ModelName ModelType {  
    1:      <model parameters> lmin=2 lmax=4 wmin=1 wmax=2  
    2:      <model parameters> lmin=1 lmax=2 wmin=2 wmax=4  
    3:      <model parameters> lmin=2 lmax=4 wmin=4 wmax=6  
}
```

Then for a given instance

```
M1 1 2 3 4 ModelName w=3 l=1.5
```

the program would search all the models in the model group with the name `ModelName` and then pick the first model whose geometric range satisfies the selection criteria. In the preceding example, the auto model selector program would choose `ModelName.2`.

The user must specify both length (`l`) and width (`w`) on the device instance line to enable automatic model selection.

Output Parameters

- 1 `weff` (m) Effective channel width.
- 2 `leff` (m) Effective channel length.
- 3 `rgbi` (Ω) Gate bias-independent resistance.

Spectre Circuit Simulator Reference

Component Statements Part 1

Operating-Point Parameters

- 1 `type=n` Transistor type.
Possible values are n or p.
- 2 `region=triode` Estimated operating region.
Possible values are off, triode, sat, subth, or breakdown.
- 3 `reversed` Reverse mode indicator.
Possible values are no or yes.
- 4 `ids` (A) Resistive drain-to-source current.
- 5 `vgs` (V) Gate-source voltage.
- 6 `vds` (V) Drain-source voltage.
- 7 `vbs` (V) Bulk-source voltage.
- 8 `vth` (V) Threshold voltage.
- 9 `vdsat` (V) Drain-source saturation voltage.
- 10 `gm` (S) Common-source transconductance.
- 11 `gds` (S) Common-source output conductance.
- 12 `gmbs` (S) Body-transconductance.
- 13 `betaeff` (A/V²) Effective beta.
- 14 `cjd` (F) Drain-bulk junction capacitance.
- 15 `cjs` (F) Source-bulk junction capacitance.
- 16 `cgg` (F) dQ_g/dV_g .
- 17 `cgd` (F) dQ_g/dV_d .
- 18 `cgs` (F) dQ_g/dV_s .
- 19 `cgb` (F) dQ_g/dV_{bk} .

Spectre Circuit Simulator Reference

Component Statements Part 1

- 20 `cdg (F)dQd_dVg.`
- 21 `cdd (F)dQd_dVd.`
- 22 `cds (F)dQd_dVs.`
- 23 `cdb (F)dQd_dVb.`
- 24 `csd (F)dQs_dVd.`
- 25 `css (F)dQs_dVs.`
- 26 `csb (F)dQs_dVb.`
- 27 `cbg (F)dQb_dVg.`
- 28 `cbd (F)dQb_dVd.`
- 29 `cbs (F)dQb_dVs.`
- 30 `cbb (F)dQb_dVb.`
- 31 `ron (Ω)On-resistance.`
- 32 `id (A)Resistive drain current.`
- 33 `ibulk (A)Resistive bulk current.`
- 34 `pwr (W)Power at op point.`
- 35 `gmoverid (1/V)Gm/Ids.`
- 36 `rdeff (Ω)Effective drain resistance.`
- 37 `rseff (Ω)Effective source resistance.`
- 38 `rgbd (Ω)Gate bias-dependent resistance.`
- 39 `igidl (A)Gate-to-body tunneling current.`
- 40 `igd (A)Gate-to-drain tunneling current.`

Spectre Circuit Simulator Reference

Component Statements Part 1

- 42 `igs` (A) Gate-to-source tunneling current.
- 43 `igb` (A) Gate-to-bulk tunneling current.
- 44 `igcs` (A) Gate-to-channel (source side) tunneling current.
- 45 `igcd` (A) Gate-to-channel (drain side) tunneling current.
- 46 `gbs` (S) bulk-source diode conductance.
- 47 `gbd` (S) bulk-drain diode conductance.

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

<code>a0</code> M-24	<code>dvt0</code> M-16	<code>mbewl</code> M-304	<code>rdwmin</code> M-61
<code>a1</code> M-27	<code>dvt0w</code> M-21	<code>meto</code> M-171	<code>region</code> OP-2
<code>a2</code> M-28	<code>dvt1</code> M-17	<code>min</code> M-113	<code>region</code> I-10
<code>acde</code> M-216	<code>dvt1w</code> M-22	<code>min</code> I-24	<code>reversed</code> OP-3
<code>acnqsmod</code> M-209	<code>dvt2</code> M-18	<code>minr</code> M-119	<code>rgatemod</code> M-101
<code>acnqsmod</code> I-12	<code>dvt2w</code> M-23	<code>minv</code> M-94	<code>rgatemod</code> I-14
<code>ad</code> M-230	<code>dvtp0</code> M-19	<code>mj</code> M-179	<code>rgbd</code> OP-39
<code>ad</code> I-4	<code>dvtp1</code> M-20	<code>mjd</code> M-181	<code>rgbi</code> O-3
<code>af</code> M-271	<code>dwb</code> M-53	<code>mjs</code> M-180	<code>rgeomod</code> I-17
<code>agidl</code> M-120	<code>dwc</code> M-210	<code>mjsw</code> M-189	<code>rgeomod</code> M-116
<code>ags</code> M-29	<code>dwg</code> M-52	<code>mjswd</code> M-191	<code>ron</code> OP-32
<code>aigbacc</code> M-126	<code>dwj</code> M-108	<code>mjswg</code> M-198	<code>rseff</code> OP-38
<code>aigbinv</code> M-130	<code>ef</code> M-272	<code>mjswgd</code> M-200	<code>rsh</code> M-102
<code>aigc</code> M-135	<code>eg</code> M-243	<code>mjswgs</code> M-199	<code>rshg</code> M-103
<code>aigsd</code> M-138	<code>egidl</code> M-123	<code>mjsws</code> M-190	<code>rsd</code> M-62
<code>alarm</code> M-295	<code>eigbinv</code> M-133	<code>mobmod</code> M-67	<code>rsdmin</code> M-63
<code>alpha0</code> M-98	<code>em</code> M-277	<code>moind</code> M-217	<code>tcj</code> M-264
<code>alpha1</code> M-99	<code>epsrox</code> M-31	<code>mvt0</code> M-303	<code>tcjsw</code> M-266
<code>as</code> I-3	<code>eta0</code> M-96	<code>mvtw1</code> M-301	<code>tcjswg</code> M-267
<code>as</code> M-229	<code>etab</code> M-97	<code>mvtw12</code> M-302	<code>tlev</code> M-241
<code>at</code> M-249	<code>eu</code> M-73	<code>n</code> M-156	<code>tlevc</code> M-242
<code>b0</code> M-25	<code>fc</code> M-185	<code>ndep</code> M-35	<code>tnoia</code> M-280
<code>b1</code> M-26	<code>fcsd</code> M-204	<code>nf</code> M-112	<code>tnoib</code> M-281

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Component Statements Part 1

beta0 M-100	flkmod M-278	nf I-23	tnoimod M-269
betaeff OP-13	fnoimod M-268	nfactor M-90	tnom M-239
bgidl M-121	fprout M-75	ngate M-38	toxe M-32
bigbacc M-127	fullreinit M-238	ngcon M-111	toxmx M-54
bigbinv M-131	gamma1 M-12	nigbacc M-129	toxp M-33
bigc M-136	gamma2 M-13	nigbinv M-134	toxref M-146
bigsd M-139	gap1 M-244	nigc M-142	tpb M-259
binunit M-56	gap2 M-245	njd M-158	tpbsw M-261
bvd M-206	gbd OP-47	njs M-157	tpbswg M-262
bvj M-298	gbmin M-290	noff M-218	trise I-13
bvs M-205	gbs OP-46	noia M-273	trise M-240
capmod M-207	gds OP-11	noib M-274	trnqsmode M-208
cbb OP-31	geomod I-16	noic M-275	trnqsmode I-11
cbd OP-29	geomod M-115	nrd M-233	type M-1
cbg OP-28	gm OP-10	nrd I-7	type OP-1
cbs OP-30	gmbs OP-12	nrs M-234	u0 M-68
cdb OP-23	gmoverid OP-36	nrs I-8	ua M-70
cdd OP-21	ibulk OP-34	nsd M-36	ual M-250
cdg OP-20	id OP-33	nsub M-37	ub M-71
cds OP-22	ids OP-4	ntnoi M-279	ubl M-251
cdsc M-87	igb OP-43	ntox M-145	uc M-72
cdscb M-88	igbmod M-125	paramchk M-237	uc1 M-252
cdscd M-89	igcd OP-45	pb M-182	ute M-254
cf M-214	igcmode M-124	pbd M-184	vbm M-15
cgb OP-19	igcs OP-44	pbs M-183	vbox M-299
cgbo M-170	igd OP-41	pbsw M-192	vbs OP-7
cgd OP-17	igidl OP-40	pbswd M-194	vbx M-14
cgdl M-173	igs OP-42	pbswg M-201	vds OP-6
cgdo M-169	ijthdfwd M-165	pbswgd M-203	vdsat OP-9
cgg OP-16	ijthdrev M-163	pbswgs M-202	version M-235
cgidl M-122	ijthsfwd M-164	pbsws M-193	vfb M-3
cgs OP-18	ijthsrev M-162	pclm M-76	vfbcv M-215
cgs1 M-172	imax M-296	pd I-6	vgs OP-5
cgso M-168	imelt M-160	pd M-232	voff M-92
cigbacc M-128	is M-155	pdiblc1 M-77	voffcv M-219
cigbinv M-132	jmax M-297	pdiblc2 M-78	voffl M-93
cigc M-137	jmelt M-161	pdiblc3 M-79	vsat M-69
cigsd M-140	js M-148	pdits M-84	vth OP-8
cit M-91	jsd M-150	pditsd M-86	vtho M-2
cj M-176	jss M-149	pditsl M-85	w M-227
cjd M-178	jswd M-152	permod M-114	w I-1
cjd OP-14	jswgd M-154	phin M-4	w0 M-9
cjs OP-15	jswgs M-153	pigcd M-144	warn M-300
cjs M-177	jsws M-151	poxedge M-143	weff O-1
cjsw M-186	k1 M-5	prt M-253	wint M-41
cjswd M-188	k2 M-6	prwb M-64	wl M-47
cjswg M-195	k3 M-7	prwg M-65	wlc M-224

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Component Statements Part 1

cjswgd	M-197	k3b	M-8	ps	M-231	wln	M-48
cjswgs	M-196	keta	M-30	ps	I-5	wmax	M-291
cjsws	M-187	kf	M-270	pscbe1	M-80	wmin	M-292
ckappad	M-175	kt1	M-246	pscbe2	M-81	wnoi	M-276
ckappas	M-174	kt11	M-247	pta	M-258	wr	M-66
clc	M-212	kt2	M-248	ptp	M-260	ww	M-49
cle	M-213	l	I-2	pvag	M-82	wwc	M-225
csb	OP-27	l	M-228	pwr	OP-35	wwl	M-51
csd	OP-25	leff	O-2	rbdb	I-21	wwlc	M-226
csg	OP-24	level	M-236	rbdb	M-288	wnn	M-50
css	OP-26	lint	M-40	rbodmod	I-15	xgl	M-110
cta	M-263	ll	M-42	rbodmod	M-282	xgw	M-109
ctp	M-265	llc	M-221	rbpb	I-18	xj	M-39
delta	M-83	lln	M-43	rbpb	M-285	xjbvd	M-167
diomod	M-147	lmax	M-293	rbpd	I-19	xjbvs	M-166
dlc	M-211	lmin	M-294	rbpd	M-286	xl	M-118
dlcig	M-141	lpe0	M-10	rbps	I-20	xpart	M-220
dmcg	M-104	lpeb	M-11	rbps	M-287	xrcrg1	M-283
dmcgt	M-107	lw	M-44	rbsb	M-289	xrcrg2	M-284
dmci	M-105	lwc	M-222	rbsb	I-22	xt	M-55
dmdg	M-106	lwl	M-46	rdeff	OP-37	xti	M-255
drout	M-74	lwlc	M-223	rdsmo	M-57	xtid	M-257
dskip	M-159	lwn	M-45	rdsw	M-58	xtis	M-256
dsub	M-95	m	I-9	rdswmin	M-59	xw	M-117
dtox	M-34	mbe0	M-305	rdw	M-60		

BTA SOI Transistor (btasoi)

Description

BTASOI is an SOI model developed by BTA Technology based on bsim3v3. It is a new, simple and compact SOI model that can accommodate both the fully-depleted, FD and partially-depleted, PD modes, adopting the transition voltage, V_{tr} , for the definition of body condition. It can also simulate the special characteristics of SOI devices such as kink effect and reduction of saturation current due to self-heating. Simulation results with this model are in excellent agreement with the experimental data for 0.25um SIMOX technology. BTASOI devices require that you use a model statement.

If you want to get more information about this model, please contact BTA Technology at <http://www.btat.com>

This device is supported within altergroups.

Spectre Circuit Simulator Reference

Component Statements Part 1

Sample Instance Statement

```
m5 (1 2 0 0) nchmod l=1.5u w=100u as=450p ad=450p pd=209u ps=209u m=1
```

Sample Model Statement

```
model nchmod btasoi type=n b3v3mod=no version=3.1 vtho=0.62 k1=0.672 k2=0.038  
nlx=1.14e-7 dvt0=4.1 a0=1.08 nch=2.65e17 u0=4.01e-2 a1=0 a2=1 ags=9.8e-4  
vsat=1.77e5
```

Instance Definition

```
Name d g s [bg] [b] ModelName parameter=value ...
```

Instance Parameters

- 1 w (m) Channel width.
- 2 l (m) Channel length.
- 3 as (m²) Area of source diffusion.
- 4 ad (m²) Area of drain diffusion.
- 5 ps (m) Perimeter of source diffusion.
- 6 pd (m) Perimeter of drain diffusion.
- 7 nrd (m/m) Number of squares of drain diffusion.
- 8 nrs (m/m) Number of squares of source diffusion.
- 9 m=1 Multiplicity factor (number of MOSFETs in parallel).
- 10 region=triode Estimated operating region.
Possible values are off, triode, sat, or subth.
- 11 trise Temperature rise from ambient.
- 12 rbody (Ω) Body resistance.

Model Definition

```
model modelName btasoi parameter=value ...
```

Model Parameters

Device Type Parameters

- 1 `type=n` Transistor type.
Possible values are n or p.
- 2 `b3v3mod=no` B3v3 compatible flag.
Possible values are no or yes.
- 3 `version=3.1` Model version selector.
- 4 `btasoiver=1.0` BTASOI Model version selector.

Threshold Voltage Parameters

- 5 `vtho (V)` Threshold voltage at zero body bias for long-channel devices. For enhancement-mode devices, $v_{tho} > 0$ for n-channel and $v_{th} < 0$ for p-channel. Default value is calculated from other model parameters.
- 6 `k1=0.5 \sqrt{V}` Body-effect coefficient.
- 7 `k2=-0.0186` Charge-sharing parameter.
- 8 `k3=80` Narrow width coefficient.
- 9 `k3b=0 $1/V$` Narrow width coefficient.
- 10 `w0=2.5e-6 m` Narrow width coefficient.
- 11 `nlx=1.74e-7 m` Lateral nonuniform doping coefficient.
- 12 `gamma1 (\sqrt{V})` Body-effect coefficient near the surface.
- 13 `gamma2 (\sqrt{V})` Body-effect coefficient in the bulk.
- 14 `vbx (V)` Threshold voltage transition body voltage.
- 15 `vbm=-3 V` Maximum applied body voltage.
- 16 `dvt0=2.2` First coefficient of short-channel effects.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 17 $dvt1=0.53$ Second coefficient of short-channel effects.
- 18 $dvt2=-0.032 \ 1/v$ Body-bias coefficient of short-channel effects.
- 19 $dvt0w=0$ First coefficient of narrow-width effects.
- 20 $dvt1w=5.3e6$ Second coefficient of narrow-width effects.
- 21 $dvt2w=-0.032 \ 1/v$ Body-bias coefficient of narrow-width effects.
- 22 $a0=1$ Nonuniform depletion width effect coefficient.
- 23 $b0=0 \ m$ Bulk charge coefficient due to narrow width effect.
- 24 $b1=0 \ m$ Bulk charge coefficient due to narrow width effect.
- 25 $a1=0$ No-saturation coefficient.
- 26 $a2=1$ No-saturation coefficient.
- 27 $ags=0 \ F/m^2 \ v$ Gate-bias dependence of A_{bulk} .
- 28 $keta=-0.047 \ 1/v$ Body-bias coefficient for non-uniform depletion width effect.

Process Parameters

- 29 $n_{sub}=6e16 \ cm^{-3}$ Substrate doping concentration.
- 30 $n_{ch}=1.7e17 \ cm^{-3}$ Peak channel doping concentration.
- 31 $n_{gate} \ (cm^{-3})$ Poly-gate doping concentration.
- 32 $xj=0.15e-6 \ m$ Source/drain junction depth.
- 33 $lint=0 \ m$ Lateral diffusion for one side.
- 34 $wint=0 \ m$ Width reduction for one side.
- 35 $ll=0 \ m$ Length dependence of δL .
- 36 $lln=1$ Length exponent of δL .
- 37 $lw=0 \ m$ Width dependence of δL .

Spectre Circuit Simulator Reference

Component Statements Part 1

- 38 `lwn=1` Width exponent of delta L.
- 39 `lwl=0` m^2 Area dependence of delta L.
- 40 `wl=0` m Length dependence of delta W.
- 41 `wln=1` Length exponent of delta W.
- 42 `ww=0` m Width dependence of delta W.
- 43 `wn=1` Width exponent of delta W.
- 44 `wwl=0` m^2 Area dependence of delta W.
- 45 `dwg=0` m/\sqrt{v} Gate-bias dependence of channel width.
- 46 `dwb=0` m/\sqrt{v} Body-bias dependence of channel width.
- 47 `tox=1.5e-8` m Gate oxide thickness.
- 48 `tbox=4e-7` m Buried oxide thickness.
- 49 `tsi=8e-8` m Silicon film thickness.
- 50 `xt=1.55e-7` m Doping depth.
- 51 `rdsw=0` $\Omega \mu m$ Width dependence of drain-source resistance.
- 52 `prwb=0` $1/\sqrt{v}$ Body-effect coefficient for R_{ds} .
- 53 `prwg=0` $1/v$ Gate-effect coefficient for R_{ds} .
- 54 `wr=1` Width offset for parasitic resistance.
- 55 `xl=0` m Length variation due to masking and etching.
- 56 `xw=0` m Width variation due to masking and etching.
- 57 `binunit=1` Bin parameter unit selector. 1 for microns and 2 for meters.

Mobility Parameters

- 58 `mobmod=1` Mobility model selector.

Spectre Circuit Simulator Reference

Component Statements Part 1

59 $u_0=670 \text{ cm}^2/\text{V}$ sLow-field surface mobility at t_{nom} . Default is 250 for PMOS.

60 $vsat=8e4 \text{ m/s}$ Carrier saturation velocity at t_{nom} .

61 $ua=2.25e-9 \text{ m/v}$ First-order mobility reduction coefficient.

62 $ub=5.87e-19 \text{ m}^2/\text{v}^2$
Second-order mobility reduction coefficient.

63 $uc=-4.65e-11 \text{ m/v}^2$
Body-bias dependence of mobility. Default is -0.046 and unit is 1/V for $mobmod=3$.

Output Resistance Parameters

64 $drout=0.56$ DIBL effect on output resistance coefficient.

65 $pclm=1.3$ Channel length modulation coefficient.

66 $pdiblc1=0.39$ First coefficient of drain-induced barrier lowering.

67 $pdiblc2=8.6e-3$ Second coefficient of drain-induced barrier lowering.

68 $pdiblc_b=0 \text{ 1/v}$ Body-effect coefficient for DIBL.

69 $pscbe1=4.24e8 \text{ V/m}$ First coefficient of substrate current body effect.

70 $pscbe2=1e-5 \text{ m/v}$ Second coefficient of substrate current body effect.

71 $pvag=0$ Gate dependence of Early voltage.

72 $delta=0.01 \text{ v}$ Effective drain voltage smoothing parameter.

Subthreshold Parameters

73 $cdsc=2.4e-4 \text{ F/m}^2$ Source/drain and channel coupling capacitance.

74 $cdsc_b=0 \text{ F/m}^2 \text{ v}$ Body-bias dependence of $cdsc$.

75 $cdsc_d=0 \text{ F/m}^2 \text{ v}$ Drain-bias dependence of $cdsc$.

76 $nfactor=1$ Subthreshold swing coefficient.

Spectre Circuit Simulator Reference

Component Statements Part 1

77 `cit=0` `F`Interface trap parameter for subthreshold swing.

78 `voff=-0.08` `v`Threshold voltage offset.

79 `dsub=drout` DIBL effect in subthreshold region.

80 `eta0=0.08` DIBL coefficient subthreshold region.

81 `etab=-0.07` `1/v`Body-bias dependence of `et0`.

Substrate Current Parameters

82 `alpha0=0` `m/v`Substrate current impact ionization coefficient.

83 `beta0=30` `1/v`Substrate current impact ionization exponent.

Parasitic Resistance Parameters

84 `rsh=0` Ω/sqr Source/drain diffusion sheet resistance.

85 `rs=0` Ω Source resistance.

86 `rd=0` Ω Drain resistance.

87 `rsc=0` Ω Source contact resistance.

88 `rdc=0` Ω Drain contact resistance.

89 `rss=0` Ω `m`Scalable source resistance.

90 `rdd=0` Ω `m`Scalable drain resistance.

91 `hdif=0` `m`Length of heavily doped diffusion.

92 `ldif=0` `m`Lateral diffusion beyond the gate.

93 `minr=0.1` Ω Minimum source/drain resistance.

Junction Diode Model Parameters

94 `js` (A/m^2) Bulk junction reverse saturation current density.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 95 `jsw=0` A/m Sidewall junction reverse saturation current density.
- 96 `is=1e-14` A Bulk junction reverse saturation current.
- 97 `n=1` Junction emission coefficient.
- 98 `dskip=yes` Use simple piecewise linear model for diode currents below $0.1 \cdot i_{abstol}$.
Possible values are `no` or `yes`.
- 99 `imelt=`imaxA`` Explosion current.
- 100 `imelt1=imax` A/m Explosion current density for `is1`.
- 101 `imelt2=imax` A/m Explosion current density for `is2`.
- 102 `imelt3=imax` A/m Explosion current density for `is3`.

Overlap Capacitance Parameters

- 103 `cgso` (F/m) Gate-source overlap capacitance.
- 104 `cgdo` (F/m) Gate-drain overlap capacitance.
- 105 `cgbo=2` `Dwc` `Cox` F/m
Gate-bulk overlap capacitance. The default value is 0 if
version=3.0.
- 106 `meto=0` m Metal overlap in fringing field.
- 107 `cgsl=0` F/m Gate-source overlap capacitance in LDD region.
- 108 `cgdl=0` F/m Gate-drain overlap capacitance in LDD region.
- 109 `ckappa=0.6` Overlap capacitance fitting parameter.

Junction Capacitance Model Parameters

- 110 `cbs=0` F Bulk-source zero-bias junction capacitance.
- 111 `cbd=0` F Bulk-drain zero-bias junction capacitance.
- 112 `cj=5e-4` F/m² Zero-bias junction bottom capacitance density.

Spectre Circuit Simulator Reference

Component Statements Part 1

113 $m_j=1/2$ Bulk junction bottom grading coefficient.

114 $p_b=1$ ψ Bulk junction built-in potential.

115 $f_c=0.5$ Forward-bias depletion capacitance threshold.

116 $c_{jsw}=5e-10$ F/m Zero-bias junction sidewall capacitance density.

117 $m_{jsw}=0.33$ Bulk junction sidewall grading coefficient.

118 $p_{bsw}=1$ ψ Side-wall junction built-in potential.

119 $c_{jswg}=c_{jsw}$ F/m Zero-bias gate-side junction capacitance density.

120 $m_{jswg}=m_{jsw}$ Gate-side junction grading coefficient.

121 $p_{bswg}=p_{bsw}$ ψ Gate-side junction built-in potential.

122 $f_{csw}=f_c$ Side-wall forward-bias depletion capacitance threshold.

123 $\tau_{au}=0$ s Transit time.

Charge Model Selection Parameters

124 $capmod=2$ Intrinsic charge model.

125 $dwc=wint$ m Delta W for capacitance model.

126 $dlc=lint$ m Delta L for capacitance model.

127 $clc=1e-7$ m Intrinsic capacitance fitting parameter.

128 $c_{le}=0.6$ Intrinsic capacitance fitting parameter.

129 c_f (F/m) Fringe capacitance parameter.

130 $v_{fbcv}=-1$ Flat-band voltage for $capmod=0$.

131 $x_{part}=0$ Drain/source channel charge partition in saturation for BSIM charge model, use 0.0 for 40/60, 0.5 for 50/50, or 1.0 for 0/100.

Spectre Circuit Simulator Reference

Component Statements Part 1

Default Instance Parameters

- 132 $w=5e-6$ m Default channel width.
- 133 $l=5e-6$ m Default channel length.
- 134 $as=0$ m² Default area of source diffusion.
- 135 $ad=0$ m² Default area of drain diffusion.
- 136 $ps=0$ m Default perimeter of source diffusion.
- 137 $pd=0$ m Default perimeter of drain diffusion.
- 138 $nrd=0$ m/m Default number of squares of drain diffusion.
- 139 $nrs=0$ m/m Default number of squares of source diffusion.

Temperature Effects Parameters

- 140 t_{nom} (C) Parameters measurement temperature. Default set by `options`.
- 141 $t_{max}=500$ C Maximum device temperature above ambient.
- 142 $t_{rise}=0$ C Temperature rise from ambient.
- 143 $selft=0$ Self heating option.
- 144 $t_{lev}=0$ DC temperature selector.
- 145 $t_{levc}=0$ AC temperature selector.
- 146 $eg=1.12452$ V Energy band gap.
- 147 $gap1=7.02e-4$ V/C Band gap temperature coefficient.
- 148 $gap2=1108$ C Band gap temperature offset.
- 149 $kt1=-0.11$ V Temperature coefficient for threshold voltage.
- 150 $kt1l=0$ V m Temperature coefficient for threshold voltage.
- 151 $kt2=0.022$ Temperature coefficient for threshold voltage.

Spectre Circuit Simulator Reference

Component Statements Part 1

152 $at=3.3e4 \text{ m/s}$ Temperature coefficient for v_{sat} .

153 $ua1=4.31e-9 \text{ m/v}$ Temperature coefficient for ua .

154 $ub1=-7.61e-18 \text{ m}^2/\text{v}^2$
Temperature coefficient for ub .

155 $uc1=-5.5e-11 \text{ m/v}^2$
Temperature coefficient for uc . Default is -0.056 for $mobmod=3$.

156 $prt=0 \text{ } \Omega$ Temperature coefficient for R_{ds} .

157 $trs=0 \text{ 1/C}$ Temperature parameter for source resistance.

158 $trd=0 \text{ 1/C}$ Temperature parameter for drain resistance.

159 $ute=-1.5$ Mobility temperature exponent.

160 $dt1=0$ First temperature coefficient for τ .

161 $dt2=0$ Second temperature coefficient for τ .

162 $xti=3$ Saturation current temperature exponent.

163 $xti1=3$ Saturation current temperature exponent.

164 $xti2=xti1$ Saturation current temperature exponent.

165 $xti3=xti1$ Saturation current temperature exponent.

166 $ptc=0 \text{ V/C}$ Surface potential temperature coefficient.

167 $pta=0 \text{ V/C}$ Junction potential temperature coefficient.

168 $ptp=0 \text{ V/C}$ Sidewall junction potential temperature coefficient.

169 $cta=0 \text{ 1/C}$ Junction capacitance temperature coefficient.

170 $ctp=0 \text{ 1/C}$ Sidewall junction capacitance temperature coefficient.

Noise Model Parameters

171 $noimod=1$ Noise model selector.

Spectre Circuit Simulator Reference

Component Statements Part 1

172 `kf=0` Flicker (1/f) noise coefficient.

173 `af=1` Flicker (1/f) noise exponent.

174 `ef=1` Flicker (1/f) noise frequency exponent.

175 `noia=1e20` Oxide trap density coefficient. Default is $9.9e18$ for pmos.

176 `noib=5e4` Oxide trap density coefficient. Default is $2.4e3$ for pmos.

177 `noic=-1.4e-12` Oxide trap density coefficient. Default is $1.4e-8$ for pmos.

178 `em=4.1e7 V/m` Maximum electric field.

Auto Model Selector Parameters

179 `wmax=1 m` Maximum channel width for which the model is valid.

180 `wmin=0 m` Minimum channel width for which the model is valid.

181 `lmax=1 m` Maximum channel length for which the model is valid.

182 `lmin=0 m` Minimum channel length for which the model is valid.

Operating Region Warning Control Parameters

183 `alarm=none` Forbidden operating region.
Possible values are `none`, `off`, `triode`, `sat`, `subth`, or `rev`.

184 `imax=1 A` Maximum allowable current.

185 `bvj= ∞ V` Junction reverse breakdown voltage.

186 `vbox=1e9 tox V` Oxide breakdown voltage.

187 `warn=on` Parameter to turn warnings on and off.
Possible values are `off` or `on`.

SOI Specific Parameters

188 `vbtho=10 V` Back-gate threshold voltage.

Spectre Circuit Simulator Reference

Component Statements Part 1

189 `vtr0=0.3` V_{tr} Long-channel transition body voltage at $V_{ds}=0$.

190 `knk=0.01` V_{tr} smoothing factor.

191 `dice=0` Drain-induced charge-sharing parameter.

192 `dvtrd=0` V_{tr} dependence on V_{ds} .

193 `dvtrg=1` V_{tr} dependence on V_{gs} .

194 `dvtrbg=1.0` Smoothing factor for back-gate bias.

195 `a0bg=0` Back-gate saturation region coefficient.

196 `dbg=1` Diode fully depletion adjustment factor.

197 `dvtr=1.0` Diode back-gate dependence factor.

198 `vbgf=0.0` Flat-band voltage for back-gate.

199 `rth0=0` Ω Self-heating thermal resistance.

200 `cth0=1` F Self-heating thermal capacitance.

201 `l1=0` V_{gs} dependence of characteristic length.

202 `aii=0` First parameter for critical field.

203 `bii=0` Second parameter for critical field.

204 `cii=0` Gate dependence of critical field.

205 `dii=0` Body dependence of critical field.

206 `ndiode=1` Diode non-ideality factor.

207 `nt=1` Reverse tunneling non-ideality factor.

208 `is1=1e-16` A First diode parameter.

209 `is2=0` A Second diode parameter.

210 `is3=0` A Tunneling diode parameter.

Spectre Circuit Simulator Reference

Component Statements Part 1

211 `edl=2e-6` `mElectron` diffusion length.

212 `kb=0` `mParasitic` bipolar base width.

213 `delacc=0.02` `vCapacitance` smoothing parameter in accumulation region.

214 `delr=0.01` `vVbs` smoothing parameter for C-V.

215 `dqsq=8e-3` `vVtr` smoothing parameter for C-V.

216 `a0cv=0.1` `A0` for C-V calculation.

217 `qgvd0=1` `Cgd` fitting parameter.

The `jmel` parameter is used to aid convergence and prevent numerical overflow. The junction characteristics of the FET are accurately modeled for current (density) up to `jmel`. For current density above `jmel`, the junction is modeled as a linear resistor and a warning is printed.

Auto Model Selection

Many models need to be characterized for different geometries in order to obtain accurate results for model development. The model selector program automatically searches for a model with the length and width range specified in the instance statement and uses this model in the simulations.

For the auto model selector program to find a specific model, the models to be searched should be grouped together within braces. Such a group is called a model group. An opening brace is required at the end of the line defining each model group. Every model in the group is given a name followed by a colon and the list of parameters. Also, the four geometric parameters `lmax`, `lmin`, `wmax`, and `wmin` should be given. The selection criteria to choose a model is as follows:

```
lmin <= inst_length < lmax and wmin <= inst_width < wmax
```

Example

```
model ModelName ModelType {
  1:    <model parameters> lmin=2 lmax=4 wmin=1 wmax=2
  2:    <model parameters> lmin=1 lmax=2 wmin=2 wmax=4
  3:    <model parameters> lmin=2 lmax=4 wmin=4 wmax=6
}
```

Then for a given instance

```
M1 1 2 3 4 ModelName w=3 l=1.5
```

Spectre Circuit Simulator Reference

Component Statements Part 1

the program would search all the models in the model group with the name `ModelName` and then pick the first model whose geometric range satisfies the selection criteria. In the preceding example, the auto model selector program would choose `ModelName.2`.

The user must specify both length (*l*) and width (*w*) on the device instance line to enable automatic model selection.

Output Parameters

- 1 `weff` (m) Effective channel width.
- 2 `leff` (m) Effective channel length.
- 3 `rtheff` (Ω) Effective thermal resistance.
- 4 `ctheff` (F) Effective thermal capacitance.
- 5 `rseff` (Ω) Effective source resistance.
- 6 `rdeff` (Ω) Effective drain resistance.

Operating-Point Parameters

- 1 `type=n` Transistor type.
Possible values are `n` or `p`.
- 2 `region=triode` Estimated operating region.
Possible values are `off`, `triode`, `sat`, or `subth`.
- 3 `reversed` Reverse mode indicator.
Possible values are `no` or `yes`.
- 4 `vgs` (V) Gate-source voltage.
- 5 `vds` (V) Drain-source voltage.
- 6 `vbs` (V) Bulk-source voltage.
- 7 `ids` (A) Resistive drain-to-source current.
- 8 `isub` (A) Resistive substrate current.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 9 `ibd (A)` Resistive bulk-to-drain junction current.
- 10 `ibs (A)` Resistive bulk-to-source junction current.
- 11 `vth (V)` Threshold voltage.
- 12 `vdsat (V)` Drain-source saturation voltage.
- 13 `gm (S)` Common-source transconductance.
- 14 `gds (S)` Common-source output conductance.
- 15 `gmbs (S)` Body-transconductance.
- 16 `ueff (cm2/V s)` Effective mobility.
- 17 `betaeff (A/V2)` Effective beta.
- 18 `cjd (F)` Drain-bulk junction capacitance.
- 19 `cjs (F)` Source-bulk junction capacitance.
- 20 `cgg (F)` dQ_g/dV_g .
- 21 `cgd (F)` dQ_g/dV_d .
- 22 `cgs (F)` dQ_g/dV_s .
- 23 `cgb (F)` dQ_g/dV_{bk} .
- 24 `cdg (F)` dQ_d/dV_g .
- 25 `cdd (F)` dQ_d/dV_d .
- 26 `cds (F)` dQ_d/dV_s .
- 27 `cdb (F)` dQ_d/dV_b .
- 28 `csg (F)` dQ_s/dV_g .
- 29 `csd (F)` dQ_s/dV_d .
- 30 `css (F)` dQ_s/dV_s .

Spectre Circuit Simulator Reference

Component Statements Part 1

- 31 `csb (F)dQs_dVb`.
- 32 `cbg (F)dQb_dVg`.
- 33 `cbd (F)dQb_dVd`.
- 34 `cbs (F)dQb_dVs`.
- 35 `cbb (F)dQb_dVb`.
- 36 `ron (Ω)` On-resistance.
- 37 `id (A)` Total resistive drain current.
- 38 `is (A)` Total resistive source current.
- 39 `ib (A)` Total resistive bulk current.
- 40 `pwr (W)` Power at op point.
- 41 `gmoverid (1/V)` Gm/Ids.
- 42 `tdev (C)` Temperature rise from ambient.

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

<code>Ctheff</code>	O-4	<code>dice</code>	M-191	<code>ll</code>	M-201	<code>rss</code>	M-89
<code>a0</code>	M-22	<code>dii</code>	M-205	<code>ldif</code>	M-92	<code>rth0</code>	M-199
<code>a0bg</code>	M-195	<code>dlc</code>	M-126	<code>leff</code>	O-2	<code>rtheff</code>	O-3
<code>a0cv</code>	M-216	<code>dqsq</code>	M-215	<code>lint</code>	M-33	<code>selft</code>	M-143
<code>a1</code>	M-25	<code>drout</code>	M-64	<code>ll</code>	M-35	<code>tau</code>	M-123
<code>a2</code>	M-26	<code>dskip</code>	M-98	<code>lln</code>	M-36	<code>tbox</code>	M-48
<code>ad</code>	I-4	<code>dsub</code>	M-79	<code>lmax</code>	M-181	<code>tdev</code>	OP-42
<code>ad</code>	M-135	<code>dt1</code>	M-160	<code>lmin</code>	M-182	<code>tlev</code>	M-144
<code>af</code>	M-173	<code>dt2</code>	M-161	<code>lw</code>	M-37	<code>tlevc</code>	M-145
<code>ags</code>	M-27	<code>dvt0</code>	M-16	<code>lwl</code>	M-39	<code>tmax</code>	M-141

Spectre Circuit Simulator Reference

Component Statements Part 1

aii	M-202	dvt0w	M-19	lwn	M-38	tnom	M-140
alarm	M-183	dvt1	M-17	m	I-9	tox	M-47
alpha0	M-82	dvt1w	M-20	meto	M-106	trd	M-158
as	I-3	dvt2	M-18	minr	M-93	trise	M-142
as	M-134	dvt2w	M-21	mj	M-113	trise	I-11
at	M-152	dvtr	M-197	mjsw	M-117	trs	M-157
b0	M-23	dvtrbg	M-194	mjswg	M-120	tsi	M-49
b1	M-24	dvtrd	M-192	mobmod	M-58	type	OP-1
b3v3mod	M-2	dvtrg	M-193	n	M-97	type	M-1
beta0	M-83	dwb	M-46	nch	M-30	u0	M-59
betaeff	OP-17	dwc	M-125	ndiode	M-206	ua	M-61
bii	M-203	dwg	M-45	nfactor	M-76	ual	M-153
binunit	M-57	edl	M-211	ngate	M-31	ub	M-62
btasoiver	M-4	ef	M-174	nlx	M-11	ubl	M-154
bvj	M-185	eg	M-146	noia	M-175	uc	M-63
capmod	M-124	em	M-178	noib	M-176	uc1	M-155
cbb	OP-35	eta0	M-80	noic	M-177	ueff	OP-16
cbd	M-111	etab	M-81	noimod	M-171	ute	M-159
cbd	OP-33	fc	M-115	nrd	I-7	vbgf	M-198
cbg	OP-32	fcs	M-122	nrd	M-138	vbm	M-15
cbs	M-110	gamma1	M-12	nrs	M-139	vbox	M-186
cbs	OP-34	gamma2	M-13	nrs	I-8	vbs	OP-6
cdb	OP-27	gap1	M-147	nsub	M-29	vbtho	M-188
cdd	OP-25	gap2	M-148	nt	M-207	vbx	M-14
cdg	OP-24	gds	OP-14	pb	M-114	vds	OP-5
cds	OP-26	gm	OP-13	pbsw	M-118	vdsat	OP-12
cdsc	M-73	gmbs	OP-15	pbswg	M-121	version	M-3
cdscb	M-74	gmoverid	OP-41	pclm	M-65	vfbcv	M-130
cdscd	M-75	hdif	M-91	pd	M-137	vgs	OP-4
cf	M-129	ib	OP-39	pd	I-6	voff	M-78
cgb	OP-23	ibd	OP-9	pdiblc1	M-66	vsat	M-60
cgbo	M-105	ibs	OP-10	pdiblc2	M-67	vth	OP-11
cgd	OP-21	id	OP-37	pdiblc3	M-68	vtho	M-5
cgdl	M-108	ids	OP-7	prt	M-156	vtr0	M-189
cgdo	M-104	imax	M-184	prwb	M-52	w	M-132
cgg	OP-20	imelt	M-99	prwg	M-53	w	I-1
cgs	OP-22	imelt1	M-100	ps	M-136	w0	M-10
cgs1	M-107	imelt2	M-101	ps	I-5	warn	M-187
cgso	M-103	imelt3	M-102	pscbel	M-69	weff	O-1
cii	M-204	is	M-96	pscbel2	M-70	wint	M-34
cit	M-77	is	OP-38	pta	M-167	wl	M-40
cj	M-112	is1	M-208	ptc	M-166	wln	M-41
cjd	OP-18	is2	M-209	ptp	M-168	wmax	M-179
cjs	OP-19	is3	M-210	pvag	M-71	wmin	M-180
cjsw	M-116	isub	OP-8	pwr	OP-40	wr	M-54
cjswg	M-119	js	M-94	qgvd0	M-217	ww	M-42
ckappa	M-109	jsw	M-95	rbody	I-12	ww1	M-44

Spectre Circuit Simulator Reference

Component Statements Part 1

clc	M-127	k1	M-6	rd	M-86	wn	M-43
cle	M-128	k2	M-7	rdc	M-88	xj	M-32
csb	OP-31	k3	M-8	rdd	M-90	xl	M-55
csd	OP-29	k3b	M-9	rdeff	O-6	xpart	M-131
csg	OP-28	kb	M-212	rdsw	M-51	xt	M-50
css	OP-30	keta	M-28	region	OP-2	xti	M-162
cta	M-169	kf	M-172	region	I-10	xti1	M-163
cth0	M-200	knk	M-190	reversed	OP-3	xti2	M-164
ctp	M-170	kt1	M-149	ron	OP-36	xti3	M-165
dbg	M-196	kt11	M-150	rs	M-85	xw	M-56
delacc	M-213	kt2	M-151	rsc	M-87		
delr	M-214	l	M-133	rseff	O-5		
delta	M-72	l	I-2	rsh	M-84		

Two Terminal Capacitor (capacitor)

Description

You can assign the capacitance or let Spectre compute it from the physical length and width of the capacitor. In either case, the capacitance can be a function of temperature or applied voltage.

This device is supported within altergroups.

If the C(inst) is not given,

$$C(\text{inst}) = C(\text{model})$$

if C(model) is given, and

$$C(\text{inst}) = C_j \cdot (L - 2 \cdot \text{etch}) \cdot (W - 2 \cdot \text{etch}) + 2 \cdot C_{jsw} \cdot (W + L - 4 \cdot \text{etch})$$

if C(model) is not given.

If the polynomial coefficients vector (coeffs=[c1 c2 ...]) is specified, the capacitor is nonlinear and the capacitance is

$$\begin{aligned} C(V) &= dQ(V) / dV \\ &= C(\text{inst}) \cdot (1 + c_1 \cdot V + c_2 \cdot V^2 + \dots) \end{aligned}$$

or

$$Q(V) = C(\text{inst}) \cdot V \cdot (1 + 1/2 \cdot c_1 \cdot V + 1/3 \cdot c_2 \cdot V^2 + \dots)$$

Spectre Circuit Simulator Reference

Component Statements Part 1

where c_k is the k th entry in the coefficient vector.

The value of the capacitor as a function of the temperature is given by:

$$C(T) = C(tnom) * [1 + tc1 * (T - tnom) + tc2 * (T - tnom)^2].$$

where

$$T = trise(inst) + temp$$

if $trise(inst)$ is given, and

$$T = trise(model) + temp$$

if $trise(inst)$ is not given.

Sample Instance Statement

Without model:

```
c2 (1 0) capacitor c=2.5u w=2u l=2.5u tc1=1e-8
```

With model:

```
c2 (1 0) proc_cap c=2.5u w=2u l=2.5u tc1=1e-8
```

Sample Model Statement

```
model proc_cap capacitor c=2u tc1=1.2e-8 tnom=25 w=4u l=4u cjsw=2.4e-10
```

Instance Definition

```
Name 1 2 ModelName parameter=value ...
```

```
Name 1 2 capacitor parameter=value ...
```

Instance Parameters

1 c (F) Capacitance.

2 w (m) Capacitor width.

3 l (m) Capacitor length.

4 $m=1$ Multiplicity factor.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 5 `scale=1` Scale factor.
- 6 `trise (C)` Temperature rise from ambient.
- 7 `tc1 (1/C)` Linear temperature coefficient.
- 8 `tc2 (C-2)` Quadratic temperature coefficient.
- 9 `ic (V)` Initial condition.

The instance parameter `scale`, if specified, overrides the value given by the `option` parameter `scale`. The `w` and `l` parameters are scaled by the resulting `scale`, and the `option` parameter `scalem`. The values of `w` and `l` printed by Spectre are those given in the input file, and these values might not have the correct units if the scaling factors are not unity. The actual capacitor dimensions are stored in the output parameters. You can obtain these dimensions with the `info` statement.

Model Definition

```
model modelName capacitor parameter=value ...
```

Model Parameters

- 1 `c=0 F` Default capacitance.
- 2 `tc1=0 1/C` Linear temperature coefficient.
- 3 `tc2=0 C-2` Quadratic temperature coefficient.
- 4 `trise=0 C` Default `trise` value for instance.
- 5 `tnom (C)` Parameters measurement temperature. Default set by `options`.
- 6 `w=0 m` Default capacitor width.
- 7 `l=0 m` Default capacitor length.
- 8 `etch=0 m` Narrowing due to side etching.
- 9 `cj=0 F/m2` Bottom capacitance density.
- 10 `cjsw=0 F/m` Sidewall capacitance.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 11 `scalec=1` Capacitance scaling factor.
- 12 `coeffs=[...]` Vector of polynomial capacitance coefficients.
- 13 `rforce=1` Ω Resistance used when forcing initial conditions.

Output Parameters

- 1 `leff` (m) Effective capacitor length.
- 2 `weff` (m) Effective capacitor width.
- 3 `ceff` (F) Effective capacitance.

Operating-Point Parameters

- 1 `cap` (F) Capacitance at operating point.

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

<code>c</code> I-1	<code>etch</code> M-8	<code>scale</code> I-5	<code>trise</code> M-4
<code>c</code> M-1	<code>ic</code> I-9	<code>scalec</code> M-11	<code>trise</code> I-6
<code>cap</code> OP-1	<code>l</code> I-3	<code>tc1</code> M-2	<code>w</code> I-2
<code>ceff</code> O-3	<code>l</code> M-7	<code>tc1</code> I-7	<code>w</code> M-6
<code>cj</code> M-9	<code>leff</code> O-1	<code>tc2</code> I-8	<code>weff</code> O-2
<code>cjsw</code> M-10	<code>m</code> I-4	<code>tc2</code> M-3	
<code>coeffs</code> M-12	<code>rforce</code> M-13	<code>tnom</code> M-5	

Linear Current Controlled Current Source (cccs)

Description

A current-controlled source senses the current with a probe device. A valid probe is a component instance in the circuit that naturally computes current. For example, probes can be voltage sources (independent or controlled), inductors, transmission lines, microstrip lines, N-ports, and transformers. If the probe device computes more than one current (such as transmission lines, microstrip lines, and N-ports), the index of the probe port through which the controlling current flows needs to be specified. Positive current exits the source node and enters the sink node of the controlled source.

This device is supported within altergroups.

Sample Instance Statement

```
vcs (pos gnd) cccs gain=2.5 probe=v1 m=1 //Note that v1 is an instance of a voltage source
```

Instance Definition

```
Name sink src cccs parameter=value ...
```

Instance Parameters

- 1 `gain=0` A/A Current gain.
- 2 `m=1` Multiplicity factor.
- 3 `probeDevice` through which the controlling current flows.
- 4 `port=0` Index of the probe port through which the controlling current flows.

Operating-Point Parameters

- 1 `i` (A) Input current.
- 2 `v` (V) Output voltage.
- 3 `pwr` (W) Power dissipation.

Linear Current Controlled Voltage Source (ccvs)

Description

A current-controlled source senses the current with a probe device. A valid probe is a component instance in the circuit that naturally computes current. For example, probes can be voltage sources (independent or controlled), inductors, transmission lines, microstrip lines, N-ports, and transformers. If the probe device computes more than one current (such as transmission lines, microstrip lines, and N-ports), the index of the probe port through which the controlling current flows needs to be specified. Current through the controlled voltage source is calculated and is defined to be positive if it flows from the positive terminal, through the source, to the negative terminal.

This device is supported within altergroups.

Sample Instance Statement

```
vvs (pos gnd) ccvs rm=1 probe=v1 m=1 //Note that v1 is an instance of a voltage
source
```

Instance Definition

```
Name p n ccvs parameter=value ...
```

Instance Parameters

- 1 `rm=0` Ω Transresistance.
- 2 `probeDevice` through which the controlling current flows.
- 3 `port=0` Index of the probe port through which the controlling current flows.
- 4 `m=1` Multiplicity factor.

Operating-Point Parameters

- 1 `i` (A) Output current.
- 2 `v` (V) Output voltage.
- 3 `pwr` (W) Power dissipation.

Circuit Reduced Order Model (cktrom)

Description

The circuit reduced order model is described by a set of partial differential equations in the form of:

$$\mathbf{x} = \mathbf{A}\mathbf{x} + \mathbf{B}\mathbf{u} \quad (1)$$

$$\mathbf{y} = \mathbf{C}\mathbf{x} + \mathbf{D}\mathbf{u} \quad (2)$$

where Eqn.(1) is the state equation, Eqn.(2) is the output equation, A is nxn matrix, B is nxm, C is mxn, and D is an mxm matrix. x is a vector of state variables. Input u is a vector of voltages at all the ports and output y is a vector of electric current at all the ports. The number of inputs is always equal to the number of outputs. The order of the terminals in the input must be consistent with the matrix equations. In the input file, the matrices A, B, C and D are in the form of long vectors with row order.

This device is not supported within altergroup.

Sample Instance Statement

```
rom3 (net11 0) cktrom a=[ -2.022852e+14  2.583012e+13  9.553125e+13  9.627727e+13
1.533971e+13  9.987851e+13  4.592012e+13  -1.671024e+14  2.296589e+13  -2.719915e+14
7.668472e+12  -1.564519e+14  8.543123e+13  3.395689e+13  -3.863150e+14  -1.101618e+14
-5.415116e+14  -2.303841e+14  9.627728e+13  -1.711915e+14  -1.001818e+14  -
8.123120e+14  -2.272715e+14  -9.965181e+14  1.514961e+14  7.668372e+12  -6.415116e+14
-3.272715e+14  -2.852751e+15  -3.564466e+14  9.999851e+13  -1.761619e+14  -1.312841e+14
-8.967181e+14  -4.563456e+14  -4.068747e+15 ]
b=[ 3.366776e+06  5.932470e+05  -1.508475e+06  4.349182e+06  -3.128869e+06  -
2.995677e+06  -2.831481e+06  2.708942e+06  -4.968876e+06  -3.338945e+06  -3.278564e+06
3.925648e+06 ]
c=[ -3.111296e+06  1.593292e+06  3.324594e+06  3.083731e+06  5.887179e+06
3.766094e+06  -5.049263e+05  -4.275158e+06  3.035578e+06  -3.666385e+06  3.424639e+06
-3.832285e+06 ]
d=[ 1.254627e-01  0.000000e+00  0.000000e+00  1.236790e-01 ]
```

Instance Definition

```
Name  sink0  src0  [sink1]  [src1]  [sink2]  [src2]  [sink3]  [src3]  ...  cktrom
parameter=value ...
```

Instance Parameters

1 m=1 Multiplicity factor.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 2 `a=[. . .]` Coefficient matrix A of state equations.
- 3 `b=[. . .]` Coefficient Matrix B of state equations.
- 4 `c=[. . .]` Coefficient matrix C of output equations.
- 5 `d=[. . .]` Coefficient matrix D of output equations.

Operating-Point Parameters

- 1 `i=[. . .]` APort currents.
- 2 `v=[. . .]` VPort voltages.
- 3 `pwr` (W) Power dissipation.

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

<code>a</code>	I-2	<code>c</code>	I-4	<code>i</code>	OP-1	<code>pwr</code>	OP-3
<code>b</code>	I-3	<code>d</code>	I-5	<code>m</code>	I-1	<code>v</code>	OP-2

Magnetic Core with Hysteresis (core)

Description

This component models the magnetic hysteresis, with air gap, frequency, and temperature effects. The model is based on the AWB model for magnetic cores and windings. The user has to specify the cores material and geometric parameters to model the hysteresis.

The material parameters to specify are the B_r , B_m and H_c of the core. The geometric parameters are the area, magnetic path length and the air gap of the core.

You can specify the magnetic path length in one of the following ways:

Spectre Circuit Simulator Reference

Component Statements Part 1

- Give the length directly in cm.
- Or give the outer and inner diameter of the core.

Cores without terminals represent complete magnetic loops. Cores with terminals are fragments that you can use as building blocks to build models of complicated core structures. For example, you can use the following set of core fragments to model an E core:

```
W1  elp  elm  winding turns=80 core=C1
W3a e2p  e2c  winding turns=80 core=C3
W3b e2c  e2m  winding turns=80 core=C3
C1  m1    0    permalloy area=1 len=2
C2  m1    0    permalloy area=2 len=2
C3  m1    0    permalloy area=1 len=2
model permalloy core ...
```

There are three parallel core fragments representing each of the three fingers on the E. One 80 turn winding is connected to core fragment C1. A center-tapped 160 turn winding (implemented as a pair of windings) are wrapped around core fragment C3. Node m1 is a magnetic node whose value is in magnetomotive force and flow is flux.

You can calculate the frequency and temperature dependency of the core model by specifying the frequency loss parameters and the temperature effects parameters. You can make all the core parameters vary in temperature, including the permeability, saturation flux, and core loss. For frequency losses, a static model refers to a value that you type in for frequency. This model does not adjust the shape of the B-H loop in response to power dissipation or rate of rise of the applied currents and voltages during transient analysis.

This device is not supported within altergroup.

The hysteresis is modeled by different regions whose equations are:

$\phi = \phi_{ir} + (\phi_{is} - \phi_{ir}) F / (F + H_a)$ for region number 1

$\phi = \phi_{is} * (F - F_c) / (F - H_b)$ for region number 2

where

ϕ = flux density

F = magnetomotive force

ϕ_{ir} = residual flux density

ϕ_{is} = Saturated flux density

F_c = Coercive magnetic force

Spectre Circuit Simulator Reference

Component Statements Part 1

Ha and Hb = shape parameters.

Sample Instance Statement

```
c1 (1 0) core_mod area=1.2 len=8.1 id=0.55 gap=0.25
```

Sample Model Statement

```
model core_mod core len=7.7 area=0.85 br=1e3 bm=5e3 hc_tl=0.2 p1_f1=2.08 f1=10e3  
p2_f2=50 f2=100K bflux=1e3 density=4.75
```

Instance Definition

Name ... ModelName parameter=value ...

Instance Parameters

- 1 area (cm²) Effective magnetic cross-sectional area of core.
- 2 len (cm) Effective length of magnetic path.
- 3 id (cm) Inner diameter of toroidal core.
- 4 od (cm) Outer diameter of toroidal core.
- 5 gap (cm) Gap length.
- 6 m=1 Multiplicity factor.

Model Definition

```
model modelName core parameter=value ...
```

Model Parameters

- 1 br=1 gauss Residual flux density.
- 2 bm=1 gauss Saturation flux density.
- 3 hc=1 oersteds Coercive magnetizing force (value of H where B equals 0).
- 4 area=1 cm² Effective magnetic cross-sectional area of core.

Spectre Circuit Simulator Reference

Component Statements Part 1

5 `len=1 cm`Effective length of magnetic path.

6 `id (cm)`Inner diameter of toroidal core.

7 `od (cm)`Outer diameter of toroidal core.

8 `gap=0.0 cm`Gap length.

Initial Conditions

9 `b0 (gauss)`Initial condition for core.

Frequency Loss Parameters

10 `freq (Hz)`Core operating frequency.

11 `p1_f1 (W/Kg)`Core power loss at frequency `f1`.

12 `f1 (Hz)`Reference frequency for power loss.

13 `p2_f2 (W/Kg)`Core power loss at frequency `f2`.

14 `f2 (Hz)`Reference frequency for power loss.

15 `bflux (gauss)`Reference flux density.

16 `density (g/cm3)`Core density.

Temperature Effects Parameters

17 `temp (C)`Core operating temperature.

18 `bm_t1 (gauss)`Saturated flux density B_m at T_1 .

19 `br_t1 (gauss)`Residual flux density B_r at T_1 .

20 `hc_t1 (oersteds)`Coercive force H_c at T_1 .

21 `t1 (C)`Reference temperature.

Spectre Circuit Simulator Reference

Component Statements Part 1

Operating-Point Parameters

- 1 `b (gauss)` Flux density of the core.
- 2 `h (oersteds)` Magnetic field strength.

Parameter Index

In the following index, `I` refers to instance parameters, `M` refers to the model parameters section, `O` refers to the output parameters section, and `OP` refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of `M-35` means the 35th model parameter.

<code>area</code>	<code>I-1</code>	<code>br_t1</code>	<code>M-19</code>	<code>hc</code>	<code>M-3</code>	<code>od</code>	<code>I-4</code>
<code>area</code>	<code>M-4</code>	<code>density</code>	<code>M-16</code>	<code>hc_t1</code>	<code>M-20</code>	<code>p1_f1</code>	<code>M-11</code>
<code>b</code>	<code>OP-1</code>	<code>f1</code>	<code>M-12</code>	<code>id</code>	<code>I-3</code>	<code>p2_f2</code>	<code>M-13</code>
<code>b0</code>	<code>M-9</code>	<code>f2</code>	<code>M-14</code>	<code>id</code>	<code>M-6</code>	<code>t1</code>	<code>M-21</code>
<code>bflux</code>	<code>M-15</code>	<code>freq</code>	<code>M-10</code>	<code>len</code>	<code>M-5</code>	<code>temp</code>	<code>M-17</code>
<code>bm</code>	<code>M-2</code>	<code>gap</code>	<code>I-5</code>	<code>len</code>	<code>I-2</code>		
<code>bm_t1</code>	<code>M-18</code>	<code>gap</code>	<code>M-8</code>	<code>m</code>	<code>I-6</code>		
<code>br</code>	<code>M-1</code>	<code>h</code>	<code>OP-2</code>	<code>od</code>	<code>M-7</code>		

Logic-to-Analog Converter (d2a)

Description

The logic-to-analog converter converts a binary signal from a logic simulator to an analog waveform.

This device is not supported within altergroup.

Sample Instance Statement

```
d2a_1 (net1 net2) d2a src="99991" val0=0 val1=2.5 valx=1.25 rise=200p fall=200p m=2
//99991 is an analog net
```

Instance Definition

```
Name p n d2a parameter=value ...
```

Spectre Circuit Simulator Reference

Component Statements Part 1

Instance Parameters

- 1 `src` The foreign simulator's name for the source of the analog signal.
- 2 `nestlev=0` Number of nesting levels to ignore in the hierarchical name. This should be used to skip over extra levels that do not exist in the co-simulator.
- 3 `val0=0` `v` Final value for logical 0.
- 4 `val1=5` `v` Final value for logical 1.
- 5 `valx (V)` Final value for logical X.
- 6 `valz (V)` Final value for logical Z.
- 7 `rise=1ns` `s` Time for transition from `val0` to `val1`.
- 8 `fall=1ns` `s` Time for transition from `val1` to `val0`.
- 9 `ron=100` Ω Output resistance when in active state.
- 10 `m=1` Multiplicity factor.

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

<code>fall</code>	I-8	<code>rise</code>	I-7	<code>val0</code>	I-3	<code>valz</code>	I-6
<code>m</code>	I-10	<code>ron</code>	I-9	<code>val1</code>	I-4		
<code>nestlev</code>	I-2	<code>src</code>	I-1	<code>valx</code>	I-5		

Delay Line (delay)

Description

The delay line model is a four terminal device with zero output impedance and infinite input impedance. The output between nodes *p* and *n* is the input voltage between nodes *ps* and *ns* delayed by the time delay *td* and scaled by *gain*.

This device is not supported within altergroup.

Sample Instance Statement

```
dll(outp outn cntrlp cntrln) delay td=10n gain=1.5
```

Instance Definition

```
Name p n ps ns delay parameter=value ...
```

Instance Parameters

- 1 *td*=0.0 *s* Time delay.
- 2 *gain*=1 Gain parameter.
- 3 *m*=1 Multiplicity factor.

Operating-Point Parameters

- 1 *v* (V) Output voltage.

Diode Level 500 (dio500)

Description

The *dio500* model provides a detailed description of the diode currents in forward and reverse biased Si-diodes. It is described in the Philips Bipolar Modelbook (Dec.93) as *Diode level 500*. Information on how to obtain this document can be found on Source Link by searching for Philips.

Spectre Circuit Simulator Reference

Component Statements Part 1

(c) Philips Electronics N.V. 1994

In extension to the modelbook description a minimum conductance `gmin` is inserted between the diode nodes to aid convergence. The value of `gmin` is set by an options statement, default is `gmin = 1.0e-12 S`.

The `imax` parameter is used to aid convergence and to prevent numerical overflow. The junction characteristics of the diode are accurately modeled for currents up to `imax`. For currents above `imax`, the junction is modeled as a linear resistor, and a warning is printed.

This device is supported within altergroups.

This device is dynamically loaded from the shared object `/vobs/spectre_dev/tools.sun4v/spectre/lib/cmi/2.0.doc/libphilips.so`

Sample Instance Statement

```
d1 (pnode 0) phdiode area=2
```

Sample Model Statement

```
model phdiode dio500 is=3.5e-12 rs=26.3 n=2.7 imax=1e20 vlc=1.8 vbr=9.63 cj=2.65e-11  
dta=12.88 tau=7.5e-10 tnom=25
```

Instance Definition

Name `a k` ModelName parameter=value ...

Instance Parameters

- 1 `area=1.0`Multiplication factor.
- 2 `mult`Alias of area factor.
- 3 `m=1.0`Multiplicity factor.
- 4 `region=fwd`Estimated DC operating region, used as a convergence aid.
Possible values are `fwd`, `rev` or `brk`.

Model Definition

model modelName dio500 parameter=value ...

Model Parameters

- 1 $i_s=7.13e-13$ A Saturation current.
- 2 $n=1.044$ Junction emission coefficient.
- 3 $v_{lc}=0.0$ V Voltage dependence at low forward currents.
- 4 $v_{br}=7.459$ V Breakdown voltage.
- 5 $emv_{br}=1.36e+06$ V/cm
Electric field at breakdown.
- 6 $csr_h=7.44e-07$ A/cm
Shockley-Read-Hall generation.
- 7 $c_{bbt}=3.255$ A/V Band to band tunneling.
- 8 $ctat=3.31e-06$ A/cm
Trap assisted tunneling.
- 9 $r_s=0.0$ Ω Series resistance.
- 10 $\tau=500.0e-12$ s Transit time.
- 11 $c_j=7.0e-12$ F Zero-bias depletion capacitance.
- 12 $v_d=0.9$ V Diffusion voltage.
- 13 $p=0.4$ Grading coefficient.
- 14 t_{ref} (C) Reference temperature. Default set by option t_{nom} .
- 15 t_{nom} (C) Alias of t_{ref} .
- 16 t_r (C) Alias of t_{ref} .
- 17 $v_g=1.206$ V Bandgap voltage.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 18 `ptrs`=0.0 Power for temperature dependence of `rs`.
- 19 `kf`=0.0 Flicker noise coefficient.
- 20 `af`=1.0 Flicker noise exponent.
- 21 `dta`=0.0 K Difference between device temperature and ambient temperature.
- 22 `trise` (K) Alias of `dta`.
- 23 `imax`=1.0 A Explosion current.

Operating-Point Parameters

- 1 `vak` (V) Diode voltage, measured from anode to cathode (including `rs`).
- 2 `id` (A) Total resistive diode current.
- 3 `qd` (Coul) Diffusion charge.
- 4 `qt` (Coul) Depletion charge.
- 5 `rst` (Ω) Series resistance (temperature updated).
- 6 `rl` (Ω) AC linearized resistance.
- 7 `cl` (F) AC linearized capacitance.
- 8 `pwr` (W) Power dissipation.

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

<code>a_f</code>	M-20	<code>i_d</code>	OP-2	<code>p_{wr}</code>	OP-8	<code>tr</code>	M-16
<code>area</code>	I-1	<code>i_{max}</code>	M-23	<code>q_d</code>	OP-3	<code>t_{ref}</code>	M-14
<code>cbbt</code>	M-7	<code>i_s</code>	M-1	<code>q_t</code>	OP-4	<code>trise</code>	M-22

Spectre Circuit Simulator Reference

Component Statements Part 1

cj	M-11	kf	M-19	region	I-4	vak	OP-1
cl	OP-7	m	I-3	rl	OP-6	vbr	M-4
csr	M-6	mult	I-2	rs	M-9	vd	M-12
ctat	M-8	n	M-2	rst	OP-5	vg	M-17
dta	M-21	p	M-13	tau	M-10	vlc	M-3
emvbr	M-5	ptrs	M-18	tnom	M-15		

Junction Diode (diode)

Description

The junction diode model includes nonlinear junction capacitance and reverse breakdown.

This device is supported within altergroups.

To examine the equations used for this component, consult the [Spectre Circuit Simulator Device Model Equations](#) manual.

Sample Instance Statement

```
d0 (dp dn) pdiode l=3e-4 w=2.5e-4 area=1
```

Sample Model Statement

```
model pdiode diode is=1.8e-5 rs=1.43 n=1.22 nz=2.31 gleak=6.2e-5 rsw=10 isw=6.1e-10  
ibv=0.95e-3 tgs=2 ik=1.2e7 fc=0.5 cj=1.43e-3 pb=0.967 mj=0.337 cjsw=2.76e-9  
vjsw=0.94 jmax=1e20
```

Instance Definition

Name a c modelName parameter=value ...

In forward operation, the voltage on the anode (a) is more positive than the voltage on the cathode (c).

Instance Parameters

- 1 areaJunction area factor.
- 2 perimJunction perimeter factor.
- 3 l=1e-6 mDrawn length of junction.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 4 `w=1e-6` `m`Drawn width of junction.
- 5 `m=1` Multiplicity factor.
- 6 `scale=1` Scale factor.
- 7 `region=on` Estimated operating region.
Possible values are `off`, `on` or `breakdown`.
- 8 `trise (C)` Temperature rise from ambient.

The instance parameter `scale`, if specified, overrides the value given by the option parameter `scale`. If the model parameter `allow_scaling` is set to `yes` then, the `area`, `perim`, `l` and `w` parameters are scaled by `scale`. By default `allow_scaling` is set to `no` and no scaling of geometry parameters will occur. The values of `area`, `perim`, `l` and `w` printed out by spectre are those given in the input, and these values might not have the correct units if the scaling factors are not unity.

Model Definition

```
model modelName diode parameter=value ...
```

Model Parameters

Model selector parameters

- 1 `level=1` Model selector. 1 = junction and 2 = Fowler-Nordheim.

Process parameters

- 2 `etch=0` `m`Narrowing due to etching per side.
- 3 `etchl=etch` `m`Length reduction due to etching per side.

Junction diode parameters

- 4 `js=1e-14` `A`Saturation current (*area).
- 5 `jsw=0` `A`Sidewall saturation current (*perim).
- 6 `n=1` Emission coefficient.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 7 `ns=1` Sidewall emission coefficient.
- 8 `ik= ∞` A High-level injection knee current (*area).
- 9 `ikp=ik` A High-level injection knee current for sidewall (*area).
- 10 `area=1` Junction area factor.
- 11 `perim=0` Junction perimeter factor.
- 12 `allow_scaling=no` Allow scale option and instance scale parameter to affect diode instance geometry parameters.
Possible values are `no` or `yes`.

Capacitive parameters

- 13 `tt=0` s Transit time.
- 14 `cd=0` F Linear capacitance (*area).
- 15 `cjo=0` F Zero-bias junction capacitance (*area).
- 16 `vj=1` V Junction potential.
- 17 `m=0.5` Grading coefficient.
- 18 `cjsw=0` F Zero-bias sidewall junction capacitance (*perim).
- 19 `vjsw=1` V Sidewall junction potential.
- 20 `mjsw=0.33` Sidewall grading coefficient.
- 21 `fc=0.5` Forward-bias depletion capacitance threshold.

Breakdown parameters

- 22 `bv= ∞` V Reverse breakdown voltage. Note: `bv=0` is not the same as `bv=infinity`.
- 23 `ibv=0.001` A Current at breakdown voltage (*area).
- 24 `nz=1` Emission coefficient for Zener diode.

Spectre Circuit Simulator Reference

Component Statements Part 1

25 `bvj= ∞` `v` Voltage at which junction breakdown warning is issued.

Parasitic resistance parameters

26 `rs=0` Ω Series resistance (/area).

27 `rsw=0` Ω Sidewall series resistance (/perim).

28 `gleak=0` `s` Bottom junction leakage conductance (*area).

29 `gleaksw=0` `s` Sidewall junction leakage conductance (*perim).

30 `minr=0.1` Ω Minimum series resistance.

Temperature effects parameters

31 `tlev=0` DC temperature selector.

32 `tlevc=0` AC temperature selector.

33 `eg=1.124481` `v` Band gap. Note: when not specified, the default value is temperature dependent. It is 1.124481 at temp=27C.

34 `gap1=7.02e-4` `v/C` Band gap temperature coefficient.

35 `gap2=1108` `c` Band gap temperature offset.

36 `xti=3` Saturation current temperature exponent.

37 `tbv1=0` `1/C` Linear temperature coefficient for `bv`.

38 `tbv2=0` `C-2` Quadratic temperature coefficient for `bv`.

39 `tnom (C)` Parameters measurement temperature. Default set by `options`.

40 `trise=0` `c` Temperature rise from ambient.

41 `trs=0` `1/C` Linear temperature coefficient for parasitic resistance.

42 `trs2=0` `C-2` Quadratic temperature coefficient for parasitic resistance.

43 `tgs=0` `1/C` Linear temperature coefficient for leakage conductance.

Spectre Circuit Simulator Reference

Component Statements Part 1

44 `tgs2=0` C^{-2} Quadratic temperature coefficient for leakage conductance.

45 `cta=0` $1/C$ Junction capacitance temperature coefficient.

46 `ctp=0` $1/C$ Sidewall junction capacitance temperature coefficient.

47 `pta=0` V/C Junction potential temperature coefficient.

48 `ptp=0` V/C Sidewall junction potential temperature coefficient.

Junction diode model control parameters

49 `jmelt=jmax` A Explosion current (*area).

50 `jmax=1` A Maximum allowable current (*area).

51 `dskip=yes` Use simple piece-wise linear model for diode currents below $0.1 \cdot i_{abstol}$.
Possible values are `no` or `yes`.

Fowler-Nordheim diode parameters

52 `if=1e-10` A/V^{nf} Forward Fowler-Nordheim current coefficient (*area).

53 `ir=if` A/V^{nr} Reverse Fowler-Nordheim current coefficient (*area).

54 `ecrf=2.55e10` V/m Forward critical field.

55 `ecrr=ecrf` V/m Reverse critical field.

56 `nf=2` Forward voltage power.

57 `nr=nf` Reverse voltage power.

58 `tox=1e-8` m Thickness of insulating layer.

Noise model parameters

59 `kf=0` Flicker noise (1/f) coefficient.

60 `af=1` Flicker noise (1/f) exponent.

Spectre Circuit Simulator Reference

Component Statements Part 1

Both of these parameters have current density counterparts, `jmax` and `jmelt`, that you can specify if you want the absolute current values to depend on the device area.

Operating-Point Parameters

- 1 `region=on` Estimated operating region.
Possible values are `off`, `on` or `breakdown`.
- 2 `v` (V) Extrinsic diode voltage.
- 3 `i` (A) Resistive diode current.
- 4 `pwr` (W) Power dissipation.
- 5 `res` (Ω) Resistance of intrinsic diode.
- 6 `cap` (F) Junction capacitance.
- 7 `resp` (Ω) Resistance of intrinsic sidewall diode.
- 8 `capp` (F) Sidewall junction capacitance.

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

<code>af</code>	M-60	<code>fc</code>	M-21	<code>m</code>	I-5	<code>scale</code>	I-6
<code>allow_scaling</code>	M-12	<code>gap1</code>	M-34	<code>minr</code>	M-30	<code>tbv1</code>	M-37
<code>area</code>	I-1	<code>gap2</code>	M-35	<code>mjsw</code>	M-20	<code>tbv2</code>	M-38
<code>area</code>	M-10	<code>gleak</code>	M-28	<code>n</code>	M-6	<code>tgs</code>	M-43
<code>bv</code>	M-22	<code>gleaksw</code>	M-29	<code>nf</code>	M-56	<code>tgs2</code>	M-44
<code>bvj</code>	M-25	<code>i</code>	OP-3	<code>nr</code>	M-57	<code>tlev</code>	M-31
<code>cap</code>	OP-6	<code>ibv</code>	M-23	<code>ns</code>	M-7	<code>tlevc</code>	M-32
<code>capp</code>	OP-8	<code>if</code>	M-52	<code>nz</code>	M-24	<code>tnom</code>	M-39
<code>cd</code>	M-14	<code>ik</code>	M-8	<code>perim</code>	M-11	<code>tox</code>	M-58
<code>cjo</code>	M-15	<code>ikp</code>	M-9	<code>perim</code>	I-2	<code>trise</code>	I-8
<code>cjsw</code>	M-18	<code>ir</code>	M-53	<code>pta</code>	M-47	<code>trise</code>	M-40
<code>cta</code>	M-45	<code>jmax</code>	M-50	<code>ptp</code>	M-48	<code>trs</code>	M-41

Spectre Circuit Simulator Reference

Component Statements Part 1

ctp	M-46	jmelt	M-49	pwr	OP-4	trs2	M-42
dskip	M-51	js	M-4	region	OP-1	tt	M-13
ecrf	M-54	jsw	M-5	region	I-7	v	OP-2
ecrr	M-55	kf	M-59	res	OP-5	vj	M-16
eg	M-33	l	I-3	resp	OP-7	vjsw	M-19
etch	M-2	level	M-1	rs	M-26	w	I-4
etchl	M-3	m	M-17	rsw	M-27	xti	M-36

EKV MOSFET Transistor (ekv)

Description

The EPFL-EKV mosfet model was developed by Electronics Laboratories, Swiss Federal Institute of Technology (EPFL), Switzerland. The detailed description of the model and equations can be found in the [Spectre Circuit Simulator Device Model Equations](#) manual. EKV transistors require that you use a model statement.

This device is supported within altergroups.

Sample Instance Statement

```
mn1 (dn gn sn 0) ekvnmos w=1.5u l=1u ad=2.6p as=2.6p pd=6.6p ps=6.6p nrd=1.54  
nrs=1.54
```

Sample Model Statement

```
model ekvnmos ekv type=n update=2.6 xqc=0.4 cox=3.4e-3 xj=0.145e-6 vto=0.6  
gamma=0.71 phi=0.967 kp=155e-6 e0=88e6 iba=200e6 ibb=350e6 tnom=25 tcv=1.55e-3  
bex=-1.45 kf=1e-27 af=1 hdif=0.94e-6 rsh=512 jsw=1.5e-10
```

Instance Definition

Name d g s b ModelName parameter=value ...

Instance Parameters

- 1 w (m) Channel width.
- 2 l (m) Channel length.
- 3 as (m²) Area of source diffusion.
- 4 ad (m²) Area of drain diffusion.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 5 `ps (m)` Perimeter of source diffusion.
- 6 `pd (m)` Perimeter of drain diffusion.
- 7 `nrd (m/m)` Number of squares of drain diffusion.
- 8 `nrs (m/m)` Number of squares of source diffusion.
- 9 `rdc (Ω)` Drain contact resistance.
- 10 `rsc (Ω)` Source contact resistance.
- 11 `m=1` Multiplicity factor (number of MOSFETs in parallel).
- 12 `ns=1` Series Multiplicity factor (number of MOSFETs in series).
- 13 `region=triode` Estimated operating region.
Possible values are `off`, `triode`, `sat`, or `subth`.
- 14 `trise` Temperature rise from ambient.

Model Definition

`model modelName ekv parameter=value ...`

Model Parameters

Device type parameters

- 1 `type=n` Transistor type.
Possible values are `n` or `p`.

Process parameters

- 2 `tox=2e-8 m` Gate oxide thickness.
- 3 `cox=7e-4 F/m2` Gate oxide capacitance. (Overrides `Tox`).
- 4 `xj=1.0e-7 m` Metallurgical junction depth.
- 5 `dw=0 m` Channel Width Correction.

Spectre Circuit Simulator Reference

Component Statements Part 1

6 `dl=0` `mChannel Length Correction.`

7 `nfs=0` cm^{-2} Fast surface state density.

8 `nsub=1.13e16` cm^{-3}
Channel doping concentration.

Drain current model parameters

9 `vto=0.5` VThreshold voltage at zero body bias.

10 `gamma=1.0` $\sqrt{\text{V}}$ Body-effect parameter.

11 `phi=0.7` VSurface potential at strong inversion.

12 `kp=5.0e-5` A/V^2 Transconductance parameter.

13 `e0=1.0e12` V/mVertical Critical Field.

14 `ucrit=2.0e6` V/cmLongitudinal Critical field for mobility degradation.

15 `theta=0.0` $1/\text{V}$ Mobility reduction coefficient.

16 `uo` ($\text{cm}^2/\text{V s}$)Carrier surface mobility.

17 `vmax` (m/s)Carrier saturation velocity.

18 `vfb` (V)Flat-band voltage.

19 `lambda=0.5`Channel length modulation parameter.

20 `weta=0.25`Narrow Channel Effect Coefficient.

21 `leta=0.1`Short Channel Effect Coefficient.

22 `xw=0` `m`Width variation due to masking and etching.

23 `xl=0` `m`Length variation due to masking and etching.

24 `meto=0` `m`Metal overlap in fringing field.

Spectre Circuit Simulator Reference

Component Statements Part 1

Impact ionization parameters

- 25 `iba=0 1/m` First Impact Ionization Coefficient.
- 26 `ibb=3.0e8 V/m` Second Impact Ionization Coefficient.
- 27 `ibc=0` Third Impact Ionization Coefficient.
- 28 `ibn=1.0` Saturation velocity factor for impact ionization.

Reverse Short Channel parameters

- 29 `q0=0 A s/m2` Reverse short channel peak charge density.
- 30 `lk=2.9e-7 m` Reverse short channel characteristic length.

Charge model selection parameters

- 31 `xqc=0.0` Drain/source channel charge partition in saturation for charge models, e.g. use 0.4 for 40/60, 0.5 for 50/50, 0 for 0/100.

Junction diode model parameters

- 32 `is=1e-14 A` Bulk junction reverse saturation current.
- 33 `js (A/m2)` Bulk junction reverse saturation current density.
- 34 `jsw=0 A/m` Bulk junction reverse saturation sidewall current density.
- 35 `n=1` Junction emission coefficient.
- 36 `dskip=yes` Use simple piece-wise linear model for diode currents below $0.1 \cdot i_{abstol}$. Possible values are `no` or `yes`.
- 37 `imelt='imaxA'` Explosion current, diode is linearized beyond this current to aid convergence.

Junction capacitance model parameters

- 38 `cbd=0 F` Bulk-drain zero-bias p-n capacitance.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 39 $c_{bs}=0$ F Bulk-source zero-bias p-n capacitance.
- 40 $c_j=0$ F/m² Zero-bias junction bottom capacitance density.
- 41 $c_{jsw}=0$ F/m Zero-bias junction sidewall capacitance density.
- 42 $m_j=0.5$ Bulk junction bottom grading coefficient.
- 43 $m_{jsw}=0.33$ Bulk junction sidewall grading coefficient.
- 44 $c_{jswg}=0$ F/m Gate-side zero-bias junction sidewall capacitance density.
- 45 $m_{jswg}=0.33$ Gate-side bulk junction sidewall grading coefficient.
- 46 $p_{bswg}=0.8$ V Gate-side junction built-in potential.
- 47 $f_c=0.5$ Forward-bias capacitance coefficient.
- 48 $p_b=0.8$ V Bulk p-n bottom contact potential.
- 49 $p_{bsw}=0.8$ V Side-wall contact potential.
- 50 $t_t=0.0$ V Bulk p-n transit time.
- 51 $f_{csw}=0.5$ Side-wall forward-bias depletion capacitance threshold.

Overlap capacitance parameters

- 52 $c_{gso}=0$ F/m Gate-source overlap capacitance.
- 53 $c_{gdo}=0$ F/m Gate-drain overlap capacitance.
- 54 $c_{gbo}=0$ F/m Gate-bulk overlap capacitance.

Parasitic resistance parameters

- 55 $r_s=0$ Ω Source resistance.
- 56 $r_d=0$ Ω Drain resistance.
- 57 $r_{sh}=0$ Ω/sqr Source/drain diffusion sheet resistance.

Spectre Circuit Simulator Reference

Component Statements Part 1

58 `rss=0` Ω mScalable source resistance.

59 `rdd=0` Ω mScalable drain resistance.

60 `rsc=0` Ω Source contact resistance.

61 `rdc=0` Ω Drain contact resistance.

62 `minr=0.1` Ω Minimum source/drain resistance.

63 `ldif=0` mLateral diffusion beyond the gate.

64 `hdif=0` mLength of heavily doped diffusion.

Short distance matching parameters

65 `avto=0` V mArea related threshold voltage mismatch parameter.

66 `akp=0` mArea related gain mismatch parameter.

67 `agamma=0` \sqrt{V} m
Area related body effect mismatch parameter.

Operating region warning control parameters

68 `alarm=none`Forbidden operating region.
Possible values are none, off, triode, sat, subth, or rev.

69 `imax=1` AMaximum current, currents above this limit generate a warning.

70 `jmax=1e8` A/m²Maximum current density, currents above this limit generate a warning.

71 `vbox=1e9` `tox` VOxide breakdown voltage.

72 `bvj= ∞` VJunction reverse breakdown voltage.

Temperature effects parameters

73 `tnom` (C)Parameters measurement temperature. Default set by `options`.

74 `trise=0` CTemperature rise from ambient.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 75 $t_{cv}=1.0e-3$ V/C Threshold voltage temperature coefficient.
- 76 $b_{ex}=-1.5$ Mobility temperature exponent.
- 77 $u_{cex}=0.8$ Longitudinal critical field temp. exponent.
- 78 $i_{bbt}=9.0e-4$ 1/C Temperature coefficient for IBB.
- 79 $x_{ti}=3$ Saturation current temperature exponent.
- 80 $t_{lev}=0$ DC temperature selector.
- 81 $t_{levc}=0$ AC temperature selector.
- 82 $eg=1.12452$ V Energy band gap.
- 83 $gap1=7.02e-4$ V/C Band gap temperature coefficient.
- 84 $gap2=1108$ C Band gap temperature offset.
- 85 $tr1=0.6$ First source-drain resistance temperature coefficient.
- 86 $tr2=0.6$ Second source-drain resistance temperature coefficient.
- 87 $p_{tc}=0$ V/C Surface potential temperature coefficient.
- 88 $p_{ta}=0$ V/C Junction potential temperature coefficient.
- 89 $p_{tp}=0$ V/C Sidewall junction potential temperature coefficient.
- 90 $c_{ta}=0$ 1/C Junction capacitance temperature coefficient.
- 91 $c_{tp}=0$ 1/C Sidewall junction capacitance temperature coefficient.

Default instance parameters

- 92 $w=3e-6$ m Default channel width.
- 93 $l=3e-6$ m Default channel length.
- 94 $a_s=0$ m² Default area of source diffusion.
- 95 $a_d=0$ m² Default area of drain diffusion.

Spectre Circuit Simulator Reference

Component Statements Part 1

96 `ps=0` `m`Default perimeter of source diffusion.

97 `pd=0` `m`Default perimeter of drain diffusion.

98 `nrd=0` `m/m`Default number of squares of drain diffusion.

99 `nrs=0` `m/m`Default number of squares of source diffusion.

Noise model parameters

100 `noisemod=1` Noise model selector.

101 `kf=0` Flicker (1/f) noise coefficient.

102 `af=1` Flicker (1/f) noise exponent.

103 `ef=1` Flicker (1/f) noise frequency exponent.

Model selection parameters

104 `nqs=0` Nonquasi-static flag.

105 `satlim=exp(4)` Ratio defining saturation limit.

106 `ekvint=0.0` Interpolation function selector.

107 `scalem=1.0` Model scaling factor.

108 `update=2.6` Model version selector.

Auto Model Selector parameters

109 `wmax=1.0` `m`Maximum channel width for which the model is valid.

110 `wmin=0.0` `m`Minimum channel width for which the model is valid.

111 `lmax=1.0` `m`Maximum channel length for which the model is valid.

112 `lmin=0.0` `m`Minimum channel length for which the model is valid.

Spectre Circuit Simulator Reference

Component Statements Part 1

Imax and Imelt

The `imax` parameter aids convergence and prevents numerical overflow. The junction characteristics of the device are accurately modeled for current up to `imax`. If `imax` is exceeded during iterations, the linear model is substituted until the current drops below `imax` or until convergence is achieved. If convergence is achieved with the current exceeding `imax`, the results are inaccurate, and Spectre prints a warning.

A separate model parameter, `imelt`, is used as a limit warning for the junction current. This parameter can be set to the maximum current rating of the device. When any component of the junction current exceeds `imelt`, note that base and collector currents are composed of many exponential terms, Spectre issues a warning and the results become inaccurate. The junction current is linearized above the value of `imelt` to prevent arithmetic exception, with the exponential term replaced by a linear equation at `imelt`.

Both of these parameters have current density counterparts, `jmax` and `jimelt`, that you can specify if you want the absolute current values to depend on the device area.

Auto Model Selection

Many models need to be characterized for different geometries in order to obtain accurate results for model development. The model selector program automatically searches for a model with the length and width range specified in the instance statement and uses this model in the simulations.

For the auto model selector program to find a specific model, the models to be searched should be grouped together within braces. Such a group is called a model group. An opening brace is required at the end of the line defining each model group. Every model in the group is given a name followed by a colon and the list of parameters. Also, the four geometric parameters `lmax`, `lmin`, `wmax`, and `wmin` should be given. The selection criteria to choose a model is as follows:

```
lmin <= inst_length < lmax  and  wmin <= inst_width  < wmax
```

Example

```
model ModelName ModelType {
  1:    <model parameters> lmin=2 lmax=4 wmin=1 wmax=2
  2:    <model parameters> lmin=1 lmax=2 wmin=2 wmax=4
  3:    <model parameters> lmin=2 lmax=4 wmin=4 wmax=6
}
```

Then for a given instance

```
M1 1 2 3 4 ModelName w=3 l=1.5
```

Spectre Circuit Simulator Reference

Component Statements Part 1

the program would search all the models in the model group with the name `ModelName` and then pick the first model whose geometric range satisfies the selection criteria. In the preceding example, the auto model selector program would choose `ModelName.2`.

The user must specify both length (l) and width (w) on the device instance line to enable automatic model selection.

Output Parameters

- 1 `weff` (m) Effective channel width.
- 2 `leff` (m) Effective channel length.
- 3 `rseff` (Ω) Effective source resistance.
- 4 `rdeff` (Ω) Effective drain resistance.

Operating-Point Parameters

- 1 `type=n` Transistor type.
Possible values are n or p.
- 2 `region=triode` Estimated operating region.
Possible values are off, triode, sat, or subth.
- 3 `reversed` Reverse mode indicator.
Possible values are no or yes.
- 4 `ids` (A) Resistive drain-to-source current.
- 5 `vgs` (V) Gate-source voltage.
- 6 `vds` (V) Drain-source voltage.
- 7 `vbs` (V) Bulk-source voltage.
- 8 `vp` (V) Pinchoff voltage.
- 9 `vth` (V) Threshold voltage.
- 10 `vdss` (V) Drain-source saturation voltage.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 11 `gm (S)` Common-source transconductance.
- 12 `gds (S)` Common-source output conductance.
- 13 `gmbs (S)` Body-transconductance.
- 14 `nfac` Slope factor.
- 15 `if (A)` Forward current.
- 16 `ir (A)` Reverse current.
- 17 `irprime (A)` Reverse current.
- 18 `isub (A)` Substrate Current.
- 19 `ibd (A)` Bulk-drain junction current.
- 20 `ibs (A)` Bulk-source junction current.
- 21 `pwr (W)` Power at op point.
- 22 `gmoverid (1/V)` G_m/I_{ds} .
- 23 `gamma (\sqrt{V})` Body-effect parameter.
- 24 `cjd (F)` Drain-bulk junction capacitance.
- 25 `cjs (F)` Source-bulk junction capacitance.
- 26 `cgg (F)` Gate-gate capacitance.
- 27 `cgd (F)` Gate-drain capacitance.
- 28 `cgs (F)` Gate-source capacitance.
- 29 `cgb (F)` Gate-bulk capacitance.
- 30 `cdg (F)` Drain-gate capacitance.
- 31 `cdd (F)` Drain-drain capacitance.
- 32 `cds (F)` Drain-source capacitance.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 33 `cdb` (F) Drain-bulk capacitance.
- 34 `csg` (F) Source-gate capacitance.
- 35 `csd` (F) Source-drain capacitance.
- 36 `css` (F) Source-source capacitance.
- 37 `csb` (F) Source-bulk capacitance.
- 38 `cbg` (F) Bulk-gate capacitance.
- 39 `cbd` (F) Bulk-drain capacitance.
- 40 `cbs` (F) Bulk-source capacitance.
- 41 `cbb` (F) Bulk-bulk capacitance.
- 42 `vm` (V) Early voltage.
- 43 `vovrdr` (V) Overdrive voltage.
- 44 `tau` (s) NQS time constant.
- 45 `tau0` (s) Intrinsic time constant.
- 46 `ron` (Ω) On-resistance.

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

<code>ad</code>	I-4	<code>ef</code>	M-103	<code>meto</code>	M-24	<code>satlim</code>	M-105
<code>ad</code>	M-95	<code>eg</code>	M-82	<code>minr</code>	M-62	<code>scalem</code>	M-107
<code>af</code>	M-102	<code>ekvint</code>	M-106	<code>mj</code>	M-42	<code>tau</code>	OP-44
<code>agamma</code>	M-67	<code>fc</code>	M-47	<code>mjsw</code>	M-43	<code>tau0</code>	OP-45
<code>akp</code>	M-66	<code>fcs</code>	M-51	<code>mjswg</code>	M-45	<code>tcv</code>	M-75
<code>alarm</code>	M-68	<code>gamma</code>	OP-23	<code>n</code>	M-35	<code>theta</code>	M-15

Spectre Circuit Simulator Reference

Component Statements Part 1

as	I-3	gamma	M-10	nfac	OP-14	tlev	M-80
as	M-94	gap1	M-83	nfs	M-7	tlevc	M-81
avto	M-65	gap2	M-84	noisemod	M-100	tnom	M-73
bex	M-76	gds	OP-12	nqs	M-104	tox	M-2
bvj	M-72	gm	OP-11	nrd	M-98	tr1	M-85
cbb	OP-41	gmbs	OP-13	nrd	I-7	tr2	M-86
cbd	M-38	gmoverid	OP-22	nrs	M-99	trise	I-14
cbd	OP-39	hdif	M-64	nrs	I-8	trise	M-74
cbg	OP-38	iba	M-25	ns	I-12	tt	M-50
cbs	OP-40	ibb	M-26	nsub	M-8	type	M-1
cbs	M-39	ibbt	M-78	pb	M-48	type	OP-1
cdb	OP-33	ibc	M-27	pbsw	M-49	ucex	M-77
cdd	OP-31	ibd	OP-19	pbswg	M-46	ucrit	M-14
cdg	OP-30	ibn	M-28	pd	M-97	uo	M-16
cds	OP-32	ibs	OP-20	pd	I-6	update	M-108
cgb	OP-29	ids	OP-4	phi	M-11	vbox	M-71
cgbo	M-54	if	OP-15	ps	I-5	vbs	OP-7
cgd	OP-27	imax	M-69	ps	M-96	vds	OP-6
cgdo	M-53	imelt	M-37	pta	M-88	vdss	OP-10
cgg	OP-26	ir	OP-16	ptc	M-87	vfb	M-18
cgs	OP-28	irprime	OP-17	ptp	M-89	vgs	OP-5
cgso	M-52	is	M-32	pwr	OP-21	vm	OP-42
cj	M-40	isub	OP-18	q0	M-29	vmax	M-17
cjd	OP-24	jmax	M-70	rd	M-56	vovrdr	OP-43
cjs	OP-25	js	M-33	rdc	I-9	vp	OP-8
cjsw	M-41	jsw	M-34	rdc	M-61	vth	OP-9
cjswg	M-44	kf	M-101	rdd	M-59	vto	M-9
cox	M-3	kp	M-12	rdeff	O-4	w	M-92
csb	OP-37	l	M-93	region	OP-2	w	I-1
csd	OP-35	l	I-2	region	I-13	weff	O-1
csg	OP-34	lambda	M-19	reversed	OP-3	weta	M-20
css	OP-36	ldif	M-63	ron	OP-46	wmax	M-109
cta	M-90	leff	O-2	rs	M-55	wmin	M-110
ctp	M-91	leta	M-21	rsc	M-60	xj	M-4
dl	M-6	lk	M-30	rsc	I-10	xl	M-23
dskip	M-36	lmax	M-111	rseff	O-3	xqc	M-31
dw	M-5	lmin	M-112	rsh	M-57	xti	M-79
e0	M-13	m	I-11	rss	M-58	xw	M-22

Ratiometric Fourier Analyzer (fourier)

Description

The ratiometric Fourier analyzer measures the Fourier coefficients of two different signals at a specified fundamental frequency without loading the circuit. The algorithm used is based on the Fourier integral rather than the discrete Fourier transform and therefore is not subject to aliasing. Even on broad-band signals, it computes a small number of Fourier coefficients accurately and efficiently. Therefore, this Fourier analyzer is suitable on clocked sinusoids generated by sigma-delta converters, pulse-width modulators, digital-to-analog converters, sample-and-holds, and switched-capacitor filters as well as on the traditional low-distortion sinusoids produced by amplifiers or filters.

The analyzer is active only during a transient analysis. For each signal, the analyzer prints the magnitude and phase of the harmonics along with the total harmonic distortion at the end of the transient analysis. The total harmonic distortion is found by summing the power in all of the computed harmonics except DC and the fundamental. Consequently, the distortion is not accurate if you request an insufficient number of harmonics. The Fourier analyzer also prints the ratio the spectrum of the first signal to the fundamental of the second, so you can use the analyzer to compute large signal gains and immittances directly.

If you are concerned about accuracy, perform an additional Fourier transform on a pure sinusoid generated by an independent source. Because both transforms use the same time points, the relative errors measured with the known pure sinusoid are representative of the errors in the other transforms. In practice, this second Fourier transform is performed on the reference signal. To increase the accuracy of the Fourier transform, use the `points` parameter to increase the number of points. Tightening `reltol` and setting `errpreset=conservative` are two other measures to consider.

The accuracy of the magnitude and phase for each harmonic is independent of the number of harmonics computed. Thus, increasing the number of harmonics (while keeping `points` constant) does not change the magnitude and phase of the low order harmonics, but it does improve the accuracy of the total harmonic distortion computation. However, if you do not specify `points`, you can increase accuracy by requesting more harmonics, which creates more points.

The large number of points required for accurate results is not a result of aliasing. Many points are needed because a quadratic polynomial interpolates the waveform between the time-points. If you use too few time-points the polynomials deviate slightly from the true waveform between time-points and all of the computed Fourier coefficients are slightly in error. The algorithm that computes the Fourier integral does accept unevenly spaced time-points, but because it uses quadratic interpolation, it is usually more accurate using time-steps that are small and nearly evenly spaced.

Spectre Circuit Simulator Reference

Component Statements Part 1

This device is not supported within altergroup.

Sample Instance Statement

```
four1 (1 0) fourmod harms=50
```

Sample Model Statement

```
model fourmod fourier fund=900M points=2500 order=2
```

Instance Definition

```
Name [p] [n] [pr] [nr] ModelName parameter=value ...
```

Name [p] [n] [pr] [nr] fourier parameter=value ...

The signal between terminals `p` and `n` is the test or numerator signal. The signal between terminals `pr` and `nr` is the reference or denominator signal. Fourier analysis is performed on terminal currents by specifying the `term` or `refterm` parameters. If both `term` and `p` or `n` are specified, then the terminal current becomes the numerator and the node voltages become the denominator. By mixing voltages and currents, it is possible to compute large signal immittances.

Instance Parameters

- 1 `fund (Hz)` Fundamental frequency.
- 2 `points=20 maxharm`
Minimum number of time points.
- 3 `active=yes` Whether Fourier analysis should be performed or skipped.
Possible values are `no` or `yes`.
- 4 `order=2` Order of interpolation.
- 5 `term` Terminal used to measure current for test (numerator) channel.
- 6 `refterm` Terminal used to measure current for reference (denominator) channel.
- 7 `harmsvec=[...]` Array of desired harmonics for test (numerator) channel.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 8 `harms=9` Number of harmonics for test (numerator) channel, if an array is not given. The harmonics start from `firstharm` and go up to `firstharm + harms - 1`.
- 9 `refharmsvec=[...]` Array of desired harmonics for reference (denominator) channel.
- 10 `refharms=9` Number of harmonics for reference (denominator) channel, if an array is not given. The harmonics start from `reffirstharm` and go up to `reffirstharm + harms - 1`.
- 11 `scale=1` Scale factor for ratioed results.
- 12 `firstharm=1` First harmonic computed for test (numerator) channel.
- 13 `reffirstharm=1` First harmonic computed for reference (denominator) channel.
- 14 `normharm=1` Normalizing harmonic for test (numerator) channel.
- 15 `refnormharm=1` Normalizing harmonic for reference (denominator) channel.
- 16 `where=logfile` Where Fourier results should be printed.
Possible values are `screen`, `logfile` or `both`.

Model Definition

`model modelName fourier parameter=value ...`

Model Parameters

- 1 `fund (Hz)` Fundamental frequency.
- 2 `points=20 maxharm`
Minimum number of time points.
- 3 `harms=9` Desired number of harmonics.
- 4 `active=yes` Whether Fourier analysis should be performed or skipped.
Possible values are `no` or `yes`.
- 5 `order=2` Order of interpolation.
- 6 `firstharm=1` First harmonic computed for test (numerator) channel.

Spectre Circuit Simulator Reference

Component Statements Part 1

7 `reffirstharm=1` First harmonic computed for reference (denominator) channel.

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

active	I-3	harms	I-8	points	I-2	refnormharm	I-15
active	M-4	harms	M-3	points	M-2	refterm	I-6
firstharm	M-6	harmsvec	I-7	reffirstharm	M-7	scale	I-11
firstharm	I-12	normharm	I-14	reffirstharm	I-13	term	I-5
fund	I-1	order	M-5	refharms	I-10	where	I-16
fund	M-1	order	I-4	refharmsvec	I-9		

GaAs MESFET (gaas)

Description

The GaAs MESFET model was derived from the model by H. Statz and others at Raytheon. This model is completely symmetric and is modified slightly to make it charge conserving. GaAs MESFET instances require that you use a model statement.

This device is supported within altergroups.

There are some convergence problems with this model because of C_{gs} going to zero beyond pinchoff. The problems occur when the gate is driven from an inductive source, and there is no other capacitance at the gate. To prevent these problems, avoid setting C_{gd} to zero and add sidewall capacitance to the gate-source and gate-drain junctions. A good estimate for these capacitors is $C = \pi * \epsilon * w / 2$ where w is the gate width in microns and $\epsilon = 0.116$ fF/micron.

To examine the equations used for this component, consult the [Spectre Circuit Simulator Device Model Equations](#) manual.

Sample Instance Statement

```
m1 (1 2 0) nmes area=1 m=2
```

Spectre Circuit Simulator Reference

Component Statements Part 1

Sample Model Statement

```
model nmes gaas type=n vto=-2 beta=0.06 lambda=0 b=0.25 rs=3.65 alpha=1.9 rd=1.98  
is=1.1e-9 n=1.28 fc=0.5 cgs=0.365e-12
```

Instance Definition

```
Name d g s modelName parameter=value ...
```

Instance Parameters

- 1 area=1 Junction area factor.
- 2 m=1 Multiplicity factor.
- 3 region=fwd Estimated operating region.
Possible values are off, triode, sat, subth, or breakdown.

Model Definition

```
model modelName gaas parameter=value ...
```

Model Parameters

Device type parameters

- 1 type=n Transistor type.
Possible values are n or p.

Drain current parameters

- 2 vto=-2 V Pinch-off voltage.
- 3 beta=0.0001 A/V² Transconductance parameter.
- 4 lambda=0 1/V Channel length modulation parameter.
- 5 b=0.3 1/V Doping tail extending parameter.
- 6 alpha=2 1/V Saturation voltage parameter.

Spectre Circuit Simulator Reference

Component Statements Part 1

Parasitic resistance parameters

- 7 `rd=0` Ω Drain resistance (/area).
- 8 `rs=0` Ω Source resistance (/area).
- 9 `rg=0` Ω Gate resistance (/area).
- 10 `minr=0.1` Ω Minimum source/drain/gate resistance.

Junction diode model parameters

- 11 `is=1e-14` AGate saturation current (*area).
- 12 `n=1`Emission coefficient for the gate junction.
- 13 `imelt='imaxA'` Explosion current (*area).
- 14 `dskip=yes`Use simple piece-wise linear model for diode currents below $0.1 \cdot i_{abstol}$.
Possible values are `no` or `yes`.

Junction capacitance model parameters

- 15 `capmod=2`Charge model selector.
- 16 `cgs=0` FGate-source zero-bias junction capacitance (*area).
- 17 `cgd=0` FGate-drain zero-bias junction capacitance (*area).
- 18 `pb=1` VGate junction potential.
- 19 `fc=0.5`Junction capacitor forward-bias threshold.
- 20 `delta=0.2` VGate capacitance pinch-off transition width.

Temperature effects parameters

- 21 `tnom (C)`Parameters measurement temperature. Default set by options.
- 22 `trise=0` cTemperature rise from ambient.
- 23 `xTi=3`Temperature exponent for effect on `is`.

Spectre Circuit Simulator Reference

Component Statements Part 1

Operating region warning control parameters

- 24 `alarm=none` Forbidden operating region.
Possible values are `none`, `off`, `triode`, `sat`, `subth`, or `rev`.
- 25 `imax=1` A Maximum allowable current (*area).
- 26 `bvj= ∞` V Junction reverse breakdown voltage.

Noise model parameters

- 27 `kf=0` Flicker noise (1/f) coefficient.
- 28 `af=1` Flicker noise (1/f) exponent.

`Imax` and `Imelt`

The `imax` parameter aids convergence and prevents numerical overflow. The junction characteristics of the device are accurately modeled for current up to `imax`. If `imax` is exceeded during iterations, the linear model is substituted until the current drops below `imax` or until convergence is achieved. If convergence is achieved with the current exceeding `imax`, the results are inaccurate, and Spectre prints a warning.

A separate model parameter, `imelt`, is used as a limit warning for the junction current. This parameter can be set to the maximum current rating of the device. When any component of the junction current exceeds `imelt`, note that base and collector currents are composed of many exponential terms, Spectre issues a warning and the results become inaccurate. The junction current is linearized above the value of `imelt` to prevent arithmetic exception, with the exponential term replaced by a linear equation at `imelt`.

The `bv` parameter detects the junction breakdown only. The breakdown currents of the junctions are not modeled.

Operating-Point Parameters

- 1 `type=n` Transistor type.
Possible values are `n` or `p`.
- 2 `region=fwd` Estimated operating region.
Possible values are `off`, `triode`, `sat`, `subth`, or `breakdown`.
- 3 `ids` (A) Resistive drain current.

Spectre Circuit Simulator Reference

Component Statements Part 1

- 4 `vth` (V) Threshold voltage.
- 5 `vg`s (V) Gate-source voltage.
- 6 `vds` (V) Drain-source voltage.
- 7 `vdsat` (V) Drain saturation voltage.
- 8 `gm` (S) Common-source transconductance.
- 9 `gds` (S) Common-source output conductance.
- 10 `cgs` (F) Gate-source capacitance.
- 11 `cgd` (F) Gate-drain capacitance.
- 12 `ig` (A) Resistive gate current.
- 13 `pwr` (W) Power at operating point.

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

<code>af</code> M-28	<code>cgs</code> M-16	<code>kf</code> M-27	<code>rs</code> M-8
<code>alarm</code> M-24	<code>delta</code> M-20	<code>lambda</code> M-4	<code>tnom</code> M-21
<code>alpha</code> M-6	<code>dskip</code> M-14	<code>m</code> I-2	<code>trise</code> M-22
<code>area</code> I-1	<code>fc</code> M-19	<code>minr</code> M-10	<code>type</code> M-1
<code>b</code> M-5	<code>gds</code> OP-9	<code>n</code> M-12	<code>type</code> OP-1
<code>beta</code> M-3	<code>gm</code> OP-8	<code>pb</code> M-18	<code>vds</code> OP-6
<code>bvj</code> M-26	<code>ids</code> OP-3	<code>pwr</code> OP-13	<code>vdsat</code> OP-7
<code>capmod</code> M-15	<code>ig</code> OP-12	<code>rd</code> M-7	<code>vg</code> s OP-5
<code>cgd</code> OP-11	<code>imax</code> M-25	<code>region</code> OP-2	<code>vth</code> OP-4
<code>cgd</code> M-17	<code>imelt</code> M-13	<code>region</code> I-3	<code>vto</code> M-2
<code>cgs</code> OP-10	<code>is</code> M-11	<code>rg</code> M-9	<code>x</code> ti M-23

Component Statements Part 2

This chapter discusses the following topics:

- n [Hetero-Junction Bipolar Transistor \(hbt\)](#) on page 266
- n [HV MOS Transistor \(hvmos\)](#) on page 275
- n [Two Terminal Inductor \(inductor\)](#) on page 291
- n [Current Probe \(iprobe\)](#) on page 299
- n [Independent Current Source \(isource\)](#) on page 299
- n [Junction Field Effect Transistor \(jfet\)](#) on page 304
- n [MOS Level-0 Transistor \(mos0\)](#) on page 323
- n [MOS Level-1 Transistor \(mos1\)](#) on page 327
- n [MOS Level-2 Transistor \(mos2\)](#) on page 372
- n [MOS Level-3 Transistor \(mos3\)](#) on page 388
- n [Microstrip Line \(msline\)](#) on page 450
- n [Multi-Conductor Transmission Line \(mtline\)](#) on page 452
- n [Mutual Inductor \(mutual inductor\)](#) on page 457
- n [Set Node Quantities \(node\)](#) on page 463
- n [Linear N Port \(nport\)](#) on page 464
- n [Parameter Value Tester \(paramtest\)](#) on page 467
- n [Polynomial Current Controlled Current Source \(pcccs\)](#) on page 468
- n [Polynomial Current Controlled Voltage Source \(pccvs\)](#) on page 469
- n [Physical Resistor \(phy_res\)](#) on page 471
- n [Independent Resistive Source \(port\)](#) on page 478

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Component Statements Part 2

- n [Polynomial Voltage Controlled Current Source \(pvccs\)](#) on page 484
- n [Polynomial Voltage Controlled Voltage Source \(pvcvs\)](#) on page 485
- n [Quantity Information \(quantity\)](#) on page 486
- n [Four Terminal Relay \(relay\)](#) on page 492
- n [Two Terminal Resistor \(resistor\)](#) on page 494
- n [s-Domain Linear Current Controlled Current Source \(scccs\)](#) on page 500
- n [s-Domain Current Controlled Voltage Source \(sccvs\)](#) on page 502
- n [s-Domain Linear Voltage Controlled Current Source \(svccs\)](#) on page 504
- n [s-Domain Voltage Controlled Voltage Source \(svcvs\)](#) on page 506
- n [Ideal Switch \(switch\)](#) on page 508
- n [Transmission Line \(tline\)](#) on page 509
- n [GaAs MESFET \(tom2\)](#) on page 516
- n [Linear Two Winding Ideal Transformer \(transformer\)](#) on page 521
- n [VBIC Bipolar Transistor \(vbic\)](#) on page 523
- n [Linear Voltage Controlled Current Source \(vccs\)](#) on page 533
- n [Linear Voltage Controlled Voltage Source \(vcvs\)](#) on page 534
- n [Independent Voltage Source \(vsource\)](#) on page 534
- n [Winding for Magnetic Core \(winding\)](#) on page 539
- n [z-Domain Linear Current Controlled Current Source \(zcccs\)](#) on page 540
- n [z-Domain Current Controlled Voltage Source \(zccvs\)](#) on page 543
- n [z-Domain Linear Voltage Controlled Current Source \(zvccs\)](#) on page 545
- n [z-Domain Voltage Controlled Voltage Source \(zvcvs\)](#) on page 548

Hetero-Junction Bipolar Transistor (hbt)

Description

The HBT (Hetero-junction Bipolar Transistor) model was developed by UCSD as part of the ARPA High Speed Circuit Design Program. The model has four external electrical nodes, one thermal node, and up to nine internals depending on the complexity of the model users specified. Detailed description of the model and equations can be found in the Spectre Circuit Simulator Device Model Equations manual.

This device is supported within altergroups.

Sample Instance Statement

```
q7 (net5 net2 0) hbtmod m=1 top=25
```

Sample Model Statement

```
model hbtmod hbt type=npn bf=500 br=1000 xtb=-2.4 xti=0 xcjc=0.83 mje=0.34 fc=0.5  
eg=1.2 ise=5.5e-15 vjc=0.84 vaf=40 cjc=5.1e-15
```

Instance Definition

```
Name c b e [t] [s] ModelName parameter=value ...
```

It is not necessary to specify the substrate and thermal terminal. If left unspecified, the substrate node is connected to ground while the thermal node is fixed to the ambient temperature. However, you must specify the thermal node if you specify the substrate node.

Instance Parameters

- 1 `area=1` Transistor area factor.
- 2 `m=1` Multiplicity factor.
- 3 `top (C)` Average device operating temperature.
- 4 `region=fwd` Estimated operating region.
Possible values are `off`, `fwd`, `rev`, or `sat`.

Spectre Circuit Simulator Reference

Component Statements Part 2

Model Definition

model modelName hbt parameter=value ...

Model Parameters

Structural Parameters

- 1 type=npn Transistor type.
Possible values are npn or pnp.

Saturation Current Parameters

- 2 is=1e-25 A Saturation value for forward collector current (*area).
3 ise=1e-25 A Saturation value for nonideal base current. (*area).
4 isex=1e-25 A Saturation current for emitter leakage diode (*area).
5 isc=1e-20 A Saturation value for intrinsic BC junction current. (*area).
6 iscx=1e-20 A Saturation value for extrinsic B-C junction current (*area).
7 ics=1e-30 A Saturation value for C-S junction current (*area).

Emission Coefficient Parameters

- 8 nf=1 Forward collector current ideality factor.
9 nr=1 Reverse ideality factor.
10 ne=2 Nonideal base forward current ideality factor.
11 nex=2 Ideality factor for emitter leakage diode.
12 nc=2 Intrinsic B-C junction ideality factor.
13 ncx=2 Ideality factor for extrinsic B-C junction.
14 ncs=2 Ideality factor for C-S junction.

Spectre Circuit Simulator Reference

Component Statements Part 2

Current Gain Parameters

- 15 `bf=1000 A/A` Forward ideal current gain (beta).
- 16 `br=1000 A/A` Reverse ideal current gain.
- 17 `isa=1e10 A` Collector E-B barrier limiting current (*area).
- 18 `na=2` Collector E-B barrier ideality factor.
- 19 `isb=1e10 A` Collector B-C barrier limiting current (*area).
- 20 `nb=2` Collector B-C barrier ideality factor.
- 21 `ik=1e10 A` Knee current for dc high-level injection effect (*area).

Early Voltage Parameters

- 22 `vaf=500 v` Forward Early voltage.
- 23 `var=500 v` Reverse Early voltage.

Breakdown Voltage Parameters

- 24 `bkdn=no` Flag denoting B-C breakdown should be included.
Possible values are `no` or `yes`.
- 25 `bvc=50 A` Collector-base breakdown voltage BV_{cbo} .
- 26 `nbc=8` Exponent for B-C multiplication factor versus voltage.
- 27 `fa=0.9` Factor for specification of avalanche voltage.
- 28 `imax=1 A` Maximum allowable base current (*area).
- 29 `imelt=10 A` Explosion current (*area).

Parasitic Resistance Parameters

- 30 `rbi=0 Ω` Intrinsic base resistance (/area).
- 31 `rbx=0 Ω` Extrinsic base resistance (/area).

Spectre Circuit Simulator Reference

Component Statements Part 2

32 `rci=0` Ω Intrinsic collector resistance (/area).

33 `rcx=0` Ω Extrinsic collector resistance (/area).

34 `re=0` Ω Emitter resistance (/area).

35 `rex=0` Ω Extrinsic emitter leakage diode series resistance (/area).

Junction Capacitance Parameters

36 `cje=0` F B-E depletion capacitance at zero bias (*area).

37 `vje=1.6` V B-E built-in potential for Cj.

38 `mje=0.5` Exponent for voltage variation of B-E Cj.

39 `cemin=0` F Minimum B-E capacitance (*area).

40 `fce=0.8` Factor for start of high bias B-E Cj approximation.

41 `cjc=0` F Intrinsic B-C depletion capacitance at zero bias (*area).

42 `vjc=1.4` V Intrinsic B-C built-in potential for Cj.

43 `mjc=0.33` Exponent for voltage variation of Intrinsic B-C Cj.

44 `ccmin=0` F Minimum B-C capacitance (*area).

45 `fc=0.8` Factor for start of high bias B-C Cj approximation.

46 `cjcx=0` F Extrinsic B-C depletion capacitance at zero bias (*area).

47 `vjcx=1.4` V Extrinsic B-C built-in potential for Cj.

48 `mjcx=0.33` B-C junction exponent.

49 `cxmin=0` F Minimum extrinsic B-C capacitance (*area).

50 `xcjc=0` Fraction of B-C capacitance tied to external base node.

51 `cjs=0` F B-S depletion capacitance at zero bias (*area).

52 `vjs=1.4` V B-S built-in potential for Cj.

Spectre Circuit Simulator Reference

Component Statements Part 2

53 `mjs=0.5` Exponent for voltage variation of C-S Cj.

Transit Time and Excess Phase Parameters

54 `tfb=0` sBase transit time.

55 `tbexs=0` Excess B-E heterojunction transit time.

56 `tbcxs=0` Excess B-C heterojunction transit time.

57 `tfc0=0` sCollector forward transit time.

58 `icrit0=1e3` ACritical current for intrinsic Cj variation.

59 `itc=0` ACharacteristic current for Tfc.

60 `itc2=0` ACharacteristic current for Tfc.

61 `vtc=1e3` VCharacteristic voltage for Tfc.

62 `tkrk=0` sForward transit time for Kirk effect.

63 `vkrk=1e3` VCharacteristic voltage for Kirk effect.

64 `ikrk=1e3` ACharacteristic voltage for Kirk effect.

65 `tr=0` sReverse charge storage time for intrinsic B-C junction.

66 `trx=0` sReverse charge storage time for extrinsic B-C junction.

67 `fex=0` sFactor to determine excess phase.

Temperature Effects Parameters

68 `selft=no` Flag denoting self-heating.
Possible values are `no` or `yes`.

69 `tnom (C)` Parameters measurement temperature. Default set by options.

70 `top=27` CAverage device operating temperature.

71 `rth=0` Ω Thermal resistance of device.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 72 `cth=0` F Thermal capacitance of device.
- 73 `xti=2` Exponent for `is` temperature dependence.
- 74 `xtb=2` Exponent for beta temperature dependence.
- 75 `tne=0` Coefficient for `ne` temperature dependence.
- 76 `tnc=0` Coefficient for `nc` temperature dependence.
- 77 `tnex=0` Coefficient for `nex` temperature dependence.
- 78 `eae=0` V Activation energy for `isa` temperature dependence.
- 79 `eac=0` V Activation energy for `isb` temperature dependence.
- 80 `aaa=0` V Activation energy for `ise` temperature dependence.
- 81 `eab=0` V Activation energy for `isc` temperature dependence.
- 82 `eax=0` V Activation energy for `isex` temperature dependence.
- 83 `xre=0` Exponent for `re` temperature dependence.
- 84 `xrex=0` Exponent for `rex` temperature dependence.
- 85 `xrb=0` Exponent for `rb` temperature dependence.
- 86 `xrc=0` Exponent for `rc` temperature dependence.
- 87 `tvje=0` V/C Coefficient for `vje` temperature dependence.
- 88 `tvjc=0` V/C Coefficient for `vjc` temperature dependence.
- 89 `tvjcx=0` V/C Coefficient for `vjcx` temperature dependence.
- 90 `tvjs=0` V/C Coefficient for `vjs` temperature dependence.
- 91 `xtitc=0` Exponent for `itc` temperature dependence.
- 92 `xtitc2=0` Exponent for `itc2` temperature dependence.
- 93 `xttf=0` Exponent for `tf` temperature dependence.

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Component Statements Part 2

- 94 `xttkrk=0` Exponent for `tkrk` temperature exponent.
- 95 `xtvkrk=0` Exponent for `vkrrk` temperature dependence.
- 96 `xtikrk=0` Exponent for `ikrrk` temperature dependence.
- 97 `xrt=0` Exponent for `rth` temperature dependence.
- 98 `eg=1.5` `v` Activation energy for `is` temperature dependence.
- 99 `dtmax=1000` `c` Maximum expected temperature rise above heat sink.

Noise Model Parameters

- 100 `kfn=0` Flicker (1/f) noise coefficient.
- 101 `afn=1` Flicker (1/f) noise exponent.
- 102 `bfn=1` Flicker noise frequency exponent.

`Imax` and `Imelt`

The `imax` parameter aids convergence and prevents numerical overflow. The junction characteristics of the device are accurately modeled for current up to `imax`. If `imax` is exceeded during iterations, the linear model is substituted until the current drops below `imax` or until convergence is achieved. If convergence is achieved with the current exceeding `imax`, the results are inaccurate, and Spectre prints a warning.

A separate model parameter, `imelt`, is used as a limit warning for the junction current. This parameter can be set to the maximum current rating of the device. When any component of the junction current exceeds `imelt`, note that base and collector currents are composed of many exponential terms, Spectre issues a warning and the results become inaccurate. The junction current is linearized above the value of `imelt` to prevent arithmetic exception, with the exponential term replaced by a linear equation at `imelt`.

Operating-Point Parameters

- 1 `type=npn` Transistor type.
Possible values are `npn` or `pnv`.
- 2 `region=fwd` Estimated operating region.
Possible values are `off`, `fwd`, `rev`, or `sat`.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 3 `vbe (V)` Base-emitter voltage.
- 4 `vbc (V)` Base-collector voltage.
- 5 `vce (V)` Collector-emitter voltage.
- 6 `vcs (V)` XC-substrate voltage.
- 7 `temp (C)` Device temperature.
- 8 `ith (A)` Thermal source.
- 9 `ice (A)` Intrinsic B-C current.
- 10 `ibe (A)` Intrinsic B-E current.
- 11 `ics (A)` C-S junction current.
- 12 `ibei (A)` B-E junction current.
- 13 `ibci (A)` B-C junction current.
- 14 `ibex (A)` XB-E junction current.
- 15 `ibcx (A)` XB-C junction current.
- 16 `ibk (A)` Breakdown current.
- 17 `dice_dvbe (S)` Intrinsic dI_{ce}/dV_{be} .
- 18 `dice_dvbc (S)` Intrinsic dI_{ce}/dV_{bc} .
- 19 `dibe_dvbe (S)` Intrinsic dI_{be}/dV_{be} .
- 20 `dibe_dvbc (S)` Intrinsic dI_{be}/dV_{bc} .
- 21 `dqbe_dvbe (F)` Intrinsic dQ_{be}/dV_{be} .
- 22 `dqbe_dvbc (F)` Intrinsic dQ_{be}/dV_{bc} .
- 23 `dqbc_dvbe (F)` Intrinsic dQ_{bc}/dV_{be} .
- 24 `dqbc_dvbc (F)` Intrinsic dQ_{bc}/dV_{bc} .

Spectre Circuit Simulator Reference

Component Statements Part 2

25 `cbcx` (F) XB-C junction capacitance.

26 `cbcxx` (F) EXTB-C junction capacitance.

27 `ccs` (F) Substrate junction capacitance.

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of M-35 means the 35th model parameter.

<code>afn</code>	M-101	<code>fc</code>	M-45	<code>nb</code>	M-20	<code>tvjcx</code>	M-89
<code>area</code>	I-1	<code>fce</code>	M-40	<code>nbc</code>	M-26	<code>tvje</code>	M-87
<code>bf</code>	M-15	<code>fex</code>	M-67	<code>nc</code>	M-12	<code>tvjs</code>	M-90
<code>bf_n</code>	M-102	<code>ibci</code>	OP-13	<code>ncs</code>	M-14	<code>type</code>	M-1
<code>bkd_n</code>	M-24	<code>ibcx</code>	OP-15	<code>ncx</code>	M-13	<code>type</code>	OP-1
<code>br</code>	M-16	<code>ibe</code>	OP-10	<code>ne</code>	M-10	<code>vaf</code>	M-22
<code>bvc</code>	M-25	<code>ibei</code>	OP-12	<code>nex</code>	M-11	<code>var</code>	M-23
<code>cbc_x</code>	OP-25	<code>ibex</code>	OP-14	<code>nf</code>	M-8	<code>vbc</code>	OP-4
<code>cbc_{xx}</code>	OP-26	<code>ibk</code>	OP-16	<code>nr</code>	M-9	<code>vbe</code>	OP-3
<code>ccmin</code>	M-44	<code>ice</code>	OP-9	<code>rbi</code>	M-30	<code>vce</code>	OP-5
<code>ccs</code>	OP-27	<code>icrit0</code>	M-58	<code>rbx</code>	M-31	<code>vcs</code>	OP-6
<code>cemin</code>	M-39	<code>ics</code>	M-7	<code>rci</code>	M-32	<code>vjc</code>	M-42
<code>cjc</code>	M-41	<code>ics</code>	OP-11	<code>rcx</code>	M-33	<code>vjcx</code>	M-47
<code>cjcx</code>	M-46	<code>ik</code>	M-21	<code>re</code>	M-34	<code>vje</code>	M-37
<code>cje</code>	M-36	<code>ikrk</code>	M-64	<code>region</code>	I-4	<code>vjs</code>	M-52
<code>cjs</code>	M-51	<code>imax</code>	M-28	<code>region</code>	OP-2	<code>vkrk</code>	M-63
<code>cth</code>	M-72	<code>imelt</code>	M-29	<code>rex</code>	M-35	<code>vtc</code>	M-61
<code>cxmin</code>	M-49	<code>is</code>	M-2	<code>rth</code>	M-71	<code>xcjc</code>	M-50
<code>dibe_{dvbc}</code>	OP-20	<code>isa</code>	M-17	<code>sel_{ft}</code>	M-68	<code>xrb</code>	M-85
<code>dibe_{dvbe}</code>	OP-19	<code>isb</code>	M-19	<code>tbcxs</code>	M-56	<code>xrc</code>	M-86
<code>dice_{dvbc}</code>	OP-18	<code>isc</code>	M-5	<code>tbexs</code>	M-55	<code>xre</code>	M-83
<code>dice_{dvbe}</code>	OP-17	<code>iscx</code>	M-6	<code>temp</code>	OP-7	<code>xrex</code>	M-84
<code>dqbc_{dvbc}</code>	OP-24	<code>ise</code>	M-3	<code>tfb</code>	M-54	<code>xrt</code>	M-97
<code>dqbc_{dvbe}</code>	OP-23	<code>isex</code>	M-4	<code>tfc0</code>	M-57	<code>xtb</code>	M-74
<code>dqbe_{dvbc}</code>	OP-22	<code>itc</code>	M-59	<code>tkrk</code>	M-62	<code>xti</code>	M-73
<code>dqbe_{dvbe}</code>	OP-21	<code>itc2</code>	M-60	<code>tnc</code>	M-76	<code>xtikrk</code>	M-96
<code>dtmax</code>	M-99	<code>ith</code>	OP-8	<code>tne</code>	M-75	<code>xtitc</code>	M-91
<code>ea_a</code>	M-80	<code>kfn</code>	M-100	<code>tnex</code>	M-77	<code>xtitc2</code>	M-92
<code>eab</code>	M-81	<code>m</code>	I-2	<code>tnom</code>	M-69	<code>xttf</code>	M-93
<code>eac</code>	M-79	<code>mjc</code>	M-43	<code>top</code>	M-70	<code>xttkrk</code>	M-94

Spectre Circuit Simulator Reference

Component Statements Part 2

eae	M-78	mjcx	M-48	top	I-3	xtvkrk	M-95
eax	M-82	mje	M-38	tr	M-65		
eg	M-98	mjs	M-53	trx	M-66		
fa	M-27	na	M-18	tvjc	M-88		

HV MOS Transistor (hvmos)

Description

HV (High-Voltage) MOS transistor model is a deep submicron, high-voltage MOSFET model. It is based on the BSIM3v3 version 3.1. Major enhancements include current-crowding effect at high gate bias, asymmetric source-drain structure, mobility reduction, transconductance reduction under high V_{gs} at saturation region, forward and reverse mode, self-heating, and more flexible gate-dependent output characteristics. HVMOS can be used for high voltage IC design applications such as Flash memory with asymmetric LDD structures, LCD drivers, CCD, E2PROM and LDMOS applications.

Like BSIM3v3, the HVMOS transistor model also allows the binning option to achieve even higher accuracy. The binning equation is given by

$$P = P_0 + P_l / L_{eff} + P_w / W_{eff} + P_p / (L_{eff} * W_{eff})$$

Only the P_0 parameters are listed. P_l , P_w , and P_p are not shown but can be recognized. The names of P_l , P_w , and P_p are identical to that of P_0 but with a prefix of l , w , and p , respectively. HVMOS transistors require that you use a model statement.

This device is supported within altergroups.

Sample Instance Statement

```
m1 (1 2 0 0) hvmos w=1.5u l=1u ad=2.6p as=2.6p pd=6.6p ps=6.6p nrd=1.54 nrs=1.54
```

Sample Model Statement

```
model hvmos hvmos vtho=0.53 w0=2.14e-6 nlx=1.8e-7 nch=2.3e18 xj=0.22e-6 k1=0.48  
k2=-0.02 drout=1.1 rsh=10 cgso=2.4e-10 cgdo=2.4e-10
```

Instance Definition

```
Name d g s b ModelName parameter=value ...
```

Spectre Circuit Simulator Reference

Component Statements Part 2

Instance Parameters

- 1 `w` (m) Channel width.
- 2 `l` (m) Channel length.
- 3 `as` (m²) Area of source diffusion.
- 4 `ad` (m²) Area of drain diffusion.
- 5 `ps` (m) Perimeter of source diffusion.
- 6 `pd` (m) Perimeter of drain diffusion.
- 7 `nrd` (m/m) Number of squares of drain diffusion.
- 8 `nrs` (m/m) Number of squares of source diffusion.
- 9 `ld` (m) Length of drain diffusion region.
- 10 `ls` (m) Length of source diffusion region.
- 11 `m=1` Multiplicity factor (number of MOSFETs in parallel).
- 12 `region=triode` Estimated operating region.
Possible values are `off`, `triode`, `sat`, or `subth`.
- 13 `trise` Temperature rise from ambient.

Model Definition

`model modelName hvmos parameter=value ...`

Model Parameters

Device Type Parameters

- 1 `type=n` Transistor type.
Possible values are `n` or `p`.

Spectre Circuit Simulator Reference

Component Statements Part 2

Threshold Voltage Parameters

- 2 `vtho` (V) Threshold voltage at zero body bias for long-channel devices. For enhancement-mode devices, $v_{tho} > 0$ for n-channel and $v_{tho} < 0$ for p-channel.
- 3 `k1`=0.5 \sqrt{V} Body-effect coefficient.
- 4 `k2`=-0.0186 Charge-sharing parameter.
- 5 `k3`=80 Narrow width coefficient.
- 6 `k3b`=0 $1/V$ Narrow width coefficient.
- 7 `w0`=2.5e-6 m Narrow width coefficient.
- 8 `nlx`=1.74e-7 m Lateral nonuniform doping coefficient.
- 9 `dvt0`=2.2 First coefficient of short-channel effects.
- 10 `dvt1`=0.53 Second coefficient of short-channel effects.
- 11 `dvt2`=-0.032 $1/V$ Body-bias coefficient of short-channel effects.
- 12 `a0f`=1 Forward nonuniform depletion width effect coefficient.
- 13 `a0r`=`a0f` Reverse nonuniform depletion width effect coefficient.
- 14 `b0`=0 m Bulk charge coefficient due to narrow width effect.
- 15 `b1`=0 m Bulk charge coefficient due to narrow width effect.
- 16 `a1`=0 No-saturation coefficient.
- 17 `a2`=1 No-saturation coefficient.
- 18 `ags`=0 F/m^2 V Gate-bias dependence of A_{bulk} .
- 19 `ketaf`=-0.047 $1/V$ Body-bias coefficient for non-uniform depletion width effect.
- 20 `ketar`=`ketaf` $1/V$ Reverse body-bias coefficient for non-uniform depletion width effect.

Spectre Circuit Simulator Reference

Component Statements Part 2

Process Parameters

- 21 `nch=1.7e17 cm-3` Peak channel doping concentration.
- 22 `xj=0.15e-6 m` Source/drain junction depth.
- 23 `lint=0 m` Lateral diffusion for one side.
- 24 `wint=0 m` Width reduction for one side.
- 25 `ll=0 m` Length dependence of delta L.
- 26 `lln=1` Length exponent of delta L.
- 27 `lw=0 m` Width dependence of delta L.
- 28 `lwn=1` Width exponent of delta L.
- 29 `lwl=0 m2` Area dependence of delta L.
- 30 `lmin=0 m` The minimum channel length for which the model is still valid.
- 31 `lmax=1 m` The maximum channel length for which the model is still valid.
- 32 `wl=0 m` Length dependence of delta W.
- 33 `wln=1` Length exponent of delta W.
- 34 `ww=0 m` Width dependence of delta W.
- 35 `wwn=1` Width exponent of delta W.
- 36 `wwl=0 m2` Area dependence of delta W.
- 37 `wmin=0 m` The minimum channel width for which the model is still valid.
- 38 `wmax=1 m` The maximum channel width for which the model is still valid.
- 39 `dwg=0 m/ \sqrt{v}` Gate-bias dependence of channel width.
- 40 `dwb=0 m/ \sqrt{v}` Body-bias dependence of channel width.
- 41 `tox=1.5e-8 m` Gate oxide thickness.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 42 `rd0=0` Ω Fixed drain resistance.
- 43 `rs0=0` Ω Fixed source resistance.
- 44 `rdw=0` Ω μm Width dependence of drain resistance.
- 45 `rsw=0` Ω μm Width dependence of source resistance.
- 46 `prwb=0` $1/\sqrt{v}$ Body-effect coefficient for R_{ds} .
- 47 `prwg=0` $1/\sqrt{v}$ Gate-effect coefficient for R_{ds} .
- 48 `wr=1` Width offset for parasitic resistance.
- 49 `binunit=2` Bin parameter unit selector. 1 for microns and 2 for meters.

Mobility Parameters

- 50 `mobmod=1` Mobility model selector.
- 51 `u0f=670` $\text{cm}^2/\text{V} \cdot \text{s}$ Forward low-field surface mobility at t_{nom} . Default is 250 for PMOS.
- 52 `u0r=u0f` $\text{cm}^2/\text{V} \cdot \text{s}$ Reverse low-field surface mobility at t_{nom} .
- 53 `vsatf=8e4` m/s Forward carrier saturation velocity at t_{nom} .
- 54 `dvsatf=0` m/s Forward gate-bias dependence of saturation velocity.
- 55 `dvsatbf=0` m/s Forward body-bias dependence of saturation velocity.
- 56 `vsatr=vsatf` m/s Reverse carrier saturation velocity at t_{nom} .
- 57 `dvsatr=dvsatf` m/s Reverse gate-bias dependence of saturation velocity.
- 58 `dvsatbr=dvsatbf` m/s
Reverse body-bias dependence of saturation velocity.
- 59 `uaf=2.25e-9` m/v Forward first-order mobility reduction coefficient.
- 60 `ubf=5.87e-19` m^2/v^2
Forward second-order mobility reduction coefficient.

Spectre Circuit Simulator Reference

Component Statements Part 2

61 `ucf=-4.65e-11 m/v2`

Forward body-bias dependence of mobility. Default is -0.046 and unit is 1/V for mobmod=3.

62 `udf=0 m/v2`Forward source-resistance dependence of mobility.

63 `uar=uaf m/v`Reverse first-order mobility reduction coefficient.

64 `ubr=ubf m2/v2`Reverse second-order mobility reduction coefficient.

65 `ucr=ucf m/v2`Reverse body-bias dependence of mobility.

66 `udr=udf m/v2`Reverse source-resistance dependence of mobility.

Output Resistance Parameters

67 `drout=0.56`DIBL effect on output resistance coefficient.

68 `pclmf=1.3`Forward channel length modulation coefficient.

69 `pclmr=pclmf`Reverse channel length modulation coefficient.

70 `pdiblc1f=0.39`Forward first coefficient of drain-induced barrier lowering.

71 `pdiblc1r=pdiblc1f`
Reverse first coefficient of drain-induced barrier lowering.

72 `pdiblc2f=8.6e-3`Forward second coefficient of drain-induced barrier lowering.

73 `pdiblc2r=pdiblc2f`
Reverse second coefficient of drain-induced barrier lowering.

74 `pdiblcbf=0 1/v`Body-effect coefficient for DIBL.

75 `pdiblcbr=pdiblcbf 1/v`
Reverse body-effect coefficient for DIBL.

76 `pscbelf=4.24e8 V/m`
First coefficient of substrate current body effect.

77 `pscbe2f=1e-5 m/v`Second coefficient of substrate current body effect.

78 `pscbeg=0 V/m`Third coefficient of substrate current body effect.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 79 `pscbe1r=pscbe1f` V/m
Reverse first coefficient of substrate current body effect.
- 80 `pscbe2r=pscbe2f` m/v
Reverse second coefficient of substrate current body effect.
- 81 `pclmgf=0` Forward gate dependence of V_{a1m} .
- 82 `pclmgr=pclmgf` Reverse gate dependence of V_{a1m} .
- 83 `pclmbf=0` Forward body dependence of V_{a1m} .
- 84 `pclmbr=pclmbf` Reverse body dependence of V_{a1m} .
- 85 `pdiblgf=0` Forward gate dependence of V_{a1bl} .
- 86 `pdiblgr=pdiblgf` Reverse gate dependence of V_{a1bl} .
- 87 `delta=0.01` V Effective drain voltage smoothing parameter.

Subthreshold Parameters

- 88 `cdsc=2.4e-4` F/m² Source/drain and channel coupling capacitance.
- 89 `cdscb=0` F/m² V Body-bias dependence of $cdsc$.
- 90 `cdscd=0` F/m² V Drain-bias dependence of $cdsc$.
- 91 `nfactor=1` Subthreshold swing coefficient.
- 92 `cit=0` F Interface trap parameter for subthreshold swing.
- 93 `voff=-0.08` V Threshold voltage offset.
- 94 `dsub=drout` DIBL effect in subthreshold region.
- 95 `eta0f=0.08` DIBL coefficient subthreshold region.
- 96 `etabf=-0.07` 1/V Body-bias dependence of $et0$.
- 97 `eta0r=eta0f` Reverse DIBL coefficient subthreshold region.
- 98 `etabr=etabf` 1/V Body-bias dependence of $eta0r$.

Spectre Circuit Simulator Reference

Component Statements Part 2

Substrate current parameters

99 $\alpha_0=0$ m/v Substrate current impact ionization coefficient.

100 $\beta_0=30$ 1/v Substrate current impact ionization exponent.

Parasitic Resistance Parameters

101 $r_{sh}=0$ Ω/sqr Source/drain diffusion sheet resistance.

102 $r_s=0$ Ω Source resistance.

103 $r_d=0$ Ω Drain resistance.

104 $l_{gcs}=0$ m Gate-to-contact length of source side.

105 $l_{gcd}=0$ m Gate-to-contact length of drain side.

106 $r_{sc}=0$ Ω Source contact resistance.

107 $r_{dc}=0$ Ω Drain contact resistance.

108 $r_{ss}=0$ Ω m Scalable source resistance.

109 $r_{dd}=0$ Ω m Scalable drain resistance.

110 $s_c=\infty$ m Spacing between contacts.

111 $l_{dif}=0$ m Lateral diffusion beyond the gate.

112 $h_{dif}=0$ m Length of heavily doped diffusion .

113 $\text{min}r=0.1$ Ω Minimum source/drain resistance.

Junction Diode Model Parameters

114 j_s (A/m^2) Bulk junction reverse saturation current density.

115 $i_s=1\text{e-}14$ A Bulk junction reverse saturation current.

116 $n=1$ Junction emission coefficient.

Spectre Circuit Simulator Reference

Component Statements Part 2

117 `dskip=yes` Use simple piece-wise linear model for diode currents below $0.1 \cdot i_{abstol}$.
Possible values are `no` or `yes`.

118 `imelt=imaxA` Explosion current.

119 `jmelt=jmaxA/m2`
Explosion current density.

Overlap Capacitance Parameters

120 `cgs0` (F/m) Gate-source overlap capacitance.

121 `cgd0` (F/m) Gate-drain overlap capacitance.

122 `cgbo` (F/m) Gate-bulk overlap capacitance.

123 `meto=0` *m* Metal overlap in fringing field.

124 `cgs1=0` F/m Gate-source overlap capacitance in LDD region.

125 `cgdl=0` F/m Gate-drain overlap capacitance in LDD region.

126 `ckappa=0.6` Overlap capacitance fitting parameter.

127 `deltaacc=0.1` *v* Capacitance smoothing parameter.

Junction Capacitance Model Parameters

128 `cbs=0` F Bulk-source zero-bias junction capacitance.

129 `cbd=0` F Bulk-drain zero-bias junction capacitance.

130 `cj=5e-4` F/m^2 Zero-bias junction bottom capacitance density.

131 `mj=1/2` Bulk junction bottom grading coefficient.

132 `pb=0.8` *v* Bulk junction built-in potential.

133 `fc=0.5` Forward-bias depletion capacitance threshold.

134 `cjsw=5e-10` F/m Zero-bias junction sidewall capacitance density.

Spectre Circuit Simulator Reference

Component Statements Part 2

135 `mjsw=1/3` Bulk junction sidewall grading coefficient.

136 `pbsw=0.8` `v` Side-wall junction built-in potential.

137 `fcsw=0.5` Side-wall forward-bias depletion capacitance threshold.

Charge Model Selection Parameters

138 `capmod=2` Intrinsic charge model.

139 `nqsmode=0` Non-quasi static model selector. Set to 1 to turn on nqs.

140 `dwc=wint` `m` Delta W for capacitance model.

141 `dlc=lint` `m` Delta L for capacitance model.

142 `clc=1e-7` `m` Intrinsic capacitance fitting parameter.

143 `cle=0.6` Intrinsic capacitance fitting parameter.

144 `cf` (F/m) Fringe capacitance parameter.

145 `a0cvf=a0f` A0 for C-V calculation.

146 `a0cvr=a0r` Reverse A0 for C-V calculation.

147 `qgvd0f=1` Cgd fitting parameter.

148 `qgvd0r=qgvd0f` Reverse Cgd fitting parameter.

149 `elm=5` Elmore constant of the channel.

150 `xpart=0` Drain/source channel charge partition in saturation for BSIM charge model, use 0.0 for 40/60, 0.5 for 50/50, or 1.0 for 0/100.

Default Instance Parameters

151 `w=5e-6` `m` Default channel width.

152 `l=5e-6` `m` Default channel length.

153 `as=0` `m`² Default area of source diffusion.

Spectre Circuit Simulator Reference

Component Statements Part 2

154 `ad=0` m^2 Default area of drain diffusion.

155 `ps=0` m Default perimeter of source diffusion.

156 `pd=0` m Default perimeter of drain diffusion.

157 `nrd=0` m/m Default number of squares of drain diffusion.

158 `nrs=0` m/m Default number of squares of source diffusion.

159 `xw=0` m Width variation due to masking and etching.

160 `xl=0` m Length variation due to masking and etching.

Temperature Effects Parameters

161 `tnom` (C) Parameters measurement temperature. Default set by `options`.

162 `trise=0` C Temperature rise from ambient.

163 `tlev=0` DC temperature selector.

164 `tlevc=0` AC temperature selector.

165 `eg=1.12452` V Energy band gap.

166 `gap1=7.02e-4` V/C Band gap temperature coefficient.

167 `gap2=1108` C Band gap temperature offset.

168 `kt1=-0.11` V Temperature coefficient for threshold voltage.

169 `kt1l=0` V m Temperature coefficient for threshold voltage.

170 `kt2=0.022` Temperature coefficient for threshold voltage.

171 `atf=3.3e4` m/s Temperature coefficient for `vsatf`.

172 `atr=atf` m/s Temperature coefficient for `vsatr`.

173 `at1f=0` m/s Temperature coefficient for `dvsatf`.

174 `at1r=at1f` m/s Temperature coefficient for `dvsatr`.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 175 $ua1f=4.31e-9 \text{ m/v}$ Temperature coefficient for ua .
- 176 $ub1f=-7.61e-18 \text{ m}^2/\text{v}^2$
Temperature coefficient for ub .
- 177 $uc1f=-5.5e-11 \text{ m/v}^2$
Temperature coefficient for uc . Default is -0.056 for $mobmod=3$.
- 178 $ud1f=0 \text{ m/v}^2$ Temperature coefficient for ud .
- 179 $ua1r=ua1f \text{ m/v}$ Temperature coefficient for uar .
- 180 $ub1r=ub1f \text{ m}^2/\text{v}^2$ Temperature coefficient for ubr .
- 181 $uc1r=uc1f \text{ m/v}^2$ Temperature coefficient for ucr .
- 182 $ud1r=0 \text{ m/v}^2$ Temperature coefficient for udr .
- 183 $rth=0 \text{ } \Omega$ Self-heating thermal resistance.
- 184 $rthg=0 \text{ 1/v}$ Gate-effect coefficient for Rth .
- 185 $rthb=0 \text{ 1/v}$ Body-effect coefficient for Rth .
- 186 $prt=0 \text{ } \Omega$ Temperature coefficient for Rds .
- 187 $trs=0 \text{ 1/C}$ Temperature parameter for source resistance.
- 188 $trd=0 \text{ 1/C}$ Temperature parameter for drain resistance.
- 189 $ute=-1.5$ Mobility temperature exponent.
- 190 $xti=3$ Saturation current temperature exponent.
- 191 $ptc=0 \text{ V/C}$ Surface potential temperature coefficient.
- 192 $tcv=0 \text{ V/C}$ Threshold voltage temperature coefficient.
- 193 $pta=0 \text{ V/C}$ Junction potential temperature coefficient.
- 194 $ptp=0 \text{ V/C}$ Sidewall junction potential temperature coefficient.
- 195 $cta=0 \text{ 1/C}$ Junction capacitance temperature coefficient.

Spectre Circuit Simulator Reference

Component Statements Part 2

196 `ctp=0` $1/C$ Sidewall junction capacitance temperature coefficient.

Noise Model Parameters

197 `noimod=1` Noise model selector.

198 `kf=0` Flicker (1/f) noise coefficient.

199 `af=1` Flicker (1/f) noise exponent.

200 `ef=1` Flicker (1/f) noise frequency exponent.

201 `noia=1e20` Oxide trap density coefficient. Default is $9.9e18$ for pmos.

202 `noib=5e4` Oxide trap density coefficient. Default is $2.4e3$ for pmos.

203 `noic=-1.4e-8` Oxide trap density coefficient. Default is $1.4e-8$ for pmos.

Operating Region Warning Control Parameters

204 `alarm=none` Forbidden operating region.
Possible values are `none`, `off`, `triode`, `sat`, `subth`, or `rev`.

205 `imax=1` A Maximum allowable current.

206 `jmax=1e8` A/m² Maximum allowable current density.

207 `bvj= ∞` V Junction reverse breakdown voltage.

208 `vbox=1e9` tox V Oxide breakdown voltage.

Cross-Term Dependent Parameters

The `imax` (`jmax`) parameter is used to aid convergence and prevent numerical overflow. The junction characteristics of the FET are accurately modeled for current (density) up to `imax` (`jmax`). For currents (density) above `imax` (`jmax`), the junction is modeled as a linear resistor and a warning is printed.

Output Parameters

1 `weff` (m) Effective channel width.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 2 `leff` (m) Effective channel length.
- 3 `rseff` (Ω) Effective source resistance.
- 4 `rdeff` (Ω) Effective drain resistance.

Operating-Point Parameters

- 1 `type=n` Transistor type.
Possible values are `n` or `p`.
- 2 `region=triode` Estimated operating region.
Possible values are `off`, `triode`, `sat`, or `subth`.
- 3 `reversed` Reverse mode indicator.
Possible values are `no` or `yes`.
- 4 `ids` (A) Resistive drain-to-source current.
- 5 `vgs` (V) Gate-source voltage.
- 6 `vds` (V) Drain-source voltage.
- 7 `vbs` (V) Bulk-source voltage.
- 8 `vth` (V) Threshold voltage.
- 9 `vdsat` (V) Drain-source saturation voltage.
- 10 `gm` (S) Common-source transconductance.
- 11 `gds` (S) Common-source output conductance.
- 12 `gmbs` (S) Body-transconductance.
- 13 `betaeff` (A/V^2) Effective β .
- 14 `cjd` (F) Drain-bulk junction capacitance.
- 15 `cjs` (F) Source-bulk junction capacitance.
- 16 `cgg` (F) C_{gg} .

Spectre Circuit Simulator Reference

Component Statements Part 2

- 17 `cgd (F)Cgd.`
- 18 `cgs (F)Cgs.`
- 19 `cgb (F)Cgb.`
- 20 `cdg (F)Cdg.`
- 21 `cdd (F)Cdd.`
- 22 `cds (F)Cds.`
- 23 `cdb (F)Cdb.`
- 24 `csg (F)Csg.`
- 25 `csd (F)Csd.`
- 26 `css (F)Css.`
- 27 `csb (F)Csb.`
- 28 `cbg (F)Cbg.`
- 29 `cbd (F)Cbd.`
- 30 `cbs (F)Cbs.`
- 31 `cbb (F)Cbb.`
- 32 `ron (Ω)On-resistance.`
- 33 `id (A)Resistive drain current.`
- 34 `ibulk (A)Resistive bulk current.`
- 35 `pwr (W)Power at op point.`
- 36 `gmoverid (1/V)Gm/Ids.`

Spectre Circuit Simulator Reference

Component Statements Part 2

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

a0cvf	M-145	dskip	M-117	mj	M-131	rsw	M-45
a0cvr	M-146	dsub	M-94	mjsw	M-135	rth	M-183
a0f	M-12	dvsatbf	M-55	mobmod	M-50	rthb	M-185
a0r	M-13	dvsatbr	M-58	n	M-116	rthg	M-184
a1	M-16	dvsatf	M-54	nch	M-21	sc	M-110
a2	M-17	dvsatr	M-57	nfactor	M-91	tcv	M-192
ad	I-4	dvt0	M-9	nlx	M-8	tlev	M-163
ad	M-154	dvt1	M-10	noia	M-201	tlevc	M-164
af	M-199	dvt2	M-11	noib	M-202	tnom	M-161
ags	M-18	dwb	M-40	noic	M-203	tox	M-41
alarm	M-204	dwc	M-140	noimod	M-197	trd	M-188
alpha0	M-99	dwg	M-39	nqsmod	M-139	trise	M-162
as	M-153	ef	M-200	nrd	I-7	trise	I-13
as	I-3	eg	M-165	nrd	M-157	trs	M-187
atlf	M-173	elm	M-149	nrs	I-8	type	OP-1
atlr	M-174	eta0f	M-95	nrs	M-158	type	M-1
atf	M-171	eta0r	M-97	pb	M-132	u0f	M-51
atr	M-172	etabf	M-96	pbsw	M-136	u0r	M-52
b0	M-14	etabr	M-98	pclmbf	M-83	ualf	M-175
b1	M-15	fc	M-133	pclmbr	M-84	ualr	M-179
beta0	M-100	fcs	M-137	pclmf	M-68	uaf	M-59
betaeff	OP-13	gap1	M-166	pclmgf	M-81	uar	M-63
binunit	M-49	gap2	M-167	pclmgr	M-82	ubl	M-176
bvj	M-207	gds	OP-11	pclmr	M-69	ublr	M-180
capmod	M-138	gm	OP-10	pd	I-6	ubf	M-60
cbb	OP-31	gmbs	OP-12	pd	M-156	ubr	M-64
cbd	OP-29	gmoverid	OP-36	pdiblc1f	M-70	uclf	M-177
cbd	M-129	hdif	M-112	pdiblc1r	M-71	uclr	M-181
cbg	OP-28	ibulk	OP-34	pdiblc2f	M-72	ucf	M-61
cbs	OP-30	id	OP-33	pdiblc2r	M-73	ucr	M-65
cbs	M-128	ids	OP-4	pdiblc1bf	M-74	udlf	M-178
cdb	OP-23	imax	M-205	pdiblc1br	M-75	udlr	M-182
cdd	OP-21	imelt	M-118	pdiblgf	M-85	udf	M-62
cdg	OP-20	is	M-115	pdiblgr	M-86	udr	M-66
cds	OP-22	jmax	M-206	prt	M-186	ute	M-189
cdsc	M-88	jmelt	M-119	prwb	M-46	vbox	M-208
cdscb	M-89	js	M-114	prwg	M-47	vbs	OP-7

Spectre Circuit Simulator Reference

Component Statements Part 2

cdscd M-90	k1 M-3	ps M-155	vds OP-6
cf M-144	k2 M-4	ps I-5	vdsat OP-9
cgb OP-19	k3 M-5	pscbelf M-76	vgs OP-5
cgbo M-122	k3b M-6	pscbelr M-79	voff M-93
cgd OP-17	ketaf M-19	pscbe2f M-77	vsatf M-53
cgdl M-125	ketar M-20	pscbe2r M-80	vsatr M-56
cgdo M-121	kf M-198	pscbeg M-78	vth OP-8
cgg OP-16	kt1 M-168	pta M-193	vtho M-2
cgs OP-18	kt1l M-169	ptc M-191	w I-1
cgs1 M-124	kt2 M-170	ptp M-194	w M-151
cgso M-120	l I-2	pwr OP-35	w0 M-7
cit M-92	l M-152	qgvd0f M-147	weff O-1
cj M-130	ld I-9	qgvd0r M-148	wint M-24
cjd OP-14	ldif M-111	rd M-103	wl M-32
cjs OP-15	leff O-2	rd0 M-42	wln M-33
cjsw M-134	lgcd M-105	rdc M-107	wmax M-38
ckappa M-126	lgcs M-104	rdd M-109	wmin M-37
clc M-142	lint M-23	rdeff O-4	wr M-48
cle M-143	ll M-25	rdw M-44	ww M-34
csb OP-27	lln M-26	region I-12	wwl M-36
csd OP-25	lmax M-31	region OP-2	wwn M-35
csg OP-24	lmin M-30	reversed OP-3	xj M-22
css OP-26	ls I-10	ron OP-32	xl M-160
cta M-195	lw M-27	rs M-102	xpart M-150
ctp M-196	lw1 M-29	rs0 M-43	xti M-190
delta M-87	lwn M-28	rsc M-106	xw M-159
deltaacc M-127	m I-11	rseff O-3	
dlc M-141	meto M-123	rsh M-101	
drout M-67	minr M-113	rss M-108	

Two Terminal Inductor (inductor)

Description

The inductance of this component can be a function of temperature or branch current. If you do not specify the inductance in the instance statement, it is taken from the model.

This device is supported within altergroups.

If the polynomial coefficients vector (coeffs=[c1 c2 ...]) is specified, the inductor is nonlinear and the inductance is

$$L(I) = L(\text{inst}) * (1 + c1 * I + c2 * I^2 + \dots).$$

Spectre Circuit Simulator Reference

Component Statements Part 2

The branch flux as a function of current is

$$\text{Flux}(I) = L(\text{inst}) * I * (1 + 1/2 * c1 * I + 1/3 * c2 * I^2 + \dots)$$

where c_k is the k th entry in the coefficient vector.

The value of the inductor as a function of the temperature is given by:

$$L(T) = L(\text{tnom}) * [1 + tc1 * (T - \text{tnom}) + tc2 * (T - \text{tnom})^2].$$

where $T = \text{trise}(\text{inst}) + \text{temp}$

if $\text{trise}(\text{inst})$ is given,

otherwise

$T = \text{trise}(\text{model}) + \text{temp}$

Sample Instance Statement

Without model:

```
133 (0 net29) inductor l=10e-9 r=1 m=1
```

With model:

```
133 (0 net29) ind l=10e-9 r=1 m=1
```

Sample Model Statement

```
model ind inductor l=6e-9 r=1 tc1=1e-12 tc2=1e-12 tnom=25
```

Instance Definition

```
Name 1 2 ModelName parameter=value ...
```

```
Name 1 2 inductor parameter=value ...
```

Instance Parameters

1 l (H) Inductance.

2 r (Ω) Resistance.

3 $m=1$ Multiplicity factor.

4 trise Temperature rise from ambient.

Spectre Circuit Simulator Reference

Component Statements Part 2

5 `ic (A)` Initial condition.

6 `isnoisy=yes` Should inductor resistance generate noise.
Possible values are `no` or `yes`.

Model Definition

`model modelName inductor parameter=value ...`

Model Parameters

1 `l=0 H` Default inductance.

2 `r=0 Ω` Default resistance.

3 `tc1=0 1/C` Linear temperature coefficient.

4 `tc2=0 C^{-2}` Quadratic temperature coefficient.

5 `trise=0 C` Default `trise` value for instance.

6 `tnom (C)` Parameters measurement temperature. Default set by `options`.

7 `rforce=1e9 Ω^2` Resistance used when forcing nodesets and initial conditions.

8 `coeffs=[...]` Vector of polynomial inductance coefficients.

9 `scalei=1` Inductance scaling factor.

Noise Model Parameters

10 `kf=0` Flicker (1/f) noise coefficient.

11 `af=2` Flicker (1/f) noise exponent.

Output Parameters

1 `indef (H)` Effective inductance.

Spectre Circuit Simulator Reference

Component Statements Part 2

Operating-Point Parameters

- 1 `ind` (H) Inductance at operating point.
- 2 `i` (A) Current at operating point.

Parameter Index

In the following index, `I` refers to instance parameters, `M` refers to the model parameters section, `O` refers to the output parameters section, and `OP` refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of `M-35` means the 35th model parameter.

<code>af</code>	<code>M-11</code>	<code>indeff</code>	<code>O-1</code>	<code>m</code>	<code>I-3</code>	<code>tc1</code>	<code>M-3</code>
<code>coeffs</code>	<code>M-8</code>	<code>isnoisy</code>	<code>I-6</code>	<code>r</code>	<code>M-2</code>	<code>tc2</code>	<code>M-4</code>
<code>i</code>	<code>OP-2</code>	<code>kf</code>	<code>M-10</code>	<code>r</code>	<code>I-2</code>	<code>tnom</code>	<code>M-6</code>
<code>ic</code>	<code>I-5</code>	<code>l</code>	<code>M-1</code>	<code>rforce</code>	<code>M-7</code>	<code>trise</code>	<code>I-4</code>
<code>ind</code>	<code>OP-1</code>	<code>l</code>	<code>I-1</code>	<code>scalei</code>	<code>M-9</code>	<code>trise</code>	<code>M-5</code>

Interconnect Capacitance (intcap)

Description

`Intcap` is a model for the calculation of the interconnect capacitance, which takes into account the local layer composition and the tracks spacing width. It is described in the Philips MOST Modelbook (Dec.96) as `INTCAP` model.

(c) Philips Electronics N.V. 1993,1996

The model is extended by the device parameters `lxbelps`, `lxbelin` and `lxbelins`, according to a specification by H.Okel (I&A Hamburg).

This device is supported within `altergroups`.

This device is dynamically loaded from the shared object `/vobs/spectre_dev/tools.sun4v/spectre/lib/cmi/2.0.doc/libphilips.so`

Spectre Circuit Simulator Reference

Component Statements Part 2

Sample Instance Statement

```
intc (net9 net12) intconcap m=1 ael=2.5e-15 ain=2e-15 aps=1.8e-15
```

Sample Model Statement

```
model intconcap intcap cbps=1.5e-13 cebpsm=0.9e-15 cebpsi=0.83e-15 cbin=1.45e-13  
cbins=1.4e-13
```

Instance Definition

Name n1 n2 ModelName parameter=value ...

Instance Parameters

- 1 m=1 Multiplicity factor.
- 2 ael=0.0 m² The common area of EL track of the reference electrode.
- 3 ain=0.0 m² The common area of IN track of the reference electrode.
- 4 ains=0.0 m² The common area of INS track of the reference electrode.
- 5 aps=0.0 m² The common area of PS track of the reference electrode.
- 6 lbel=0.0 m The sum of periphery length of EL-segments common to node n2 downwards.
- 7 lbin=0.0 m The sum of periphery length of IN-segments to node n2 downwards.
- 8 lbins=0.0 m The sum of periphery length of INS-segments common to node n2 downwards.
- 9 lbps=0.0 m The sum of periphery length of PS-segments common to node n2 downwards.
- 10 lfbel=0.0 m The sum of periphery length-factor products EL downwards.
- 11 lfbin=0.0 m The sum of periphery length-factor products IN downwards.
- 12 lfbins=0.0 m The sum of periphery length-factor products INS downwards.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 13 `lfbps=0.0` mThe sum of periphery length-factor products `PS` downwards.
- 14 `lftel=0.0` mThe sum of periphery length-factor products `EL` upwards.
- 15 `lftin=0.0` mThe sum of periphery length-factor products `IN` upwards.
- 16 `lftins=0.0` mThe sum of periphery length-factor products `INS` upwards.
- 17 `lftps=0.0` mThe sum of periphery length-factor products `PS` upwards.
- 18 `ltel=0.0` mThe sum of periphery length of `EL`-segments common to node `n2` upwards.
- 19 `ltin=0.0` mThe sum of periphery length of `IN`-segments common to node `n2` upwards.
- 20 `ltins=0.0` mThe sum of periphery length of `INS`-segments common to node `n2` upwards.
- 21 `ltps=0.0` mThe sum of periphery length of `PS`-segments common to node `n2` upwards.
- 22 `ldsel=0.0` mThe sum of Li/Si quotients for `EL` tracks.
- 23 `ldsins=0.0` mThe sum of Li/Si quotients for `IN` tracks.
- 24 `ldsins=0.0` mThe sum of Li/Si quotients for `INS` tracks.
- 25 `ldsp=0.0` mThe sum of Li/Si quotients for `PS` tracks.
- 26 `lxbins=0.0` mThe sum of Li/Si quotients for an `IN` track in parallel with an `PS` track.
- 27 `lxbinsin=0.0` mThe sum of Li/Si quotients for an `INS` track in parallel with an `IN` track.
- 28 `lxbinsps=0.0` mThe sum of Li/Si quotients for an `INS` track in parallel with an `PS` track.
- 29 `lxbelps=0.0` mThe sum of Li/Si quotients for an `EL` track in parallel with an `PS` track.
- 30 `lxbelin=0.0` mThe sum of Li/Si quotients for an `EL` track in parallel with an `IN` track.
- 31 `lxbelins=0.0` mThe sum of Li/Si quotients for an `EL` track in parallel with an `INS` track.

The Spectre option `scale`, default value is 1.0, scales the geometric parameters. The actual areas (parameters starting with letter `a`) are equal

$$axxx * scale ^ 2$$

Spectre Circuit Simulator Reference

Component Statements Part 2

The actual lengths (parameters starting with letter l) are equal

$l_{xxx} * scale$

Model Definition

model modelName intcap parameter=value ...

Model Parameters

- 1 $cbps=0.0 \text{ F/m}^2$ Bottom capacitance, PS to node n2.
- 2 $cebpsm=0.0 \text{ F/m}$ Edge to bottom capacitance (PS), 1.0um spacing.
- 3 $cebpsi=0.0 \text{ F/m}$ Edge to bottom capacitance (PS), single track.
- 4 $cetpsm=0.0 \text{ F/m}$ Edge to top capacitance (PS), 1.0um spacing.
- 5 $cetpsi=0.0 \text{ F/m}$ Edge to top capacitance (PS), single track.
- 6 $cbin=0.0 \text{ F/m}^2$ Bottom capacitance, IN to node n2.
- 7 $cebinm=0.0 \text{ F/m}$ Edge to bottom capacitance (IN), 1.0um spacing.
- 8 $cebini=0.0 \text{ F/m}$ Edge to bottom capacitance (IN), single track.
- 9 $cetinm=0.0 \text{ F/m}$ Edge to top capacitance (IN), 1.0um spacing.
- 10 $cetini=0.0 \text{ F/m}$ Edge to top capacitance (IN), single track.
- 11 $cbins=0.0 \text{ F/m}^2$ Bottom capacitance, INS to node n2.
- 12 $cebinsm=0.0 \text{ F/m}$ Edge to bottom capacitance (INS), 1.0um spacing.
- 13 $cebinsi=0.0 \text{ F/m}$ Edge to bottom capacitance (INS), single track.
- 14 $cetinsm=0.0 \text{ F/m}$ Edge to top capacitance (INS), 1.0um spacing.
- 15 $cetinsi=0.0 \text{ F/m}$ Edge to top capacitance (INS), single track.
- 16 $cbel=0.0 \text{ F/m}^2$ Bottom capacitance, EL to node n2.

Spectre Circuit Simulator Reference

Component Statements Part 2

17 `cebelm=0.0` F/mEdge to bottom capacitance (EL), 1.0um spacing.

18 `cebeli=0.0` F/mEdge to bottom capacitance (EL), single track.

19 `cetelm=0.0` F/mEdge to top capacitance (EL), 1.0um spacing.

20 `ceteli=0.0` F/mEdge to top capacitance (EL), single track.

21 `cecps=0.0` F/mLateral capacitance (PS), 1.0um spacing.

22 `cecin=0.0` F/mLateral capacitance (IN), 1.0um spacing.

23 `cecins=0.0` F/mLateral capacitance (INS), 1.0um spacing.

24 `cecel=0.0` F/mLateral capacitance (EL), 1.0um spacing.

Output Parameters

1 `cap` (F) Total Capacitance.

Parameter Index

In the following index, I refers to instance parameters, M refers to the model parameters section, O refers to the output parameters section, and OP refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of M-35 means the 35th model parameter.

<code>ael</code>	I-2	<code>cebinsm</code>	M-12	<code>cetpsm</code>	M-4	<code>lftin</code>	I-15
<code>ain</code>	I-3	<code>cebpsi</code>	M-3	<code>lbel</code>	I-6	<code>lftins</code>	I-16
<code>ains</code>	I-4	<code>cebpsm</code>	M-2	<code>lbin</code>	I-7	<code>lftps</code>	I-17
<code>aps</code>	I-5	<code>cecel</code>	M-24	<code>lbins</code>	I-8	<code>ltel</code>	I-18
<code>cap</code>	O-1	<code>cecin</code>	M-22	<code>lbsps</code>	I-9	<code>ltin</code>	I-19
<code>cbel</code>	M-16	<code>cecins</code>	M-23	<code>ldsel</code>	I-22	<code>ltins</code>	I-20
<code>cbin</code>	M-6	<code>cecps</code>	M-21	<code>ldsins</code>	I-23	<code>ltps</code>	I-21
<code>cbins</code>	M-11	<code>ceteli</code>	M-20	<code>ldsps</code>	I-25	<code>lxbelin</code>	I-30
<code>cbps</code>	M-1	<code>cetelm</code>	M-19	<code>lfbel</code>	I-10	<code>lxbelins</code>	I-31
<code>cebeli</code>	M-18	<code>cetini</code>	M-10	<code>lfbins</code>	I-11	<code>lxbelps</code>	I-29
<code>cebelm</code>	M-17	<code>cetinsm</code>	M-14	<code>lfbps</code>	I-13	<code>lxbinsps</code>	I-28
<code>cebini</code>	M-8	<code>cetpsi</code>	M-5	<code>lftel</code>	I-14	<code>lxbinsin</code>	I-27
<code>cebinm</code>	M-7					<code>lxbinsps</code>	I-28
<code>cebinsi</code>	M-13					<code>m</code>	I-1

Current Probe (iprobe)

Description

Current through the probe is computed and is defined to be positive if it flows from the input node, through the probe, to the output node. The current variable is given the name of the `iprobe` instance, so you cannot create an `iprobe` with the same name as a circuit node.

This device is not supported within altergroup.

Sample Instance Statement

```
ip (1 0) iprobe
```

Instance Definition

```
Name in out iprobe
```

Independent Current Source (isource)

Description

The value of the DC current as a function of the temperature is given by:

$$I(T) = I(tnom) * [1 + tc1 * (T - tnom) + tc2 * (T - tnom)^2].$$

Sample Instance Statement

```
i1 (in 0) isource dc=0 type=pulse delay=10n val0=0 val1=500u period=500n rise=1n  
fall=1n width=250n
```

Instance Definition

```
Name sink src isource parameter=value ...
```

Positive current exits the source node and enters the sink node.

Instance Parameters

1 `dc=0` ADC value.

Spectre Circuit Simulator Reference

Component Statements Part 2

General Waveform Parameters

- 2 `type=dc` Waveform type.
Possible values are `dc`, `pulse`, `pwl`, `sine`, or `exp`.
- 3 `fundname` Name of the fundamental frequency. Must be specified if the source is active during a `pdisto` analysis or it is the active clock during an `envlp` analysis.
- 4 `delay=0 s` Waveform delay time.

Pulse Waveform Parameters

- 5 `val0=0 A` Zero value used in pulse and exponential waveforms.
- 6 `val1=1 A` One value used in pulse and exponential waveforms.
- 7 `period= ∞ s` Period of waveform.
- 8 `rise (s)` Rise time for pulse waveform (time for transition from `val0` to `val1`).
- 9 `fall (s)` Fall time for pulse waveform (time for transition from `val1` to `val0`).
- 10 `width= ∞ s` Pulse width (duration of `val1`).

PWL Waveform Parameters

- 11 `file` Name of file containing waveform.
- 12 `wave=[...]` Vector of time/value pairs that defines waveform.
- 13 `offset=0 A` ADC offset for the PWL waveform.
- 14 `scale=1` Scale factor for the PWL waveform.
- 15 `stretch=1` Scale factor for time given for the PWL waveform.
- 16 `allbrkpts` All the points in the PWL waveform are breakpoints if set to yes. Default is yes if the number of points is less than 20.
Possible values are `no` or `yes`.
- 17 `pwlperiod (s)` Period of the periodic PWL waveform.

Spectre Circuit Simulator Reference

Component Statements Part 2

18 `twidth=pwlperiod/1000 s`

Transition width used when making PWL waveforms periodic.

Sinusoidal Waveform Parameters

19 `sinedc=dc` ADC level for sinusoidal waveforms.

20 `ampl=1` A Peak amplitude of sinusoidal waveform.

21 `freq=0` Hz Frequency of sinusoidal waveform.

22 `sinephase=0` °Phase of sinusoid when `t=delay`.

23 `ampl2=1` A Peak amplitude of second sinusoidal waveform.

24 `freq2=0` Hz Frequency of second sinusoidal waveform.

25 `sinephase2=0` °Phase of second sinusoid when `t=delay`.

26 `fundname2` Name of the fundamental frequency associated with `freq2`. Must be specified if `freq2` is used in a `pdisto` analysis.

27 `fmodindex=0` FM index of modulation for sinusoidal waveform.

28 `fmodfreq=0` Hz FM modulation frequency for sinusoidal waveform.

29 `ammodindex=0` AM index of modulation for sinusoidal waveform.

30 `ammodfreq=0` Hz AM modulation frequency for sinusoidal waveform.

31 `ammodphase=0` °AM phase of modulation for sinusoidal waveform.

32 `damp=0` 1/s Damping factor for sinusoidal waveform.

Exponential Waveform Parameters

33 `td1=0` s Rise start time for exponential wave.

34 `taul (s)` Rise time constant for exponential wave.

35 `td2 (s)` Fall start time for exponential wave.

Spectre Circuit Simulator Reference

Component Statements Part 2

36 `tau2 (s)` Fall time constant for exponential wave.

Noise Parameters

37 `noisefile` Name of file containing excess spot noise data in the form of frequency-noise pairs.

38 `noisevec=[...]` A^2/Hz Excess spot noise as a function of frequency in the form of frequency-noise pairs.

Small Signal Parameters

39 `mag=0` A Small signal current.

40 `phase=0` $^\circ$ Small signal phase.

41 `xfmag=1` A/A Transfer function analysis magnitude.

42 `pacmag=0` A Periodic AC analysis magnitude.

43 `pacphase=0` $^\circ$ Periodic AC analysis phase.

Multiplication Factor Parameters

44 `m=1` Multiplicity factor.

Temperature Effects Parameters

45 `tc1=0` $1/C$ First order temperature coefficient.

46 `tc2=0` C^{-2} Second order temperature coefficient.

47 `tnom=27` C Parameter measurement temperature. Default set by options.

If you do not specify the DC value, it is assumed to be the `time=0` value of the waveform.

In DC analyses, the only active parameters are `dc`, `m`, and the temperature coefficient parameters. In AC analyses, the only active parameters are `m`, `mag` and `phase`. In transient analyses, all parameters are active except the small signal parameters and the noise parameters. The `type` parameter selects which type of waveform is generated. You may

Spectre Circuit Simulator Reference

Component Statements Part 2

specify parameters for more than one waveform type, and use the `alter` statement to change the waveform type between analyses.

A vector of time-value pairs describes the piecewise linear waveform. As an alternative, you can read the waveform from a file. In this case, you give time-value pairs one pair per line with a space or tab between the time and the value.

If you set `allbrkpts` to `yes`, you force the simulator to place time points at each point specified in a PWL waveform during a transient analysis. This can be very expensive for waveforms with many points. If you set `allbrkpts` to `no`, Spectre inspects the waveform, looking for abrupt changes, and forces time points only at those changes.

The PWL waveform is periodic if you specify `pwlperiod`. If the value of the waveform specified is not exactly the same at both its beginning and its end, then you must provide a nonzero value `twidth`. Before repeating, the waveform changes linearly in an interval of `twidth` from its value at `(period - twidth)` to its value at the beginning of the waveform. Thus `twidth` must always be less than `period`.

You can give the excess noise of the source as a file or specify it with a vector of frequency-noise pairs. For a file, give the frequency-noise pairs one pair per line with a space or tab between the frequency and noise values.

Operating-Point Parameters

- 1 `i` (A) Current through the source.
- 2 `v` (V) Voltage across the source.
- 3 `pwr` (W) Power dissipation.

Parameter Index

In the following index, `I` refers to instance parameters, `M` refers to the model parameters section, `O` refers to the output parameters section, and `OP` refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of `M-35` means the 35th model parameter.

<code>allbrkpts</code>	I-16	<code>freq</code>	I-21	<code>phase</code>	I-40	<code>td1</code>	I-33
<code>ammodfreq</code>	I-30	<code>freq2</code>	I-24	<code>pwlperiod</code>	I-17	<code>td2</code>	I-35
<code>ammodindex</code>	I-29	<code>fundname</code>	I-3	<code>pwr</code>	OP-3	<code>tnom</code>	I-47
<code>ammodphase</code>	I-31	<code>fundname2</code>	I-26	<code>rise</code>	I-8	<code>twidth</code>	I-18

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Component Statements Part 2

ampl	I-20	i	OP-1	scale	I-14	type	I-2
ampl2	I-23	m	I-44	sinedc	I-19	v	OP-2
damp	I-32	mag	I-39	sinephase	I-22	val0	I-5
dc	I-1	noise	file I-37	sinephase2	I-25	val1	I-6
delay	I-4	noisevec	I-38	stretch	I-15	wave	I-12
fall	I-9	offset	I-13	taul	I-34	width	I-10
file	I-11	pacmag	I-42	tau2	I-36	xfmag	I-41
fmmodfreq	I-28	pacphase	I-43	tcl	I-45		
fmmodindex	I-27	period	I-7	tc2	I-46		

Junction Field Effect Transistor (jfet)

Description

The JFET model is derived from the FET model of Shichman and Hodges. JFETs require that you use a model statement.

This device is supported within altergroups.

Sample Instance Statement

```
jf1 (net1 net2 0) jmod area=1
```

Sample Model Statement

```
model jmod jfet beta=9e-5 lambda=0 type=n vt0=-18.7 rd=10 rs=10 cgs=1.3e-13 pb=0.65
```

Instance Definition

Name d g s [b] ModelName parameter=value ...

You do not have to specify the back gate terminal when you use the four-terminal model. If left unspecified, the substrate is connected to ground.

Instance Parameters

- 1 area=1 Junction area factor.
- 2 m=1 Multiplicity factor.
- 3 region=triode Estimated operating region.
Possible values are off, triode, sat, subth, or breakdown.

Spectre Circuit Simulator Reference

Component Statements Part 2

Model Definition

`model modelName jfet parameter=value ...`

Model Parameters

Device Type Parameters

- 1 `type=n` Transistor type.
Possible values are n or p.

Drain Current Model Parameters

- 2 `level=1` Drain current model level selector.
- 3 `vto=-2` $V_{Pinchoff}$ voltage.
- 4 `beta=0.0001` A/V^2 Transconductance parameter.
- 5 `lambda=0` $1/V$ Channel length modulation parameter.
- 6 `lambda1=0` $1/V$ Gate dependence of channel length modulation parameter.
- 7 `np=2` Power-law exponent.
- 8 `alpha=2` Triode-to-saturation transition parameter.
- 9 `io=0` A Subthreshold current parameter.
- 10 `ns=1` Subthreshold swing parameter.
- 11 `ai=0` $1/V$ Impact ionization current coefficient.
- 12 `bi=0` V Impact ionization current exponent.

Four Terminal Threshold Voltage Parameters

- 13 `vtop=0.6` V_{Back} gate to channel junction potential.
- 14 `vtos=1.2` V Threshold voltage slope.
- 15 `vtoe=0.33` Threshold voltage exponent.

Spectre Circuit Simulator Reference

Component Statements Part 2

16 `vtoc=-3.3` V Threshold voltage constant.

Parasitic Resistance Parameters

17 `rd=0` Ω Drain resistance (/area).

18 `rs=0` Ω Source resistance (/area).

19 `rg=0` Ω Gate resistance (/area).

20 `rb=0` Ω Back gate resistance (/area).

21 `minr=0.1` Ω Minimum source/drain/gate resistance.

Junction Diode Model Parameters

22 `is=1e-14` AGate saturation current (*area).

23 `n=1`Emission coefficient for G-D and G-S junctions.

24 `imelt='imaxA'` Explosion current (*area).

25 `dskip=yes`Use simple piece-wise linear model for diode currents below $0.1 \cdot i_{abstol}$.
Possible values are `no` or `yes`.

Junction Capacitance Model Parameters

26 `tt=0` sTransit time.

27 `cgs=0` FGate-source zero-bias junction capacitance (*area).

28 `cgd=0` FGate-drain zero-bias junction capacitance (*area).

29 `mj=1/2`Junction grading coefficient.

30 `pb=1` V Gate-junction potential.

31 `fc=0.5`Junction capacitor forward-bias threshold.

Spectre Circuit Simulator Reference

Component Statements Part 2

Four Terminal Junction Parameters

- 32 `isb=1e-14` A Back gate-saturation current (*area).
- 33 `nb=1` Emission coefficient for back gate-junctions.
- 34 `cgb=0` F Back gate-source zero-bias junction capacitance (*area).
- 35 `cgbd=0` F Back gate-drain zero-bias junction capacitance (*area).
- 36 `mjb=1/2` Back gate-junction grading coefficient.
- 37 `pbb=1` V Back gate-junction potential.

Temperature Effect Parameters

- 38 `tnom (C)` Parameters measurement temperature. Default set by `options`.
- 39 `trise=0` C Temperature rise from ambient.
- 40 `xti=3` Temperature exponent for effect on `is`.
- 41 `tlev=0` DC temperature selector.
- 42 `tlevc=0` AC temperature selector.
- 43 `eg=1.12452` V Energy band gap.
- 44 `gap1=7.02e-4` V/C Band gap temperature coefficient.
- 45 `gap2=1108` C Band gap temperature offset.
- 46 `tcv=0` 1/C Threshold voltage temperature coefficient.
- 47 `bto=0` C Transconductance parameter temperature offset.
- 48 `bte=0` Transconductance parameter temperature exponent.
- 49 `lto=0` C Channel length modulation parameters temperature offset.
- 50 `lte=0` Channel length modulation parameters temperature exponent.
- 51 `tc1=0` 1/C Linear temperature coefficient for parasitic resistors.

Spectre Circuit Simulator Reference

Component Statements Part 2

52 `tc2=0` C^{-2} Quadratic temperature coefficient for parasitic resistors.

Operating Region Warning Control Parameters

53 `alarm=none` Forbidden operating region.
Possible values are `none`, `off`, `triode`, `sat`, `subth`, or `rev`.

54 `imax=1` A Maximum allowable current (*area).

55 `bvj= ∞` V Junction reverse breakdown voltage.

Noise Parameters

56 `kf=0` Flicker noise (1/f) coefficient.

57 `af=1` Flicker noise (1/f) exponent.

58 `kfd=0` Flicker noise (1/f) coefficient for gate diodes.

59 `afg=1` Flicker noise (1/f) exponent for gate diodes.

`Imax` and `Imelt`

The `imax` parameter aids convergence and prevents numerical overflow. The junction characteristics of the device are accurately modeled for current up to `imax`. If `imax` is exceeded during iterations, the linear model is substituted until the current drops below `imax` or until convergence is achieved. If convergence is achieved with the current exceeding `imax`, the results are inaccurate, and Spectre prints a warning.

A separate model parameter, `imelt`, is used as a limit warning for the junction current. This parameter can be set to the maximum current rating of the device. When any component of the junction current exceeds `imelt`, note that base and collector currents are composed of many exponential terms, Spectre issues a warning and the results become inaccurate. The junction current is linearized above the value of `imelt` to prevent arithmetic exception, with the exponential term replaced by a linear equation at `imelt`.

The `bv` parameter is used to detect the junction breakdown only. The breakdown currents of the junctions are not modeled.

Spectre Circuit Simulator Reference

Component Statements Part 2

Operating-Point Parameters

- 1 `type=n` Transistor type.
Possible values are `n` or `p`.
- 2 `region=triode` Estimated operating region.
Possible values are `off`, `triode`, `sat`, `subth`, or `breakdown`.
- 3 `ids` (A) Resistive drain current.
- 4 `vgs` (V) Gate-source voltage.
- 5 `vds` (V) Drain-source voltage.
- 6 `vth` (V) Threshold at op point.
- 7 `vdsat` (V) Drain saturation voltage.
- 8 `gm` (S) Common-source transconductance.
- 9 `gds` (S) Common-source output conductance.
- 10 `cgs` (F) Gate-source capacitance.
- 11 `cgd` (F) Gate-drain capacitance.
- 12 `ig` (A) Resistive gate current.
- 13 `pwr` (W) Power at op point.
- 14 `qd` (V) Threshold at op point.
- 15 `qg` (V) Threshold at op point.
- 16 `qs` (V) Threshold at op point.
- 17 `qb` (V) Threshold at op point.

Parameter Index

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Component Statements Part 2

description for that parameter. For example, a reference of M-35 means the 35th model parameter.

af	M-57	gap1	M-44	mj	M-29	tc2	M-52
afg	M-59	gap2	M-45	mjb	M-36	tcv	M-46
ai	M-11	gds	OP-9	n	M-23	tlev	M-41
alarm	M-53	gm	OP-8	nb	M-33	tlevc	M-42
alpha	M-8	ids	OP-3	np	M-7	tnom	M-38
area	I-1	ig	OP-12	ns	M-10	trise	M-39
beta	M-4	imax	M-54	pb	M-30	tt	M-26
bi	M-12	imelt	M-24	pbb	M-37	type	M-1
bte	M-48	io	M-9	pwr	OP-13	type	OP-1
bto	M-47	is	M-22	qb	OP-17	vds	OP-5
bvj	M-55	isb	M-32	qd	OP-14	vdsat	OP-7
cgbd	M-35	kf	M-56	qg	OP-15	vgs	OP-4
cgbs	M-34	kfd	M-58	qs	OP-16	vth	OP-6
cgd	OP-11	lambda	M-5	rb	M-20	vto	M-3
cgd	M-28	lambda1	M-6	rd	M-17	vtoc	M-16
cgs	M-27	level	M-2	region	OP-2	vtoe	M-15
cgs	OP-10	lte	M-50	region	I-3	vtop	M-13
dskip	M-25	lto	M-49	rg	M-19	vtos	M-14
eg	M-43	m	I-2	rs	M-18	xti	M-40
fc	M-31	minr	M-21	tc1	M-51		

Junction Capacitor (juncap)

Description

The juncap model is intended to describe the behavior of the diodes that are formed by the source, drain or well-to-bulk junctions in MOS devices. It is described in the Philips MOST Modelbook (Dec.93) as JUNCAP model. Information on how to obtain this document can be found on Source Link by searching for Philips.

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In extension to the modelbook description a minimum conductance g_{min} is inserted between the juncap nodes, to aid convergence. The value of g_{min} is set by an options statement, default = 1e-12 S.

The $imax$ parameter is used to aid convergence and to prevent numerical overflow. The junction characteristics of the junction capacitor are accurately modeled for currents up to

Spectre Circuit Simulator Reference

Component Statements Part 2

`imax`. For currents above `imax`, the junction is modeled as a linear resistor and a warning is printed.

This device is supported within altergroups.

This device is dynamically loaded from the shared object `/vobs/spectre_dev/tools.sun4v/spectre/lib/cmi/2.0.doc/libphilips.so`

Sample Instance Statement

```
c2 (1 2) capmod ab=7e-12 lg=5e-6 region=rev
```

Sample Model Statement

```
model capmod juncap type=n cjbr=0.2 cjgr=0.2 cjsr=0.2 tref=25 jsgbr=2e-3 jsdbr=0.28e-3  
jsggr=1e-5 jsdgr=0.33e-6 vdsr=0.8 vdgr=0.8 vdbr=0.8
```

Instance Definition

Name `n` [b] ModelName parameter=value ...

Instance Parameters

- 1 `ab=1.0 scale2 m2`
Diffusion area. Scale set by option scale.
- 2 `ls=1.0 scale m`Length of the sidewall of the diffusion area `ab` which is not under the gate. Scale set by option scale.
- 3 `lg=1.0 scale m`Length of the sidewall of the diffusion area `ab` which is under the gate. Scale set by option scale.
- 4 `m=1.0`Multiplicity factor.
- 5 `region=rev`Estimated DC operating region, used as a convergence aid.
Possible values are `fwd` or `rev`.

Model Definition

model modelName juncap parameter=value ...

Spectre Circuit Simulator Reference

Component Statements Part 2

Model Parameters

Structural parameters

- 1 `type=n` Type of the juncap device.
Possible values are n or p.
- 2 `vb (V)` Not used for juncap model.
- 3 `bv (V)` Alias of `vb`.
- 4 `level` Not used for juncap model.

Current parameters

- 5 `jsgbr=1.0e-3 A/m2`
Bottom saturation-current density due to electron-hole generation at reference voltage.
- 6 `jsdbr=1.0e-3 A/m2`
Bottom saturation-current density due to diffusion from back contact.
- 7 `jsgsr=1.0e-3 A/m` Sidewall saturation-current density due to electron-hole generation at reference voltage.
- 8 `jsdsr=1.0e-3 A/m` Sidewall saturation-current density due to diffusion from back contact.
- 9 `jsggr=1.0e-3 A/m` Gate edge saturation-current density due to electron-hole generation at reference voltage.
- 10 `jsdgr=1.0e-3 A/m` Gate edge saturation-current density due to diffusion from back contact.
- 11 `imax=1.0 A` Explosion current.

Temperature effects parameters

- 12 `dta=0.0 K` Temperature offset of the juncap element with respect to ambient temperature.

Spectre Circuit Simulator Reference

Component Statements Part 2

13 `trise=0.0` κ Alias of `dta`.

14 `tr` (C) Temperature at which the parameters have been determined. Default set by option `tnom`.

15 `tref` (C) Alias of `tr`. Default set by option `tnom`.

16 `tnom` (C) Alias of `tr`. Default set by option `tnom`.

Junction capacitance parameters

17 `cjbr=1.0e-12` F/m² Bottom junction capacitance at reference voltage.

18 `cjsr=1.0e-12` F/m Sidewall junction capacitance at reference voltage.

19 `cjgr=1.0e-12` F/m Gate edge junction capacitance at reference voltage.

Emission coefficient parameters

20 `nb=1.0` Emission coefficient of the bottom forward current.

21 `ns=1.0` Emission coefficient of the sidewall forward current.

22 `ng=1.0` Emission coefficient of the gate-edge forward current.

Voltage parameters

23 `vr=0.0` V Voltage at which parameters have been determined.

24 `vdbr=1.0` V Diffusion voltage of the bottom junction at reference temperature.

25 `vdsr=1.0` V Diffusion voltage of the sidewall junction at reference temperature.

26 `vdgr=1.0` V Diffusion voltage of the gate edge junction at reference temperature.

Grading coefficient parameters

27 `pb=0.4` Bottom-junction grading coefficient.

28 `ps=0.4` Sidewall-junction grading coefficient.

Spectre Circuit Simulator Reference

Component Statements Part 2

29 $p_g=0.4$ Gate edge-junction grading coefficient.

Output Parameters

- 1 c_{jb} (F) Capacitance of bottom area a_b .
- 2 c_{js} (F) Capacitance of locos-edge l_s .
- 3 c_{jg} (F) Capacitance of gate-edge l_g .
- 4 i_{sdb} (A) Diffusion saturation-current of bottom area a_b .
- 5 i_{sds} (A) Diffusion saturation-current of locos-edge l_s .
- 6 i_{sdg} (A) Diffusion saturation-current of gate-edge l_g .
- 7 i_{sgb} (A) Generation saturation-current of bottom area a_b .
- 8 i_{sgs} (A) Generation saturation-current of locos-edge l_s .
- 9 i_{sgg} (A) Generation saturation-current of gate-edge l_g .
- 10 v_{db} (V) Diffusion voltage of bottom area a_b .
- 11 v_{ds} (V) Diffusion voltage of locos-edge l_s .
- 12 v_{dg} (V) Diffusion voltage of gate-edge l_g .

Operating-Point Parameters

- 1 v (V) Diode bias voltage ($v = v_a - v_k$).
- 2 i (A) Total resistive current from anode to cathode ($i = i_a = -i_k$).
- 3 g_m (S) Total differential conductance.
- 4 q (Coul) Total junction charge ($q = q_a = -q_k$).
- 5 c (F) Total capacitance.
- 6 pwr (W) Power.

Spectre Circuit Simulator Reference

Component Statements Part 2

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

ab	I-1	isdb	O-4	lg	I-3	tr	M-14
bv	M-3	isdg	O-6	ls	I-2	tref	M-15
c	OP-5	isds	O-5	m	I-4	trise	M-13
cjb	O-1	isgb	O-7	nb	M-20	type	M-1
cjbr	M-17	isgg	O-9	ng	M-22	v	OP-1
cjg	O-3	isgs	O-8	ns	M-21	vb	M-2
cjgr	M-19	jsdbr	M-6	pb	M-27	vdb	O-10
cjs	O-2	jsdgr	M-10	pg	M-29	vdbr	M-24
cjsr	M-18	jsdsr	M-8	ps	M-28	vdg	O-12
dta	M-12	jsgbr	M-5	pwr	OP-6	vdgr	M-26
gm	OP-3	jsggr	M-9	q	OP-4	vds	O-11
i	OP-2	jsgsr	M-7	region	I-5	vdsr	M-25
imax	M-11	level	M-4	tnom	M-16	vr	M-23

MISN Field Effect Transistor (misnan)

Description

The MISN model is formulated in terms of solutions for the boundary surface potentials of the channel and has the inherent property of continuous modeling. It is an inhouse MOSFET model of NORTEL. The MISN model requires a model statement.

This device is not supported within altergroup.

This device is dynamically loaded from the shared object /vobs/spectre_dev/tools.sun4v/spectre/lib/cmi/2.0.doc/libnortel.so

Sample Instance Statement

```
mn1 (1 2 0 0) nch w=1.5u l=1u ad=2.6p as=2.6p pd=6.6p ps=6.6p
```

Spectre Circuit Simulator Reference

Component Statements Part 2

Sample Model Statement

```
model nch misnan type=n cox=4.4e-6 dop=2e17 phi=-0.43 xj=0.23 scrat=1.4 mu=400  
rws=250 is=0.98e-13 cjgo=2e-13 noimdl=1
```

Instance Definition

Name d g s b modelName parameter=value ...

Instance Parameters

- 1 w=1e-5 mChannel width.
- 2 l=3e-6 mChannel length.
- 3 as=3e-11 m²Area of source diffusion.
- 4 ad=3e-11 m²Area of drain diffusion.
- 5 ps=2.6e-5 mPerimeter of source diffusion.
- 6 pd=2.6e-5 mPerimeter of drain diffusion.
- 7 m=1Multiplicity factor (number of MOSFETs in parallel).
- 8 region=triodeEstimated DC operating region, used as a convergence aid.
Possible values are off, triode, sat, or subthresh.

Model Definition

model modelName misnan parameter=value ...

Model Parameters

Intrinsic MOS parameters

- 1 type=nTransistor gender.
Possible values are n or p.

Spectre Circuit Simulator Reference

Component Statements Part 2

2 `cox=4.309e-7 F/cm2`

Gate oxide cap per unit area.

3 `dop=1.665e17 cm-3`

Substrate doping. Default = 2.58e17 for pmos.

4 `phi=-0.55 vGate` Fermi potential.

5 `qss=-5.078e-8 Coul/cm2`

Effective gate oxide charge per unit area. Default = 1.05e-8 for pmos.

6 `dopldd=3.2e17 cm-3`

LDD region doping concentration. Default = 3.2e19 for pmos.

Geometry parameters

7 `lvar=0 μm`Gate length correction.

8 `wvar=0 μm`Gate width correction.

9 `dls=0.0273 μm`Sideway diffusion length of source region. Default = 0.037 for pmos.

10 `dld=0.0273 μm`Sideway diffusion length of drain region. Default = 0.037 for pmos.

11 `dl=0.07 μm`Sideways diffusion length of S/D regions. Default = 0.04 for pmos.

12 `dw=0.032 μm`Electrical channel width correction. Default = 0.018 for pmos.

Threshold voltage parameters

13 `xj=0.24 μm`Source/drain-to-substrate junction depth. Default = 0.31 for pmos.

14 `scrat=1.5`Short channel threshold voltage ratio. Default = 0.7 for pmos.

15 `scind=1.45`Short channel threshold voltage index. Default = 1.42 for pmos.

16 `ncrat=0.17`Narrow channel threshold voltage ratio. Default = 0.095 for pmos.

17 `athp=7.5`Factor controlling peak magnitude effect. Default = 3.5 for pmos.

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Component Statements Part 2

18 `athl=2e4` $1/\text{cm}$ Factor controlling channel length dependence effect. Default = 4e4 for pmos.

19 `athb=-1.7e-3`Factor controlling substrate bias dependence effect. Default = -6e-3.

Mobility parameters

20 `mu=577` $\text{cm}^2/\text{V}\cdot\text{s}$ Low-field carrier mobility. Default = 120 for pmos.

21 `mutxp=1.72`Temperature coefficient for the carrier mobility. Default = 1.01 for pmos.

22 `kg=1.4e-7` cm/V Gate field factor. Default = 1.685e-7.

23 `v0=3.21e7` cm/s Scattering limited velocity. Default = 2.45e7.

24 `v0txp=-6.3`Temp coefficient for scattering limited velocity. Default = -5 for pmos.

25 `find=1.25`Field mobility index factor. Default = 1.9 for pmos.

26 `gfc=9.1e-10`Gate voltage dependence of enhanced gate-field scattering. Default = 1.05e-10 for pmos.

27 `gfc=3e-5`Drain voltage dependence of enhanced gate-field scattering. Default = 2.3e-3 for pmos.

28 `gfmb=1.45e-3`Factor controlling substrate bias dependence of enhanced gate-field scattering. Default 3.3e-3 for pmos.

29 `csf=1.06e-11`Drain voltage dependence of coulombic scattering. Default = 1.35e-12 for pmos.

30 `csfb=1.61e-3`Body voltage dependence of coulombic scattering. Default = 8.5e-3 for pmos.

Saturation parameters

31 `dprat=15`Drain region/channel doping ration. Default = 2 for pmos.

32 `satpr=0.2`Saturation region shaping factor. Default = 1.0 for pmos.

33 `sbdr=0.3535534`Primary parameter controlling the onset of saturation.

Spectre Circuit Simulator Reference

Component Statements Part 2

34 `sadr=5` Secondary parameter controlling the onset of saturation.

Capacitance parameters

35 `sccf=0.25` Inner fringing factor for the N+ S/D.

Extrinsic parameters

36 `rws=480 Ω μm` Source series resistance. Default = 1180 for pmos.

37 `rwd=480 Ω μm` Drain series resistance. Default = 1180 for pmos.

38 `rsd=-1 Ω μm` Drain/source series resistance. Negative value for asymmetrical devices.

39 `rgsh=0 Ω μm` Gate series resistance.

40 `wtgf=0.28 μm` Width of transition from gate to field oxide under poly.

41 `cpts=5.7e-9 F/cm2`
Poly-to-substrate capacitance per unit area.

42 `cgfrs=1e-12 F/cm` Gate-source overlap fringing field capacitance.

43 `cgfrd=1e-12 F/cm` Gate-drain overlap fringing field capacitance.

44 `cgfr=1.36e-12 F/cm`
Gate overlap fringing field capacitance.

Junction parameters

45 `is=1.02e-12 A/cm2`
Sat current per unit area of S/D region-injection component.
Default = 9.21e-13 for pmos.

46 `isg=1e-20 A/cm` Sat current per unit length of gate oxide periphery-injection component. Default = 1.17e-20.

47 `isf=1e-20 A/cm` Sat current per unit length of field oxide periphery-injection component. Default = 1.17e-20.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 48 $ig=1.31e-10$ A/cm²
Sat current per unit area of S/D region-generation component.
Default = $8.27e-10$.
- 49 $igg=6.99e-14$ A/cm Sat current per unit length of gate oxide periphery-generation/
recombination component. Default = $6.47e-14$.
- 50 $igf=6.99e-14$ A/cm Sat current per unit length of field oxide periphery-generation/
recombination component. Default = $6.47e-14$.
- 51 $cjo=9.39e-8$ F/cm²
Zero bias junction capacitance per unit area. Default = $1.273e-7$
for pmos.
- 52 $ena=0.387$ Junction capacitance coefficient for the area component. Default = 0.472 for
pmos.
- 53 $cjgo=2.085e-12$ F/cm
Zero bias junction cap per unit length of gate oxide periphery.
Default = $1.864e-12$ for pmos.
- 54 $eng=0.322$ Junction cap coefficient for gate oxide periphery component. Default = 0.334
for pmos.
- 55 $cjfo=3.037e-12$ F/cm
Zero bias junction cap per unit length of field oxide periphery.
Default = $3.077e-12$ for pmos.
- 56 $enf=0.322$ Junction cap coefficient for field oxide periphery component. Default = 0.334
for pmos.

Noise parameters

- 57 $noimdl=1$ Noise model selector.
- 58 $nt=1.6e10$ cm⁻² Surface trap density. Default = $4e9$ for pmos.
- 59 $nttx=-4$ Surface trap density temperature coefficient.
- 60 $fidx=0.85$ Flicker noise frequency coefficient.
- 61 $beta=1$ Thermal noise proportional constant.

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Component Statements Part 2

62 `sgma=3e-16`Capture cross section. Default = 3e-15 for pmos.

63 `xtau=1e-81/E` depth.

64 `wbar=1`Barrier height for tunneling. Default = 4 for pmos.

65 `dept=3e-7`Depth of trap distribution.

Operating-Point Parameters

1 `vgs (V)`Gate-source voltage.

2 `vds (V)`Drain-source voltage.

3 `vbs (V)`Bulk-source voltage.

4 `id (A)`Drain current.

5 `vth (V)`Threshold voltage.

6 `vdsat (V)`Drain-source saturation voltage.

7 `gm (S)`Common-source transconductance.

8 `gd (S)`Common-source output conductance.

9 `gs (S)`Body-transconductance.

10 `gmb (S)`Body transconductance.

11 `gjs (S)`Drain-bulk junction conductance.

12 `ibs (A)`Drain-bulk junction current.

13 `gjd (S)`Source-bulk junction conductance.

14 `ibd (A)`Source-bulk junction current.

15 `qgg (Coul)`Gate charge.

16 `qss (Coul)`Source charge.

17 `qdd (Coul)`Drain charge.

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Component Statements Part 2

18 `qbb (Coul)` Bulk charge.

19 `cgg (F)` Cgg.

20 `cgs (F)` Cgs.

21 `cgd (F)` Cgd.

22 `cgb (F)` Cgb.

23 `csg (F)` Csg.

24 `css (F)` Css.

25 `csd (F)` Csd.

26 `csb (F)` Csb.

27 `cdg (F)` Cdg.

28 `cds (F)` Cds.

29 `cdd (F)` Cdd.

30 `cdb (F)` Cdb.

31 `cbg (F)` Cbg.

32 `cbs (F)` Cbs.

33 `cbd (F)` Cbd.

34 `cbb (F)` Cbb.

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the

Spectre Circuit Simulator Reference

Component Statements Part 2

description for that parameter. For example, a reference of M-35 means the 35th model parameter.

ad	I-4	csd	OP-25	ibd	OP-14	region	I-8
as	I-3	csf	M-29	ibs	OP-12	rgsh	M-39
athb	M-19	csfb	M-30	id	OP-4	rsd	M-38
athl	M-18	csg	OP-23	ig	M-48	rwd	M-37
athp	M-17	css	OP-24	igf	M-50	rws	M-36
beta	M-61	dept	M-65	igg	M-49	sadr	M-34
cbb	OP-34	dl	M-11	is	M-45	satpr	M-32
cbd	OP-33	dld	M-10	isf	M-47	sbdpr	M-33
cbg	OP-31	dls	M-9	isg	M-46	sccf	M-35
cbs	OP-32	dop	M-3	kg	M-22	scind	M-15
cdb	OP-30	dopldd	M-6	l	I-2	scrat	M-14
cdd	OP-29	dprat	M-31	lvar	M-7	sgma	M-62
cdg	OP-27	dw	M-12	m	I-7	type	M-1
cds	OP-28	ena	M-52	mu	M-20	v0	M-23
cgb	OP-22	enf	M-56	mutxp	M-21	v0txp	M-24
cgd	OP-21	eng	M-54	ncrat	M-16	vbs	OP-3
cgfr	M-44	fidx	M-60	noimdl	M-57	vds	OP-2
cgfrd	M-43	find	M-25	nt	M-58	vdsat	OP-6
cgfrs	M-42	gd	OP-8	nttx	M-59	vgs	OP-1
cgg	OP-19	gfc	M-26	pd	I-6	vth	OP-5
cgs	OP-20	gfcM	M-27	phi	M-4	w	I-1
cjfo	M-55	gfmb	M-28	ps	I-5	wbar	M-64
cjgo	M-53	gjd	OP-13	qbb	OP-18	wtgf	M-40
cjo	M-51	gjs	OP-11	qdd	OP-17	wvar	M-8
cox	M-2	gm	OP-7	qgg	OP-15	xj	M-13
cpts	M-41	gmb	OP-10	qss	M-5	xtau	M-63
csb	OP-26	gs	OP-9	qss	OP-16		

MOS Level-0 Transistor (mos0)

Description

The MOS0 model is a simplified MOS level-1 model. The MOS0 DC drain current model is different from the Shichman and Hodges model because body effects are not modeled. The intrinsic MOS gate capacitances are replaced by the following linear overlap capacitances:

Gate to source/drain (capmod = overlap)

Gate to bulk (capmod = bulk)

Spectre Circuit Simulator Reference

Component Statements Part 2

Gate, source, and drain to ground (`capmod = gnd`)

MOS0 is usually used as a MOS switch. This model recognizes all the MOS and BSIM instance parameters but only uses `l` and `w`, ignoring all other parameters. MOS0 transistors require that you use a model statement.

This device is not supported within altergroup.

Sample Instance Statement

```
mp1 (0 1 2 2) pchmod0 l=2u w=30u ad=120p as=75p pd=36u ps=6u
```

Sample Model Statement

```
model pchmod0 mos0 type=p vto=-0.683 tox=0.21e-7 ld=0.45e-6 tnom=27
```

Instance Definition

```
Name d g s b ModelName parameter=value ...
```

Instance Parameters

- 1 `w` (m) Channel width.
- 2 `l` (m) Channel length.
- 3 `m=1` Multiplicity factor (number of MOSFETs in parallel).

Model Definition

```
model modelName mos0 parameter=value ...
```

Model Parameters

Device Type Parameters

- 1 `type=n` Transistor type.
Possible values are `n` or `p`.

Drain Current Model Parameters

- 2 `vto=0` `v` Threshold voltage at zero body bias.

Spectre Circuit Simulator Reference

Component Statements Part 2

3 `kp=2.0718e-5 A/V2`

Transconductance parameter.

4 `lambda=0.02 1/V`Channel length modulation parameter.

5 `tox=1e-7 m`Gate oxide thickness.

6 `ld=0 m`Lateral diffusion.

7 `wd=0 m`Field-oxide encroachment.

Charge Model Selection Parameters

8 `capmod=gnd`Intrinsic charge model.

Possible values are none, overlap, bulk, or gnd.

Temperature Parameters

9 `tnom (C)`Parameters measurement temperature. Default set by `options`.

10 `trise=0 C`Temperature rise from ambient.

Default Device Parameters

11 `w=3e-6 m`Default channel width.

12 `l=3e-6 m`Default channel length.

Operating-Point Parameters

1 `type=n`Transistor type.

Possible values are n or p.

2 `id (A)`Resistive drain current.

3 `vgs (V)`Gate-source voltage.

4 `vds (V)`Drain-source voltage.

5 `vbs (V)`Bulk-source voltage.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 6 `vth` (V) Threshold voltage.
- 7 `vdsat` (V) Drain-source saturation voltage.
- 8 `gm` (S) Common-source transconductance.
- 9 `gds` (S) Common-source output conductance.
- 10 `cgs` (F) Gate-source capacitance.
- 11 `cgd` (F) Gate-drain capacitance.
- 12 `cgate` (F) Gate-Ground capacitance.
- 13 `ron` (Ω) On-resistance.
- 14 `pwr` (W) Power at op point.

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

<code>capmod</code>	M-8	<code>l</code>	I-2	<code>tox</code>	M-5	<code>vth</code>	OP-6
<code>cgate</code>	OP-12	<code>l</code>	M-12	<code>trise</code>	M-10	<code>vto</code>	M-2
<code>cgd</code>	OP-11	<code>lambda</code>	M-4	<code>type</code>	OP-1	<code>w</code>	M-11
<code>cgs</code>	OP-10	<code>ld</code>	M-6	<code>type</code>	M-1	<code>w</code>	I-1
<code>gds</code>	OP-9	<code>m</code>	I-3	<code>vbs</code>	OP-5	<code>wd</code>	M-7
<code>gm</code>	OP-8	<code>pwr</code>	OP-14	<code>vds</code>	OP-4		
<code>id</code>	OP-2	<code>ron</code>	OP-13	<code>vdsat</code>	OP-7		
<code>kp</code>	M-3	<code>tnom</code>	M-9	<code>vgs</code>	OP-3		

MOS Level-1 Transistor (mos1)

Description

The MOS1 model is derived from the FET model of Shichman and Hodges. The velocity saturation and the mobility variation effects can also be incorporated into MOS1. Three charge models are available. MOS1 transistors require that you use a model statement.

This device is supported within altergroups.

Sample Instance Statement

```
nchl (1 2 0 0) nchmdl l=2u w=15u ad=60p as=37.5p pd=23u ps=6u
```

Sample Model Statement

```
model nchmdl mos1 vto=0.78 gamma=0.56 kp=0.8675e-4 tox=0.21e-7 nsub=0.21e17  
ld=0.55e-6 capmod=yang vmax=4e5 theta=0.19 cbs=11e-15 cbd=10e-15 lambda=0.1
```

Instance Definition

Name d g s b ModelName parameter=value ...

Instance Parameters

- 1 w (m) Channel width.
- 2 l (m) Channel length.
- 3 as (m²) Area of source diffusion.
- 4 ad (m²) Area of drain diffusion.
- 5 ps (m) Perimeter of source diffusion.
- 6 pd (m) Perimeter of drain diffusion.
- 7 nrd (m/m) Number of squares of drain diffusion.
- 8 nrs (m/m) Number of squares of source diffusion.
- 9 ld (m) Length of drain diffusion region.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 10 `ls (m)` Length of source diffusion region.
- 11 `m=1` Multiplicity factor (number of MOSFETs in parallel).
- 12 `region=triode` Estimated operating region.
Possible values are `off`, `triode`, `sat`, `subth`, or `breakdown`.
- 13 `trise` Temperature rise from ambient.
- 14 `degradation=no` Hot-electron degradation flag.
Possible values are `no` or `yes`.

Model Definition

`model modelName mosl parameter=value ...`

Model Parameters

Device Type Parameters

- 1 `type=n` Transistor type.
Possible values are `n` or `p`.

Drain Current Model Parameters

- 2 `vto=0` V Threshold voltage at zero body bias.
- 3 `kp=2.0718e-5` A/V^2
Transconductance parameter.
- 4 `lambda=0` $1/V$ Channel length modulation parameter.
- 5 `phi=0.7` V Surface potential at strong inversion.
- 6 `gamma=0` \sqrt{V} Body-effect parameter.
- 7 `uo=600` $cm^2/V \cdot s$ Carrier surface mobility.
- 8 `vmax= ∞` m/s Carrier saturation velocity.
- 9 `theta=0` $1/V$ Mobility modulation coefficient.

Spectre Circuit Simulator Reference

Component Statements Part 2

Process Parameters

- 10 $n_{sub}=1.13e16 \text{ cm}^{-3}$ Channel doping concentration.
- 11 $n_{ss}=0 \text{ cm}^{-2}$ Surface state density.
- 12 $n_{fs}=0 \text{ cm}^{-2}$ Fast surface state density.
- 13 $t_{pg}=+1$ Type of gate (+1 = opposite of substate, -1 = same as substate, 0 = aluminum).
- 14 $l_d=0 \text{ m}$ Lateral diffusion.
- 15 $w_d=0 \text{ m}$ Field-oxide encroachment.
- 16 $x_w=0 \text{ m}$ Width variation due to masking and etching.
- 17 $x_l=0 \text{ m}$ Length variation due to masking and etching.
- 18 $t_{ox}=1e-7 \text{ m}$ Gate oxide thickness.

Impact Ionization Parameters

- 19 $a_{i0}=0 \text{ 1/V}$ Impact ionization current coefficient.
- 20 $l_{ai0}=0 \text{ }\mu\text{m/V}$ Length sensitivity of a_{i0} .
- 21 $w_{ai0}=0 \text{ }\mu\text{m/V}$ Width sensitivity of a_{i0} .
- 22 $b_{i0}=0 \text{ V}$ Impact ionization current exponent.
- 23 $l_{bi0}=0 \text{ }\mu\text{m V}$ Length sensitivity of b_{i0} .
- 24 $w_{bi0}=0 \text{ }\mu\text{m V}$ Width sensitivity of b_{i0} .

Overlap Capacitance Parameters

- 25 $c_{gso}=0 \text{ F/m}$ Gate-source overlap capacitance.
- 26 $c_{gdo}=0 \text{ F/m}$ Gate-drain overlap capacitance.
- 27 $c_{gbo}=0 \text{ F/m}$ Gate-bulk overlap capacitance.

Spectre Circuit Simulator Reference

Component Statements Part 2

28 `meto=0` `m`Metal overlap in fringing field.

Charge Model Selection Parameters

29 `capmod=bsim`Intrinsic charge model.

Possible values are `none`, `meyer`, `yang`, or `bsim`.

30 `xpart=1`Drain/source channel charge partition in saturation for BSIM charge model, use 0.0 for 40/60, 0.5 for 50/50, or 1.0 for 0/100.

31 `xqc=0`Drain/source channel charge partition in saturation for charge models, e.g. use 0.4 for 40/60, 0.5 for 50/50, 0 for 0/100.

Parasitic Resistance Parameters

32 `rs=0` Ω Source resistance.

33 `rd=0` Ω Drain resistance.

34 `rss=0` Ω `m`Scalable source resistance.

35 `rdd=0` Ω `m`Scalable drain resistance.

36 `rsh=0` Ω/sqr Source/drain diffusion sheet resistance.

37 `rsc=0` Ω Source contact resistance.

38 `rdc=0` Ω Drain contact resistance.

39 `minr=0.1` Ω Minimum source/drain resistance.

40 `ldif=0` `m`Lateral diffusion beyond the gate.

41 `hdif=0` `m`Length of heavily doped diffusion.

42 `lgcs=0` `m`Gate-to-contact length of source side.

43 `lgcd=0` `m`Gate-to-contact length of drain side.

44 `sc= ∞` `m`Spacing between contacts.

Spectre Circuit Simulator Reference

Component Statements Part 2

Junction Diode Model Parameters

- 45 `js` (A/m²) Bulk junction reverse saturation current density.
- 46 `is=1e-14 A` Bulk junction reverse saturation current.
- 47 `n=1` Junction emission coefficient.
- 48 `dskip=yes` Use simple piece-wise linear model for diode currents below $0.1 \cdot i_{abstol}$.
Possible values are `no` or `yes`.
- 49 `imelt='imaxA'` Explosion current, diode is linearized beyond this current to aid convergence.
- 50 `jmelt='jmaxA/m'2`
Explosion current density, diode is linearized beyond this current to aid convergence.

Junction Capacitance Model Parameters

- 51 `cbs=0 F` Bulk-source zero-bias junction capacitance.
- 52 `cbd=0 F` Bulk-drain zero-bias junction capacitance.
- 53 `cj=0 F/m2` Zero-bias junction bottom capacitance density.
- 54 `mj=1/2` Bulk junction bottom grading coefficient.
- 55 `pb=0.8 V` Bulk junction built-in potential.
- 56 `fc=0.5` Forward-bias depletion capacitance threshold.
- 57 `cjsw=0 F/m` Zero-bias junction sidewall capacitance density.
- 58 `mjsw=1/3` Bulk junction sidewall grading coefficient.
- 59 `pbsw=0.8 V` Side-wall junction built-in potential.
- 60 `fcsw=0.5` Side-wall forward-bias depletion capacitance threshold.

Spectre Circuit Simulator Reference

Component Statements Part 2

Operating Region Warning Control Parameters

- 61 `alarm=none` Forbidden operating region.
Possible values are `none`, `off`, `triode`, `sat`, `subth`, or `rev`.
- 62 `imax=1` A Maximum current, currents above this limit generate a warning.
- 63 `jmax=1e8` A/m² Maximum current density, currents above this limit generate a warning.
- 64 `bvj= ∞` V Junction reverse breakdown voltage.
- 65 `vbox=1e9` `tox` V Oxide breakdown voltage.

Temperature Effects Parameters

- 66 `tnom` (C) Parameters measurement temperature. Default set by `options`.
- 67 `trise=0` C Temperature rise from ambient.
- 68 `uto=0` C Mobility temperature offset.
- 69 `ute=-1.5` Mobility temperature exponent.
- 70 `tlev=0` DC temperature selector.
- 71 `tlevc=0` AC temperature selector.
- 72 `eg=1.12452` V Energy band gap.
- 73 `gap1=7.02e-4` V/C Band gap temperature coefficient.
- 74 `gap2=1108` C Band gap temperature offset.
- 75 `flex=0` Temperature exponent for `ucrit`.
- 76 `lamex=0` 1/C Temperature parameter for `lambda` and `kappa`.
- 77 `trs=0` 1/C Temperature parameter for source resistance.
- 78 `trd=0` 1/C Temperature parameter for drain resistance.
- 79 `xti=3` Saturation current temperature exponent.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 80 `ptc=0` V/C Surface potential temperature coefficient.
- 81 `tcv=0` V/C Threshold voltage temperature coefficient.
- 82 `pta=0` V/C Junction potential temperature coefficient.
- 83 `ptp=0` V/C Sidewall junction potential temperature coefficient.
- 84 `cta=0` 1/C Junction capacitance temperature coefficient.
- 85 `ctp=0` 1/C Sidewall junction capacitance temperature coefficient.

Default Instance Parameters

- 86 `w=3e-6` m Default channel width.
- 87 `l=3e-6` m Default channel length.
- 88 `as=0` m² Default area of source diffusion.
- 89 `ad=0` m² Default area of drain diffusion.
- 90 `ps=0` m Default perimeter of source diffusion.
- 91 `pd=0` m Default perimeter of drain diffusion.
- 92 `nrd=0` m/m Default number of squares of drain diffusion.
- 93 `nrs=0` m/m Default number of squares of source diffusion.
- 94 `ldd=0` m Default length of drain diffusion region.
- 95 `lds=0` m Default length of source diffusion region.

Noise Model Parameters

- 96 `noisemod=1` Noise model selector.
- 97 `kf=0` Flicker (1/f) noise coefficient.
- 98 `af=1` Flicker (1/f) noise exponent.

Spectre Circuit Simulator Reference

Component Statements Part 2

99 `ef=1` Flicker (1/f) noise frequency exponent.

100 `wnoi=1e-5` mChannel width at which noise parameters were extracted.

Auto Model Selector Parameters

101 `wmax=1.0` mMaximum channel width for which the model is valid.

102 `wmin=0.0` mMinimum channel width for which the model is valid.

103 `lmax=1.0` mMaximum channel length for which the model is valid.

104 `lmin=0.0` mMinimum channel length for which the model is valid.

Degradation Parameters

105 `degramod=spectre` Degradation model selector.
Possible values are `spectre` or `bert`.

106 `degradation=no` Hot-electron degradation flag.
Possible values are `no` or `yes`.

107 `dvthc=1` vDegradation coefficient for threshold voltage.

108 `dvthe=1` Degradation exponent for threshold voltage.

109 `duoc=1` sDegradation coefficient for transconductance.

110 `duoe=1` Degradation exponent for transconductance.

111 `crivth=0.1` vMaximum allowable threshold voltage shift.

112 `criuo=10%` Maximum allowable normalized mobility change.

113 `crigm=10%` Maximum allowable normalized transconductance change.

114 `criids=10%` Maximum allowable normalized drain current change.

115 `wnom=5e-6` mNominal device width in degradation calculation.

116 `lnom=1e-6` mNominal device length in degradation calculation.

Spectre Circuit Simulator Reference

Component Statements Part 2

117 `vbsn=0` `v`Substrate voltage in degradation calculation.

118 `vdsni=0.1` `v`Drain voltage in I_{ds} degradation calculation.

119 `vgsni=5` `v`Gate voltage in I_{ds} degradation calculation.

120 `vdsng=0.1` `v`Drain voltage in G_m degradation calculation.

121 `vgsng=5` `v`Gate voltage in G_m degradation calculation.

Spectre Stress Parameters

122 `esat=1.1e7` `V/m`Critical field in V_{dsat} calculation.

123 `esatg=2.5e6` `1/m`Gate voltage dependence of $esat$.

124 `vpg=-0.25`Gate voltage modifier.

125 `vpb=-0.13`Gate voltage modifier.

126 `subc1=2.24e-5`Substrate current coefficient.

127 `subc2=-0.1e-5` `1/V`Substrate current coefficient.

128 `sube=6.4`Substrate current exponent.

129 `strc=1`Stress coefficient.

130 `stre=1`Stress exponent.

BERT Stress Parameters

131 `h0=1`Aging coefficient.

132 `hgd=0` `1/V`Bias dependence of $h0$.

133 `m0=1`Aging exponent.

134 `mgd=0` `1/V`Bias dependence of $m0$.

135 `ecrit0=1.1e5` `V/cm`Critical electric field.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 136 `lecrit0=0` $\mu\text{m V/cm}$ Length dependence of `ecrit0`.
- 137 `wecrit0=0` $\mu\text{m V/cm}$ Width dependence of `ecrit0`.
- 138 `ecritg=0` $1/\text{cm}$ Gate voltage dependence of `ecrit0`.
- 139 `lecritg=0` $\mu\text{m/cm}$ Length dependence of `ecritg`.
- 140 `wecritg=0` $\mu\text{m/cm}$ Width dependence of `ecritg`.
- 141 `ecritb=0` $1/\text{cm}$ Substrate voltage dependence of `ecrit0`.
- 142 `lecritb=0` $\mu\text{m/cm}$ Length dependence of `ecritb`.
- 143 `wecritb=0` $\mu\text{m/cm}$ Width dependence of `ecritb`.
- 144 `lc0=1`Substrate current coefficient.
- 145 `llc0=0` μm Length dependence of `lc0`.
- 146 `wlc0=0` μm Width dependence of `lc0`.
- 147 `lc1=1`Substrate current coefficient.
- 148 `llc1=0` μm Length dependence of `lc1`.
- 149 `wlc1=0` μm Width dependence of `lc1`.
- 150 `lc2=1`Substrate current coefficient.
- 151 `llc2=0` μm Length dependence of `lc2`.
- 152 `wlc2=0` μm Width dependence of `lc2`.
- 153 `lc3=1`Substrate current coefficient.
- 154 `llc3=0` μm Length dependence of `lc3`.
- 155 `wlc3=0` μm Width dependence of `lc3`.
- 156 `lc4=1`Substrate current coefficient.
- 157 `llc4=0` μm Length dependence of `lc4`.

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Component Statements Part 2

158 `wlc4=0` μm Width dependence of `lc4`.

159 `lc5=1` Substrate current coefficient.

160 `llc5=0` μm Length dependence of `lc5`.

161 `wlc5=0` μm Width dependence of `lc5`.

162 `lc6=1` Substrate current coefficient.

163 `llc6=0` μm Length dependence of `lc6`.

164 `wlc6=0` μm Width dependence of `lc6`.

165 `lc7=1` Substrate current coefficient.

166 `llc7=0` μm Length dependence of `lc7`.

167 `wlc7=0` μm Width dependence of `lc7`.

Imax and Imelt

The `imax` parameter aids convergence and prevents numerical overflow. The junction characteristics of the device are accurately modeled for current up to `imax`. If `imax` is exceeded during iterations, the linear model is substituted until the current drops below `imax` or until convergence is achieved. If convergence is achieved with the current exceeding `imax`, the results are inaccurate, and Spectre prints a warning.

A separate model parameter, `imelt`, is used as a limit warning for the junction current. This parameter can be set to the maximum current rating of the device. When any component of the junction current exceeds `imelt`, note that base and collector currents are composed of many exponential terms, Spectre issues a warning and the results become inaccurate. The junction current is linearized above the value of `imelt` to prevent arithmetic exception, with the exponential term replaced by a linear equation at `imelt`.

Both of these parameters have current density counterparts, `jmax` and `jimelt`, that you can specify if you want the absolute current values to depend on the device area.

Auto Model Selection

Many models need to be characterized for different geometries in order to obtain accurate results for model development. The model selector program automatically searches for a

Spectre Circuit Simulator Reference

Component Statements Part 2

model with the length and width range specified in the instance statement and uses this model in the simulations.

For the auto model selector program to find a specific model, the models to be searched should be grouped together within braces. Such a group is called a model group. An opening brace is required at the end of the line defining each model group. Every model in the group is given a name followed by a colon and the list of parameters. Also, the four geometric parameters `lmax`, `lmin`, `wmax`, and `wmin` should be given. The selection criteria to choose a model is as follows:

```
lmin <= inst_length < lmax  and  wmin <= inst_width  < wmax
```

Example

```
model ModelName ModelType {  
    1:      <model parameters> lmin=2 lmax=4 wmin=1 wmax=2  
    2:      <model parameters> lmin=1 lmax=2 wmin=2 wmax=4  
    3:      <model parameters> lmin=2 lmax=4 wmin=4 wmax=6  
}
```

Then for a given instance

```
M1 1 2 3 4 ModelName w=3 l=1.5
```

the program would search all the models in the model group with the name `ModelName` and then pick the first model whose geometric range satisfies the selection criteria. In the preceding example, the auto model selector program would choose `ModelName.2`.

The user must specify both length (`l`) and width (`w`) on the device instance line to enable automatic model selection.

Output Parameters

- 1 `weff` (m) Effective channel width.
- 2 `leff` (m) Effective channel length.
- 3 `rseff` (Ω) Effective source resistance.
- 4 `rdeff` (Ω) Effective drain resistance.
- 5 `aseff` (m^2) Effective area of source diffusion.
- 6 `adeff` (m^2) Effective area of drain diffusion.
- 7 `pseff` (m) Effective perimeter of source diffusion.

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Component Statements Part 2

- 8 `pdeff (m)` Effective perimeter of source diffusion.
- 9 `isseff (A)` Effective source-bulk junction reverse saturation current.
- 10 `isdeff (A)` Effective drain-bulk junction reverse saturation current.
- 11 `cbseff (F)` Effective zero-bias source-bulk junction capacitance.
- 12 `cbdeff (F)` Effective zero-bias drain-bulk junction capacitance.

Operating-Point Parameters

- 1 `type=n` Transistor type.
Possible values are `n` or `p`.
- 2 `region=triode` Estimated operating region.
Possible values are `off`, `triode`, `sat`, `subth`, or `breakdown`.
- 3 `degradation=no` Hot-electron degradation flag.
Possible values are `no` or `yes`.
- 4 `reversed` Reverse mode indicator.
Possible values are `no` or `yes`.
- 5 `ids (A)` Resistive drain-to-source current.
- 6 `vgs (V)` Gate-source voltage.
- 7 `vds (V)` Drain-source voltage.
- 8 `vbs (V)` Bulk-source voltage.
- 9 `vth (V)` Threshold voltage.
- 10 `vdsat (V)` Drain-source saturation voltage.
- 11 `gm (S)` Common-source transconductance.
- 12 `gds (S)` Common-source output conductance.
- 13 `gmbs (S)` Body-transconductance.
- 14 `gameff (\sqrt{V})` Effective body effect coefficient.

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Component Statements Part 2

- 15 `betaeff` (A/V²) Effective beta.
- 16 `cbd` (F) Drain-bulk junction capacitance.
- 17 `cbs` (F) Source-bulk junction capacitance.
- 18 `cgs` (F) Gate-source capacitance.
- 19 `cgd` (F) Gate-drain capacitance.
- 20 `cgb` (F) Gate-bulk capacitance.
- 21 `ron` (Ω) On-resistance.
- 22 `id` (A) Resistive drain current.
- 23 `ibulk` (A) Resistive bulk current.
- 24 `pwr` (W) Power at op point.
- 25 `gmoverid` (1/V) Gm/Ids.
- 26 `isub` (A) Substrate current.
- 27 `stress` Hot-electron stress.
- 28 `age` (s) Device age.
- 29 `he_vdsat` (V) Hot Electron Vdsat.

Parameter Index

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<code>ad</code>	I-4	<code>gap2</code>	M-74	<code>lmax</code>	M-103	<code>tlevc</code>	M-71
<code>ad</code>	M-89	<code>gds</code>	OP-12	<code>lmin</code>	M-104	<code>tnom</code>	M-66
<code>adeff</code>	O-6	<code>gm</code>	OP-11	<code>lnom</code>	M-116	<code>tox</code>	M-18
<code>af</code>	M-98	<code>gmbs</code>	OP-13	<code>ls</code>	I-10	<code>tpg</code>	M-13

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Component Statements Part 2

age	OP-28	gmoverid	OP-25	m	I-11	trd	M-78
ai0	M-19	h0	M-131	m0	M-133	trise	M-67
alarm	M-61	hdif	M-41	meto	M-28	trise	I-13
as	I-3	he_vdsat	OP-29	mgd	M-134	trs	M-77
as	M-88	hgd	M-132	minr	M-39	type	M-1
aseff	O-5	ibulk	OP-23	mj	M-54	type	OP-1
betaeff	OP-15	id	OP-22	mjsw	M-58	uo	M-7
bi0	M-22	ids	OP-5	n	M-47	ute	M-69
bvj	M-64	imax	M-62	nfs	M-12	uto	M-68
capmod	M-29	imelt	M-49	noisemod	M-96	vbox	M-65
cbd	M-52	is	M-46	nrd	M-92	vbs	OP-8
cbd	OP-16	isdeff	O-10	nrd	I-7	vbsn	M-117
cbdeff	O-12	isseff	O-9	nrs	M-93	vds	OP-7
cbs	OP-17	isub	OP-26	nrs	I-8	vdsat	OP-10
cbs	M-51	jmax	M-63	nss	M-11	vdsng	M-120
cbseff	O-11	jmelt	M-50	nsub	M-10	vdsni	M-118
cgb	OP-20	js	M-45	pb	M-55	vgs	OP-6
cgbo	M-27	kf	M-97	pbsw	M-59	vgsng	M-121
cgd	OP-19	kp	M-3	pd	M-91	vgsni	M-119
cgdo	M-26	l	I-2	pd	I-6	vmax	M-8
cgs	OP-18	l	M-87	pdeff	O-8	vpb	M-125
cgso	M-25	lai0	M-20	phi	M-5	vpg	M-124
cj	M-53	lambda	M-4	ps	I-5	vth	OP-9
cjsw	M-57	lamex	M-76	ps	M-90	vto	M-2
crigm	M-113	lbi0	M-23	pseff	O-7	w	I-1
criids	M-114	lc0	M-144	pta	M-82	w	M-86
criuo	M-112	lc1	M-147	ptc	M-80	wai0	M-21
crivth	M-111	lc2	M-150	ptp	M-83	wbi0	M-24
cta	M-84	lc3	M-153	pwr	OP-24	wd	M-15
ctp	M-85	lc4	M-156	rd	M-33	wecrit0	M-137
degradation	OP-3	lc5	M-159	rdc	M-38	wecritb	M-143
degradation	I-14	lc6	M-162	rdd	M-35	wecritg	M-140
degradation	M-106	lc7	M-165	rdeff	O-4	weff	O-1
degramod	M-105	ld	M-14	region	OP-2	wlc0	M-146
dskip	M-48	ld	I-9	region	I-12	wlc1	M-149
duoc	M-109	ldd	M-94	reversed	OP-4	wlc2	M-152
duoe	M-110	ldif	M-40	ron	OP-21	wlc3	M-155
dvthc	M-107	lds	M-95	rs	M-32	wlc4	M-158
dvthe	M-108	lecrit0	M-136	rsc	M-37	wlc5	M-161
ecrit0	M-135	lecritb	M-142	rseff	O-3	wlc6	M-164
ecritb	M-141	lecritg	M-139	rsh	M-36	wlc7	M-167
ecritg	M-138	leff	O-2	rss	M-34	wmax	M-101
ef	M-99	lgcd	M-43	sc	M-44	wmin	M-102
eg	M-72	lgcs	M-42	strc	M-129	wnoi	M-100
esat	M-122	llc0	M-145	stre	M-130	wnom	M-115
esatg	M-123	llc1	M-148	stress	OP-27	xl	M-17
flex	M-75	llc2	M-151	subcl	M-126	xpart	M-30

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Component Statements Part 2

fc	M-56	llc3	M-154	subc2	M-127	xqc	M-31
fcs	M-60	llc4	M-157	sube	M-128	xti	M-79
gameff	OP-14	llc5	M-160	tcv	M-81	xw	M-16
gamma	M-6	llc6	M-163	theta	M-9		
gap1	M-73	llc7	M-166	tlev	M-70		

Compact MOS-Transistor Distortion Model (mos1000)

Description

The mos10.00 model is an experimental model based on the thesis of Ronald van Langevelde: "A compact MOSFET Model for Distortion Analysis in Analog Circuit Design", Technische Universiteit Eindhoven, 1998.

Note: In noise analysis, mos10.00 instances will not generate any contribution, since there are no noise sources included (yet) in the mos10.00 model.

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In extension to the description a minimum conductance `gmin` is inserted between the drain and source node, to aid convergence. The value of `gmin` is set by an options statement, default = 1e-12 S.

This device is supported within altergroups.

This device is dynamically loaded from the shared object

```
/vobs/spectre_dev/tools.sun4v/spectre/lib/cmi/2.0.doc/libphilips.so
```

Instance Definition

```
Name d g s [b] ModelName parameter=value ...
```

Instance Parameters

- 1 `w=1.0 scale` mDrawn channel width in the lay-out. Scale set by option scale.
- 2 `l=1.0 scale` mDrawn channel length in the lay-out. Scale set by option scale.
- 3 `mult=1` Number of devices in parallel.
- 4 `area=1` Alias of `mult`.

Spectre Circuit Simulator Reference

Component Statements Part 2

5 `region=triode` Estimated DC operating region, used as a convergence aid.
Possible values are `off`, `triode`, `sat`, or `subth`.

6 `m=1` Multiplicity factor.

Model Definition

`model modelName mos1000 parameter=value ...`

Model Parameters

Device type parameters

1 `type=n` Transistor gender.
Possible values are `n` or `p`.

Geometry Parameters

2 `ler=1.0e-6 m` Effective channel length of the reference transistor.

3 `wer=1e-6 m` Effective channel width of the reference transistor.

4 `lvar=0.0 m` Difference between the actual and the programmed poly-silicon gate length.

5 `lap=45.0e-9 m` Effective channel length reduction per side.

6 `wvar=-5.0e-9 m` Difference between the actual and the programmed field-oxide opening.

7 `wot=50.0e-9 m` Effective channel width reduction per side.

Threshold-Voltage Parameters

8 `vfbr=-518.9e-03 v` Flat-band voltage for reference transistor.

9 `stvf=-1.2e-03 V/K`
Coefficient of temperature dependence of `vf`.

10 `slvf=24.0e-09 V m`
Coefficient of length dependence of `vf`.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 11 $sl2vfb=-1.1e-15 \text{ V m}^2$
Second coefficient of length dependence of vfb .
- 12 $swvfb=4.400e-09 \text{ V m}$
Coefficient of the width dependency of vfb .
- 13 $kor=368.0e-03 \sqrt{V}$
Body effect coefficient for the reference transistor.
- 14 $slko=-8.240e-09 \sqrt{V} \text{ m}$
Coefficient of the length dependence of k_o .
- 15 $sl2ko=-2.260e-15 \sqrt{V} \text{ m}^2$
Second coefficient of the length dependence of k_o .
- 16 $swko=5.86e-09 \sqrt{V} \text{ m}$
Coefficient of the width dependence of k_o .
- 17 $phibr=0.6 \text{ V}$ Surface potential at strong inversion.

Channel-Current Parameters

- 18 $betsq=370.9e-06 \text{ A/V}^2$
Gain factor for an infinite square transistor.
- 19 $etabet=1.6$ Exponent of the temperature dependence of the gain factor.
- 20 $thesrr=16.10e-3 \text{ 1/V}^2$
Mobility degradation parameter due to surface roughness scattering.
- 21 $stthesr=0.0 \text{ 1/(V}^2 \text{ K)}$
Coefficient of the temperature dependence of $thesr$.
- 22 $swthesr=0.0 \text{ 1/(V}^2 \text{ m)}$
Coefficient of the width dependence of $thesr$.
- 23 $thephrr=0.055 \text{ 1/V}$ Mobility degradation parameter due to phonon scattering.
- 24 $sttheph=0.0 \text{ 1/(V K)}$
Coefficient of the temperature dependence of $theph$.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 25 $swtheph=0.0 \text{ } 1/(V \text{ } m)$
Coefficient of the width dependence of $theph$.
- 26 $etamobr=1.6$ Effective field parameter for dependence on depletion charge.
- 27 $swetamob=0.0 \text{ } 1/m$ Coefficient of the width dependence of $etamobr$.
- 28 $thersq=0.155 \text{ } 1/V$ Coefficient of gate voltage independent part of series resistance.
- 29 $swther=0.0 \text{ } 1/(V \text{ } m)$
Coefficient of the width dependence of $ther$.
- 30 $ther1=0.0 \text{ } V$ Numerator of gate voltage independent part of series resistance.
- 31 $ther2=1.0 \text{ } V$ Denominator of gate voltage independent part of series resistance.
- 32 $thenr=0.480 \text{ } 1/V$ Velocity saturation parameter due to optical phonon scattering.
- 33 $stthen=0.0 \text{ } 1/(V \text{ } K)$
Coefficient of the temperature dependence of $then$.
- 34 $swthen=0.0 \text{ } 1/(V \text{ } m)$
Coefficient of the width dependence of $then$.
- 35 $thep=0.0 \text{ } 1/V$ Velocity saturation parameter due to acoustic phonon scattering.
- 36 $stthep=0.0 \text{ } 1/(V \text{ } K)$
Coefficient of the temperature dependence of $thep$.
- 37 $swthep=0.0 \text{ } 1/(V \text{ } m)$
Coefficient of the width dependence of $thep$.
- 38 $gthep=1.0$ Velocity saturation factor due to acoustic phonon scattering.
- 39 $thethr=3.227e-3 \text{ } 1/V^3$
Coefficient of self-heating.
- 40 $sltheth=2.460e-9 \text{ } 1/(V^3 \text{ } m)$
Coefficient of the length dependence of $theth$.
- 41 $swtheth=0.0 \text{ } 1/(V^3 \text{ } m)$
Coefficient of the width dependence of $theth$.

Spectre Circuit Simulator Reference

Component Statements Part 2

Sub-threshold parameters

- 42 `sdiblo=2.030e-03 1/√V`
Drain-induced barrier lowering parameter.
- 43 `sdiblexp=1.340` Exponent of the length dependence of `sdibl`.
- 44 `dphi=0.800 V` Parameter for short-channel subthreshold behaviour.

Saturation Parameters

- 45 `ssfsq=6.250e-03 1/√V`
Static feedback parameter.
- 46 `swssf=0.0 1/(√V m)`
Coefficient of the width dependence of `ssf`.
- 47 `alpsq=0.010 m` Characteristic length parameter for channel length modulation.
- 48 `swalp=0.0 m` Coefficient of the width dependence of `alp`.
- 49 `vp=0.075 V` Characteristic voltage of channel-length modulation.

Smoothing Parameters

- 50 `mexpo=0.093` Smoothing factor.
- 51 `mexpl=0.065` Coefficient of the length dependence of `mexp`.

Weak-Avalanche Parameters

- 52 `a1r=6` Factor of the weak-avalanche current.
- 53 `sta1=0.0 1/K` Coefficient of the temperature dependence of `a1`.
- 54 `s1a1=1.30e-6 m` Coefficient of the length dependence of `a1`.
- 55 `swa1=3.0e-06 m` Coefficient of the width dependence of `a1`.
- 56 `a2r=38.0 V` Exponent of the weak-avalanche current.
- 57 `s1a2=1.00e-06 V m` Coefficient of the length dependence of `a2`.

Spectre Circuit Simulator Reference

Component Statements Part 2

58 `swa2=2.00e-06` V/m Coefficient of the width dependence of `a2`.

59 `a3r=0.650` Factor of the drain-source voltage above which weak-avalanche occurs.

60 `s1a3=-550.0e-06` m Coefficient of the length dependence of `a3`.

61 `swa3=0.0` m Coefficient of the width dependence of `a3`.

Charge Parameters

62 `tox=4.5e-09` m Thickness of the oxide layer.

63 `col=320e-12` F/m Gate overlap capacitance per unit channel width.

Temperature Parameters

64 `tr (C)` Reference temperature. Default set by option `tnom`.

65 `tref (C)` Alias of `tr`. Default set by option `tnom`.

66 `tnom (C)` Alias of `tr`. Default set by option `tnom`.

67 `dta=0.0` K Temperature offset of the device.

68 `trise=0.0` K Alias of `dta`.

Output Parameters

1 `le (m)` Effective channel length.

2 `we (m)` Effective channel width.

3 `vfb (V)` Flat-band voltage.

4 `ko (\sqrt{V})` Body effect coefficient.

5 `phib (V)` Surface potential at strong inversion.

6 `bet (A/V^2)` Gain factor.

7 `thesr ($1/V^2$)` Mobility degradation parameter due to surface roughness scattering.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 8 `theph` (1/V) Mobility degradation parameter due to phonon scattering.
- 9 `etamob` Effective field parameter for dependence on depletion charge.
- 10 `ther` (1/V) Coefficient of gate voltage independent part of series resistance.
- 11 `ther1` (V) Numerator of gate voltage independent part of series resistance.
- 12 `ther2` (V) Denominator of gate voltage independent part of series resistance.
- 13 `then` (1/V) Velocity saturation parameter due to optical phonon scattering.
- 14 `thep` (1/V) Velocity saturation parameter due to acoustic phonon scattering.
- 15 `gthep` Velocity saturation factor due to acoustic phonon scattering.
- 16 `theth` (1/V³) Coefficient of self-heating.
- 17 `sdibl` (1/√V) Drain-induced barrier lowering parameter.
- 18 `dphi` (V) Parameter for short-channel subthreshold behaviour.
- 19 `ssf` (1/√V) Static feedback parameter.
- 20 `alp` (m) Characteristic length parameter for channel length modulation.
- 21 `vp` (V) Characteristic voltage of channel-length modulation.
- 22 `mexp` Smoothing factor.
- 23 `phit` (V) Thermal voltage.
- 24 `a1` Factor of the weak-avalanche current.
- 25 `a2` (V) Exponent of the weak-avalanche current.
- 26 `a3` Factor of the drain-source voltage above which weak-avalanche occurs.
- 27 `cox` (F) Gate-to-channel capacitance (* mult).
- 28 `cgdo` (F) Gate-drain overlap capacitance (* mult).
- 29 `cgso` (F) Gate-source overlap capacitance (* mult).

Spectre Circuit Simulator Reference

Component Statements Part 2

Operating-Point Parameters

- 1 `ide` (A) Resistive drain current.
- 2 `ige` (A) Resistive gate current.
- 3 `ise` (A) Resistive source current.
- 4 `ibe` (A) Resistive bulk current.
- 5 `vds` (V) Drain-source voltage.
- 6 `vgs` (V) Gate-source voltage.
- 7 `vsb` (V) Source-bulk voltage.
- 8 `ids` (A) Resistive drain current.
- 9 `idb` (A) Resistive drain-bulk current.
- 10 `isb` (A) Resistive source-bulk current.
- 11 `iavl` (A) Substrate current.
- 12 `pwr` (W) Power.
- 13 `vto` (V) Threshold voltage at zero back-bias.
- 14 `vts` (V) V_{ts} .
- 15 `vgt` (V) Effective gate drive including backbias and drain effects.
- 16 `vdss` (V) Saturation voltage at actual bias.
- 17 `vsat` (V) Saturation limit.
- 18 `gm` (S) Transconductance ($d\,ids / d\,vgs$).
- 19 `gmb` (S) Bulk transconductance ($d\,ids / d\,vbs$).
- 20 `gds` (S) Output conductance ($d\,ids / d\,vds$).
- 21 `cdd` (F) Capacitance ($d\,qd / d\,vd$).

Spectre Circuit Simulator Reference

Component Statements Part 2

- 22 `cdg` (F) Capacitance ($-d q_d / d v_g$).
- 23 `cds` (F) Capacitance ($-d q_d / d v_s$).
- 24 `cdb` (F) Capacitance ($-d q_d / d v_b$).
- 25 `cgd` (F) Capacitance ($-d q_g / d v_d$).
- 26 `cgg` (F) Capacitance ($d q_g / d v_g$).
- 27 `cgs` (F) Capacitance ($-d q_g / d v_s$).
- 28 `cgb` (F) Capacitance ($-d q_g / d v_b$).
- 29 `csd` (F) Capacitance ($-d q_s / d v_d$).
- 30 `csq` (F) Capacitance ($-d q_s / d v_g$).
- 31 `css` (F) Capacitance ($d q_s / d v_s$).
- 32 `csb` (F) Capacitance ($-d q_s / d v_b$).
- 33 `cbd` (F) Capacitance ($-d q_b / d v_d$).
- 34 `cbg` (F) Capacitance ($-d q_b / d v_g$).
- 35 `cbs` (F) Capacitance ($-d q_b / d v_s$).
- 36 `cbb` (F) Capacitance ($d q_b / d v_b$).
- 37 `u` Transistor gain (g_m/g_d).
- 38 `rout` (Ω) Small signal output resistance ($1/g_d$).
- 39 `vearly` (V) Equivalent Early voltage ($|i_d|/g_d$).
- 40 `keff` (\sqrt{V}) Describes body effect at actual bias.
- 41 `beff` (S/V) Effective beta at actual bias in the simple MOS model ($2*|i_d|/v_{gt}^2$).
- 42 `fug` (Hz) Unity gain frequency at actual bias ($g_m/(2*\pi*c_{in})$).

Spectre Circuit Simulator Reference

Component Statements Part 2

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

a1	O-24	etamobr	M-26	sl2ko	M-15	ther1	M-30
alr	M-52	fug	OP-42	sl2vfb	M-11	ther1	O-11
a2	O-25	gds	OP-20	sla1	M-54	ther2	O-12
a2r	M-56	gm	OP-18	sla2	M-57	ther2	M-31
a3	O-26	gmb	OP-19	sla3	M-60	thersq	M-28
a3r	M-59	gthep	M-38	slko	M-14	thesr	O-7
alp	O-20	gthep	O-15	sltheth	M-40	thesrr	M-20
alpsq	M-47	iavl	OP-11	slvfb	M-10	theth	O-16
area	I-4	ibe	OP-4	ssf	O-19	thethr	M-39
beff	OP-41	idb	OP-9	ssfsq	M-45	tnom	M-66
bet	O-6	ide	OP-1	stal	M-53	tox	M-62
betsq	M-18	ids	OP-8	stthen	M-33	tr	M-64
cbb	OP-36	ige	OP-2	stthep	M-36	tref	M-65
cbd	OP-33	isb	OP-10	sttheph	M-24	trise	M-68
cbg	OP-34	ise	OP-3	stthesr	M-21	type	M-1
cbs	OP-35	keff	OP-40	stvfb	M-9	u	OP-37
cdb	OP-24	ko	O-4	swal	M-55	vds	OP-5
cdd	OP-21	kor	M-13	swa2	M-58	vdss	OP-16
cdg	OP-22	l	I-2	swa3	M-61	vearly	OP-39
cds	OP-23	lap	M-5	swalp	M-48	vfb	O-3
cgb	OP-28	le	O-1	swetamob	M-27	vfbr	M-8
cgd	OP-25	ler	M-2	swko	M-16	vgs	OP-6
cgdo	O-28	lvar	M-4	swssf	M-46	vgt	OP-15
cgg	OP-26	m	I-6	swthen	M-34	vp	O-21
cgs	OP-27	mexp	O-22	swthep	M-37	vp	M-49
cgso	O-29	mexpl	M-51	swtheph	M-25	vsat	OP-17
col	M-63	mexpo	M-50	swther	M-29	vsb	OP-7
cox	O-27	mult	I-3	swthesr	M-22	vto	OP-13
csb	OP-32	phib	O-5	swtheth	M-41	vts	OP-14
csd	OP-29	phibr	M-17	swvfb	M-12	w	I-1
csg	OP-30	phit	O-23	then	O-13	we	O-2
css	OP-31	pwr	OP-12	thenr	M-32	wer	M-3
dphi	O-18	region	I-5	thep	O-14	wot	M-7
dphi	M-44	rout	OP-38	theph	O-8	wvar	M-6
dta	M-67	sdibl	O-17	thephhr	M-23		
etabet	M-19	sdiblexp	M-43	thepr	M-35		
etamob	O-9	sdiblo	M-42	ther	O-10		

Compact MOS-Transistor Distortion Model (mos1100)

Description

The mos1100 model is based on the thesis of Ronald van Langevelde: "A compact MOSFET Model for Distortion Analysis in Analog Circuit Design", Technische Universiteit Eindhoven, 1998.

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In extension to the description a minimum conductance g_{min} is inserted between the drain and source node, to aid convergence. The value of g_{min} is set by an options statement, default = 1e-12 S.

This device is supported within altergroups.

This device is dynamically loaded from the shared object

`/vobs/spectre_dev/tools.sun4v/spectre/lib/cmi/2.0.doc/libphilips.so`

Instance Definition

Name d g s [b] ModelName parameter=value ...

Instance Parameters

- 1 w=1.0 scale mDrawn channel width in the layout. Scale set by option scale..
- 2 l=1.0 scale mDrawn channel length in the layout. Scale set by option scale..
- 3 mult=1Number of devices in parallel.
- 4 area=1Alias of mult.
- 5 region=triodeEstimated DC operating region, used as a convergence aid.
Possible values are off, triode, sat, or subth.
- 6 m=1Multiplicity factor.

Model Definition

model modelName mos1100 parameter=value ...

Spectre Circuit Simulator Reference

Component Statements Part 2

Model Parameters

Device Type Parameters

- 1 `type=n` Transistor gender.
Possible values are n or p.

Geometry Parameters

- 2 `ler=1.0e-6` m Effective channel length of the reference transistor.
- 3 `wer=1e-5` m Effective channel width of the reference transistor.
- 4 `lvar=0.0` m Difference between the actual and the programmed poly-silicon gate length.
- 5 `lap=4.0e-8` m Effective channel length reduction per side.
- 6 `wvar=0.0` m Difference between the actual and the programmed field-oxide opening.
- 7 `wot=0.0` m Effective channel width reduction per side.

Threshold-Voltage Parameters

- 8 `vfbr=-1.050` V Flat-band voltage for reference transistor.
- 9 `stvfb=0.5e-03` V/K Coefficient of temperature dependence of `vfbr`.
- 10 `kor=0.5` \sqrt{V} Body effect coefficient for the reference transistor.
- 11 `slko=0.0` \sqrt{V} m
Coefficient of the length dependence of k_0 .
- 12 `sl2ko=0.0` \sqrt{V} m²
Second coefficient of the length dependence of k_0 .
- 13 `swko=0.0` \sqrt{V} m
Coefficient of the width dependence of k_0 .
- 14 `phibr=0.95` V Surface potential at strong inversion.
- 15 `slphib=0.0` Vm Coefficient of the length dependence of `phib`.

Spectre Circuit Simulator Reference

Component Statements Part 2

16 `sl2phib=0.0` V m^2 Second coefficient of the length dependence of phib.

17 `swphib=0.0` V m^2 Coefficient of the width dependence of phib.

Channel-Current Parameters

18 `fbet1=0.0` Relative mobility decrease due to first lateral profile.

19 `lp1=0.8e-6` Characteristic length of first lateral profile.

20 `lp2=0.8e-6` Characteristic length of second lateral profile.

21 `fbet2=0.0` Relative mobility decrease due to second lateral profile.

22 `betsq=370.9e-06` A/V^2
Gain factor for an infinite square transistor.

23 `etabet=1.3` Exponent of the temperature dependence of the gain factor.

24 `thesrr=0.4` $1/\text{V}$ Coefficient of the mobility reduction due to surface roughness scattering.

25 `swthesr=0.0` m Coefficient of the width dependence of thesr.

26 `thephr=1.29e-2` $1/\text{V}$
Coefficient of the mobility reduction due to phonon scattering.

27 `swtheph=0.0` m Coefficient of the width dependence of theph.

28 `etaph=1.75` Exponent of the temperature dependence of theph.

29 `etamobr=1.4` Effective field parameter for dependence on depletion/inversion charge.

30 `stetamob=0.0` $1/\text{K}$ Coefficient of the temperature dependence of etamob.

31 `swetamob=0.0` m Coefficient of the width dependence of etamob.

32 `nur=1.0` Exponent of the field dependence of the mobility model minus 1.

33 `nuexp=5.25` Exponent of the temperature dependence of parameter nu.

34 `therr=0.155` $1/\text{V}$ Coefficient of the series resistance.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 35 `swther=0.0` m Coefficient of the width dependence of `ther`.
- 36 `etar=0.95` Exponent of the temperature dependence of `ther`.
- 37 `thesatr=0.5` $1/v$ Velocity saturation parameter due to optical/acoustic phonon scattering.
- 38 `etasat=1.04` $1/v$ Exponent of the temperature dependence of `thesat`.
- 39 `swthesat=0.0` m Coefficient of the width dependence of `thesat`.
- 40 `slthesat=1.0` Coefficient of length dependence of `thesat`.
- 41 `thesatexp=1.0` Exponent of length dependence of `thesat`.

Drain-Feedback Parameters

- 42 `thethr=1e-3` $1/v^3$ Coefficient of self-heating.
- 43 `thethexp=1.0` Exponent of the length dependence of `theth`.
- 44 `ssfr=6.25e-3` $1/\sqrt{v}$
Static feedback parameter.
- 45 `swssf=0.0` m Coefficient of the width dependence of `ssf`.
- 46 `slssf=1.0e-6` m Coefficient of the length dependence of `ssf`.
- 47 `alpr=0.010` Factor of the channel length modulation.
- 48 `swalp=0.0` m Coefficient of the width dependence of `alp`.
- 49 `slalp=1.0` m Coefficient of the length dependence of `alp`.
- 50 `alpexp=1.0` Exponent of the length dependence of `alp`.
- 51 `vp=5.0e-2` v Characteristic voltage of channel-length modulation.

Sub-Threshold Parameters

- 52 `sdiblo=2e-03` $1/\sqrt{v}$
Drain-induced barrier lowering parameter.

Spectre Circuit Simulator Reference

Component Statements Part 2

53 `sdiblexp=1.35` Exponent of the length dependence of `sdibl`.

54 `mor=0.0` Parameter for short-channel subthreshold slope.

55 `moexp=1.34` Exponent of the length dependence of `mo`.

Smoothing Parameter

56 `lmin=1.5e-7 m` Minimum effective channel length in technology, used for calculation of smoothing factor `m`.

Weak-Avalanche Parameters

57 `a1r=6` Factor of the weak-avalanche current.

58 `sla1=0.0 m` Coefficient of the length dependence of `a1`.

59 `swa1=0.0 m` Coefficient of the width dependence of `a1`.

60 `sta1=0.0 1/K` Coefficient of the temperature dependence of `a1`.

61 `a2r=38.0 v` Exponent of the weak-avalanche current.

62 `sla2=0.0 v m` Coefficient of the length dependence of `a2`.

63 `swa2=0.0 v m` Coefficient of the width dependence of `a2`.

64 `a3r=1.0` Factor of the drain-source voltage above which weak-avalanche occurs.

65 `sla3=0.0 m` Coefficient of the length dependence of `a3`.

66 `swa3=0.0 m` Coefficient of the width dependence of `a3`.

Gate Current Parameters

67 `iginvr=0.0 A/V2` Gain factor for intrinsic gate tunnelling current in inversion.

68 `igaccr=0.0 A/V2` Gain factor for intrinsic gate tunnelling current in accumulation.

69 `igovr=0.0 A/V2` Gain factor for Source/Drain overlap gate tunnelling current.

Spectre Circuit Simulator Reference

Component Statements Part 2

Charge parameters

70 $t_{ox}=3.2e-09$ m Thickness of the oxide layer.

71 $c_{ol}=3.2e-10$ F/m Gate overlap capacitance per unit channel width.

Noise Parameters

72 $n_{tr}=1.656e-20$ J Coefficient of the thermal noise.

73 $n_{far}=1.573e22$ $1/(V_m^4)$
First coefficient of the flicker noise.

74 $n_{fbr}=4.752e8$ $1/(V_m^2)$
Second coefficient of the flicker noise.

75 $n_{fcr}=0.0$ $1/V$ Third coefficient of the flicker noise.

Temperature Parameters

76 t_r (C) Reference temperature. Default set by option t_{nom} .

77 t_{ref} (C) Alias of t_r . Default set by option t_{nom} .

78 t_{nom} (C) Alias of t_r . Default set by option t_{nom} .

79 $dta=0.0$ K Temperature offset of the device.

80 $t_{rise}=0.0$ K Alias of dta .

Other Parameters

81 $k_{pinv}=0.0$ $1/\sqrt{V}$
Inverse of body-effect factor of the poly-silicon gate.

82 $ther1=0.0$ V Numerator of gate voltage dependent part of series resistance.

83 $ther2=1.0$ V Denominator of gate voltage dependent part of series resistance.

84 $v_p=5.0e-2$ V Characteristic voltage of channel-length modulation.

85 $binv=48.0$ V Probability factor for intrinsic gate tunnelling current in inversion.

Spectre Circuit Simulator Reference

Component Statements Part 2

86 `bacc=48.0` \sqrt{V} Probability factor for intrinsic gate tunnelling current in accumulation.

87 `vfbov=0.0` \sqrt{V} Flat-band voltage for the Source/Drain overlap extensions.

88 `kov=2.5` \sqrt{V} Body-effect factor for the Source/Drain overlap extensions.

89 `gatenoise=0.0`Flag for in/exclusion of induced gate thermal noise.

Output Parameters

1 `vto` (V)Zero-bias threshold voltage.

2 `le` (m)Effective channel length.

3 `we` (m)Effective channel width.

4 `ko` (\sqrt{V}) Body-effect factor.

5 `phib` (V)Surface potential at the onset of strong inversion.

6 `bet` (A/V^2)Gain factor.

7 `thesr` ($1/V$)Mobility degradation parameter due to surface roughness scattering.

8 `theph` ($1/V$)Mobility degradation parameter due to phonon scattering.

9 `etamob`Effective field parameter for dependence on depletion charge.

10 `nu`Exponent of field dependence of mobility model.

11 `ther` ($1/V$)Coefficient of series resistance.

12 `thesat` ($1/V$)Velocity saturation parameter due to optical/acoustic phonon scattering.

13 `theth` ($1/V^3$)Coefficient of self-heating.

14 `sdibl` ($1/\sqrt{V}$) Drain-induced barrier lowering parameter.

15 `mo`Parameter for (short-channel) subthreshold slope.

16 `ssf` ($1/\sqrt{V}$) Static-feedback parameter.

17 `alp`Factor of channel length modulation.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 18 `mexp` Smoothing factor.
- 19 `a1` Factor of the weak-avalanche current.
- 20 `a2 (V)` Exponent of the weak-avalanche current.
- 21 `a3` Factor of the drain-source voltage above which weak-avalanche occurs.
- 22 `iginv (A/V2)` Gain factor for intrinsic gate tunnelling current in inversion.
- 23 `igacc (A/V2)` Gain factor for intrinsic gate tunnelling current in accumulation.
- 24 `igov (A/V2)` Gain factor for Source/Drain overlap tunnelling current.
- 25 `cox (F)` Oxide capacitance for the intrinsic channel (* mult).
- 26 `cgdo (F)` Oxide capacitance for the gate-drain overlap (* mult).
- 27 `cgs0 (F)` Oxide capacitance for the gate-source overlap (* mult).
- 28 `nt (J)` Thermal noise coefficient.
- 29 `nfa (1/(Vm4))` First coefficient of the flicker noise.
- 30 `nfb (1/(Vm2))` Second coefficient of the flicker noise.
- 31 `nfc (1/V)` Third coefficient of the flicker noise.
- 32 `tox (m)` Thickness of gate oxide layer.

Operating-Point Parameters

- 1 `nfcr=0.0 1/V` Third coefficient of the flicker noise.
- 2 `ide (A)` Resistive drain current.
- 3 `ige (A)` Resistive gate current.
- 4 `ise (A)` Resistive source current.
- 5 `ibe (A)` Resistive bulk current.
- 6 `isb (A)` Resistive source-bulk current.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 7 `idb` (A) Resistive drain-bulk current.
- 8 `pwr` (W) Power.
- 9 `ids` (A) Drain current, excl. avalanche and tunnel currents.
- 10 `iavl` (A) Substrate current due to weak-avalanche.
- 11 `igs` (A) Gate-to-source current due to direct tunnelling.
- 12 `igd` (A) Gate-to-drain current due to direct tunnelling.
- 13 `igb` (A) Gate-to-bulk current due to direct tunnelling.
- 14 `vds` (V) Drain-source voltage.
- 15 `vgs` (V) Gate-source voltage.
- 16 `vsb` (V) Source-bulk voltage.
- 17 `vts` (V) Threshold voltage including back-bias effects.
- 18 `vth` (V) Threshold voltage including back-bias and drain-bias effects.
- 19 `vgt` (V) Effective gate drive voltage including back-bias and drain voltage effects.
- 20 `vdss` (V) Drain saturation voltage at actual bias.
- 21 `vsat` (V) Saturation limit.
- 22 `gm` (S) Transconductance ($d\,ids / d\,vgs$).
- 23 `gmb` (S) Substrate-transconductance ($d\,ids / d\,vbs$).
- 24 `gds` (S) Output conductance ($d\,ids / d\,vds$).
- 25 `cdd` (F) Capacitance ($d\,qd / d\,vd$).
- 26 `cdg` (F) Capacitance ($- d\,qd / d\,vg$).
- 27 `cds` (F) Capacitance ($- d\,qd / d\,vs$).
- 28 `cdb` (F) Capacitance ($- d\,qd / d\,vb$).

Spectre Circuit Simulator Reference

Component Statements Part 2

- 29 `cgd` (F) Capacitance ($-d qg / d vd$).
- 30 `cgg` (F) Capacitance ($d qg / d vg$).
- 31 `cgs` (F) Capacitance ($-d qg / d vs$).
- 32 `cgb` (F) Capacitance ($-d qg / d vb$).
- 33 `csd` (F) Capacitance ($-d qs / d vd$).
- 34 `csq` (F) Capacitance ($-d qs / d vg$).
- 35 `css` (F) Capacitance ($d qs / d vs$).
- 36 `csb` (F) Capacitance ($-d qs / d vb$).
- 37 `cbd` (F) Capacitance ($-d qb / d vd$).
- 38 `cbg` (F) Capacitance ($-d qb / d vg$).
- 39 `cbs` (F) Capacitance ($-d qb / d vs$).
- 40 `cbb` (F) Capacitance ($d qb / d vb$).
- 41 `u` Transistor gain (gm/gds).
- 42 `rout` (Ω) Small-signal output resistance ($1/gds$).
- 43 `vearly` (V) Equivalent Early voltage ($|id|/gds$).
- 44 `keff` (\sqrt{V}) Body effect parameter.
- 45 `beff` (A/V^2) Gain factor.
- 46 `fug` (Hz) Unity gain frequency at actual bias ($gm/(2*\pi*cin)$).
- 47 `sqrtsfw` (V/\sqrt{Hz})
Input-referred RMS white noise voltage density.
- 48 `sqrtsff` (V/\sqrt{Hz})
Input-referred RMS white noise voltage density at 1 kHz.
- 49 `fknee` (Hz) Cross-over frequency above which white noise is dominant.

Spectre Circuit Simulator Reference

Component Statements Part 2

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

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cbs	OP-39	igov	O-24	sl2ko	M-12	tox	O-32
cdb	OP-28	igovr	M-69	sl2phib	M-16	tox	M-70
cdd	OP-25	igs	OP-11	slal	M-58	tr	M-76
cdg	OP-26	isb	OP-6	sla2	M-62	tref	M-77
cds	OP-27	ise	OP-4	sla3	M-65	trise	M-80
cgb	OP-32	keff	OP-44	slalp	M-49	type	M-1
cgd	OP-29	ko	O-4	slko	M-11	u	OP-41
cgdo	O-26	kor	M-10	slphib	M-15	vds	OP-14
cgg	OP-30	kov	M-88	slssf	M-46	vdss	OP-20
cgs	OP-31	kpinv	M-81	slthesat	M-40	vearly	OP-43
cgso	O-27	l	I-2	sqrtsff	OP-48	vfbov	M-87
col	M-71	lap	M-5	sqrtsfw	OP-47	vfbr	M-8
cox	O-25	le	O-2	ssf	O-16	vgs	OP-15
csb	OP-36	ler	M-2	ssfr	M-44	vgt	OP-19
csd	OP-33	lmin	M-56	stal	M-60	vp	M-84
csg	OP-34	lp1	M-19	stetamob	M-30	vp	M-51
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Spectre Circuit Simulator Reference

Component Statements Part 2

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MOS Level-15 Transistor (mos15)

Description

The MOS15 model is the AMS level 15 model which is the modified Berkeley SPICE level-2 model with the DC model replaced by that of AMS. It is an analytical one-dimensional model that incorporates most of the second-order small-size effects. A smoother version of the level-15 model (with continuous Gds at Vdsat) was also developed. Three charge models are available. MOS15 transistors require the use of a model statement.

This device is not supported within altergroup.

This device is dynamically loaded from the shared object

/vobs/spectre_dev/tools.sun4v/spectre/lib/cmi/2.0.doc/libstmodels.so

Instance Definition

Name d g s b ModelName parameter=value ...

Instance Parameters

- 1 w (m) Channel width.
- 2 l (m) Channel length.
- 3 as (m²) Area of source diffusion.
- 4 ad (m²) Area of drain diffusion.
- 5 ps (m) Perimeter of source diffusion.
- 6 pd (m) Perimeter of drain diffusion.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 7 `nrd` (m/m) Number of squares of drain diffusion.
- 8 `nrs` (m/m) Number of squares of source diffusion.
- 9 `ld` (m) Length of drain diffusion region.
- 10 `ls` (m) Length of source diffusion region.
- 11 `m=1` Multiplicity factor (number of MOSFETs in parallel).
- 12 `region=triode` Estimated operating region.
Possible values are `off`, `triode`, `sat`, or `subth`.
- 13 `trise` Temperature rise from ambient.

Model Definition

`model modelName mos15 parameter=value ...`

Model Parameters

Device Type Parameters

- 1 `type=n` Transistor type.
Possible values are `n` or `p`.

Drain Current Model Parameters

- 2 `vto=0` `v` Threshold voltage at zero body bias.
- 3 `kp=2.0718e-5` A/V^2
Transconductance parameter.
- 4 `lambda=0` $1/\text{V}$ Channel length modulation parameter.
- 5 `phi=0.7` `v` Surface potential at strong inversion.
- 6 `gamma=0` $\sqrt{\text{V}}$ Body-effect parameter.
- 7 `uo=600` $\text{cm}^2/\text{V s}$ Carrier surface mobility.
- 8 `vmax= ∞` m/s Carrier saturation velocity.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 9 `ucrit=0 V/cm`Critical field for mobility degradation.
- 10 `uexp=0`Critical field exponent for mobility degradation.
- 11 `utra=0 1/V`Transverse field for mobility.
- 12 `neff=1`Total channel charge coefficient.
- 13 `delta=0`Width effect on threshold voltage.

Process Parameters

- 14 `nsub=1.13e16 cm-3`
Channel doping concentration.
- 15 `nss=0 cm-2`Surface state density.
- 16 `nfs=0 cm-2`Fast surface state density.
- 17 `tpg=+1`Type of gate (+1 = opposite of substate, -1 = same as substate, 0 = aluminum).
- 18 `tox=1e-7 m`Gate oxide thickness.
- 19 `ld=0 m`Lateral diffusion.
- 20 `wd=0 m`Field-oxide encroachment.
- 21 `xw=0 m`Width variation due to masking and etching.
- 22 `xl=0 m`Length variation due to masking and etching.
- 23 `xj=0 m`Source/drain junction depth.

Impact Ionization Parameters

- 24 `ai0=0 1/V`Impact ionization current coefficient.
- 25 `lai0=0 μm/V`Length sensitivity of `ai0`.
- 26 `wai0=0 μm/V`Width sensitivity of `ai0`.
- 27 `bi0=0 V`Impact ionization current exponent.

Spectre Circuit Simulator Reference

Component Statements Part 2

28 `lbi0=0` μm `vLength` sensitivity of `bi0`.

29 `wbi0=0` μm `vWidth` sensitivity of `bi0`.

Overlap Capacitance Parameters

30 `cgso=0` F/m Gate-source overlap capacitance.

31 `cgdo=0` F/m Gate-drain overlap capacitance.

32 `cgbo=0` F/m Gate-bulk overlap capacitance.

33 `meto=0` m Metal overlap in fringing field.

Charge Model Selection Parameters

34 `capmod=bsim` Intrinsic charge model.

Possible values are `none`, `meyer`, `yang`, or `bsim`.

35 `xpart=1` Drain/source channel charge partition in saturation for BSIM charge model, use 0.0 for 40/60, 0.5 for 50/50, or 1.0 for 0/100.

36 `xqc=0` Drain/source channel charge partition in saturation for charge models, e.g. use 0.4 for 40/60, 0.5 for 50/50, 0 for 0/100.

Parasitic Resistance Parameters

37 `rs=0` Ω Source resistance.

38 `rd=0` Ω Drain resistance.

39 `rsh=0` Ω/sqr Source/drain diffusion sheet resistance.

40 `rss=0` Ω `mScalable` source resistance.

41 `rdd=0` Ω `mScalable` drain resistance.

42 `rsc=0` Ω Source contact resistance.

43 `rdc=0` Ω Drain contact resistance.

44 `minr=0.1` Ω Minimum source/drain resistance.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 45 `ldif=0` `m`Lateral diffusion beyond the gate.
- 46 `hdif=0` `m`Length of heavily doped diffusion.
- 47 `lgcs=0` `m`Gate-to-contact length of source side.
- 48 `lgcd=0` `m`Gate-to-contact length of drain side.
- 49 `sc= ∞` `m`Spacing between contacts.

Junction Diode Model Parameters

- 50 `js` (A/m^2) Bulk junction reverse saturation current density.
- 51 `is=1e-14` `A` Bulk junction reverse saturation current.
- 52 `n=1` Junction emission coefficient.
- 53 `dskip=yes` Use simple piece-wise linear model for diode currents below $0.1 \cdot \text{iabstol}$.
Possible values are `no` or `yes`.
- 54 `imax=1` `A` Explosion current.
- 55 `jmax=1e8` A/m^2 Explosion current density.

Junction Capacitance Model Parameters

- 56 `cbs=0` `F` Bulk-source zero-bias junction capacitance.
- 57 `cbd=0` `F` Bulk-drain zero-bias junction capacitance.
- 58 `cj=0` F/m^2 Zero-bias junction bottom capacitance density.
- 59 `mj=1/2` Bulk junction bottom grading coefficient.
- 60 `pb=0.8` `V` Bulk junction built-in potential.
- 61 `fc=0.5` Forward-bias depletion capacitance threshold.
- 62 `cjsw=0` F/m Zero-bias junction sidewall capacitance density.
- 63 `mjsw=1/3` Bulk junction sidewall grading coefficient.

Spectre Circuit Simulator Reference

Component Statements Part 2

64 `pbsw=0.8` v Side-wall junction built-in potential.

65 `fcsw=0.5` Side-wall forward-bias depletion capacitance threshold.

Operating Region Warning Control Parameters

66 `alarm=none` Forbidden operating region.
Possible values are `none`, `off`, `triode`, `sat`, `subth`, or `rev`.

67 `bvj= ∞` v Junction reverse breakdown voltage.

Temperature Effects Parameters

68 `tnom (C)` Parameters measurement temperature. Default set by options.

69 `trise=0` c Temperature rise from ambient.

70 `uto=0` c Mobility temperature offset.

71 `ute=-1.5` Mobility temperature exponent.

72 `tlev=0` DC temperature selector.

73 `tlevc=0` AC temperature selector.

74 `eg=1.12452` v Energy band gap.

75 `gap1=7.02e-4` v/C Band gap temperature coefficient.

76 `gap2=1108` c Band gap temperature offset.

77 `flex=0` Temperature exponent for `ucrit`.

78 `lamex=0` $1/C$ Temperature parameter for `lambda` and `kappa`.

79 `trs=0` $1/C$ Temperature parameter for source resistance.

80 `trd=0` $1/C$ Temperature parameter for drain resistance.

81 `xti=3` Saturation current temperature exponent.

82 `ptc=0` v/C Surface potential temperature coefficient.

Spectre Circuit Simulator Reference

Component Statements Part 2

83 `tcv=0` V/C Threshold voltage temperature coefficient.

84 `pta=0` V/C Junction potential temperature coefficient.

85 `ptp=0` V/C Sidewall junction potential temperature coefficient.

86 `cta=0` 1/C Junction capacitance temperature coefficient.

87 `ctp=0` 1/C Sidewall junction capacitance temperature coefficient.

Default Instance Parameters

88 `w=3e-6` m Default channel width.

89 `l=3e-6` m Default channel length.

90 `as=0` m² Default area of source diffusion.

91 `ad=0` m² Default area of drain diffusion.

92 `ps=0` m Default perimeter of source diffusion.

93 `pd=0` m Default perimeter of drain diffusion.

94 `nrd=0` m/m Default number of squares of drain diffusion.

95 `nrs=0` m/m Default number of squares of source diffusion.

96 `ldd=0` m Default length of drain diffusion region.

97 `lds=0` m Default length of source diffusion region.

Noise Model Parameters

98 `kf=0` Flicker (1/f) noise coefficient.

99 `af=1` Flicker (1/f) noise exponent.

100 `ef=1` Flicker (1/f) noise frequency exponent.

101 `noisemod=1` Noise model selector.

Spectre Circuit Simulator Reference

Component Statements Part 2

The `imax` (`jmax`) parameter is used to aid convergence and prevent numerical overflow. The junction characteristics of the FET are accurately modeled for current (density) up to `imax` (`jmax`). For currents (density) above `imax` (`jmax`), the junction is modeled as a linear resistor and a warning is printed.

Output Parameters

- 1 `weff` (m) Effective channel width.
- 2 `leff` (m) Effective channel length.
- 3 `rseff` (Ω) Effective source resistance.
- 4 `rdeff` (Ω) Effective drain resistance.

Operating-Point Parameters

- 1 `type=n` Transistor type.
Possible values are `n` or `p`.
- 2 `region=triode` Estimated operating region.
Possible values are `off`, `triode`, `sat`, or `subth`.
- 3 `reversed` Reverse mode indicator.
Possible values are `no` or `yes`.
- 4 `id` (A) Resistive drain current.
- 5 `vgs` (V) Gate-source voltage.
- 6 `vds` (V) Drain-source voltage.
- 7 `vbs` (V) Bulk-source voltage.
- 8 `vth` (V) Threshold voltage.
- 9 `vdsat` (V) Drain-source saturation voltage.
- 10 `gm` (S) Common-source transconductance.
- 11 `gds` (S) Common-source output conductance.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 12 `gmbs` (S) Body-transconductance.
- 13 `gameff` (\sqrt{V}) Effective body effect coefficient.
- 14 `betaeff` (A/V^2) Effective beta.
- 15 `cbd` (F) Drain-bulk junction capacitance.
- 16 `cbs` (F) Source-bulk junction capacitance.
- 17 `cgs` (F) Gate-source capacitance.
- 18 `cgd` (F) Gate-drain capacitance.
- 19 `cgb` (F) Gate-bulk capacitance.
- 20 `ron` (Ω) On-resistance.
- 21 `ib` (A) Resistive bulk current.
- 22 `pwr` (W) Power at op point.

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

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<code>ad</code> I-4	<code>gds</code> OP-11	<code>noisemod</code> M-101	<code>tnom</code> M-68
<code>af</code> M-99	<code>gm</code> OP-10	<code>nrd</code> M-94	<code>tox</code> M-18
<code>ai0</code> M-24	<code>gmbs</code> OP-12	<code>nrd</code> I-7	<code>tpg</code> M-17
<code>alarm</code> M-66	<code>hdif</code> M-46	<code>nrs</code> M-95	<code>trd</code> M-80
<code>as</code> I-3	<code>ib</code> OP-21	<code>nrs</code> I-8	<code>trise</code> I-13
<code>as</code> M-90	<code>id</code> OP-4	<code>nss</code> M-15	<code>trise</code> M-69
<code>betaeff</code> OP-14	<code>imax</code> M-54	<code>nsub</code> M-14	<code>trs</code> M-79
<code>bi0</code> M-27	<code>is</code> M-51	<code>pb</code> M-60	<code>type</code> M-1
<code>bvj</code> M-67	<code>jmax</code> M-55	<code>pbsw</code> M-64	<code>type</code> OP-1
<code>capmod</code> M-34	<code>js</code> M-50	<code>pd</code> I-6	<code>ucrit</code> M-9
<code>cbd</code> M-57	<code>kf</code> M-98	<code>pd</code> M-93	<code>uexp</code> M-10

Spectre Circuit Simulator Reference

Component Statements Part 2

cbd	OP-15	kp	M-3	phi	M-5	uo	M-7
cbs	OP-16	l	I-2	ps	I-5	ute	M-71
cbs	M-56	l	M-89	ps	M-92	uto	M-70
cgb	OP-19	lai0	M-25	pta	M-84	utra	M-11
cgbo	M-32	lambda	M-4	ptc	M-82	vbs	OP-7
cgd	OP-18	lamex	M-78	ptp	M-85	vds	OP-6
cgdo	M-31	lbi0	M-28	pwr	OP-22	vdsat	OP-9
cgs	OP-17	ld	I-9	rd	M-38	vgs	OP-5
cgso	M-30	ld	M-19	rdc	M-43	vmax	M-8
cj	M-58	ldd	M-96	rdd	M-41	vth	OP-8
cjsw	M-62	ldif	M-45	rdeff	O-4	vto	M-2
cta	M-86	lds	M-97	region	I-12	w	M-88
ctp	M-87	leff	O-2	region	OP-2	w	I-1
delta	M-13	lgcd	M-48	reversed	OP-3	wai0	M-26
dskip	M-53	lgcs	M-47	ron	OP-20	wbi0	M-29
ef	M-100	ls	I-10	rs	M-37	wd	M-20
eg	M-74	m	I-11	rsc	M-42	weff	O-1
flex	M-77	meto	M-33	rseff	O-3	xj	M-23
fc	M-61	minr	M-44	rsh	M-39	xl	M-22
fcsw	M-65	mj	M-59	rss	M-40	xpart	M-35
gameff	OP-13	mjsw	M-63	sc	M-49	xqc	M-36
gamma	M-6	n	M-52	tcv	M-83	xti	M-81
gap1	M-75	neff	M-12	tlev	M-72	xw	M-21

MOS Level-2 Transistor (mos2)

Description

The MOS2 model is the level-2 model from Berkeley SPICE. The MOS2 model is an analytical, one-dimensional model that incorporates most of the second-order small-size effects. A smoother version of the level-2 model (with continuous G_{ds} at V_{dsat}) is also available. Three charge models are available. MOS2 transistors require that you use a model statement.

This device is supported within altergroups.

Sample Instance Statement

```
mn2 (1 2 0 0) nch2 w=10u ad=20p as=20p ps=24u pd=24u
```

Sample Model Statement

```
model nch2 mos2 type=n vto=0.66 lambda=0.018 gamma=0.6 nsub=0.213e16 kp=0.978e-4  
tpg=-1 vmax=6e4 ucrit=1e7 utra=0.1 uexp=0.2 is=0
```

Spectre Circuit Simulator Reference

Component Statements Part 2

Instance Definition

Name d g s b modelName parameter=value ...

Instance Parameters

- 1 w (m) Channel width.
- 2 l (m) Channel length.
- 3 as (m²) Area of source diffusion.
- 4 ad (m²) Area of drain diffusion.
- 5 ps (m) Perimeter of source diffusion.
- 6 pd (m) Perimeter of drain diffusion.
- 7 nrd (m/m) Number of squares of drain diffusion.
- 8 nrs (m/m) Number of squares of source diffusion.
- 9 ld (m) Length of drain diffusion region.
- 10 ls (m) Length of source diffusion region.
- 11 m=1 Multiplicity factor (number of MOSFETs in parallel).
- 12 region=triode Estimated operating region.
Possible values are off, triode, sat, subth, or breakdown.
- 13 trise Temperature rise from ambient.
- 14 degradation=no Hot-electron degradation flag.
Possible values are no or yes.

Model Definition

model modelName mos2 parameter=value ...

Spectre Circuit Simulator Reference

Component Statements Part 2

Model Parameters

Device Type Parameters

- 1 `type=n` Transistor type.
Possible values are `n` or `p`.

Drain Current Model Parameters

- 2 `vto=0` V Threshold voltage at zero body bias.
- 3 `kp=2.0718e-5` A/V^2
Transconductance parameter.
- 4 `lambda=0` $1/V$ Channel length modulation parameter.
- 5 `phi=0.7` V Surface potential at strong inversion.
- 6 `gamma=0` \sqrt{V} Body-effect parameter.
- 7 `uo=600` $cm^2/V \cdot s$ Carrier surface mobility.
- 8 `vmax=∞` m/s Carrier saturation velocity.
- 9 `ucrit=0` V/cm Critical field for mobility degradation.
- 10 `uexp=0` Critical field exponent for mobility degradation.
- 11 `utra=0` $1/V$ Transverse field for mobility.
- 12 `neff=1` Total channel charge coefficient.
- 13 `delta=0` Width effect on threshold voltage.
- 14 `smooth=yes` Drain current smoothing flag.
Possible values are `no` or `yes`.

Process Parameters

- 15 `nsub=1.13e16` cm^{-3}
Channel doping concentration.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 16 `nss=0 cm-2` Surface state density.
- 17 `nfs=0 cm-2` Fast surface state density.
- 18 `tpg=+1` Type of gate (+1 = opposite of substate, -1 = same as substate, 0 = aluminum).
- 19 `tox=1e-7 m` Gate oxide thickness.
- 20 `ld=0 m` Lateral diffusion.
- 21 `wd=0 m` Field-oxide encroachment.
- 22 `xw=0 m` Width variation due to masking and etching.
- 23 `xl=0 m` Length variation due to masking and etching.
- 24 `xj=0 m` Source/drain junction depth.

Impact Ionization Parameters

- 25 `ai0=0 1/V` Impact ionization current coefficient.
- 26 `lai0=0 μm/V` Length sensitivity of `ai0`.
- 27 `wai0=0 μm/V` Width sensitivity of `ai0`.
- 28 `bi0=0 V` Impact ionization current exponent.
- 29 `lbi0=0 μm V` Length sensitivity of `bi0`.
- 30 `wbi0=0 μm V` Width sensitivity of `bi0`.

Overlap Capacitance Parameters

- 31 `cgs0=0 F/m` Gate-source overlap capacitance.
- 32 `cgd0=0 F/m` Gate-drain overlap capacitance.
- 33 `cgb0=0 F/m` Gate-bulk overlap capacitance.
- 34 `meto=0 m` Metal overlap in fringing field.

Spectre Circuit Simulator Reference

Component Statements Part 2

Charge Model Selection Parameters

- 35 `capmod=bsim` Intrinsic charge model.
Possible values are `none`, `meyer`, `yang`, or `bsim`.
- 36 `xpart=1` Drain/source channel charge partition in saturation for BSIM charge model, use 0.0 for 40/60, 0.5 for 50/50, or 1.0 for 0/100.
- 37 `xqc=0` Drain/source channel charge partition in saturation for charge models, e.g. use 0.4 for 40/60, 0.5 for 50/50, 0 for 0/100.

Parasitic Resistance Parameters

- 38 `rs=0` Ω Source resistance.
- 39 `rd=0` Ω Drain resistance.
- 40 `rsh=0` Ω/sqr Source/drain diffusion sheet resistance.
- 41 `rss=0` Ω mScalable source resistance.
- 42 `rdd=0` Ω mScalable drain resistance.
- 43 `rsc=0` Ω Source contact resistance.
- 44 `rdc=0` Ω Drain contact resistance.
- 45 `minr=0.1` Ω Minimum source/drain resistance.
- 46 `ldif=0` m Lateral diffusion beyond the gate.
- 47 `hdif=0` m Length of heavily doped diffusion.
- 48 `lgcs=0` m Gate-to-contact length of source side.
- 49 `lgcd=0` m Gate-to-contact length of drain side.
- 50 `sc= ∞` m Spacing between contacts.

Junction Diode Model Parameters

- 51 `js` (A/m^2) Bulk junction reverse saturation current density.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 52 `is=1e-14` **A**Bulk junction reverse saturation current.
- 53 `n=1` Junction emission coefficient.
- 54 `dskip=yes` Use simple piece-wise linear model for diode currents below $0.1 \cdot i_{abstol}$.
Possible values are `no` or `yes`.
- 55 `imelt='imaxA'` Explosion current, diode is linearized beyond this current to aid convergence.
- 56 `jmelt='jmaxA/m'2`
Explosion current density, diode is linearized beyond this current to aid convergence.

Junction Capacitance Model Parameters

- 57 `cbs=0` **F**Bulk-source zero-bias junction capacitance.
- 58 `cbd=0` **F**Bulk-drain zero-bias junction capacitance.
- 59 `cj=0` **F/m²**Zero-bias junction bottom capacitance density.
- 60 `mj=1/2` Bulk junction bottom grading coefficient.
- 61 `pb=0.8` **V**Bulk junction built-in potential.
- 62 `fc=0.5` Forward-bias depletion capacitance threshold.
- 63 `cjsw=0` **F/m**Zero-bias junction sidewall capacitance density.
- 64 `mjsw=1/3` Bulk junction sidewall grading coefficient.
- 65 `pbsw=0.8` **V**Side-wall junction built-in potential.
- 66 `fcsw=0.5` Side-wall forward-bias depletion capacitance threshold.

Operating Region Warning Control Parameters

- 67 `alarm=none` Forbidden operating region.
Possible values are `none`, `off`, `triode`, `sat`, `subth`, or `rev`.
- 68 `imax=1` **A**Maximum current, currents above this limit generate a warning.

Spectre Circuit Simulator Reference

Component Statements Part 2

69 `jmax=1e8 A/m2` Maximum current density, currents above this limit generate a warning.

70 `bvj= ∞ V` Junction reverse breakdown voltage.

71 `vbox=1e9 tox V` Oxide breakdown voltage.

Temperature Effects Parameters

72 `tnom (C)` Parameters measurement temperature. Default set by `options`.

73 `trise=0 C` Temperature rise from ambient.

74 `uto=0 C` Mobility temperature offset.

75 `ute=-1.5` Mobility temperature exponent.

76 `tlev=0` DC temperature selector.

77 `tlevc=0` AC temperature selector.

78 `eg=1.12452 V` Energy band gap.

79 `gap1=7.02e-4 V/C` Band gap temperature coefficient.

80 `gap2=1108 C` Band gap temperature offset.

81 `flex=0` Temperature exponent for `ucrit`.

82 `lamex=0 1/C` Temperature parameter for `lambda` and `kappa`.

83 `trs=0 1/C` Temperature parameter for source resistance.

84 `trd=0 1/C` Temperature parameter for drain resistance.

85 `xti=3` Saturation current temperature exponent.

86 `ptc=0 V/C` Surface potential temperature coefficient.

87 `tcv=0 V/C` Threshold voltage temperature coefficient.

88 `pta=0 V/C` Junction potential temperature coefficient.

89 `ptp=0 V/C` Sidewall junction potential temperature coefficient.

Spectre Circuit Simulator Reference

Component Statements Part 2

90 `cta=0 1/C`Junction capacitance temperature coefficient.

91 `ctp=0 1/C`Sidewall junction capacitance temperature coefficient.

Default Instance Parameters

92 `w=3e-6 m`Default channel width.

93 `l=3e-6 m`Default channel length.

94 `as=0 m2`Default area of source diffusion.

95 `ad=0 m2`Default area of drain diffusion.

96 `ps=0 m`Default perimeter of source diffusion.

97 `pd=0 m`Default perimeter of drain diffusion.

98 `nrd=0 m/m`Default number of squares of drain diffusion.

99 `nrs=0 m/m`Default number of squares of source diffusion.

100 `ldd=0 m`Default length of drain diffusion region.

101 `lds=0 m`Default length of source diffusion region.

Noise Model Parameters

102 `noisemod=1`Noise model selector.

103 `kf=0`Flicker (1/f) noise coefficient.

104 `af=1`Flicker (1/f) noise exponent.

105 `ef=1`Flicker (1/f) noise frequency exponent.

106 `wnoi=1e-5 m`Channel width at which noise parameters were extracted.

Auto Model Selector Parameters

107 `wmax=1.0 m`Maximum channel width for which the model is valid.

Spectre Circuit Simulator Reference

Component Statements Part 2

108 `wmin=0.0` `m`Minimum channel width for which the model is valid.

109 `lmax=1.0` `m`Maximum channel length for which the model is valid.

110 `lmin=0.0` `m`Minimum channel length for which the model is valid.

Degradation Parameters

111 `degramod=spectre` Degradation model selector.
Possible values are `spectre` or `bert`.

112 `degradation=no` Hot-electron degradation flag.
Possible values are `no` or `yes`.

113 `dvthc=1` `v`Degradation coefficient for threshold voltage.

114 `dvthe=1` Degradation exponent for threshold voltage.

115 `duoc=1` `s`Degradation coefficient for transconductance.

116 `duoe=1` Degradation exponent for transconductance.

117 `crivth=0.1` `v`Maximum allowable threshold voltage shift.

118 `criuo=10%` Maximum allowable normalized mobility change.

119 `crigm=10%` Maximum allowable normalized transconductance change.

120 `criids=10%` Maximum allowable normalized drain current change.

121 `wnom=5e-6` `m`Nominal device width in degradation calculation.

122 `lnom=1e-6` `m`Nominal device length in degradation calculation.

123 `vbsn=0` `v`Substrate voltage in degradation calculation.

124 `vdsni=0.1` `v`Drain voltage in I_{ds} degradation calculation.

125 `vgzni=5` `v`Gate voltage in I_{ds} degradation calculation.

126 `vdsng=0.1` `v`Drain voltage in G_m degradation calculation.

127 `vgsg=5` `v`Gate voltage in G_m degradation calculation.

Spectre Circuit Simulator Reference

Component Statements Part 2

Spectre Stress Parameters

- 128 `esat=1.1e7 V/m`Critical field in Vdsat calculation.
- 129 `esatg=2.5e6 1/m`Gate voltage dependence of `esat`.
- 130 `vpg=-0.25`Gate voltage modifier.
- 131 `vpb=-0.13`Gate voltage modifier.
- 132 `subc1=2.24e-5`Substrate current coefficient.
- 133 `subc2=-0.1e-5 1/V`Substrate current coefficient.
- 134 `sube=6.4`Substrate current exponent.
- 135 `strc=1`Stress coefficient.
- 136 `stre=1`Stress exponent.

BERT Stress Parameters

- 137 `h0=1`Aging coefficient.
- 138 `hgd=0 1/V`Bias dependence of `h0`.
- 139 `m0=1`Aging exponent.
- 140 `mgd=0 1/V`Bias dependence of `m0`.
- 141 `ecrit0=1.1e5 V/cm`Critical electric field.
- 142 `lecrit0=0 μm V/cm`Length dependence of `ecrit0`.
- 143 `wecrit0=0 μm V/cm`Width dependence of `ecrit0`.
- 144 `ecritg=0 1/cm`Gate voltage dependence of `ecrit0`.
- 145 `lecritg=0 μm/cm`Length dependence of `ecritg`.
- 146 `wecritg=0 μm/cm`Width dependence of `ecritg`.
- 147 `ecritb=0 1/cm`Substrate voltage dependence of `ecrit0`.

Spectre Circuit Simulator Reference

Component Statements Part 2

148 `lecritb=0` $\mu\text{m}/\text{cm}$ Length dependence of `ecritb`.

149 `wecritb=0` $\mu\text{m}/\text{cm}$ Width dependence of `ecritb`.

150 `lc0=1`Substrate current coefficient.

151 `llc0=0` μm Length dependence of `lc0`.

152 `wlc0=0` μm Width dependence of `lc0`.

153 `lc1=1`Substrate current coefficient.

154 `llc1=0` μm Length dependence of `lc1`.

155 `wlc1=0` μm Width dependence of `lc1`.

156 `lc2=1`Substrate current coefficient.

157 `llc2=0` μm Length dependence of `lc2`.

158 `wlc2=0` μm Width dependence of `lc2`.

159 `lc3=1`Substrate current coefficient.

160 `llc3=0` μm Length dependence of `lc3`.

161 `wlc3=0` μm Width dependence of `lc3`.

162 `lc4=1`Substrate current coefficient.

163 `llc4=0` μm Length dependence of `lc4`.

164 `wlc4=0` μm Width dependence of `lc4`.

165 `lc5=1`Substrate current coefficient.

166 `llc5=0` μm Length dependence of `lc5`.

167 `wlc5=0` μm Width dependence of `lc5`.

168 `lc6=1`Substrate current coefficient.

169 `llc6=0` μm Length dependence of `lc6`.

Spectre Circuit Simulator Reference

Component Statements Part 2

170 `wlc6=0` μm Width dependence of `lc6`.

171 `lc7=1` Substrate current coefficient.

172 `llc7=0` μm Length dependence of `lc7`.

173 `wlc7=0` μm Width dependence of `lc7`.

Imax and Imelt

The `imax` parameter aids convergence and prevents numerical overflow. The junction characteristics of the device are accurately modeled for current up to `imax`. If `imax` is exceeded during iterations, the linear model is substituted until the current drops below `imax` or until convergence is achieved. If convergence is achieved with the current exceeding `imax`, the results are inaccurate, and Spectre prints a warning.

A separate model parameter, `imelt`, is used as a limit warning for the junction current. This parameter can be set to the maximum current rating of the device. When any component of the junction current exceeds `imelt`, note that base and collector currents are composed of many exponential terms, Spectre issues a warning and the results become inaccurate. The junction current is linearized above the value of `imelt` to prevent arithmetic exception, with the exponential term replaced by a linear equation at `imelt`.

Both of these parameters have current density counterparts, `jmax` and `jimelt`, that you can specify if you want the absolute current values to depend on the device area.

Auto Model Selection

Many models need to be characterized for different geometries in order to obtain accurate results for model development. The model selector program automatically searches for a model with the length and width range specified in the instance statement and uses this model in the simulations.

For the auto model selector program to find a specific model, the models to be searched should be grouped together within braces. Such a group is called a model group. An opening brace is required at the end of the line defining each model group. Every model in the group is given a name followed by a colon and the list of parameters. Also, the four geometric parameters `lmax`, `lmin`, `wmax`, and `wmin` should be given. The selection criteria to choose a model is as follows:

$$lmin \leq inst_length < lmax \text{ and } wmin \leq inst_width < wmax$$

Example

Spectre Circuit Simulator Reference

Component Statements Part 2

```
model ModelName ModelType {  
    1:      <model parameters> lmin=2 lmax=4 wmin=1 wmax=2  
    2:      <model parameters> lmin=1 lmax=2 wmin=2 wmax=4  
    3:      <model parameters> lmin=2 lmax=4 wmin=4 wmax=6  
}
```

Then for a given instance

```
M1 1 2 3 4 ModelName w=3 l=1.5
```

the program would search all the models in the model group with the name `ModelName` and then pick the first model whose geometric range satisfies the selection criteria. In the preceding example, the auto model selector program would choose `ModelName.2`.

The user must specify both length (*l*) and width (*w*) on the device instance line to enable automatic model selection.

Output Parameters

- 1 `weff` (m) Effective channel width.
- 2 `leff` (m) Effective channel length.
- 3 `rseff` (Ω) Effective source resistance.
- 4 `rdeff` (Ω) Effective drain resistance.
- 5 `aseff` (m^2) Effective area of source diffusion.
- 6 `adeff` (m^2) Effective area of drain diffusion.
- 7 `pseff` (m) Effective perimeter of source diffusion.
- 8 `pdeff` (m) Effective perimeter of source diffusion.
- 9 `isseff` (A) Effective source-bulk junction reverse saturation current.
- 10 `isdeff` (A) Effective drain-bulk junction reverse saturation current.
- 11 `cbseff` (F) Effective zero-bias source-bulk junction capacitance.
- 12 `cbdeff` (F) Effective zero-bias drain-bulk junction capacitance.

Spectre Circuit Simulator Reference

Component Statements Part 2

Operating-Point Parameters

- 1 `type=n` Transistor type.
Possible values are n or p.
- 2 `region=triode` Estimated operating region.
Possible values are off, triode, sat, subth, or breakdown.
- 3 `degradation=no` Hot-electron degradation flag.
Possible values are no or yes.
- 4 `reversed` Reverse mode indicator.
Possible values are no or yes.
- 5 `ids` (A) Resistive drain-to-source current.
- 6 `vgs` (V) Gate-source voltage.
- 7 `vds` (V) Drain-source voltage.
- 8 `vbs` (V) Bulk-source voltage.
- 9 `vth` (V) Threshold voltage.
- 10 `vdsat` (V) Drain-source saturation voltage.
- 11 `gm` (S) Common-source transconductance.
- 12 `gds` (S) Common-source output conductance.
- 13 `gmbs` (S) Body-transconductance.
- 14 `gameff` (\sqrt{V}) Effective body effect coefficient.
- 15 `betaeff` (A/V^2) Effective beta.
- 16 `cbd` (F) Drain-bulk junction capacitance.
- 17 `cbs` (F) Source-bulk junction capacitance.
- 18 `cgs` (F) Gate-source capacitance.
- 19 `cgd` (F) Gate-drain capacitance.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 20 `cgb` (F) Gate-bulk capacitance.
- 21 `ron` (Ω) On-resistance.
- 22 `id` (A) Resistive drain current.
- 23 `ibulk` (A) Resistive bulk current.
- 24 `pwr` (W) Power at op point.
- 25 `gmoverid` (1/V) G_m/I_{ds} .
- 26 `isub` (A) Substrate current.
- 27 `stress` Hot-electron stress.
- 28 `age` (s) Device age.
- 29 `he_vdsat` (V) Hot Electron V_{dsat} .

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

<code>ad</code> I-4	<code>gap2</code> M-80	<code>lmin</code> M-110	<code>tnom</code> M-72
<code>ad</code> M-95	<code>gds</code> OP-12	<code>lnom</code> M-122	<code>tox</code> M-19
<code>adeff</code> O-6	<code>gm</code> OP-11	<code>ls</code> I-10	<code>tpg</code> M-18
<code>af</code> M-104	<code>gmbs</code> OP-13	<code>m</code> I-11	<code>trd</code> M-84
<code>age</code> OP-28	<code>gmoverid</code> OP-25	<code>m0</code> M-139	<code>trise</code> I-13
<code>ai0</code> M-25	<code>h0</code> M-137	<code>meto</code> M-34	<code>trise</code> M-73
<code>alarm</code> M-67	<code>hdif</code> M-47	<code>mgd</code> M-140	<code>trs</code> M-83
<code>as</code> I-3	<code>he_vdsat</code> OP-29	<code>minr</code> M-45	<code>type</code> M-1
<code>as</code> M-94	<code>hgd</code> M-138	<code>mj</code> M-60	<code>type</code> OP-1
<code>aseff</code> O-5	<code>ibulk</code> OP-23	<code>mjsw</code> M-64	<code>ucrit</code> M-9
<code>betaeff</code> OP-15	<code>id</code> OP-22	<code>n</code> M-53	<code>uexp</code> M-10
<code>bi0</code> M-28	<code>ids</code> OP-5	<code>neff</code> M-12	<code>uo</code> M-7
<code>bvj</code> M-70	<code>imax</code> M-68	<code>nfs</code> M-17	<code>ute</code> M-75
<code>capmod</code> M-35	<code>imelt</code> M-55	<code>noisemod</code> M-102	<code>uto</code> M-74
<code>cbd</code> OP-16	<code>is</code> M-52	<code>nrd</code> M-98	<code>utra</code> M-11

Spectre Circuit Simulator Reference

Component Statements Part 2

cbd M-58	isdeff O-10	nrd I-7	vbox M-71
cbdeff O-12	isseff O-9	nrs I-8	vbs OP-8
cbs OP-17	isub OP-26	nrs M-99	vbsn M-123
cbs M-57	jmax M-69	nss M-16	vds OP-7
cbseff O-11	jmelt M-56	nsub M-15	vdsat OP-10
cgb OP-20	js M-51	pb M-61	vdsng M-126
cgbo M-33	kf M-103	pbsw M-65	vdsni M-124
cgd OP-19	kp M-3	pd I-6	vgs OP-6
cgdo M-32	l I-2	pd M-97	vgsng M-127
cgs OP-18	l M-93	pdeff O-8	vgsni M-125
cgso M-31	lai0 M-26	phi M-5	vmax M-8
cj M-59	lambda M-4	ps I-5	vpb M-131
cjsw M-63	lamex M-82	ps M-96	vpg M-130
crigm M-119	lbi0 M-29	pseff O-7	vth OP-9
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degradation M-112	lc5 M-165	rdc M-44	wd M-21
degradation OP-3	lc6 M-168	rdd M-42	wecrit0 M-143
degradation I-14	lc7 M-171	rdeff O-4	wecritb M-149
degramod M-111	ld M-20	region OP-2	wecritg M-146
delta M-13	ld I-9	region I-12	weff O-1
dskip M-54	ldd M-100	reversed OP-4	wlc0 M-152
duoc M-115	ldif M-46	ron OP-21	wlc1 M-155
duoe M-116	lds M-101	rs M-38	wlc2 M-158
dvthc M-113	lecrit0 M-142	rsc M-43	wlc3 M-161
dvthe M-114	lecritb M-148	rseff O-3	wlc4 M-164
ecrit0 M-141	lecritg M-145	rsh M-40	wlc5 M-167
ecritb M-147	leff O-2	rss M-41	wlc6 M-170
ecritg M-144	lgcd M-49	sc M-50	wlc7 M-173
ef M-105	lgcs M-48	smooth M-14	wmax M-107
eg M-78	llc0 M-151	strc M-135	wmin M-108
esat M-128	llc1 M-154	stre M-136	wnoi M-106
esatg M-129	llc2 M-157	stress OP-27	wnom M-121
flex M-81	llc3 M-160	subc1 M-132	xj M-24
fc M-62	llc4 M-163	subc2 M-133	xl M-23
fcs w M-66	llc5 M-166	sube M-134	xpart M-36
gameff OP-14	llc6 M-169	tcv M-87	xqc M-37
gamma M-6	llc7 M-172	tlev M-76	xti M-85
gap1 M-79	lmax M-109	tlevc M-77	xw M 2

MOS Level-3 Transistor (mos3)

Description

The MOS3 model is the level-3 model from Berkeley SPICE, and is a semi-empirical model. Three charge models are available. MOS3 transistors require that you use a model statement.

This device is supported within altergroups.

Sample Instance Statement

```
mp3 (0 1 2 2) pchmos3 l=2u w=30u ad=120p as=75p pd=36u ps=6u
```

Sample Model Statement

```
model pchmos3 mos3 type=p vto=-0.83 gamma=0.4511 kappa=5 ld=0.45e-6 kp=0.334e-4  
tox=0.3e-7 nsub=0.2e17 capmod=yang vmax=4.5e5 theta=0.25 cbs=10e-15 cbd=10e-15
```

Instance Definition

Name d g s b ModelName parameter=value ...

Instance Parameters

- 1 w (m) Channel width.
- 2 l (m) Channel length.
- 3 as (m²) Area of source diffusion.
- 4 ad (m²) Area of drain diffusion.
- 5 ps (m) Perimeter of source diffusion.
- 6 pd (m) Perimeter of drain diffusion.
- 7 nrd (m/m) Number of squares of drain diffusion.
- 8 nrs (m/m) Number of squares of source diffusion.
- 9 ld (m) Length of drain diffusion region.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 10 `ls (m)` Length of source diffusion region.
- 11 `m=1` Multiplicity factor (number of MOSFETs in parallel).
- 12 `region=triode` Estimated operating region.
Possible values are `off`, `triode`, `sat`, `subth`, or `breakdown`.
- 13 `trise` Temperature rise from ambient.
- 14 `degradation=no` Hot-electron degradation flag.
Possible values are `no` or `yes`.

Model Definition

`model modelName mos3 parameter=value ...`

Model Parameters

Device Type Parameters

- 1 `type=n` Transistor type.
Possible values are `n` or `p`.

Drain Current Model Parameters

- 2 `vto=0` `v` Threshold voltage at zero body bias.
- 3 `kp=2.0718e-5` A/V^2
Transconductance parameter.
- 4 `theta=0` $1/\text{V}$ Mobility modulation coefficient.
- 5 `phi=0.7` `v` Surface potential at strong inversion.
- 6 `gamma=0` $\sqrt{\text{V}}$ Body-effect parameter.
- 7 `uo=600` $\text{cm}^2/\text{V s}$ Carrier surface mobility.
- 8 `vmax= ∞` m/s Carrier saturation velocity.
- 9 `eta=0` $1/\text{V}$ Static feedback coefficient.

Spectre Circuit Simulator Reference

Component Statements Part 2

10 $\kappa=0.2$ Saturation field factor.

11 $\delta=0$ Width effect on threshold voltage.

Process Parameters

12 $n_{sub}=1.13e16 \text{ cm}^{-3}$
Channel doping concentration.

13 $n_{ss}=0 \text{ cm}^{-2}$ Surface state density.

14 $n_{fs}=0 \text{ cm}^{-2}$ Fast surface state density.

15 $tpg=+1$ Type of gate (+1 = opposite of substate, -1 = same as substate, 0 = aluminum).

16 $t_{ox}=1e-7 \text{ m}$ Gate oxide thickness.

17 $l_d=0 \text{ m}$ Lateral diffusion.

18 $w_d=0 \text{ m}$ Field-oxide encroachment.

19 $x_w=0 \text{ m}$ Width variation due to masking and etching.

20 $x_l=0 \text{ m}$ Length variation due to masking and etching.

21 $x_j=0 \text{ m}$ Source/drain junction depth.

Impact Ionization Parameters

22 $a_{i0}=0 \text{ 1/V}$ Impact ionization current coefficient.

23 $l_{ai0}=0 \text{ }\mu\text{m/V}$ Length sensitivity of a_{i0} .

24 $w_{ai0}=0 \text{ }\mu\text{m/V}$ Width sensitivity of a_{i0} .

25 $b_{i0}=0 \text{ V}$ Impact ionization current exponent.

26 $l_{bi0}=0 \text{ }\mu\text{m V}$ Length sensitivity of b_{i0} .

27 $w_{bi0}=0 \text{ }\mu\text{m V}$ Width sensitivity of b_{i0} .

Spectre Circuit Simulator Reference

Component Statements Part 2

Overlap Capacitance Parameters

28 `cgs0=0` F/mGate-source overlap capacitance.

29 `cgd0=0` F/mGate-drain overlap capacitance.

30 `cgbo=0` F/mGate-bulk overlap capacitance.

31 `meto=0` mMetal overlap in fringing field.

Charge Model Selection Parameters

32 `capmod=bsim`Intrinsic charge model.

Possible values are `none`, `meyer`, `yang`, or `bsim`.

33 `xpart=1`Drain/source channel charge partition in saturation for BSIM charge model, use 0.0 for 40/60, 0.5 for 50/50, or 1.0 for 0/100.

34 `xqc=0`Drain/source channel charge partition in saturation for charge models, e.g. use 0.4 for 40/60, 0.5 for 50/50, 0 for 0/100.

Parasitic Resistance Parameters

35 `rs=0` Ω Source resistance.

36 `rd=0` Ω Drain resistance.

37 `rsh=0` $\Omega/\text{sq}\mu\text{r}$ Source/drain diffusion sheet resistance.

38 `rss=0` Ω mScalable source resistance.

39 `rdd=0` Ω mScalable drain resistance.

40 `rsc=0` Ω Source contact resistance.

41 `rdc=0` Ω Drain contact resistance.

42 `minr=0.1` Ω Minimum source/drain resistance.

43 `ldif=0` mLateral diffusion beyond the gate.

44 `hdif=0` mLength of heavily doped diffusion.

Spectre Circuit Simulator Reference

Component Statements Part 2

45 `lgcs=0` `m`Gate-to-contact length of source side.

46 `lgcd=0` `m`Gate-to-contact length of drain side.

47 `sc=∞` `m`Spacing between contacts.

Junction Diode Model Parameters

48 `js` (A/m^2) Bulk junction reverse saturation current density.

49 `is=1e-14` `A` Bulk junction reverse saturation current.

50 `n=1` Junction emission coefficient.

51 `dskip=yes` Use simple piece-wise linear model for diode currents below $0.1 \cdot i_{abstol}$.
Possible values are `no` or `yes`.

52 `imelt='imaxA'` Explosion current, diode is linearized beyond this current to aid convergence.

53 `jmelt='jmaxA/m'^2`
Explosion current density, diode is linearized beyond this current to aid convergence.

Junction Capacitance Model Parameters

54 `cbs=0` `F` Bulk-source zero-bias junction capacitance.

55 `cbd=0` `F` Bulk-drain zero-bias junction capacitance.

56 `cj=0` `F/m^2` Zero-bias junction bottom capacitance density.

57 `mj=1/2` Bulk junction bottom grading coefficient.

58 `pb=0.8` `v` Bulk junction built-in potential.

59 `fc=0.5` Forward-bias depletion capacitance threshold.

60 `cjsw=0` `F/m` Zero-bias junction sidewall capacitance density.

61 `mjsw=1/3` Bulk junction sidewall grading coefficient.

Spectre Circuit Simulator Reference

Component Statements Part 2

62 `pbsw=0.8` `v`Side-wall junction built-in potential.

63 `fcsw=0.5`Side-wall forward-bias depletion capacitance threshold.

Operating Region Warning Control Parameters

64 `alarm=none`Forbidden operating region.
Possible values are `none`, `off`, `triode`, `sat`, `subth`, or `rev`.

65 `imax=1` `A`Maximum current, currents above this limit generate a warning.

66 `jmax=1e8` `A/m2`Maximum current density, currents above this limit generate a warning.

67 `bvj=∞` `v`Junction reverse breakdown voltage.

68 `vbox=1e9` `tox` `v`Oxide breakdown voltage.

Temperature Effects Parameters

69 `tnom` (`C`)Parameters measurement temperature. Default set by `options`.

70 `trise=0` `C`Temperature rise from ambient.

71 `uto=0` `C`Mobility temperature offset.

72 `ute=-1.5`Mobility temperature exponent.

73 `tlev=0`DC temperature selector.

74 `tlevc=0`AC temperature selector.

75 `eg=1.12452` `v`Energy band gap.

76 `gap1=7.02e-4` `V/C`Band gap temperature coefficient.

77 `gap2=1108` `C`Band gap temperature offset.

78 `flex=0`Temperature exponent for `ucrit`.

79 `lamex=0` `1/C`Temperature parameter for `lambda` and `kappa`.

80 `trs=0` `1/C`Temperature parameter for source resistance.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 81 `trd=0` 1/C Temperature parameter for drain resistance.
- 82 `xti=3` Saturation current temperature exponent.
- 83 `ptc=0` V/C Surface potential temperature coefficient.
- 84 `tcv=0` V/C Threshold voltage temperature coefficient.
- 85 `pta=0` V/C Junction potential temperature coefficient.
- 86 `ptp=0` V/C Sidewall junction potential temperature coefficient.
- 87 `cta=0` 1/C Junction capacitance temperature coefficient.
- 88 `ctp=0` 1/C Sidewall junction capacitance temperature coefficient.

Default Instance Parameters

- 89 `w=3e-6` m Default channel width.
- 90 `l=3e-6` m Default channel length.
- 91 `as=0` m² Default area of source diffusion.
- 92 `ad=0` m² Default area of drain diffusion.
- 93 `ps=0` m Default perimeter of source diffusion.
- 94 `pd=0` m Default perimeter of drain diffusion.
- 95 `nrd=0` m/m Default number of squares of drain diffusion.
- 96 `nrs=0` m/m Default number of squares of source diffusion.
- 97 `ldd=0` m Default length of drain diffusion region.
- 98 `lds=0` m Default length of source diffusion region.

Noise Model Parameters

- 99 `noisemod=1` Noise model selector.

Spectre Circuit Simulator Reference

Component Statements Part 2

100 `kf=0` Flicker (1/f) noise coefficient.

101 `af=1` Flicker (1/f) noise exponent.

102 `ef=1` Flicker (1/f) noise frequency exponent.

103 `wnoi=1e-5` `m`Channel width at which noise parameters were extracted.

Auto Model Selector Parameters

104 `wmax=1.0` `m`Maximum channel width for which the model is valid.

105 `wmin=0.0` `m`Minimum channel width for which the model is valid.

106 `lmax=1.0` `m`Maximum channel length for which the model is valid.

107 `lmin=0.0` `m`Minimum channel length for which the model is valid.

Degradation Parameters

108 `degramod=spectre` Degradation model selector.
Possible values are `spectre` or `bert`.

109 `degradation=no` Hot-electron degradation flag.
Possible values are `no` or `yes`.

110 `dvthc=1` `v`Degradation coefficient for threshold voltage.

111 `dvthe=1` Degradation exponent for threshold voltage.

112 `duoc=1` `s`Degradation coefficient for transconductance.

113 `duoe=1` Degradation exponent for transconductance.

114 `crivth=0.1` `v`Maximum allowable threshold voltage shift.

115 `criuo=10%` Maximum allowable normalized mobility change.

116 `crigm=10%` Maximum allowable normalized transconductance change.

117 `criids=10%` Maximum allowable normalized drain current change.

Spectre Circuit Simulator Reference

Component Statements Part 2

118 `wnom=5e-6` mNominal device width in degradation calculation.

119 `lnom=1e-6` mNominal device length in degradation calculation.

120 `vbsn=0` VSubstrate voltage in degradation calculation.

121 `vdsni=0.1` VDrain voltage in I_{ds} degradation calculation.

122 `vgsni=5` VGate voltage in I_{ds} degradation calculation.

123 `vdsng=0.1` VDrain voltage in G_m degradation calculation.

124 `vgsng=5` VGate voltage in G_m degradation calculation.

Spectre Stress Parameters

125 `esat=1.1e7` V/mCritical field in V_{dsat} calculation.

126 `esatg=2.5e6` 1/mGate voltage dependence of $esat$.

127 `vpg=-0.25` Gate voltage modifier.

128 `vpb=-0.13` Gate voltage modifier.

129 `subc1=2.24e-5` Substrate current coefficient.

130 `subc2=-0.1e-5` 1/VSubstrate current coefficient.

131 `sube=6.4` Substrate current exponent.

132 `strc=1` Stress coefficient.

133 `stre=1` Stress exponent.

BERT Stress Parameters

134 `h0=1` Aging coefficient.

135 `hgd=0` 1/VBias dependence of $h0$.

136 `m0=1` Aging exponent.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 137 `mgd=0` $1/V$ Bias dependence of `m0`.
- 138 `ecrit0=1.1e5` V/cm Critical electric field.
- 139 `lecrit0=0` μm V/cm Length dependence of `ecrit0`.
- 140 `wecrit0=0` μm V/cm Width dependence of `ecrit0`.
- 141 `ecritg=0` $1/cm$ Gate voltage dependence of `ecrit0`.
- 142 `lecritg=0` $\mu m/cm$ Length dependence of `ecritg`.
- 143 `wecritg=0` $\mu m/cm$ Width dependence of `ecritg`.
- 144 `ecritb=0` $1/cm$ Substrate voltage dependence of `ecrit0`.
- 145 `lecritb=0` $\mu m/cm$ Length dependence of `ecritb`.
- 146 `wecritb=0` $\mu m/cm$ Width dependence of `ecritb`.
- 147 `lc0=1` Substrate current coefficient.
- 148 `llc0=0` μm Length dependence of `lc0`.
- 149 `wlc0=0` μm Width dependence of `lc0`.
- 150 `lc1=1` Substrate current coefficient.
- 151 `llc1=0` μm Length dependence of `lc1`.
- 152 `wlc1=0` μm Width dependence of `lc1`.
- 153 `lc2=1` Substrate current coefficient.
- 154 `llc2=0` μm Length dependence of `lc2`.
- 155 `wlc2=0` μm Width dependence of `lc2`.
- 156 `lc3=1` Substrate current coefficient.
- 157 `llc3=0` μm Length dependence of `lc3`.
- 158 `wlc3=0` μm Width dependence of `lc3`.

Spectre Circuit Simulator Reference

Component Statements Part 2

159 `lc4=1` Substrate current coefficient.

160 `llc4=0` μm Length dependence of `lc4`.

161 `wlc4=0` μm Width dependence of `lc4`.

162 `lc5=1` Substrate current coefficient.

163 `llc5=0` μm Length dependence of `lc5`.

164 `wlc5=0` μm Width dependence of `lc5`.

165 `lc6=1` Substrate current coefficient.

166 `llc6=0` μm Length dependence of `lc6`.

167 `wlc6=0` μm Width dependence of `lc6`.

168 `lc7=1` Substrate current coefficient.

169 `llc7=0` μm Length dependence of `lc7`.

170 `wlc7=0` μm Width dependence of `lc7`.

Imax and Imelt

The `imax` parameter aids convergence and prevents numerical overflow. The junction characteristics of the device are accurately modeled for current up to `imax`. If `imax` is exceeded during iterations, the linear model is substituted until the current drops below `imax` or until convergence is achieved. If convergence is achieved with the current exceeding `imax`, the results are inaccurate, and Spectre prints a warning.

A separate model parameter, `imelt`, is used as a limit warning for the junction current. This parameter can be set to the maximum current rating of the device. When any component of the junction current exceeds `imelt`, note that base and collector currents are composed of many exponential terms, Spectre issues a warning and the results become inaccurate. The junction current is linearized above the value of `imelt` to prevent arithmetic exception, with the exponential term replaced by a linear equation at `imelt`.

Both of these parameters have current density counterparts, `jmax` and `jimelt`, that you can specify if you want the absolute current values to depend on the device area.

Auto Model Selection

Many models need to be characterized for different geometries in order to obtain accurate results for model development. The model selector program automatically searches for a model with the length and width range specified in the instance statement and uses this model in the simulations.

For the auto model selector program to find a specific model, the models to be searched should be grouped together within braces. Such a group is called a model group. An opening brace is required at the end of the line defining each model group. Every model in the group is given a name followed by a colon and the list of parameters. Also, the four geometric parameters `lmax`, `lmin`, `wmax`, and `wmin` should be given. The selection criteria to choose a model is as follows:

$$lmin \leq inst_length < lmax \text{ and } wmin \leq inst_width < wmax$$

Example

```
model ModelName ModelType {  
    1:      <model parameters> lmin=2 lmax=4 wmin=1 wmax=2  
    2:      <model parameters> lmin=1 lmax=2 wmin=2 wmax=4  
    3:      <model parameters> lmin=2 lmax=4 wmin=4 wmax=6  
}
```

Then for a given instance

```
M1 1 2 3 4 ModelName w=3 l=1.5
```

the program would search all the models in the model group with the name `ModelName` and then pick the first model whose geometric range satisfies the selection criteria. In the preceding example, the auto model selector program would choose `ModelName.2`.

The user must specify both length (`l`) and width (`w`) on the device instance line to enable automatic model selection.

Output Parameters

- 1 `weff` (m) Effective channel width.
- 2 `leff` (m) Effective channel length.
- 3 `rseff` (Ω) Effective source resistance.
- 4 `rdeff` (Ω) Effective drain resistance.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 5 `aseff` (m^2) Effective area of source diffusion.
- 6 `adef` (m^2) Effective area of drain diffusion.
- 7 `pseff` (m) Effective perimeter of source diffusion.
- 8 `pdef` (m) Effective perimeter of source diffusion.
- 9 `isseff` (A) Effective source-bulk junction reverse saturation current.
- 10 `isdef` (A) Effective drain-bulk junction reverse saturation current.
- 11 `cbseff` (F) Effective zero-bias source-bulk junction capacitance.
- 12 `cbdef` (F) Effective zero-bias drain-bulk junction capacitance.

Operating-Point Parameters

- 1 `type=n` Transistor type.
Possible values are `n` or `p`.
- 2 `region=triode` Estimated operating region.
Possible values are `off`, `triode`, `sat`, `subth`, or `breakdown`.
- 3 `degradation=no` Hot-electron degradation flag.
Possible values are `no` or `yes`.
- 4 `reversed` Reverse mode indicator.
Possible values are `no` or `yes`.
- 5 `ids` (A) Resistive drain-to-source current.
- 6 `vgs` (V) Gate-source voltage.
- 7 `vds` (V) Drain-source voltage.
- 8 `vbs` (V) Bulk-source voltage.
- 9 `vth` (V) Threshold voltage.
- 10 `vdsat` (V) Drain-source saturation voltage.
- 11 `gm` (S) Common-source transconductance.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 12 `gds` (S) Common-source output conductance.
- 13 `gmbs` (S) Body-transconductance.
- 14 `gameff` (\sqrt{V}) Effective body effect coefficient.
- 15 `betaeff` (A/V²) Effective beta.
- 16 `cbd` (F) Drain-bulk junction capacitance.
- 17 `cbs` (F) Source-bulk junction capacitance.
- 18 `cgs` (F) Gate-source capacitance.
- 19 `cgd` (F) Gate-drain capacitance.
- 20 `cgb` (F) Gate-bulk capacitance.
- 21 `ron` (Ω) On-resistance.
- 22 `id` (A) Resistive drain current.
- 23 `ibulk` (A) Resistive bulk current.
- 24 `pwr` (W) Power at op point.
- 25 `gmoverid` (1/V) G_m/I_{ds} .
- 26 `isub` (A) Substrate current.
- 27 `stress` Hot-electron stress.
- 28 `age` (s) Device age.
- 29 `he_vdsat` (V) Hot Electron V_{dsat} .

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the

Spectre Circuit Simulator Reference

Component Statements Part 2

description for that parameter. For example, a reference of M-35 means the 35th model parameter.

ad	M-92	gap1	M-76	lmax	M-106	tnom	M-69
ad	I-4	gap2	M-77	lmin	M-107	tox	M-16
adefeff	O-6	gds	OP-12	lnom	M-119	tpg	M-15
af	M-101	gm	OP-11	ls	I-10	trd	M-81
age	OP-28	gmbs	OP-13	m	I-11	trise	I-13
ai0	M-22	gmoverid	OP-25	m0	M-136	trise	M-70
alarm	M-64	h0	M-134	meto	M-31	trs	M-80
as	I-3	hdif	M-44	mgd	M-137	type	OP-1
as	M-91	he_vdsat	OP-29	minr	M-42	type	M-1
aseff	O-5	hgd	M-135	mj	M-57	uo	M-7
betaeff	OP-15	ibulk	OP-23	mjsw	M-61	ute	M-72
bi0	M-25	id	OP-22	n	M-50	uto	M-71
bvj	M-67	ids	OP-5	nfs	M-14	vbox	M-68
capmod	M-32	imax	M-65	noisemod	M-99	vbs	OP-8
cbd	M-55	imelt	M-52	nrd	M-95	vbsn	M-120
cbd	OP-16	is	M-49	nrd	I-7	vds	OP-7
cbdeff	O-12	isdeff	O-10	nrs	I-8	vdsat	OP-10
cbs	OP-17	isseff	O-9	nrs	M-96	vdsng	M-123
cbs	M-54	isub	OP-26	nss	M-13	vdsni	M-121
cbseff	O-11	jmax	M-66	nsub	M-12	vgs	OP-6
cgb	OP-20	jmelt	M-53	pb	M-58	vgsng	M-124
cgbo	M-30	js	M-48	pbsw	M-62	vgsni	M-122
cgd	OP-19	kappa	M-10	pd	I-6	vmax	M-8
cgdo	M-29	kf	M-100	pd	M-94	vpb	M-128
cgs	OP-18	kp	M-3	pdeff	O-8	vpg	M-127
cgso	M-28	l	M-90	phi	M-5	vth	OP-9
cj	M-56	l	I-2	ps	I-5	vto	M-2
cjsw	M-60	lai0	M-23	ps	M-93	w	M-89
crigm	M-116	lamex	M-79	pseff	O-7	w	I-1
criids	M-117	lbi0	M-26	pta	M-85	wai0	M-24
criuo	M-115	lc0	M-147	ptc	M-83	wbi0	M-27
crivth	M-114	lc1	M-150	ptp	M-86	wd	M-18
cta	M-87	lc2	M-153	pwr	OP-24	wecrit0	M-140
ctp	M-88	lc3	M-156	rd	M-36	wecritb	M-146
degradation	M-109	lc4	M-159	rdc	M-41	wecritg	M-143
degradation	OP-3	lc5	M-162	rdd	M-39	weff	O-1
degradation	I-14	lc6	M-165	rdeff	O-4	wlc0	M-149
degramod	M-108	lc7	M-168	region	I-12	wlc1	M-152
delta	M-11	ld	M-17	region	OP-2	wlc2	M-155
dskip	M-51	ld	I-9	reversed	OP-4	wlc3	M-158
duoc	M-112	ldd	M-97	ron	OP-21	wlc4	M-161
duoe	M-113	ldif	M-43	rs	M-35	wlc5	M-164

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Component Statements Part 2

dvthc	M-110	lds	M-98	rsc	M-40	wlc6	M-167
dvthe	M-111	lecrit0	M-139	rseff	O-3	wlc7	M-170
ecrit0	M-138	lecritb	M-145	rsh	M-37	wmax	M-104
ecritb	M-144	lecritg	M-142	rss	M-38	wmin	M-105
ecritg	M-141	leff	O-2	sc	M-47	wnoi	M-103
ef	M-102	lgcd	M-46	strc	M-132	wnom	M-118
eg	M-75	lgcs	M-45	stre	M-133	xj	M-21
esat	M-125	llc0	M-148	stress	OP-27	xl	M-20
esatg	M-126	llc1	M-151	subc1	M-129	xpart	M-33
eta	M-9	llc2	M-154	subc2	M-130	xqc	M-34
flex	M-78	llc3	M-157	sube	M-131	xti	M-82
fc	M-59	llc4	M-160	tcv	M-84	xw	M-19
fcs	M-63	llc5	M-163	theta	M-4		
gameff	OP-14	llc6	M-166	tlev	M-73		
gamma	M-6	llc7	M-169	tlevc	M-74		

Long Channel JFET/MOSFET Model (mos30)

Description

This long channel JFET/MOSFET model is specially developed to describe the drift region of LDMOS, EPMOS and VDMOS devices. It is described in the Philips MOST Modelbook (Dec.95) as MOS model, level 30 (Used for DMOS). Information on how to obtain this document can be found on Source Link by searching for Philips.

Note: In noise analysis, mos30 instances will not generate any contribution, since there are no noise sources included in the mos30 model.

Warning: Dont use this model. It is obsolete.

Mos30 will be removed from spectre in the next release.

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This device is supported within altergroups.

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/vobs/spectre_dev/tools.sun4v/spectre/lib/cmi/2.0.doc/libphilips.so

Sample Instance Statement

```
mn30 (1 2 0 0) nchmod area=2 mult=1
```

Spectre Circuit Simulator Reference

Component Statements Part 2

Sample Model Statement

```
model nchmod mos30 type=n tox=1.1e-5 ron=150 rsat=500 psat=2 vsat=1 vsub=0.59
cgate=1.65e-12 csub=1.1e-9 tref=25
```

Instance Definition

```
Name d g s [b] ModelName parameter=value ...
```

Instance Parameters

- 1 `mult=1` Number of devices in parallel.
- 2 `area=1` Alias of `mult`.
- 3 `region=triode` Estimated DC operating region, used as a convergence aid.
Possible values are `off`, `triode`, `sat`, or `subth`.
- 4 `m=1` Multiplicity factor.

Model Definition

```
model modelName mos30 parameter=value ...
```

Model Parameters

- 1 `type=n` Transistor gender.
Possible values are `n` or `p`.
- 2 `ron=1.0` Ω Ohmic resistance at zero bias.
- 3 `rsat=1.0` Ω Space charge resistance at zero bias.
- 4 `vsat=10.0` V Critical drain-source voltage for hot carriers.
- 5 `psat=1.0` Velocity saturation coefficient.
- 6 `vp=-1.0` V Pinch off voltage at zero gate and substrate voltages.
- 7 `tox=-1.0` cm Gate oxide thickness.
- 8 `dch=1.0e15` cm^{-3} Doping level channel.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 9 `dsub=1.0e15 cm-3`Doping level substrate.
- 10 `vsub=0.6` `v`Substrate diffusion voltage.
- 11 `vgap=1.2` `v`Bandgap voltage channel.
- 12 `cgate=0.0` `F`Gate capacitance at zero bias.
- 13 `csub=0.0` `F`Substrate capacitance at zero bias.
- 14 `tausc=0.0` `s`Space charge transit time of the channel.
- 15 `ach=0.0`Temperature coefficient resistivity of the channel.
- 16 `kf=0.0`Flicker noise coefficient.
- 17 `af=1.0`Flicker noise exponent.
- 18 `tr (C)`Reference temperature. Default set by option `tnom`.
- 19 `tref (C)`Alias of `tr`. Default set by option `tnom`.
- 20 `tnom (C)`Alias of `tr`. Default set by option `tnom`.
- 21 `dta=0.0` `K`Temperature offset of the device.
- 22 `trise=0.0` `K`Alias of `dta`.

Output Parameters

- 1 `ront (Ω)` Ohmic resistance at zero bias.
- 2 `rsat (Ω)` Space charge resistance at zero bias.
- 3 `vsatt (V)` Critical drain-source voltage for hot carriers.
- 4 `vsubt (V)` Substrate diffusion voltage.
- 5 `cgate (F)` Gate capacitance at zero bias.
- 6 `csubt (F)` Substrate capacitance at zero bias.

Spectre Circuit Simulator Reference

Component Statements Part 2

Operating-Point Parameters

- 1 `pwr` (W) Power.
- 2 `ids` (A) Total current including velocity saturation.
- 3 `qb` (Coul) Substrate charge.
- 4 `qg` (Coul) Gate charge.
- 5 `qds` (Coul) Space charge in the channel.
- 6 `gdsd` (S) Conductance ($d\,ids / d\,vd$).
- 7 `gdsg` (S) Conductance ($d\,ids / d\,vg$).
- 8 `gdss` (S) Conductance ($d\,ids / d\,vs$).
- 9 `gdsb` (S) Conductance ($d\,ids / d\,vb$).
- 10 `cbd` (F) Capacitance ($d\,qb / d\,vd$).
- 11 `cbg` (F) Capacitance ($d\,qb / d\,vg$).
- 12 `cbs` (F) Capacitance ($d\,qb / d\,vs$).
- 13 `cbb` (F) Capacitance ($d\,qb / d\,vb$).
- 14 `cgd` (F) Capacitance ($d\,qg / d\,vd$).
- 15 `cgg` (F) Capacitance ($d\,qg / d\,vg$).
- 16 `cgs` (F) Capacitance ($d\,qg / d\,vs$).
- 17 `cgb` (F) Capacitance ($d\,qg / d\,vb$).
- 18 `cdsd` (F) Capacitance ($d\,qds / d\,vd$).
- 19 `cdsg` (F) Capacitance ($d\,qds / d\,vg$).
- 20 `cdss` (F) Capacitance ($d\,qds / d\,vs$).
- 21 `cdsb` (F) Capacitance ($d\,qds / d\,vb$).

Spectre Circuit Simulator Reference

Component Statements Part 2

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

ach	M-15	cgd	OP-14	m	I-4	tox	M-7
af	M-17	cgg	OP-15	mult	I-1	tr	M-18
area	I-2	cgs	OP-16	psat	M-5	tref	M-19
cbb	OP-13	csb	M-13	pwr	OP-1	trise	M-22
cbd	OP-10	csbt	O-6	qb	OP-3	type	M-1
cbg	OP-11	dch	M-8	qds	OP-5	vgap	M-11
cbs	OP-12	dsub	M-9	qg	OP-4	vp	M-6
cdsb	OP-21	dta	M-21	region	I-3	vsat	M-4
cdsd	OP-18	gdsb	OP-9	ron	M-2	vsatt	O-3
cdsg	OP-19	gdsd	OP-6	ront	O-1	vsub	M-10
cdss	OP-20	gdsg	OP-7	rsat	O-2	vsubt	O-4
cgate	O-5	gdss	OP-8	rsat	M-3		
cgate	M-12	ids	OP-2	tausc	M-14		
cgb	OP-17	kf	M-16	tnom	M-20		

Long Channel JFET/MOSFET Model (mos3002)

Description

This long channel JFET/MOSFET model is specially developed to describe the drift region of LDMOS, EPMOS and VDMOS devices. It is described in the Philips MOST Modelbook (Dec.98) as MOS model, level 3002 (Used for DMOS). Information on how to obtain this document can be found on Source Link by searching for Philips.

Note: In noise analysis, mos3002 instances will not generate any contribution, since there are no noise sources included in the mos3002 model.

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/vobs/spectre_dev/tools.sun4v/spectre/lib/cmi/2.0.doc/libphilips.so

Spectre Circuit Simulator Reference

Component Statements Part 2

Sample Instance Statement

```
mn3 (1 2 0 0) nch3002 area=1 m=2
```

Sample Model Statement

```
model nch3002 mos3002 ron=20 rsat=150 vsat=1 tox=1.23e-5 dch=1.1e16 vsub=0.58  
csub=5.43e-13 tausc=1.2e-12 kf=1 tref=27 psat=1 dta=0
```

Instance Definition

```
Name d g s [b] ModelName parameter=value ...
```

Instance Parameters

- 1 `mult=1` Number of devices in parallel.
- 2 `area=1` Alias of `mult`.
- 3 `region=triode` Estimated DC operating region, used as a convergence aid.
Possible values are `off`, `triode`, `sat`, or `subth`.
- 4 `m=1` Multiplicity factor.

Model Definition

```
model modelName mos3002 parameter=value ...
```

Model Parameters

- 1 `type=n` Transistor gender.
Possible values are `n` or `p`.
- 2 `ron=1.0` Ω Ohmic resistance at zero bias.
- 3 `rsat=1.0` Ω Space charge resistance at zero bias.
- 4 `vsat=10.0` V Critical drain-source voltage for hot carriers.
- 5 `psat=1.0` Velocity saturation coefficient.
- 6 `vp=-1.0` V Pinch off voltage at zero gate and substrate voltages.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 7 `tox=-1.0 cm`Gate oxide thickness.
- 8 `dch=1.0e15 cm-3`Doping level channel.
- 9 `dsub=1.0e15 cm-3`Doping level substrate.
- 10 `vsub=0.6 V`Substrate diffusion voltage.
- 11 `vgap=1.2 V`Bandgap voltage channel.
- 12 `cgate=0.0 F`Gate capacitance at zero bias.
- 13 `csub=0.0 F`Substrate capacitance at zero bias.
- 14 `tausc=0.0 s`Space charge transit time of the channel.
- 15 `ach=0.0`Temperature coefficient resistivity of the channel.
- 16 `kf=0.0`Flickernoise coefficient.
- 17 `af=1.0`Flickernoise exponent.
- 18 `tr (C)`Reference temperature. Default set by option `tnom`.
- 19 `tref (C)`Alias of `tr`. Default set by option `tnom`.
- 20 `tnom (C)`Alias of `tr`. Default set by option `tnom`.
- 21 `dta=0.0 K`Temperature offset of the device.
- 22 `trise=0.0 K`Alias of `dta`.

Output Parameters

- 1 `ront (Ω)`Ohmic resistance at zero bias.
- 2 `rsat (Ω)`Space charge resistance at zero bias.
- 3 `vsatt (V)`Critical drain-source voltage for hot carriers.
- 4 `vsubt (V)`Substrate diffusion voltage.
- 5 `cgate (F)`Gate capacitance at zero bias.

Spectre Circuit Simulator Reference

Component Statements Part 2

6 `csubt` (F) Substrate capacitance at zero bias.

Operating-Point Parameters

1 `pwr` (W) Power.

2 `ids` (A) Total current including velocity saturation.

3 `qb` (Coul) Substrate charge.

4 `qg` (Coul) Gate charge.

5 `qds` (Coul) Space charge in the channel.

6 `gdsd` (S) Conductance ($d\,ids / d\,vd$).

7 `gdsg` (S) Conductance ($d\,ids / d\,vg$).

8 `gdss` (S) Conductance ($d\,ids / d\,vs$).

9 `gdsb` (S) Conductance ($d\,ids / d\,vb$).

10 `cbd` (F) Capacitance ($d\,qb / d\,vd$).

11 `cbg` (F) Capacitance ($d\,qb / d\,vg$).

12 `cbs` (F) Capacitance ($d\,qb / d\,vs$).

13 `cbb` (F) Capacitance ($d\,qb / d\,vb$).

14 `cgd` (F) Capacitance ($d\,qg / d\,vd$).

15 `cgg` (F) Capacitance ($d\,qg / d\,vg$).

16 `cgs` (F) Capacitance ($d\,qg / d\,vs$).

17 `cgb` (F) Capacitance ($d\,qg / d\,vb$).

18 `cdsd` (F) Capacitance ($d\,qds / d\,vd$).

19 `cdsg` (F) Capacitance ($d\,qds / d\,vg$).

20 `cdss` (F) Capacitance ($d\,qds / d\,vs$).

Spectre Circuit Simulator Reference

Component Statements Part 2

21 `cdsb` (F) Capacitance (d qds / d vb).

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

<code>ach</code>	M-15	<code>cgd</code>	OP-14	<code>m</code>	I-4	<code>tox</code>	M-7
<code>af</code>	M-17	<code>cgg</code>	OP-15	<code>mult</code>	I-1	<code>tr</code>	M-18
<code>area</code>	I-2	<code>cgs</code>	OP-16	<code>psat</code>	M-5	<code>tref</code>	M-19
<code>cbb</code>	OP-13	<code>csub</code>	M-13	<code>pwr</code>	OP-1	<code>trise</code>	M-22
<code>cbd</code>	OP-10	<code>csubt</code>	O-6	<code>qb</code>	OP-3	<code>type</code>	M-1
<code>cbg</code>	OP-11	<code>dch</code>	M-8	<code>qds</code>	OP-5	<code>vgap</code>	M-11
<code>cbs</code>	OP-12	<code>dsub</code>	M-9	<code>qg</code>	OP-4	<code>vp</code>	M-6
<code>cdsb</code>	OP-21	<code>dta</code>	M-21	<code>region</code>	I-3	<code>vsat</code>	M-4
<code>cdsd</code>	OP-18	<code>gdsb</code>	OP-9	<code>ron</code>	M-2	<code>vsatt</code>	O-3
<code>cdsg</code>	OP-19	<code>gdsd</code>	OP-6	<code>rnt</code>	O-1	<code>vsub</code>	M-10
<code>cdss</code>	OP-20	<code>gdsg</code>	OP-7	<code>rsat</code>	O-2	<code>vsubt</code>	O-4
<code>cgate</code>	O-5	<code>gdss</code>	OP-8	<code>rsat</code>	M-3		
<code>cgate</code>	M-12	<code>ids</code>	OP-2	<code>tausc</code>	M-14		
<code>cgb</code>	OP-17	<code>kf</code>	M-16	<code>tnom</code>	M-20		

Long Channel JFET/MOSFET Model (`mos3100`)

Description

This long channel JFET/MOSFET model is special developed to describe the drift region of LDMOS, EPMOS and VDMOS devices. It is described in the Philips MOST Modelbook (Dec.98) as MOS model, level 3002 (Used for DMOS). Information on how to obtain this document can be found on Source Link by searching for Philips.

Note: In noise analysis, `mos3100` instances will not generate any contribution, since there are no noise sources included in the `mos3100` model.

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This device is supported within altergroups.

Spectre Circuit Simulator Reference

Component Statements Part 2

This device is dynamically loaded from the shared object

`/vobs/spectre_dev/tools.sun4v/spectre/lib/cmi/2.0.doc/libphilips.so`

Instance Definition

`Name d g s [b] ModelName parameter=value ...`

Instance Parameters

- 1 `mult=1` Number of devices in parallel.
- 2 `area=1` Alias of `mult`.
- 3 `region=triode` Estimated DC operating region, used as a convergence aid.
Possible values are `off`, `triode`, `sat`, or `subth`.
- 4 `m=1` Multiplicity factor.

Model Definition

`model modelName mos3100 parameter=value ...`

Model Parameters

- 1 `type=n` Transistor gender.
Possible values are `n` or `p`.
- 2 `ron=1.0` Ω Ohmic resistance at zero bias.
- 3 `rsat=1.0` Ω Space charge resistance at zero bias.
- 4 `vsat=10.0` V Critical drain-source voltage for hot carriers.
- 5 `psat=1.0` Velocity saturation coefficient.
- 6 `vp=-1.0` V Pinch off voltage at zero gate and substrate voltages.
- 7 `tox=-1.0` m Gate oxide thickness.
- 8 `dch=1.0e21 m-3` Doping level channel.
- 9 `dsub=1.0e21 m-3` Doping level substrate.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 10 `vsub=0.6` `v`Substrate diffusion voltage.
- 11 `vgap=1.2` `v`Bandgap voltage channel.
- 12 `cgate=0.0` `F`Gate capacitance at zero bias.
- 13 `csub=0.0` `F`Substrate capacitance at zero bias.
- 14 `tausc=0.0` `s`Space charge transit time of the channel.
- 15 `ach=0.0` Temperature coefficient resistivity of the channel.
- 16 `tr (C)`Reference temperature. Default set by option `tnom`.
- 17 `tref (C)`Alias of `tr`. Default set by option `tnom`.
- 18 `tnom (C)`Alias of `tr`. Default set by option `tnom`.
- 19 `dta=0.0` `K`Temperature offset of the device.
- 20 `trise=0.0` `K`Alias of `dta`.

Output Parameters

- 1 `ront (Ω)` Ohmic resistance at zero bias.
- 2 `rsat (Ω)` Space charge resistance at zero bias.
- 3 `vsatt (V)` Critical drain-source voltage for hot carriers.
- 4 `vsubt (V)` Substrate diffusion voltage.
- 5 `cgate (F)` Gate capacitance at zero bias.
- 6 `csubt (F)` Substrate capacitance at zero bias.

Operating-Point Parameters

- 1 `pwr (W)` Power.
- 2 `ids (A)` Total current including velocity saturation.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 3 q_b (Coul) Substrate charge.
- 4 q_g (Coul) Gate charge.
- 5 q_{ds} (Coul) Space charge in the channel.
- 6 g_{dsd} (S) Conductance ($d i_{ds} / d v_d$).
- 7 g_{dsg} (S) Conductance ($d i_{ds} / d v_g$).
- 8 g_{dss} (S) Conductance ($d i_{ds} / d v_s$).
- 9 g_{dsb} (S) Conductance ($d i_{ds} / d v_b$).
- 10 c_{bd} (F) Capacitance ($d q_b / d v_d$).
- 11 c_{bg} (F) Capacitance ($d q_b / d v_g$).
- 12 c_{bs} (F) Capacitance ($d q_b / d v_s$).
- 13 c_{bb} (F) Capacitance ($d q_b / d v_b$).
- 14 c_{gd} (F) Capacitance ($d q_g / d v_d$).
- 15 c_{gg} (F) Capacitance ($d q_g / d v_g$).
- 16 c_{gs} (F) Capacitance ($d q_g / d v_s$).
- 17 c_{gb} (F) Capacitance ($d q_g / d v_b$).
- 18 c_{dsd} (F) Capacitance ($d q_{ds} / d v_d$).
- 19 c_{dsg} (F) Capacitance ($d q_{ds} / d v_g$).
- 20 c_{dss} (F) Capacitance ($d q_{ds} / d v_s$).
- 21 c_{dsb} (F) Capacitance ($d q_{ds} / d v_b$).

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the

Spectre Circuit Simulator Reference

Component Statements Part 2

description for that parameter. For example, a reference of M-35 means the 35th model parameter.

ach	M-15	cgd	OP-14	m	I-4	tnom	M-18
area	I-2	cgg	OP-15	mult	I-1	tox	M-7
cbb	OP-13	cgs	OP-16	psat	M-5	tr	M-16
cbd	OP-10	csub	M-13	pwr	OP-1	tref	M-17
cbg	OP-11	csubt	O-6	qb	OP-3	trise	M-20
cbs	OP-12	dch	M-8	qds	OP-5	type	M-1
cdsb	OP-21	dsub	M-9	qg	OP-4	vgap	M-11
cdsd	OP-18	dta	M-19	region	I-3	vp	M-6
cdsg	OP-19	gdsb	OP-9	ron	M-2	vsat	M-4
cdss	OP-20	gdsd	OP-6	ront	O-1	vsatt	O-3
cgate	O-5	gdsg	OP-7	rsat	O-2	vsub	M-10
cgate	M-12	gdss	OP-8	rsat	M-3	vsubt	O-4
cgb	OP-17	ids	OP-2	tausc	M-14		

Silicon On Isolator JFET Model (mos40)

Description

This long channel JFET/MOSFET model is special developed to describe the drift region of LDMOS, EPMOS and VDMOS devices. It is described in the Philips MOST Modelbook (Dec.98) as MOS model, level 3002 (Used for DMOS). Information on how to obtain this document can be found on Source Link by searching for Philips.

Note: In noise analysis, mos40 instances will not generate any contribution, since there are no noise sources included in the mos40 model.

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This device is supported within altergroups.

This device is dynamically loaded from the shared object

/vobs/spectre_dev/tools.sun4v/spectre/lib/cmi/2.0.doc/libphilips.so

Instance Definition

Name d g s [b] ModelName parameter=value ...

Spectre Circuit Simulator Reference

Component Statements Part 2

Instance Parameters

- 1 `mult=1` Number of devices in parallel.
- 2 `area=1` Alias of `mult`.
- 3 `region=triode` Estimated DC operating region, used as a convergence aid.
Possible values are `off`, `triode`, `sat`, or `subth`.
- 4 `m=1` Multiplicity factor.

Model Definition

`model modelName mos40 parameter=value ...`

Model Parameters

- 1 `type=n` Transistor gender.
Possible values are `n` or `p`.
- 2 `ron=1.0` Ω Ohmic resistance at zero bias.
- 3 `rsat=1.0` Ω Space charge resistance at zero bias.
- 4 `vsat=10.0` V Critical drain-source voltage for hot carriers.
- 5 `psat=1.0` Velocity saturation coefficient.
- 6 `vp=-1.0` V Pinch off voltage at zero gate and substrate voltages.
- 7 `tox=-1.0` m Gate oxide thickness.
- 8 `dch=1.0e21` m^{-3} Doping level channel.
- 9 `tbox=-1.0` m^{-3} Box thicknes.
- 10 `cbox=0.0` m^{-3} Wafer capacitance.
- 11 `cgate=0.0` F Gate capacitance at zero bias.
- 12 `tausc=0.0` s Space charge transit time of the channel.
- 13 `ach=0.0` Temperature coefficient resistivity of the channel.

Spectre Circuit Simulator Reference

Component Statements Part 2

14 `tr` (C) Reference temperature. Default set by option `tnom`.

15 `tref` (C) Alias of `tr`. Default set by option `tnom`.

16 `tnom` (C) Alias of `tr`. Default set by option `tnom`.

17 `dta=0.0` K Temperature offset of the device.

18 `trise=0.0` K Alias of `dta`.

Output Parameters

1 `ront` (Ω) Ohmic resistance at zero bias.

2 `rsat` (Ω) Space charge resistance at zero bias.

3 `vsatt` (V) Critical drain-source voltage for hot carriers.

4 `vbox` (V) Box voltage.

5 `cgate` (F) Gate capacitance at zero bias.

Operating-Point Parameters

1 `pwr` (W) Power.

2 `ids` (A) Total current including velocity saturation.

3 `qb` (Coul) Substrate charge.

4 `qg` (Coul) Gate charge.

5 `qds` (Coul) Space charge in the channel.

6 `gdsd` (S) Conductance ($d\,ids / d\,vd$).

7 `gdsg` (S) Conductance ($d\,ids / d\,vg$).

8 `gdss` (S) Conductance ($d\,ids / d\,vs$).

9 `gdsb` (S) Conductance ($d\,ids / d\,vb$).

Spectre Circuit Simulator Reference

Component Statements Part 2

- 10 `cbd` (F) Capacitance ($d q_b / d v_d$).
- 11 `cbg` (F) Capacitance ($d q_b / d v_g$).
- 12 `cbs` (F) Capacitance ($d q_b / d v_s$).
- 13 `cbb` (F) Capacitance ($d q_b / d v_b$).
- 14 `cgd` (F) Capacitance ($d q_g / d v_d$).
- 15 `cgg` (F) Capacitance ($d q_g / d v_g$).
- 16 `cgs` (F) Capacitance ($d q_g / d v_s$).
- 17 `cgb` (F) Capacitance ($d q_g / d v_b$).
- 18 `cdsd` (F) Capacitance ($d q_{ds} / d v_d$).
- 19 `cdsg` (F) Capacitance ($d q_{ds} / d v_g$).
- 20 `cdss` (F) Capacitance ($d q_{ds} / d v_s$).
- 21 `cdsb` (F) Capacitance ($d q_{ds} / d v_b$).

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

<code>ach</code>	M-13	<code>cgate</code>	M-11	<code>m</code>	I-4	<code>taus</code>	M-12
<code>area</code>	I-2	<code>cgb</code>	OP-17	<code>mult</code>	I-1	<code>tbox</code>	M-9
<code>cbb</code>	OP-13	<code>cgd</code>	OP-14	<code>psat</code>	M-5	<code>tnom</code>	M-16
<code>cbd</code>	OP-10	<code>cgg</code>	OP-15	<code>pwr</code>	OP-1	<code>tox</code>	M-7
<code>cbg</code>	OP-11	<code>cgs</code>	OP-16	<code>qb</code>	OP-3	<code>tr</code>	M-14
<code>cbox</code>	M-10	<code>dch</code>	M-8	<code>qds</code>	OP-5	<code>tref</code>	M-15
<code>cbs</code>	OP-12	<code>dta</code>	M-17	<code>qg</code>	OP-4	<code>trise</code>	M-18
<code>cdsb</code>	OP-21	<code>gdsb</code>	OP-9	<code>region</code>	I-3	<code>type</code>	M-1
<code>cdsd</code>	OP-18	<code>gdsd</code>	OP-6	<code>ron</code>	M-2	<code>vbox</code>	O-4
<code>cdsg</code>	OP-19	<code>gdsg</code>	OP-7	<code>ront</code>	O-1	<code>vp</code>	M-6

Spectre Circuit Simulator Reference

Component Statements Part 2

cdss	OP-20	gdss	OP-8	rsat	O-2	vsat	M-4
cgate	O-5	ids	OP-2	rsat	M-3	vsatt	O-3

Compact MOS-Transistor Model (mos705)

Description

The mos705 model is a compact MOS-transistor model, intended for the simulation of circuit behavior with emphasis on analog applications. It is described in the Philips MOST Modelbook (Dec.93) as MOS model, level 705.

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In extension to the modelbook description a minimum conductance `gmin` is inserted between the drain and source node, to aid convergence. The value of `gmin` is set by an options statement, default = 1e-12 S.

This device is supported within altergroups.

This device is dynamically loaded from the shared object

`/vobs/spectre_dev/tools.sun4v/spectre/lib/cmi/2.0.doc/libphilips.so`

Sample Instance Statement

```
mn1 (1 2 0 0) mna7 ln=120e-6 wn=12e-6
```

Sample Model Statement

```
model mna7 mos705 type=n vtn=0.853 betan=77e-6 tox=15e-9 vfb=-850e-3 tref=25  
subthn=3 phi=0.645 lap=100e-9 gkn=-350e-9 th1n=0.15 th2n=0.046 th3n=0.1 fnoise=1e-  
10
```

Instance Definition

Name d g s [b] ModelName parameter=value ...

Instance Parameters

1 wn=1.0 scale mDrawn channel width in the lay-out of the actual transistor. Scale set by option scale.

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Component Statements Part 2

- 2 `ln=1.0 scale m`Drawn channel length in the lay-out of the actual transistor. Scale set by option `scale`.
- 3 `w=1.0 scale m`Alias for `wn`.
- 4 `l=1.0 scale m`Alias for `ln`.
- 5 `mult=1`Number of devices in parallel.
- 6 `area=1`Alias of `mult`.
- 7 `region=triode`Estimated DC operating region, used as a convergence aid.
Possible values are `off`, `triode`, `sat`, or `subth`.
- 8 `m=1`Multiplicity factor.

Model Definition

`model modelName mos705 parameter=value ...`

Model Parameters

- 1 `type=n`Transistor gender.
Possible values are `n` or `p`.
- 2 `vtn=0` V_{th} Threshold voltage of the reference transistor at the reference temperature.
- 3 `kon=0` $\sqrt{V_K}$ K_0 of the reference transistor.
- 4 `kn=100m` $\sqrt{V_K}$ K of the reference transistor.
- 5 `vsbxn=0` V_{Vsbx} of the reference transistor.
- 6 `delvx=0` V_{Dvsbx} of the reference transistor.
- 7 `th1n=0` $1/V_{th1}$ of the reference transistor.
- 8 `th2n=0` $1/\sqrt{V_{th2}}$ of the reference transistor.
- 9 `th3n=0` $1/V_{th3}$ of the reference transistor at the reference temperature.
- 10 `gamman=0` Γ_{am} of the reference transistor.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 11 `shiftn=0` $V^{(1-n)}$ Sh of the reference transistor.
- 12 `nn=0`N of the reference transistor.
- 13 `pn=0` $1/V_P$ of the reference transistor.
- 14 `ava=0`A of the reference transistor.
- 15 `avb=1` V_B of the reference transistor.
- 16 `avc=0`C of the reference transistor.
- 17 `wref=100u` mEffective width of the reference transistor.
- 18 `wtol=0` mDifference between drawn and effective gate width.
- 19 `dvtn=0` V mNarrow-width factor of the threshold voltage at `vsbref`.
- 20 `dkon=0` \sqrt{V} mNarrow-width factor of k_o .
- 21 `dkn=0` \sqrt{V} mNarrow-width factor of k .
- 22 `dvsbxn=0` V mNarrow-width factor of `vsbx`.
- 23 `ddelvx=0` VmNarrow-width factor of `dvsbx`.
- 24 `betan=20u` A/ V^2 Gain factor of a infinite-square transistor at the reference temperature.
- 25 `dth1n=0` m/VNarrow-width factor of `thel`.
- 26 `dth2n=0` m/ \sqrt{V} Narrow-width factor of `the2`.
- 27 `dth3n=0` m/VNarrow-width factor of `the3`.
- 28 `dgamn=0` mNarrow-width factor of `gam`.
- 29 `dava=0` mNarrow-width factor of `a`.
- 30 `davb=0` V mNarrow-width factor of `b`.
- 31 `davc=0` mNarrow-width factor of `c`.
- 32 `lref=100u` mEffective length of the reference transistor.

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Component Statements Part 2

- 33 `ltol=0` mDifference between drawn and actual gate polysilicon length.
- 34 `gvtn=0` V mShort-channel factor of the threshold voltage at `vsbref`.
- 35 `gkon=0` \sqrt{V} mShort-channel factor of `ko`.
- 36 `gkn=0` \sqrt{V} mShort-channel factor of `k`.
- 37 `gvsbxn=0` V mShort-channel factor of `vsbx`.
- 38 `gdelvsn=0` V mShort-channel factor of `dvsbx`.
- 39 `gth1n=0` m/VShort-channel factor of `the1`.
- 40 `gth2n=0` m/ \sqrt{V} Short-channel factor of `the2`.
- 41 `gth3n=0` m/VShort-channel factor of `the3`.
- 42 `ggamn=0` mShort-channel factor of `gam`.
- 43 `gshift=0` $V^{(1-n)}$ m²
Short-channel factor of `sh`.
- 44 `gnn=0` mShort-channel factor of `n`.
- 45 `gpn=0` m/VShort-channel factor of `p`.
- 46 `gava=0` mShort-channel factor of `a`.
- 47 `gavb=0` V mShort-channel factor of `b`.
- 48 `gavc=0` mShort-channel factor of `c`.
- 49 `lap=0` mHalf of the effective channel-length reduction due to lateral diffusion.
- 50 `vsbref=0` VSource to bulk reference voltage for parameter determination.
- 51 `phi=600m` VDiffusion potential at the reference temperature.
- 52 `tcvt=-1m` V/KTemperature coefficient of `vto`.
- 53 `tbetan=1.5`Power temperature coefficient of `bet`.

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Component Statements Part 2

- 54 $t_{th3n}=0 \text{ } 1/(V \text{ } K)$ Temperature coefficient of t_{he3} .
- 55 $t_{gth3n}=0 \text{ } m/(V \text{ } K)$ Temperature coefficient of the length dependence of t_{he3} .
- 56 $m=1.0$ Subthreshold-slope factor at reference back bias and at the reference temperature.
- 57 $subthn=0$ Weak-inversion factor.
- 58 $v_{tr}=0 \text{ } V$ Depletion-MOS-transistor-transition voltage.
- 59 $ratio=0$ Depletion-MOS-transistor-gain ratio.
- 60 $v_{fb}=0 \text{ } V$ Flat-band voltage.
- 61 $t_{ox}=100 \text{ } n \text{ } m$ Gate-oxide thickness.
- 62 $col=0 \text{ } F/m$ Gate/drain or gate/source overlap capacitance per unit length.
- 63 $f_{noise}=0 \text{ } m^2 \text{ } V^2$ Flicker-noise factor.
- 64 $t_{noise}=0$ Thermal-noise factor.

Temperature parameters

- 65 $t_r \text{ } (C)$ Reference temperature. Default set by option t_{nom} .
- 66 $t_{ref} \text{ } (C)$ Alias of t_r . Default set by option t_{nom} .
- 67 $t_{nom} \text{ } (C)$ Alias of t_r . Default set by option t_{nom} .
- 68 $dta=0 \text{ } K$ Deviation between the temperature of the transistor and the temperature of the circuit.
- 69 $t_{rise}=0 \text{ } K$ Alias of dta .

Output Parameters

- 1 $w_{eff} \text{ } (V)$ Effective channel width of the actual transistor.
- 2 $l_{eff} \text{ } (V)$ Effective channel length of the actual transistor.
- 3 $twophif \text{ } (V)$ Diffusion potential.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 4 β (A/V²) Gain factor of the transistor.
- 5 k (\sqrt{V}) Body-effect factor.
- 6 k_0 (\sqrt{V}) Initial body-effect factor for dual k approach.
- 7 v_{sbx} (V) Transition voltage for dual k approach.
- 8 dv_{sbx} (V) Transition-voltage range for dual k approach.
- 9 v_{to} (V) Threshold voltage.
- 10 v_{on} (V) Onset voltage of the superthreshold region.
- 11 $the1$ (1/V) Gate-bias-controlled transverse-field mobility reduction factor.
- 12 $the2$ (1/ \sqrt{V}) Back-bias-controlled transverse-field mobility reduction factor.
- 13 $the3$ (1/V) Lateral-field mobility reduction factor (velocity saturation).
- 14 γ Static-drain-feedback factor.
- 15 sh ($V^{(1-n)}$) Threshold-voltage-shift factor.
- 16 n Threshold-voltage-shift exponent.
- 17 p (1/V) Back-bias-shift factor.
- 18 m_e (\sqrt{V}) Auxiliary parameter for subthreshold-slope factor.
- 19 a Weak-avalanche multiplier.
- 20 b (V) Weak-avalanche exponent factor.
- 21 c Saturation-voltage reduction factor.
- 22 c_{ox} (F) Gate capacitance.
- 23 c_{gso} (F) Gate/source-overlap capacitance.
- 24 c_{gdo} (F) Gate/drain-overlap capacitance.
- 25 v_{tre} (V) Depletion MOS transistor transition voltage.

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Component Statements Part 2

- 26 `ratio` Depletion MOS transistor gain ratio.
- 27 `vfbe (V)` Flat band voltage.
- 28 `vtemp (V)` kT/q at actual device temperature.
- 29 `gnoise (V^2)` Coefficient of the flicker noise for the actual transistor.
- 30 `unoise (J)` Coefficient of the thermal noise for the actual transistor.

Operating-Point Parameters

- 1 `ide (A)` Drain current.
- 2 `ige (A)` Gate current.
- 3 `ise (A)` Source current.
- 4 `ibe (A)` Bulk current.
- 5 `vds (V)` Drain-source voltage.
- 6 `vgs (V)` Gate-source voltage.
- 7 `vsb (V)` Source-bulk voltage.
- 8 `ids (A)` Drain-source current.
- 9 `idb (A)` Drain-bulk current.
- 10 `isb (A)` Source-bulk current.
- 11 `pwr (W)` Power.
- 12 `vts (V)` V_{to} including back-bias effects.
- 13 `vgt (V)` Effective gate drive including back-bias and drain effects.
- 14 `vdss (V)` Saturation voltage at actual bias.
- 15 `gm (S)` Transconductance ($d\,ids / d\,vgs$).
- 16 `gmb (S)` Bulk transconductance ($d\,ids / d\,vbs$).

Spectre Circuit Simulator Reference

Component Statements Part 2

- 17 `gds` (S) Output conductance ($d i_{ds} / d v_{ds}$).
- 18 `cdd` (F) Capacitance ($d q_d / d v_d$).
- 19 `cdg` (F) Capacitance ($- d q_d / d v_g$).
- 20 `cds` (F) Capacitance ($- d q_d / d v_s$).
- 21 `cdb` (F) Capacitance ($- d q_d / d v_b$).
- 22 `cgd` (F) Capacitance ($- d q_g / d v_d$).
- 23 `cgg` (F) Capacitance ($d q_g / d v_g$).
- 24 `cgs` (F) Capacitance ($- d q_g / d v_s$).
- 25 `cgb` (F) Capacitance ($- d q_g / d v_b$).
- 26 `csd` (F) Capacitance ($- d q_s / d v_d$).
- 27 `csg` (F) Capacitance ($- d q_s / d v_g$).
- 28 `css` (F) Capacitance ($d q_s / d v_s$).
- 29 `csb` (F) Capacitance ($- d q_s / d v_b$).
- 30 `cbd` (F) Capacitance ($- d q_b / d v_d$).
- 31 `cbg` (F) Capacitance ($- d q_b / d v_g$).
- 32 `cbs` (F) Capacitance ($- d q_b / d v_s$).
- 33 `cbb` (F) Capacitance ($d q_b / d v_b$).
- 34 `u` Transistor gain (g_m/g_{ds}).
- 35 `rout` (Ω) Small signal output resistance ($1/g_{ds}$).
- 36 `vearly` (V) Equivalent Early voltage ($|I_d|/g_{ds}$).
- 37 `keff` (\sqrt{V}) Describes body effect at actual bias.
- 38 `beff` (S/V) Effective beta at actual bias in the simple MOS model.

Spectre Circuit Simulator Reference

Component Statements Part 2

39 `fug` (Hz) Unity gain frequency at actual bias ($gm/(2\pi \cdot Cox)$).

40 `sqrtsfw` (V/ \sqrt{Hz})
Input-referred RMS white noise voltage (\sqrt{sth}/gm).

41 `sqrtsff` (V/ \sqrt{Hz})
Input-referred RMS 1/f noise voltage at 1kHz ($\sqrt{gnoise/1000}$).

42 `fknee` (Hz) Cross-over frequency above which white noise is dominant.

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

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<code>ava</code> M-14	<code>dth2n</code> M-26	<code>kn</code> M-4	<code>tnom</code> M-67
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<code>cgb</code> OP-25	<code>gkn</code> M-36	<code>phi</code> M-51	<code>vgt</code> OP-13
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Component Statements Part 2

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cox	O-22	gshift	M-43	sh	O-15	vtn	M-2
csb	OP-29	gth1n	M-39	shiftn	M-11	vto	O-9
csd	OP-26	gth2n	M-40	sqrtsff	OP-41	vtr	M-58
csg	OP-27	gth3n	M-41	sqrtsfw	OP-40	vtre	O-25
css	OP-28	gvsbxn	M-37	subthn	M-57	vtc	OP-12
dava	M-29	gvtc	M-34	tbetan	M-53	w	I-3
davb	M-30	ibe	OP-4	tcvt	M-52	weff	O-1
davc	M-31	idb	OP-9	tgth3n	M-55	wn	I-1
ddelvx	M-23	ide	OP-1	th1n	M-7	wref	M-17
delvx	M-6	ids	OP-8	th2n	M-8	wtol	M-18
dgamn	M-28	ige	OP-2	th3n	M-9		
dkn	M-21	isb	OP-10	thel	O-11		
dkon	M-20	ise	OP-3	the2	O-12		

Compact MOS-Transistor Model (mos902)

Description

The mos902 model is a compact MOS-transistor model, intended for the simulation of circuit behavior with emphasis on analog applications. It is described in the Philips MOST Modelbook (Feb.98) as MOS model, level 902. Information on how to obtain this document can be found on Source Link by searching for Philips.

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In extension to the modelbook description a minimum conductance `gmin` is inserted between the drain and source node, to aid convergence. The value of `gmin` is set by an options statement, default = 1e-12 S.

This device is supported within altergroups.

This device is dynamically loaded from the shared object

`/vobs/spectre_dev/tools.sun4v/spectre/lib/cmi/2.0.doc/libphilips.so`

Sample Instance Statement

```
mp1 (0 1 2 2) mos9pch w=10u l=2u area=1.5
```

Sample Model Statement

```
model mos9pch mos902 ler=0.93e-6 wer=20e-6 tref=27 vtor=1.11 kr=0.54 phibr=0.66  
vsbxr=100 thelr=0.19 slk=-0.215e-6 swk=98e-9 swthe3=7.8e-9
```

Instance Definition

Name d g s [b] ModelName parameter=value ...

Instance Parameters

- 1 w=1.0 scale mDrawn channel width in the lay-out. Scale set by option scale.
- 2 l=1.0 scale mDrawn channel length in the lay-out. Scale set by option scale.
- 3 mult=1Number of devices in parallel.
- 4 area=1Alias of mult.
- 5 region=triodeEstimated DC operating region, used as a convergence aid.
Possible values are off, triode, sat, or subth.
- 6 m=1Multiplicity factor.

Model Definition

model modelName mos902 parameter=value ...

Model Parameters

Device Type Parameters

- 1 type=nTransistor gender.
Possible values are n or p.

Geometry Parameters

- 2 ler=2.5e-6 mEffective channel length of the reference transistor.
- 3 wer=25e-6 mEffective channel width of the reference transistor.
- 4 lvar=0.3e-6 mDifference between the actual and the programmed poly-silicon gate length.
- 5 lap=0.1e-6 mEffective channel length reduction per side.

Spectre Circuit Simulator Reference

Component Statements Part 2

6 `wvar=3e-6 m` Difference between the actual and the programmed field-oxide opening.

7 `wot=1e-6 m` Effective channel width reduction per side.

8 `wdog=0 m` Characteristic drawn gate width, below which dogboning appears.

Threshold-Voltage Parameters

9 `vtor=0.8 v` Threshold voltage at zero back-bias.

10 `stvt=0.01 v/K` Coefficient of the temperature dependence of `vto`.

11 `slvt=0.5e-6 v m` Coefficient of the length dependence of `vto`.

12 `sl2vt=0 v m2` Second coefficient of the length dependence of `vto`.

13 `swvt=5e-6 v m` Coefficient of the width dependence of `vto`.

14 `kor=0.5 \sqrt{V}` Low-backbias body factor.

15 `slko=1e-6 \sqrt{V} m`
Coefficient of the length dependence of `ko`.

16 `swko=10e-6 \sqrt{V} m`
Coefficient of the width dependence of `ko`.

17 `kr=0.1 \sqrt{V}` High-backbias body factor.

18 `slk=0.5e-6 \sqrt{V} m`
Coefficient of the length dependence of `k`.

19 `swk=5e-6 \sqrt{V} m`
Coefficient of the width dependence of `k`.

20 `phibr=0.65 v` Surface potential at strong inversion.

21 `vsbxr=0.9 v` Transition voltage for the dual-k-factor model.

22 `slvsbx=0.5e-6 v m` Coefficient of the length dependence of `vsbx`.

23 `swvsbx=5e-6 v m` Coefficient of the width dependence of `vsbx`.

Spectre Circuit Simulator Reference

Component Statements Part 2

Channel-Current Parameters

- 24 $\text{betsq}=0.1\text{e-}3 \text{ A/V}^2$
Gain factor for an infinite square transistor.
- 25 $\text{etabet}=0.5$ Exponent of the temperature dependence of the gain factor.
- 26 $\text{thelr}=0.05 \text{ 1/V}$ Coefficient of the mobility reduction due to the gate-induced field.
- 27 $\text{stthelr}=3\text{e-}3 \text{ 1/(V K)}$
Coefficient of the temperature dependence of thel .
- 28 $\text{slthelr}=50\text{e-}9 \text{ m/V}$ Coefficient of the length dependence of thel .
- 29 $\text{stlthel}=5\text{e-}9 \text{ m/(V K)}$
Coefficient of the temperature dependence of slthel .
- 30 $\text{swthel}=1\text{e-}6 \text{ m/V}$ Coefficient of the width dependence of thel .
- 31 $\text{fthel}=0$ Coefficient describing the width dependence of thel for $w < w_{dog}$.
- 32 $\text{the2r}=17\text{e-}3 \text{ 1/}\sqrt{V}$
Coefficient of the mobility reduction due to the back-bias.
- 33 $\text{stthe2r}=0.1\text{e-}3 \text{ 1/}(\sqrt{V} \text{ K})$
Coefficient of the temperature dependence of the2 .
- 34 $\text{slthe2r}=5\text{e-}9 \text{ m/}\sqrt{V}$
Coefficient of the length dependence of the2 .
- 35 $\text{stlthe2}=0.5\text{e-}9 \text{ m/}(\sqrt{V} \text{ K})$
Coefficient of the temperature dependence of slthe2 .
- 36 $\text{swthe2}=0.1\text{e-}6 \text{ m/}\sqrt{V}$
Coefficient of the width dependence of the2 .
- 37 $\text{the3r}=37\text{e-}3 \text{ 1/V}$ Coefficient of the mobility reduction due to the lateral field.
- 38 $\text{stthe3r}=0.1\text{e-}3 \text{ 1/(V K)}$
Coefficient of the temperature dependence of the3 .
- 39 $\text{slthe3r}=5\text{e-}9 \text{ m/V}$ Coefficient of the length dependence of the3 .

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Component Statements Part 2

40 `slthe3=0.5e-9 m/(V K)`
Coefficient of the temperature dependence of `slthe3`.

41 `swthe3=0.1e-6 m/V`Coefficient of the width dependence of `the3`.

Drain-Feedback Parameters

42 `gam1r=40e-3 V^(1-etads)`
Coefficient for the drain induced threshold shift for large gate drive.

43 `slgam1=0.1e-6 V^(1-etads) m`
Coefficient of the length dependence of `gam1`.

44 `swgam1=1e-6 V^(1-etads) m`
Coefficient of the width dependence of `gam1`.

45 `etadsr=0.6`Exponent of the `vds` dependence of `gam1`.

46 `alpr=4e-3`Factor of the channel-length modulation.

47 `etaalp=0.5`Exponent of the length dependence of `alp`.

48 `slalp=0.14e-3 m^etaalp`
Coefficient of the length dependence of `alp`.

49 `swalp=0.1e-6 m`Coefficient of the width dependence of `alp`.

50 `vpr=0.25 V`Characteristic voltage of the channel-length modulation.

Sub-threshold Parameters

51 `gamoor=1.1e-3`Coefficient for the drain induced threshold shift at zero gate drive.

52 `slgamoo=10e-15 m2`
Coefficient of the length dependence of `gamoo`.

53 `etagamr=2`Exponent of the back-bias dependence of `gamo`.

54 `mor=0.3`Factor for the subthreshold slope.

55 `stmo=0.01 1/K`Coefficient of the temperature dependence of `mo`.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 56 $sl_{mo}=1.4e-3 \sqrt{m}$
Coefficient of the length dependence of m_o .
- 57 $et_{amr}=2$ Exponent of the back-bias dependence of m .
- 58 $zet1r=0.7$ Weak-inversion correction factor.
- 59 $et_{azet}=0.5$ Exponent of the length dependence of $zet1$.
- 60 $sl_{zet1}=0.14e-6 m^{et_{azet}}$
Coefficient of the length dependence of $zet1$.
- 61 $vsbtr=99 \text{ V}$ Limiting voltage of the vsb dependence of m and g_{amo} .
- 62 $sl_{vsbt}=10e-6 \text{ V m}$ Coefficient of the length dependence of $vsbt$.

Weak-Avalanche Parameters

- 63 $a1r=22$ Factor of the weak-avalanche current.
- 64 $sta1=0.1 \text{ 1/K}$ Coefficient of the temperature dependence of $a1$.
- 65 $sla1=10e-6 \text{ m}$ Coefficient of the length dependence of $a1$.
- 66 $swa1=0.1e-3 \text{ m}$ Coefficient of the width dependence of $a1$.
- 67 $a2r=33 \text{ V}$ Exponent of the weak-avalanche current.
- 68 $sla2=10e-6 \text{ V m}$ Coefficient of the length dependence of $a2$.
- 69 $swa2=0.1e-3 \text{ V m}$ Coefficient of the width dependence of $a2$.
- 70 $a3r=0.6$ Factor of the drain-source voltage above which weak-avalanche occurs.
- 71 $sla3=1e-6 \text{ m}$ Coefficient of the length dependence of $a3$.
- 72 $swa3=10e-6 \text{ m}$ Coefficient of the width dependence of $a3$.

Charge Parameters

- 73 $tox=20e-9 \text{ m}$ Thickness of the oxide layer.

Spectre Circuit Simulator Reference

Component Statements Part 2

74 `col=50e-12 F/m` Gate overlap capacitance per unit channel width.

Noise Parameters

75 `ntr=21e-21 J` Coefficient of the thermal noise.

76 `nfr=16e-12 V2` Coefficient of the flicker noise.

Temperature Parameters

77 `tr (C)` Reference temperature. Default set by option `tnom`.

78 `tref (C)` Alias of `tr`. Default set by option `tnom`.

79 `tnom (C)` Alias of `tr`. Default set by option `tnom`.

80 `dta=0 K` Temperature offset of the device.

81 `trise=0 K` Alias of `dta`.

Output Parameters

1 `le (m)` Effective channel length.

2 `we (m)` Effective channel width.

3 `vto (V)` Threshold voltage at zero back-bias.

4 `ko (\sqrt{V})` Low-backbias body factor.

5 `k (\sqrt{V})` High-backbias body factor.

6 `phib (V)` Surface potential at strong inversion.

7 `vsbx (V)` Transition voltage for the dual-k-factor model.

8 `bet (A/V2)` Gain factor (* mult).

9 `the1 (1/V)` Coefficient of the mobility reduction due to the gate-induced field.

10 `the2 (1/ \sqrt{V})` Coefficient of the mobility reduction due to the back-bias.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 11 `the3 (1/V)` Coefficient of the mobility reduction due to the lateral field.
- 12 `gam1 (V^(1-etads))` Coefficient for the drain induced threshold shift for large gate drive.
- 13 `etads` Exponent of the vds dependence of `gam1`.
- 14 `alp` Factor of the channel-length modulation.
- 15 `vp (V)` Characteristic voltage of the channel-length modulation.
- 16 `gamoo` Coefficient for the drain induced threshold shift at zero gate drive.
- 17 `etagam` Exponent of the back-bias dependence of `gamoo`.
- 18 `mo` Factor for the subthreshold slope.
- 19 `etam` Exponent of the back-bias dependence of `m`.
- 20 `phit (V)` Thermal voltage.
- 21 `zet1` Weak-inversion correction factor.
- 22 `vsbt (V)` Limiting voltage of the vsb dependence of `m` and `gamoo`.
- 23 `a1` Factor of the weak-avalanche current.
- 24 `a2 (V)` Exponent of the weak-avalanche current.
- 25 `a3` Factor of the drain-source voltage above which weak-avalanche occurs.
- 26 `cox (F)` Gate-to-channel capacitance (* mult).
- 27 `cgdo (F)` Gate-drain overlap capacitance (* mult).
- 28 `cgso (F)` Gate-source overlap capacitance (* mult).
- 29 `nt (J)` Coefficient of the thermal noise.
- 30 `nf (V2)` Coefficient of the flicker noise (/ mult).

Spectre Circuit Simulator Reference

Component Statements Part 2

Operating-Point Parameters

- 1 `ide` (A) Resistive drain current.
- 2 `ige` (A) Resistive gate current.
- 3 `ise` (A) Resistive source current.
- 4 `ibe` (A) Resistive bulk current.
- 5 `vds` (V) Drain-source voltage.
- 6 `vgs` (V) Gate-source voltage.
- 7 `vsb` (V) Source-bulk voltage.
- 8 `ids` (A) Resistive drain-source current.
- 9 `idb` (A) Resistive drain-bulk current.
- 10 `isb` (A) Resistive source-bulk current.
- 11 `iavl` (A) Substrate current.
- 12 `pwr` (W) Power.
- 13 `vt1` (V) V_{to} including backbias effects.
- 14 `vgt2` (V) Effective gate drive including backbias and drain effects.
- 15 `vdss1` (V) Saturation voltage at actual bias.
- 16 `vsat` (V) Saturation limit.
- 17 `gm` (S) Transconductance ($d\,ids / d\,vgs$).
- 18 `gmb` (S) Bulk transconductance ($d\,ids / d\,vbs$).
- 19 `gds` (S) Output conductance ($d\,ids / d\,vds$).
- 20 `cdd` (F) Capacitance ($d\,qd / d\,vd$).
- 21 `cdg` (F) Capacitance ($-d\,qd / d\,vg$).

Spectre Circuit Simulator Reference

Component Statements Part 2

- 22 `cds` (F) Capacitance ($-d q_d / d v_s$).
- 23 `cdb` (F) Capacitance ($-d q_d / d v_b$).
- 24 `cgd` (F) Capacitance ($-d q_g / d v_d$).
- 25 `cgg` (F) Capacitance ($d q_g / d v_g$).
- 26 `cgs` (F) Capacitance ($-d q_g / d v_s$).
- 27 `cgb` (F) Capacitance ($-d q_g / d v_b$).
- 28 `csd` (F) Capacitance ($-d q_s / d v_d$).
- 29 `csg` (F) Capacitance ($-d q_s / d v_g$).
- 30 `css` (F) Capacitance ($d q_s / d v_s$).
- 31 `csb` (F) Capacitance ($-d q_s / d v_b$).
- 32 `cbd` (F) Capacitance ($-d q_b / d v_d$).
- 33 `cbg` (F) Capacitance ($-d q_b / d v_g$).
- 34 `cbs` (F) Capacitance ($-d q_b / d v_s$).
- 35 `cbb` (F) Capacitance ($d q_b / d v_b$).
- 36 `u` Transistor gain (g_m/g_{ds}).
- 37 `rout` (Ω) Small signal output resistance ($1/g_{ds}$).
- 38 `vearly` (V) Equivalent Early voltage ($|i_d|/g_{ds}$).
- 39 `keff` (\sqrt{V}) Describes body effect at actual bias.
- 40 `beff` (S/V) Effective beta at actual bias in the simple MOS model ($2*|i_{ds}|/v_{gt}^2$).
- 41 `fug` (Hz) Unity gain frequency at actual bias ($g_m/(2*\pi*c_{in})$).
- 42 `sqrtsfw` (V/ $\sqrt{\text{Hz}}$)
Input-referred RMS white noise voltage ($\sqrt{\text{sth}}/g_m$).

Spectre Circuit Simulator Reference

Component Statements Part 2

43 `sqrtsff` (V/ $\sqrt{\text{Hz}}$)

Input-referred RMS 1/f noise voltage at 1kHz ($\sqrt{\text{nf}/1000}$)).

44 `fknee` (Hz) Cross-over frequency above which white noise is dominant.

Parameter Index

In the following index, I refers to instance parameters, M refers to the model parameters section, O refers to the output parameters section, and OP refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of M-35 means the 35th model parameter.

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Component Statements Part 2

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Compact MOS-Transistor Model (mos903)

Description

The mos903 model is a compact MOS-transistor model, intended for the simulation of circuit behavior with emphasis on analog applications. It is described in the Philips MOST Modelbook (Jun.98) as MOS model, level 903. Information on how to obtain this document can be found on Source Link by searching for Philips.

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In extension to the modelbook description a minimum conductance g_{min} is inserted between the drain and source node, to aid convergence. The value of g_{min} is set by an options statement, default = 1e-12 S.

This device is supported within altergroups.

This device is dynamically loaded from the shared object

```
/vobs/spectre_dev/tools.sun4v/spectre/lib/cmi/2.0.doc/libphilips.so
```

Sample Instance Statement

```
m_1 (1 2 0 0) mos9nch w=0.35e-6 l=0.35e-6
```

Sample Model Statement

```
model mos9nch mos903 ler=3.5e-7 wer=1e-5 lvar=0 lap=2.2e-8 wvar=0 wot=3e-8  
vtor=0.76 the1r=0.67 stthe1r=-1.76e-3 etaalp=0 slalp=0 alpr=0.01
```

Instance Definition

```
Name d g s [b] ModelName parameter=value ...
```

Spectre Circuit Simulator Reference

Component Statements Part 2

Instance Parameters

- 1 `w=1.0 scale m` Drawn channel width in the lay-out. Scale set by option `scale`.
- 2 `l=1.0 scale m` Drawn channel length in the lay-out. Scale set by option `scale`.
- 3 `mult=1` Number of devices in parallel.
- 4 `area=1` Alias of `mult`.
- 5 `region=triode` Estimated DC operating region, used as a convergence aid.
Possible values are `off`, `triode`, `sat`, or `subth`.
- 6 `m=1` Multiplicity factor.

Model Definition

`model modelName mos903 parameter=value ...`

Model Parameters

Device Type Parameters

- 1 `type=n` Transistor gender.
Possible values are `n` or `p`.

Geometry Parameters

- 2 `ler=2.5e-6 m` Effective channel length of the reference transistor.
- 3 `wer=25e-6 m` Effective channel width of the reference transistor.
- 4 `lvar=0.3e-6 m` Difference between the actual and the programmed poly-silicon gate length.
- 5 `lap=0.1e-6 m` Effective channel length reduction per side.
- 6 `wvar=3e-6 m` Difference between the actual and the programmed field-oxide opening.
- 7 `wot=1e-6 m` Effective channel width reduction per side.

Spectre Circuit Simulator Reference

Component Statements Part 2

Threshold-Voltage Parameters

- 8 `vtor=0.8` V Threshold voltage at zero back-bias.
- 9 `stvt0=0.01` V/K Coefficient of the temperature dependence of `vt0`.
- 10 `slvt0=0.5e-6` V m Coefficient of the length dependence of `vt0`.
- 11 `sl2vt0=0` V m² Second coefficient of the length dependence of `vt0`.
- 12 `swvt0=5e-6` V m Coefficient of the width dependence of `vt0`.
- 13 `kor=0.5` $\sqrt{\text{V}}$ Low-backbias body factor.
- 14 `slko=1e-6` $\sqrt{\text{V}}$ m Coefficient of the length dependence of `ko`.
- 15 `swko=10e-6` $\sqrt{\text{V}}$ m Coefficient of the width dependence of `ko`.
- 16 `kr=0.1` $\sqrt{\text{V}}$ High-backbias body factor.
- 17 `slk=0.5e-6` $\sqrt{\text{V}}$ m Coefficient of the length dependence of `k`.
- 18 `swk=5e-6` $\sqrt{\text{V}}$ m Coefficient of the width dependence of `k`.
- 19 `phibr=0.65` V Surface potential at strong inversion.
- 20 `vsbxr=0.9` V Transition voltage for the dual-k-factor model.
- 21 `slvsbx=0.5e-6` V m Coefficient of the length dependence of `vsbx`.
- 22 `swvsbx=5e-6` V m Coefficient of the width dependence of `vsbx`.

Channel-Current Parameters

- 23 `betsq=0.1e-3` A/V² Gain factor for an infinite square transistor.
- 24 `etabet=0.5` Exponent of the temperature dependence of the gain factor.

Spectre Circuit Simulator Reference

Component Statements Part 2

25 $\text{the1r}=0.05 \text{ 1/V}$ Coefficient of the mobility reduction due to the gate-induced field.

26 $\text{stthe1r}=3\text{e-}3 \text{ 1/(V K)}$
Coefficient of the temperature dependence of the1 .

27 $\text{slthe1r}=50\text{e-}9 \text{ m/V}$ Coefficient of the length dependence of the1 .

28 $\text{stlthe1}=5\text{e-}9 \text{ m/(V K)}$
Coefficient of the temperature dependence of slthe1 .

29 $\text{swthe1}=1\text{e-}6 \text{ m/V}$ Coefficient of the width dependence of the1 .

30 $\text{wdog}=0 \text{ m}$ Characteristic drawn gate width, below which dogboning appears.

31 $\text{fthe1}=0$ Coefficient describing the width dependence of the1 for $w < \text{wdog}$.

32 $\text{the2r}=17\text{e-}3 \text{ 1/}\sqrt{\text{V}}$
Coefficient of the mobility reduction due to the back-bias.

33 $\text{stthe2r}=0.1\text{e-}3 \text{ 1/(\sqrt{V K})}$
Coefficient of the temperature dependence of the2 .

34 $\text{slthe2r}=5\text{e-}9 \text{ m/}\sqrt{\text{V}}$
Coefficient of the length dependence of the2 .

35 $\text{stlthe2}=0.5\text{e-}9 \text{ m/(\sqrt{V K})}$
Coefficient of the temperature dependence of slthe2 .

36 $\text{swthe2}=0.1\text{e-}6 \text{ m/}\sqrt{\text{V}}$
Coefficient of the width dependence of the2 .

37 $\text{the3r}=37\text{e-}3 \text{ 1/V}$ Coefficient of the mobility reduction due to the lateral field.

38 $\text{stthe3r}=0.1\text{e-}3 \text{ 1/(V K)}$
Coefficient of the temperature dependence of the3 .

39 $\text{slthe3r}=5\text{e-}9 \text{ m/V}$ Coefficient of the length dependence of the3 .

40 $\text{stlthe3}=0.5\text{e-}9 \text{ m/(V K)}$
Coefficient of the temperature dependence of slthe3 .

41 $\text{swthe3}=0.1\text{e-}6 \text{ m/V}$ Coefficient of the width dependence of the3 .

Spectre Circuit Simulator Reference

Component Statements Part 2

Drain-Feedback Parameters

- 42 $\text{gam1r}=40\text{e-}3 \text{ V}^{(1-\text{etads})}$
Coefficient for the drain induced threshold shift for large gate drive.
- 43 $\text{slgam1}=0.1\text{e-}6 \text{ V}^{(1-\text{etads})} \text{ m}$
Coefficient of the length dependence of gam1 .
- 44 $\text{swgam1}=1\text{e-}6 \text{ V}^{(1-\text{etads})} \text{ m}$
Coefficient of the width dependence of gam1 .
- 45 $\text{etadsr}=0.6$ Exponent of the vds dependence of gam1 .
- 46 $\text{alpr}=4\text{e-}3$ Factor of the channel-length modulation.
- 47 $\text{etaalp}=0.5$ Exponent of the length dependence of alp .
- 48 $\text{slalp}=0.14\text{e-}3 \text{ m}^{\text{etaalp}}$
Coefficient of the length dependence of alp .
- 49 $\text{swalp}=0.1\text{e-}6 \text{ m}$ Coefficient of the width dependence of alp .
- 50 $\text{vpr}=0.25 \text{ V}$ Characteristic voltage of the channel-length modulation.

Sub-Threshold Parameters

- 51 $\text{gamoor}=1.1\text{e-}3$ Coefficient for the drain induced threshold shift at zero gate drive.
- 52 $\text{slgamoo}=10\text{e-}15 \text{ m}^2$
Coefficient of the length dependence of gamoo .
- 53 $\text{etagamr}=2$ Exponent of the back-bias dependence of gamoo .
- 54 $\text{mor}=0.3$ Factor for the subthreshold slope.
- 55 $\text{stmo}=0.01 \text{ 1/K}$ Coefficient of the temperature dependence of mo .
- 56 $\text{slmo}=1.4\text{e-}3 \sqrt{\text{m}}$
Coefficient of the length dependence of mo .
- 57 $\text{etamr}=2$ Exponent of the back-bias dependence of m .

Spectre Circuit Simulator Reference

Component Statements Part 2

- 58 `zet1r=0.7` Weak-inversion correction factor.
- 59 `etazet=0.5` Exponent of the length dependence of `zet1`.
- 60 `slzet1=0.14e-6 m^etazet`
Coefficient of the length dependence of `zet1`.
- 61 `vsbtr=99 v` Limiting voltage of the `vsb` dependence of `m` and `gamo`.
- 62 `slvsbt=10e-6 V m` Coefficient of the length dependence of `vsbt`.

Weak-Avalanche Parameters

- 63 `a1r=22` Factor of the weak-avalanche current.
- 64 `sta1=0.1 1/K` Coefficient of the temperature dependence of `a1`.
- 65 `sla1=10e-6 m` Coefficient of the length dependence of `a1`.
- 66 `swa1=0.1e-3 m` Coefficient of the width dependence of `a1`.
- 67 `a2r=33 v` Exponent of the weak-avalanche current.
- 68 `sla2=10e-6 V m` Coefficient of the length dependence of `a2`.
- 69 `swa2=0.1e-3 V m` Coefficient of the width dependence of `a2`.
- 70 `a3r=0.6` Factor of the drain-source voltage above which weak-avalanche occurs.
- 71 `sla3=1e-6 m` Coefficient of the length dependence of `a3`.
- 72 `swa3=10e-6 m` Coefficient of the width dependence of `a3`.

Charge Parameters

- 73 `tox=20e-9 m` Thickness of the oxide layer.
- 74 `col=50e-12 F/m` Gate overlap capacitance per unit channel width.

Noise Parameters

- 75 `ntr=21e-21 J` Coefficient of the thermal noise.

Spectre Circuit Simulator Reference

Component Statements Part 2

76 `nfmod=0.0` Switch that selects either old or new flicker noise model.

77 `nfr=16e-12 V2` Flicker noise coefficient of the reference transistor (for `nfmod =1`).

78 `nfcr=7.15e+22 1/(V m4)`
First coefficient of the flicker noise coefficient of the reference transistor (for `nfmod=1`).

79 `nfbr=2.16e+7 1/(V m2)`
Second coefficient of the flicker noise coefficient of the reference transistor (for `nfmod=1`).

80 `nfcr=0.0 1/V` Third coefficient of the flicker noise coefficient of the reference transistor (for `nfmod=1`).

Temperature Parameters

81 `tr (C)` Reference temperature. Default set by option `tnom`.

82 `tref (C)` Alias of `tr`. Default set by option `tnom`.

83 `tnom (C)` Alias of `tr`. Default set by option `tnom`.

84 `dta=0 K` Temperature offset of the device.

85 `trise=0 K` Alias of `dta`.

Other Parameters

86 `th3mod=1` Flag for theta3 clipping.

Output Parameters

1 `le (m)` Effective channel length.

2 `we (m)` Effective channel width.

3 `vto (V)` Threshold voltage at zero back-bias.

4 `ko (\sqrt{V})` Low-backbias body factor.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 5 k (\sqrt{V}) High-backbias body factor.
- 6 ϕ_{ib} (V) Surface potential at strong inversion.
- 7 v_{sbx} (V) Transition voltage for the dual- k -factor model.
- 8 β_{et} (A/V^2) Gain factor (* mult).
- 9 θ_{e1} ($1/V$) Coefficient of the mobility reduction due to the gate-induced field.
- 10 θ_{e2} ($1/\sqrt{V}$) Coefficient of the mobility reduction due to the back-bias.
- 11 θ_{e3} ($1/V$) Coefficient of the mobility reduction due to the lateral field.
- 12 γ_{m1} ($V^{(1-\epsilon_{tads})}$) Coefficient for the drain induced threshold shift for large gate drive.
- 13 ϵ_{tads} Exponent of the v_{ds} dependence of γ_{m1} .
- 14 α_p Factor of the channel-length modulation.
- 15 v_p (V) Characteristic voltage of the channel-length modulation.
- 16 γ_{m0} Coefficient for the drain induced threshold shift at zero gate drive.
- 17 ϵ_{tagam} Exponent of the back-bias dependence of γ_{m0} .
- 18 m_0 Factor for the subthreshold slope.
- 19 ϵ_{tam} Exponent of the back-bias dependence of m .
- 20 ϕ_{it} (V) Thermal voltage.
- 21 z_{et1} Weak-inversion correction factor.
- 22 v_{sbt} (V) Limiting voltage of the v_{sb} dependence of m and γ_{m0} .
- 23 a_1 Factor of the weak-avalanche current.
- 24 a_2 (V) Exponent of the weak-avalanche current.
- 25 a_3 Factor of the drain-source voltage above which weak-avalanche occurs.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 26 `cox` (F) Gate-to-channel capacitance (* mult).
- 27 `cgdo` (F) Gate-drain overlap capacitance (* mult).
- 28 `cgs0` (F) Gate-source overlap capacitance (* mult).
- 29 `nt` (J) Coefficient of the thermal noise.
- 30 `nf` (V²) Coefficient of the flicker noise (/ mult) (nfmod = 0).
- 31 `nfa` (1/(V m⁴)) First coefficient of the flickernoise of the actual transistor (nfmod = 1).
- 32 `nfb` (1/(V m²)) Second coefficient of the flickernoise of the actual transistor (nfmod = 1).
- 33 `nfC` (1/V) Second coefficient of the flickernoise of the actual transistor (nfmod = 1).
- 34 `tox` (m) Thickness of gate oxide layer.

Operating-Point Parameters

- 1 `ide` (A) Resistive drain current.
- 2 `ige` (A) Resistive gate current.
- 3 `ise` (A) Resistive source current.
- 4 `ibe` (A) Resistive bulk current.
- 5 `vds` (V) Drain-source voltage.
- 6 `vgs` (V) Gate-source voltage.
- 7 `vsb` (V) Source-bulk voltage.
- 8 `ids` (A) Resistive drain-source current.
- 9 `idb` (A) Resistive drain-bulk current.
- 10 `isb` (A) Resistive source-bulk current.
- 11 `iavl` (A) Substrate current.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 12 `pwr` (W) Power.
- 13 `vt1` (V) V_{to} including backbias effects.
- 14 `vgt2` (V) Effective gate drive including backbias and drain effects.
- 15 `vdss1` (V) Saturation voltage at actual bias.
- 16 `vsat` (V) Saturation limit.
- 17 `gm` (S) Transconductance ($d\,ids / d\,vgs$).
- 18 `gmb` (S) Bulk transconductance ($d\,ids / d\,vbs$).
- 19 `gds` (S) Output conductance ($d\,ids / d\,vds$).
- 20 `cdd` (F) Capacitance ($d\,qd / d\,vd$).
- 21 `cdg` (F) Capacitance ($- d\,qd / d\,vg$).
- 22 `cds` (F) Capacitance ($- d\,qd / d\,vs$).
- 23 `cdb` (F) Capacitance ($- d\,qd / d\,vb$).
- 24 `cgd` (F) Capacitance ($- d\,qg / d\,vd$).
- 25 `cgg` (F) Capacitance ($d\,qg / d\,vg$).
- 26 `cgs` (F) Capacitance ($- d\,qg / d\,vs$).
- 27 `cgb` (F) Capacitance ($- d\,qg / d\,vb$).
- 28 `csd` (F) Capacitance ($- d\,qs / d\,vd$).
- 29 `csq` (F) Capacitance ($- d\,qs / d\,vg$).
- 30 `css` (F) Capacitance ($d\,qs / d\,vs$).
- 31 `csb` (F) Capacitance ($- d\,qs / d\,vb$).
- 32 `cbd` (F) Capacitance ($- d\,qb / d\,vd$).
- 33 `cbg` (F) Capacitance ($- d\,qb / d\,vg$).

Spectre Circuit Simulator Reference

Component Statements Part 2

- 34 `cbs` (F) Capacitance ($-d q_b / d v_s$).
- 35 `cbb` (F) Capacitance ($d q_b / d v_b$).
- 36 `u` Transistor gain (g_m/g_{ds}).
- 37 `rout` (Ω) Small signal output resistance ($1/g_{ds}$).
- 38 `vearly` (V) Equivalent Early voltage ($|i_d|/g_{ds}$).
- 39 `keff` (\sqrt{V}) Describes body effect at actual bias.
- 40 `beff` (S/V) Effective beta at actual bias in the simple MOS model ($2|i_{ds}|/v_{gt}^2$).
- 41 `fug` (Hz) Unity gain frequency at actual bias ($g_m/(2\pi \cdot c_{in})$).
- 42 `sqrtsfw` (V/ $\sqrt{\text{Hz}}$)
Input-referred RMS white noise voltage ($\sqrt{\text{sth}}/g_m$).
- 43 `sqrtsff` (V/ $\sqrt{\text{Hz}}$)
Input-referred RMS 1/f noise voltage at 1kHz ($\sqrt{\text{nf}/1000}$).
- 44 `fknee` (Hz) Cross-over frequency above which white noise is dominant.

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

<code>a1</code>	O-23	<code>fthel</code>	M-31	<code>phibr</code>	M-19	<code>swvsbx</code>	M-22
<code>alr</code>	M-63	<code>fug</code>	OP-41	<code>phit</code>	O-20	<code>swvto</code>	M-12
<code>a2</code>	O-24	<code>gam1</code>	O-12	<code>pwr</code>	OP-12	<code>th3mod</code>	M-86
<code>a2r</code>	M-67	<code>gam1r</code>	M-42	<code>region</code>	I-5	<code>thel</code>	O-9
<code>a3</code>	O-25	<code>gamoo</code>	O-16	<code>rout</code>	OP-37	<code>thelr</code>	M-25
<code>a3r</code>	M-70	<code>gamoor</code>	M-51	<code>sl2vto</code>	M-11	<code>the2</code>	O-10
<code>alp</code>	O-14	<code>gds</code>	OP-19	<code>sla1</code>	M-65	<code>the2r</code>	M-32
<code>alpr</code>	M-46	<code>gm</code>	OP-17	<code>sla2</code>	M-68	<code>the3</code>	O-11
<code>area</code>	I-4	<code>gmb</code>	OP-18	<code>sla3</code>	M-71	<code>the3r</code>	M-37
<code>beff</code>	OP-40	<code>iavl</code>	OP-11	<code>slalp</code>	M-48	<code>tnom</code>	M-83

Spectre Circuit Simulator Reference

Component Statements Part 2

bet	O-8	ibe	OP-4	slgam1	M-43	tox	M-73
betsq	M-23	idb	OP-9	slgamoo	M-52	tox	O-34
cbb	OP-35	ide	OP-1	slk	M-17	tr	M-81
cbd	OP-32	ids	OP-8	slko	M-14	tref	M-82
cbg	OP-33	ige	OP-2	slmo	M-56	trise	M-85
cbs	OP-34	isb	OP-10	slthelr	M-27	type	M-1
cdb	OP-23	ise	OP-3	slthe2r	M-34	u	OP-36
cdd	OP-20	k	O-5	slthe3r	M-39	vds	OP-5
cdg	OP-21	keff	OP-39	slvsbt	M-62	vdssl	OP-15
cds	OP-22	ko	O-4	slvsbx	M-21	vearly	OP-38
cgb	OP-27	kor	M-13	slvto	M-10	vgs	OP-6
cgd	OP-24	kr	M-16	slzet1	M-60	vgt2	OP-14
cgdo	O-27	l	I-2	sqrtsff	OP-43	vp	O-15
cgg	OP-25	lap	M-5	sqrtsfw	OP-42	vpr	M-50
cgs	OP-26	le	O-1	stal	M-64	vsat	OP-16
cgso	O-28	ler	M-2	stlthel	M-28	vsb	OP-7
col	M-74	lvar	M-4	stlthe2	M-35	vsbt	O-22
cox	O-26	m	I-6	stlthe3	M-40	vsbtr	M-61
csb	OP-31	mo	O-18	stmo	M-55	vsbx	O-7
csd	OP-28	mor	M-54	stthelr	M-26	vsbxr	M-20
csg	OP-29	mult	I-3	stthe2r	M-33	vt1	OP-13
css	OP-30	nf	O-30	stthe3r	M-38	vto	O-3
dta	M-84	nfa	O-31	stvt0	M-9	vtor	M-8
etaalp	M-47	nfar	M-78	swal	M-66	w	I-1
etabet	M-24	nfb	O-32	swa2	M-69	wdog	M-30
etads	O-13	nfbr	M-79	swa3	M-72	we	O-2
etadsr	M-45	nfc	O-33	swalp	M-49	wer	M-3
etagam	O-17	nfcr	M-80	swgam1	M-44	wot	M-7
etagamr	M-53	nfmod	M-76	swk	M-18	wvar	M-6
etam	O-19	nfr	M-77	swko	M-15	zet1	O-21
etamr	M-57	nt	O-29	swthel	M-29	zet1r	M-58
etazet	M-59	ntr	M-75	swthe2	M-36		
fknee	OP-44	phib	O-6	swthe3	M-41		

Microstrip Line (msline)

Description

This is a microstrip line based on the equations of Hammerstad and Jensen. The model contains a thickness correction to the width and frequency dependent permittivity and characteristic impedance. The dispersion equations are those of Kirschning and Jansen.

This device is supported within altergroups.

Spectre Circuit Simulator Reference

Component Statements Part 2

Sample Instance Statement

```
t11 (in 0 out 0) msline l=0.15 w=0.01 h=0.01
```

Instance Definition

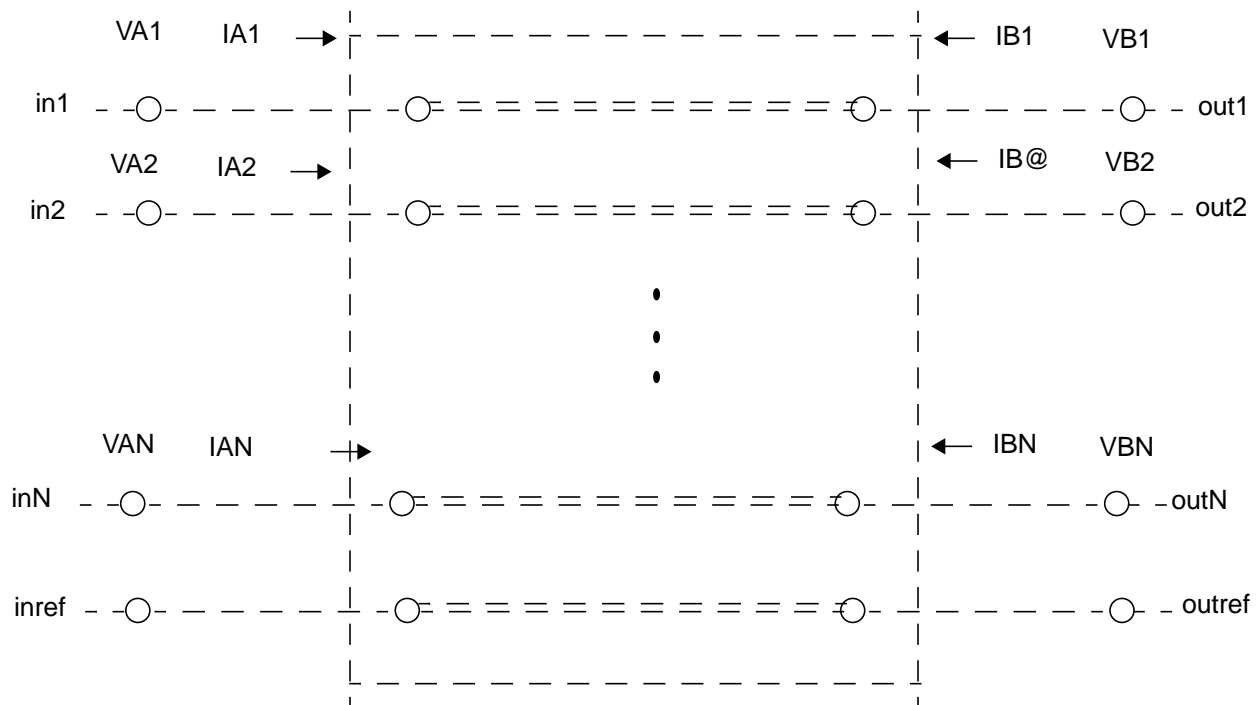
```
Name t1 b1 t2 b2 msline parameter=value ...
```

Instance Parameters

- 1 l=0 mLength.
- 2 w (m)Width.
- 3 h (m)Substrate height.
- 4 t=0 mConductor thickness.
- 5 eps=1Substrate permittivity relative to a vacuum.
- 6 m=1Multiplicity factor.
- 7 fmax=10e9 HzMaximum signal frequency.

Multi-Conductor Transmission Line (mtline)

Description



A multi-conductor transmission line (mtline) is characterized by constant RLGC matrices or frequency dependent RLGC data. An mtline can have as many conductors as there are as described in the input, however, there must be at least two conductors with one conductor used as reference to define terminal voltages. The reference conductor can be ground. The order of the conductors is the same as the order of the data in the input.

All of the conductors are assumed to have the same length, the input to mtline are conductor length, per-unit-length resistance (R), inductance (L), conductance (G), and capacitance (C) matrices. Because these matrices are generally symmetric, either full matrix description or lower half matrix description can be used. For example, to describe the resistance matrix of a four conductor mtline:

[50 10 1]

Spectre Circuit Simulator Reference

Component Statements Part 2

```
R = [ 10 50 10 ] Ohm/meter
      [ 1 10 50 ]
```

The following two model descriptions are equivalent:

```
model line mtline
```

```
+ r=[ 50 10 1
+    10 50 10
+    1 10 50 ]
+ ...
```

```
model line mtline
```

```
+ r=[ 50
+    10 50
+    1 10 50 ]
+ ...
```

Frequency dependent RLGC matrices are described in a data file through model parameter `file`. The frequency axis can be scaled with the `scale` parameter. The frequencies in the data file are then multiplied by `scale` before the simulator uses them. The default scale factor is unity. An example data file is listed below:

```
; Comments: rl.dat
```

```
FORMAT FREQ: R1:1 R2:1 R2:2
```

```
          L1:1 L2:1 L2:2
```

```
0.001e+9:  4.444 0.000383 4.444
```

```
          4.565 0.3545  4.565
```

```
0.010e+9:  4.447 0.003834 4.447
```

```
          4.565 0.3545  4.565
```

```
0.100e+9   4.476 0.03834 4.476
```

Spectre Circuit Simulator Reference

Component Statements Part 2

```
4.565 0.3545 4.565
1.000e+9 4.762 0.3834 4.762
3.103 0.2357 3.103
10.00e+9 13.96 1.082 13.96
2.718 0.2058 2.718
100.0e+9 56.88 3.294 56.88
2.531 0.1866 2.531
```

Constant matrix is the first choice of input if both the constant matrix and tabular data are provided. If only one frequency point is provided in the `file`, the RLGC matrices are treated as constant matrices.

Rational fitting is used to build a stable model for each `mtline` instance. The fitting procedure can take a long time for complicated data, the reduced order model (ROM) file option `romdatfile` is used to store and re-use the model in subsequent simulations.

Maximum signal frequency `fmax` is used to determine the relevant range of rational fitting. The inverse of `trise` in input signal can be used as an estimation of `fmax`.

This device is not supported within `altergroup`.

Sample Instance Statement

```
x1 (a1 b1 a2 b2 0 0) mtline len=0.01
+ r=[ 0.3
+     0.0 0.3 ]
+ c=[ 0.35p
+     -0.03p 0.35p ]
```

Sample Model Statement

```
model mtmodel mtline
+ r=[ 0.3
+     0.0 0.3 ]
+ c=[ 0.35p
+     -0.03p 0.35p ]
model mtmodel mtline
+ r=[ 0.3 0.0
```

Spectre Circuit Simulator Reference

Component Statements Part 2

```
+      0.0 0.3 ]
+ c=[ 0.35p -0.03p
+      -0.03p 0.35p ]
model mtmodel mtline
+ c=[ 0.35p
+      -0.03p 0.35p ]
+ file="rl.data" scale=1
```

Instance Definition

```
Name in1 out1 in2 out2 ... ModelName parameter=value ...
Name in1 out1 in2 out2 ... mtline parameter=value ...
```

The last two terminals will be used as refin and refout respectively.

Instance Parameters

- 1 len=0.01 mPhysical length of line.
- 2 m=1Multiplicity factor.
- 3 r=[...] Ω/m Resistance matrix per unit length.
- 4 l=[...] H/mInductance matrix per unit length.
- 5 g=[...] S/mConductor matrix per unit length.
- 6 c=[...] F/mCapacitance matrix per unit length.
- 7 rskin=[...] Ω/m Skin effect resistance matrix per unit length.
- 8 gdloss=[...] S/mDielectric loss conductance matrix per unit length.
- 9 fileRLGC data file that contains the frequency dependent RLGC data.
- 10 freqscale=1Frequency scale factor for frequency dependent RLGC data.
- 11 romdatfile="rom.dat"
File that contains the time-domain reduced order model (ROM).

Model Definition

```
model modelName mtline parameter=value ...
```

Model Parameters

RLGC Data Parameters

- 1 `r=[...]` Ω/m Resistance matrix per unit length.
- 2 `l=[...]` H/m Inductance matrix per unit length.
- 3 `g=[...]` S/m Conductor matrix per unit length.
- 4 `c=[...]` F/m Capacitance matrix per unit length.
- 5 `rskin=[...]` Ω/m Skin effect resistance matrix per unit length.
- 6 `gdloss=[...]` S/m Dielectric loss conductance matrix per unit length.
- 7 `file` RLGC data file that contains the frequency dependent RLGC data.
- 8 `freqscale=1` Frequency scale factor for frequency dependent RLGC data.

Rational Fitting Parameters

- 9 `fmax=1e9` Hz Maximum signal frequency used to determine the relevant range of rational fitting.
- 10 `resolution=moderate`
Frequency sampling resolution.
Possible values are `conservative`, `moderate` or `liberal`.

Note: Spectre uses rational fitting algorithm to build a stable model that approximates the desired transmission line characteristics. The accuracy of the `mtline` model is solely dependent on how well the rational approximation is over frequency range $[0, f_{\text{max}}]$.

Model parameter `resolution` controls the accuracy of the rational approximation. Tighter resolution control uses higher order rational fitting, which generally results in a more accurate model. However, a higher order model also increases simulation time and tends to be unstable. For most transmission line applications, `moderate` resolution is sufficient to guarantee accuracy and stability.

Modeling Frequency Dependent Effects

One can model the frequency dependent RLGC matrices by providing the data file using model parameter `file`. One should always try to provide accurate and sufficient data to describe the frequency dependent RLGC matrices.

In addition, the following simplified equation can be used to model skin effect with the constant RLGC matrices

$$R(f) = r + \sqrt{f} * (1 + j) * r_{skin},$$

and the following equation can be used to model dielectric loss with the constant RLGC matrices

$$G(f) = g + f / \sqrt{1 + 4 * (f / f_{max})^2} * g_{dloss},$$

where `f` stands for frequency.

Parameter Index

In the following index, `I` refers to instance parameters, `M` refers to the model parameters section, `O` refers to the output parameters section, and `OP` refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of `M-35` means the 35th model parameter.

<code>c</code>	<code>M-4</code>	<code>freqscale</code>	<code>I-10</code>	<code>l</code>	<code>I-4</code>	<code>romdatfile</code>	<code>I-11</code>
<code>c</code>	<code>I-6</code>	<code>g</code>	<code>I-5</code>	<code>len</code>	<code>I-1</code>	<code>rskin</code>	<code>M-5</code>
<code>file</code>	<code>I-9</code>	<code>g</code>	<code>M-3</code>	<code>m</code>	<code>I-2</code>	<code>rskin</code>	<code>I-7</code>
<code>file</code>	<code>M-7</code>	<code>gdloss</code>	<code>I-8</code>	<code>r</code>	<code>I-3</code>		
<code>fmax</code>	<code>M-9</code>	<code>gdloss</code>	<code>M-6</code>	<code>r</code>	<code>M-1</code>		
<code>freqscale</code>	<code>M-8</code>	<code>l</code>	<code>M-2</code>	<code>resolution</code>	<code>M-10</code>		

Mutual Inductor (`mutual_inductor`)

Description

The mutual inductor couples two previously specified inductors. There is no limit to the number of inductors that you can couple or to the number of couplings to a particular inductor, but you must specify separate mutual inductor statements for each coupling. Using the `dot` convention, place a `dot` on the first terminal of each inductor.

This device is not supported within altergroup.

The mutual inductor modifies the constitutive equations of two isolated inductors to

$$v1 = L11 \cdot di1/dt + M \cdot di2/dt$$

$$v2 = M \cdot di1/dt + L22 \cdot di2/dt$$

where the mutual inductance, M, is computed from the coupling coefficient, k, using $k = |M|/\sqrt{L11 \cdot L22}$.

Sample Instance Statement with Two Inductors

```
l1 (1 0) inductor
l2 (2 0) inductor
ml1 mutual_inductor coupling=1 ind1=l1 ind2=l2
```

Instance Definition

Name mutual_inductor parameter=value ...

Instance Parameters

- 1 coupling=0 Coupling coefficient.
- 2 ind1 Inductor to be coupled.
- 3 ind2 Inductor to be coupled.

Node Capacitance (nodcap)

Description

The nodcap model is generally used to model voltage dependent capacitances and currents of the source and drain diodes of MOS transistors and the capacitances of related interconnection areas and sidewalls (IN and PS regions). It is described in the Philips MOST Modelbook (Dec.93) as NODCAP model.

(c) Philips Electronics N.V. 1994

Spectre Circuit Simulator Reference

Component Statements Part 2

In extension to the modelbook description a minimum conductance g_{min} is inserted between the nodcap nodes, to aid convergence. The value of g_{min} is set by an options statement, default = 1e-12 S.

The i_{max} parameter is used to aid convergence and to prevent numerical overflow. The junction characteristics of the node capacitor are accurately modeled for currents up to i_{max} . For currents above i_{max} , the junction is modeled as a linear resistor and a warning is printed.

This device is supported within altergroups.

This device is dynamically loaded from the shared object

/vobs/spectre_dev/tools.sun4v/spectre/lib/cmi/2.0.doc/libphilips.so

Sample Instance Statement

```
n14 (outc vcc) pcapmod ad=191.13e-6 pdg=70e-6 pdcs=75
```

Sample Model Statement

```
model pcapmod nodcap type=p js=10e-6 pt=2 cjr=0.6e-3 vdr=750e-3 tref=27 pmr=0.4  
vr=0 kpers=4.9e-6 cox=1.77e-3
```

Instance Definition

```
Name nn [ns] ModelName parameter=value ...
```

Instance Parameters

- 1 $m=1.0$ Multiplicity factor.
- 2 $region=rev$ Estimated DC operating region, used as a convergence aid.
Possible values are fwd, rev or brk.
- 3 $cf=0.0$ F Fixed capacitance.
- 4 $ad=0.0$ $scale^2$ m^2
Diffusion area (source or drain). Scale set by option scale.
- 5 $pdcs=0.0$ $scale$ m Length of the side-wall of the diffusion area AD which is not under the gate. Scale set by option scale.
- 6 $pdg=0.0$ $scale$ m Length of the side-wall of the diffusion area AD which is under the gate. Scale set by option scale.

Spectre Circuit Simulator Reference

Component Statements Part 2

7 `ain=0.0 scale2 m2`

Area of metal interconnection over thick oxide. Scale set by option scale.

8 `ag=0.0 scale2 m2`

Area metal or poly-Si over thin oxide. Scale set by option scale.

9 `aps=0.0 scale2 m2`

Area metal of poly-Si over thick oxide. Scale set by option scale.

10 `pin=0.0 scale` mLength of the side-wall of AIN. Scale set by option scale.

11 `pps=0.0 scale` mLength of the side-wall of APS. Scale set by option scale.

Model Definition

`model modelName nodcap parameter=value ...`

Model Parameters

1 `type=n`Type of the nodcap device.

Possible values are n or p.

2 `kpjs=0.0` mConversion factor of the sidewall length and the gate edge length to its equivalent area for the saturation current.

3 `n=1.0`Emission coefficient.

4 `bv=∞` vReverse break-down voltage.

5 `imax=1.0` AExplosion current.

6 `pt=2.0`Temperature coefficient of the saturation current.

7 `vr=0.0` vVoltage at which CJR is specified.

8 `fc=0.5`Forward-bias non-ideal junction capacitance coefficient.

9 `pmr=0.5`Grading coefficient.

10 `kpers=0.0` mConversion factor of the sidewall length to its equivalent area for the junction capacitance.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 11 `kperg=0.0 m` Conversion factor of the gate edge length to its equivalent area for the junction capacitance.
- 12 `cin=0.0 F/m2` Specific capacitance of the (interconnection) metal to the substrate.
- 13 `cox=0.0 F/m2` Specific capacitance of poly-silicon or aluminum to the substrate over thin oxide.
- 14 `cps=0.0 F/m2` Specific capacitance of poly-silicon to the substrate over thick oxide.
- 15 `cins=0.0 F/m` IN-side-wall capacitance.
- 16 `cpss=0.0 F/m` PS-side-wall capacitance.
- 17 `dta=0.0 K` Temperature offset of the device with respect to TEMP.
- 18 `trise (K)` Alias of `dta`.
- 19 `tref=27.0 C` Temperature at which the parameters are specified. Default set by option `tnom`.
- 20 `tnom (C)` Alias of `tref`.
- 21 `tr (C)` Alias of `tref`.
- 22 `js=100u A/m2` Saturation current density.
- 23 `cjr=0.0 F/m2` Specific junction capacitance at $V_d = V_R$.
- 24 `vdr=0.8 v` Diffusion voltage.

Output Parameters

- 1 `jst (A/m2)` Saturation current density (temperature updated).
- 2 `cjrt (F/m2)` Specific junction capacitance at $V_d = V_R$ (temperature updated).
- 3 `vdrt (V)` Diffusion voltage (temperature updated).
- 1 `i0 (A)` Saturation current (temperature updated).

Spectre Circuit Simulator Reference

Component Statements Part 2

Operating-Point Parameters

- 1 `region=rev` Estimated DC operating region, used as a convergence aid.
Possible values are `fwd`, `rev` or `brk`.
- 2 `vsub` (V) Voltage across the node-capacitance, which is measured from NS (substrate or N-well) to NN (source or drain). `Vsub` is usually negative.
- 3 `isub` (A) Resistive leakage current.
- 4 `cap` (F) Junction capacitance.
- 5 `q` (Coul) Junction charge.
- 6 `gm` (S) Total differential conductance.
- 7 `c0` (F) Constant part of the junction capacitance.
- 8 `pwr` (W) Power.

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

<code>ad</code>	I-4	<code>cox</code>	M-13	<code>kpers</code>	M-10	<code>region</code>	OP-1
<code>ag</code>	I-8	<code>cps</code>	M-14	<code>kpjs</code>	M-2	<code>region</code>	I-2
<code>ain</code>	I-7	<code>cpss</code>	M-16	<code>m</code>	I-1	<code>tnom</code>	M-20
<code>aps</code>	I-9	<code>dta</code>	M-17	<code>n</code>	M-3	<code>tr</code>	M-21
<code>bv</code>	M-4	<code>fc</code>	M-8	<code>pdcs</code>	I-5	<code>tref</code>	M-19
<code>c0</code>	OP-7	<code>gm</code>	OP-6	<code>pdg</code>	I-6	<code>trise</code>	M-18
<code>cap</code>	OP-4	<code>i0</code>	O-1	<code>pin</code>	I-10	<code>type</code>	M-1
<code>cf</code>	I-3	<code>imax</code>	M-5	<code>pmr</code>	M-9	<code>vdr</code>	M-24
<code>cin</code>	M-12	<code>isub</code>	OP-3	<code>pps</code>	I-11	<code>vdrt</code>	O-3
<code>cins</code>	M-15	<code>js</code>	M-22	<code>pt</code>	M-6	<code>vr</code>	M-7
<code>cjr</code>	M-23	<code>jst</code>	O-1	<code>pwr</code>	OP-8	<code>vsub</code>	OP-2
<code>cjrt</code>	O-2	<code>kperg</code>	M-11	<code>q</code>	OP-5		

Set Node Quantities (node)

Description

Quantities are used to hold information about particular types of signals, such as their units, absolute tolerances, and maximum allowed change per Newton iteration. Use the `quantity` statement to create new quantities or to redefine properties of an existing quantity. Use this statement to set the quantities for a particular node.

For example, to indicate that the node `net1` is used for thermal signals, the following node statement could be used.

```
i17 (net1) node value=Temp flow=Pwr
```

`Temp` and `Pwr` are predefined quantities.

This device is not supported within altergroup.

Sample Instance Statement

```
node1 (1 2 3) node value="T" flow="W" strength=override //Must define T and W with  
quantity statement.
```

Instance Definition

```
Name 1 [2] ... node parameter=value ...
```

Instance Parameters

1 `value`Value quantity.

2 `flow`Flow quantity.

3 `strength=override`

Quantity strength.

Possible values are `indifferent`, `suggest`, `insist`, or `override`.

Linear N Port (nport)

Description

An N-port takes its characteristics from an S-parameter data file. An N-port can have as many ports as there are in the N-port described in the S-parameter data file. Each pair of terminals in the `nport` instance statement represents one port. Because there is no limit to the number of ports, there is no limit to the number of terminals. However, the terminals must be given in pairs and there must be at least one pair. The order of the pairs is the same as the order of the ports in the data file. Any missing ports should be skipped.

The S-parameter data file specifies the characteristics of the N-port. You can scale the frequency axis with the `scale` parameter. The frequencies in the data file are then multiplied by `scale` before the simulator uses them. The default scale factor is unity. S-parameters can be in one of the following formats: real-imag, mag-deg, mag-rad, db-deg, and db-rad.

If `interp=spline` is specified, the data is interpolated and extrapolated by using cubic splines on the data in polar form. A simple algorithm removes 2 pi jumps in the phase data. Frequency points where the data is measured must therefore be close enough to avoid an excessive number of jumps. Unfortunately, noisy phase data can cause unnecessary warning messages.

If `interp=rational` is specified, the data is interpolated and extrapolated using a rational function fit to the data. The degree of rational interpolation is automatically selected based on the values of `abserr` and `relerr`, unless `ratorder` is given, in which case `relerr` and `abserr` are ignored in selecting the order of the rational function interpolation. It is usually better to allow the simulator to automatically select the rational interpolation order.

If the S-parameter data contains noise, `abserr` and `relerr` should be set so that the fitting procedure can ignore the noise, for example, by setting `abserr` above the noise floor and/or relaxing `relerr` as necessary.

Because the fitting procedure can take a long time for complicated data, the reduced order model (ROM) file option is available to store and re-use the rational interpolation function in subsequent simulations.

It is not practical to rely on extrapolated data.

This device is not supported within `altergroup`.

Sample Instance Statement

```
x1 (a1 0 b1 0 b3 0) ndata file="sparam2.data"
```


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Sample Model Statement

```
model ndata nport file="sparam.data" scale=1
```

Instance Definition

```
Name t1 b1 [t2] [b2] ... ModelName parameter=value ...
```

```
Name t1 b1 [t2] [b2] ... nport parameter=value ...
```

Terminals must be given in pairs.

Instance Parameters

- 1 `m=1` Multiplicity factor.
- 2 `file` S-parameter data file name.
- 3 `scale=1` Frequency scale factor.
- 4 `interp=spline` Method to interpolate s-parameter data.
Possible values are `spline` or `rational`.

Spline Interpolation Parameters

- 5 `usewindow=yes` Use smooth data windowing function. The use of window improves time-domain amplitude resolution, set this parameter to `no` for better high-frequency resolution.
Possible values are `no` or `yes`.

Rational Interpolation Parameters

- 6 `relerr=0.01` Maximum relative allowed tolerance for rational interpolation errors.
Deviations of the nport model from supplied s-parameter data of relative magnitude less than `relerr` are generally ignored.
- 7 `abserr=1e-4` Maximum absolute allowed tolerance for rational interpolation errors.
Deviations of the nport model from supplied s-parameter data of absolute magnitude less than `abserr` are generally ignored.
- 8 `romdatfile` File used for storing time-domain reduced order model (ROM).
- 9 `ratorder` Order of rational function to use in fitting the s-parameter data. If this argument is given, `relerr` and `abserr` are ignored in selecting the order of

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the rational function interpolation. If `ratorder` is not specified then the program will attempt to select an order of rational interpolation that satisfies the criteria implied by `abserr` and `relerr`.

Noise Parameters

10 `trise (C)` Temperature rise from ambient.

11 `thermalnoise=yes` Thermal noise.
Possible values are `no` or `yes`.

Model Definition

`model modelName nport parameter=value ...`

Model Parameters

1 `fileS` S-parameter data file name.

2 `scale=1` Frequency scale factor.

3 `usewindow=yes` Use smooth data windowing function. The use of window improves time-domain amplitude resolution, set this parameter to `no` for better high-frequency resolution.
Possible values are `no` or `yes`.

4 `trise=0 C` Default temperature rise from ambient.

5 `thermalnoise=yes` Thermal noise.
Possible values are `no` or `yes`.

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the

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description for that parameter. For example, a reference of M-35 means the 35th model parameter.

abserr	I-7	m	I-1	scale	I-3	trise	I-10
file	I-2	ratorder	I-9	scale	M-2	trise	M-4
file	M-1	relerr	I-6	thermalnoise	I-11	usewindow	I-5
interp	I-4	romdatfile	I-8	thermalnoise	M-5	usewindow	M-3

Parameter Value Tester (paramtest)

Description

The parameter value tester tests the values of its parameters and prints a message if they satisfy the testers criteria. The tester therefore allows you to check the ranges of subcircuit parameters. If you specify more than one test, the message is printed if any test passes. The message is also printed if no tests are specified.

This device is not supported within altergroup.

Sample Instance Statement

```
tooShort paramtest errorif=(l < 0.2um) message="W of device is less than 0.2um"
```

Instance Definition

```
Name paramtest parameter=value ...
```

Instance Parameters

- 1 `printf`Message is printed if this value is nonzero.
- 2 `warnif`Message is printed as a warning if this value is nonzero.
- 3 `errorif`Message is printed as an error and program quits if this value is nonzero.
- 4 `message`Text of message.
- 5 `severity=status`Message severity (use if printing message without test).
Possible values are debug, status, warning, error, or fatal.

Polynomial Current Controlled Current Source (pcccs)

Description

A vector of coefficients specifies the polynomial function that defines the relationship between the output current and the controlling currents. You must specify at least one coefficient.

This device is not supported within altergroup.

For a polynomial in M variables a_1, a_2, \dots, a_m , the polynomial function $F(a_0, a_1, \dots, a_m)$ is given by

$$\begin{aligned} F = & c_0 + c_1 * a_1 + c_2 * a_2 + \dots \\ & + c_{(m+1)} * a_1^2 + c_{(m+2)} * a_1 * a_2 + \dots \\ & + c_{(2m+1)} * a_2^2 + c_{(2m+2)} * a_2 * a_3 + \dots \end{aligned}$$

where the c_s are coefficients of the polynomial terms.

Sample Instance Statement

```
vpc (net1 0) pcccs probes=[vb vc ve vlp vpn] coeffs=[0 8.8e6 -8.8e6 9e6 8e6 -9e6]
```

Instance Definition

```
Name sink src pcccs parameter=value ...
```

Instance Parameters

- 1 `coeffs=[...]` Polynomial coefficients. At least one must be given.
- 2 `probes=[...]` Devices through which the controlling currents flow.
- 3 `ports=[...]` Indices of the probe ports through which the controlling currents flow.
- 4 `gain=1` Gain Parameter.
- 5 `m=1` Multiplicity factor.

Operating-Point Parameters

- 1 `i (A)` Output current.

2 `v` (V) Output voltage.

3 `pwr` (W) Power dissipation.

Parameter Index

In the following index, `I` refers to instance parameters, `M` refers to the model parameters section, `O` refers to the output parameters section, and `OP` refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of `M-35` means the 35th model parameter.

<code>coeffs</code>	<code>I-1</code>	<code>i</code>	<code>OP-1</code>	<code>ports</code>	<code>I-3</code>	<code>pwr</code>	<code>OP-3</code>
<code>gain</code>	<code>I-4</code>	<code>m</code>	<code>I-5</code>	<code>probes</code>	<code>I-2</code>	<code>v</code>	<code>OP-2</code>

Polynomial Current Controlled Voltage Source (pccvs)

Description

The polynomial function defining the relationship between the output voltage and the controlling currents is specified by a vector of coefficients. At least one coefficient must always be specified. Current through the voltage source is calculated and is defined to be positive if it flows from the positive terminal, through the source, to the negative terminal.

This device is not supported within altergroup.

For a polynomial in M variables a_1, a_2, \dots, a_m , the polynomial function $F(a_0, a_1, \dots, a_m)$ is given by

$$\begin{aligned}
 F = & c_0 + c_1 * a_1 + c_2 * a_2 + \dots \\
 & + c_{(m+1)} * a_1^2 + c_{(m+2)} * a_1 * a_2 + \dots \\
 & + c_{(2m+1)} * a_2^2 + c_{(2m+2)} * a_2 * a_3 + \dots
 \end{aligned}$$

where the c s are coefficients of the polynomial terms.

Sample Instance Statement

```
ixy (net1 0) pccvs coeffs=[0 1 0 1] probes=[vin1 vin2] gain=2
```

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Instance Definition

Name p n pccvs parameter=value ...

Instance Parameters

- 1 `coeffs`=[...] Polynomial coefficients. At least one must be given.
- 2 `probes`=[...] Devices through which the controlling currents flow.
- 3 `ports`=[...] Indices of the probe ports through which the controlling currents flow.
- 4 `gain`=1 Gain Parameter.
- 5 `m`=1 Multiplicity factor.

Operating-Point Parameters

- 1 `i` (A) Output current.
- 2 `v` (V) Output voltage.
- 3 `pwr` (W) Power dissipation.

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

<code>coeffs</code>	I-1	<code>i</code>	OP-1	<code>ports</code>	I-3	<code>pwr</code>	OP-3
<code>gain</code>	I-4	<code>m</code>	I-5	<code>probes</code>	I-2	<code>v</code>	OP-2

Physical Resistor (phy_res)

Description

A physical resistor consists of a linear resistor (tied between $t1$ and $t2$) and two diodes (tied between $t1-t0$ and $t2-t0$). The diodes are junction diodes. Under normal operation, the two diodes are reverse biased, but the parameter `subtype` can reverse the direction of the diodes. If you do not specify $t0$, ground is assumed. The instance parameters always override model parameters. If you do not specify the instance resistance value, it is calculated from the model parameters.

This device is supported within altergroups.

If $R(inst)$ is not given and $R(model)$ is given,

$$R(inst) = R(model).$$

Otherwise,

$$R(inst) = Rsh * (L - 2 * etchl) / (W - 2 * etch).$$

If the polynomial coefficients vector (`coeffs=[c1 c2 ...]`) is specified, the nonlinear resistance is

$$\begin{aligned} R(V) &= dV / dI \\ &= R(inst) / (1 + c1 * V + c2 * V^2 + \dots) \end{aligned}$$

where

$$V = V(t1) - V(t2)$$

Here V is the controlling voltage across the resistor. It is also the controlling voltage when the model parameter `polyarg` is set to `diff`. In this form, the physical resistor is symmetric with respect to $V(t1)$ and $V(t2)$. The branch current as a function of the applied voltage is given by

$$I(V) = (V / R(inst)) * (1 + 1/2 * c1 * V + 1/3 * c2 * V^2 + \dots)$$

where c_k is the k th entry in the coefficient vector.

If the model parameter `polyarg` is set to `sum`, then the controlling voltage is defined as

$$Vsum = (V(t1) - V(t0)) + (V(t2) - V(t0)) / 2$$

Here, $Vsum$ is the controlling voltage between the resistor and the substrate, $t0$. In this case, the device becomes asymmetric with respect to $V(t1)$ and $V(t2)$. The branch current as a function of the applied voltage for this case is given by

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$$I(V) = (V / R(\text{inst})) * (1 + c1 * V_{\text{sum}} + c2 * V_{\text{sum}}^2 + \dots)$$

The large-signal conductance is given by

$$G(V) = I/V = (1 + c1 * V_{\text{sum}} + c2 * V_{\text{sum}}^2 + \dots) / R(\text{inst})$$

Since the device is asymmetrical, the small-signal conductance is not very meaningful.

The resistance as a function of temperature is given by:

$$R(T) = R(\text{tnom}) * [1 + tc1 * (T - \text{tnom}) + tc2 * (T - \text{tnom})^2]$$

where

$$T = \text{trise}(\text{inst}) + \text{temp}$$

if $\text{trise}(\text{inst})$ is given, and

$$T = \text{trise}(\text{model}) + \text{temp}$$

otherwise.

If you do not specify the junction leakage current (i_s) and j_s is specified, the leakage current is calculated from j_s and the device dimensions.

$$i_s = j_s * 0.5 * (L - 2 * \text{etchl}) * (W - 2 * \text{etch})$$

If you specify the instance capacitance or the linear model capacitance, linear capacitors are used between $t1-t0$ and $t2-t0$. Otherwise, nonlinear junction capacitors are used and the zero-bias capacitance values are calculated from the model parameters.

If $C(\text{inst})$ is not given and $C(\text{model})$ is given,

$$C(\text{inst}) = C(\text{model}).$$

Otherwise,

$$C(\text{inst}) = 0.5 * C_j * (L - 2 * \text{etchlc}) * (W - 2 * \text{etchc}) + C_{jsw} * (W + L - 2 * \text{etchc} - 2 * \text{etchlc}).$$

If the capacitance is nonlinear, the temperature model for the junction capacitance is used. Otherwise, the following equation is used.

$$C(T) = C(\text{tnom}) * [1 + tc1c * (T - \text{tnom}) + tc2c * (T - \text{tnom})^2].$$

Sample Instance Statement

```
res1 (net9 vcc) resphy l=1e-3 w=2e-6
```


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Sample Model Statement

```
model resphy phy_res rsh=85 tc1=1.53e-3 tc2=4.67e-7 etch=0 cj=1.33e-3 cjsw=3.15e-10 tc1c=9.26e-4
```

Instance Definition

```
Name 1 2 [0] ModelName parameter=value ...
```

Instance Parameters

- 1 `r` (Ω) Resistance.
- 2 `c` (F) Linear capacitance.
- 3 `l` (m) Line length.
- 4 `w` (m) Line width.
- 5 `region=normal` Estimated operating region.
Possible values are `normal` or `breakdown`.
- 6 `tc1=0` $1/C$ Linear temperature coefficient of resistor.
- 7 `tc2=0` C^{-2} Quadratic temperature coefficient of resistor.
- 8 `tc1c=0` $1/C$ Linear temperature coefficient of linear capacitor.
- 9 `tc2c=0` C^{-2} Quadratic temperature coefficient of linear capacitor.
- 10 `trise` (C) Temperature rise from ambient.
- 11 `m=1` Multiplicity factor.

The `w` and `l` parameters are scaled by the option parameters `scale` and `scalem`. The values of `w` and `l` printed by Spectre are those given in the input file. These values may not have the correct units if the scaling factors are not unity. The actual effective resistor dimensions are stored in the output parameters. You can obtain these dimensions with the `info` statement. You can delete the diodes from the device by either setting `is=0` or `subtype=poly`. You can also set both `mj` and `mjsw` to zero to make the capacitance linear but still calculated from the device geometry. If `subtype=poly`, the linear capacitors will always be used irrespective of the values of `mj` and `mjsw`.

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Model Definition

`model modelName phy_res parameter=value ...`

Model Parameters

Substrate Type Parameters

- 1 `subtype=p` Substrate type.
Possible values are `n`, `p` or `poly`.

Resistance Parameters

- 2 `r= ∞ Ω` Default resistance.
- 3 `rsh= ∞ Ω /sqr`
Sheet resistance.
- 4 `minr=0.1 Ω` Minimum resistance.
- 5 `coeffs=[...]` Vector of polynomial conductance coefficients.
- 6 `polyarg=diff` Polynomial model argument type.
Possible values are `sum` or `diff`.

Temperature Effects Parameters

- 7 `tc1=0 1/C` Linear temperature coefficient of resistor.
- 8 `tc2=0 C^{-2}` Quadratic temperature coefficient of resistor.
- 9 `tc1c=0 C^{-2}` Linear temperature coefficient of linear capacitor.
- 10 `tc2c=0 C^{-2}` Quadratic temperature coefficient of linear capacitor.
- 11 `tnom (C)` Parameters measurement temperature. Default set by `options`.
- 12 `trise=0 C` Temperature rise from ambient.

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Junction Diode Model Parameters

- 13 `is` (A) Saturation current.
- 14 `js=0` A/m² Saturation current density.
- 15 `n=1` Emission coefficient.
- 16 `eg=1.11` V Band gap.
- 17 `xti=3` Saturation current temperature exponent.
- 18 `imelt='imaxA'` Explosion current, diode is linearized beyond this current to aid convergence.
- 19 `jmelts='jmeltsA/m'2`
Explosion current density, diode is linearized beyond this current to aid convergence.
- 20 `imax=1` A Maximum current, currents above this limit generate a warning.
- 21 `jmax=1e8` A/m² Maximum current density, currents above this limit generate a warning.
- 22 `dskip=yes` Use simple piece-wise linear model for diode currents below 0.1*`iabstol`.
Possible values are `no` or `yes`.
- 23 `bvj=∞` V Junction reverse breakdown voltage.

Junction Capacitance Model Parameters

- 24 `c=0` F Default linear capacitance.
- 25 `cj=0` F/m² Zero-bias junction bottom capacitance density.
- 26 `cjsw=0` F/m Zero-bias junction sidewall capacitance density.
- 27 `mj=1/2` Junction bottom grading coefficient.
- 28 `mjsw=1/3` Junction sidewall grading coefficient.
- 29 `pb=0.8` V Junction bottom built-in potential.
- 30 `pbsw=0.8` V Junction sidewall built-in potential.

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31 $f_c=0.5$ Junction bottom capacitor forward-bias threshold.

32 $f_{csw}=0.5$ Junction sidewall capacitor forward-bias threshold.

33 $t_t=0$ s Transit time.

Device Size Parameters

34 $l=\infty$ m Default line length.

35 $w=1e-6$ m Default line width.

36 $etch=0$ m Narrowing due to etching.

37 $etchl=0$ m Length reduction due to etching.

38 $etchc=etch$ m Narrowing due to etching for capacitances.

39 $etchlc=etchl$ m Length reduction due to etching for capacitances.

40 $scaler=1$ Resistance scaling factor.

41 $scalec=1$ Capacitance scaling factor.

Noise Model Parameters

42 $k_f=0$ Flicker (1/f) noise coefficient.

43 $a_f=1$ Flicker (1/f) noise exponent.

44 $w_{dexp}=1$ Flicker (1/f) noise W exponent.

45 $l_{dexp}=1$ Flicker (1/f) noise L exponent.

46 $w_{eexp}=0$ Flicker (1/f) noise W effective exponent.

47 $l_{eexp}=0$ Flicker (1/f) noise L effective exponent.

48 $f_{exp}=1$ Flicker (1/f) noise frequency exponent.

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Output Parameters

- 1 `leff` (m) Effective line length.
- 2 `weff` (m) Effective line width.
- 3 `iseff` (A) Effective saturation current.
- 4 `reff` (Ω) Effective resistance.
- 5 `ceff` (F) Effective zero-bias capacitance.

Operating-Point Parameters

- 1 `subtype=p` Substrate type.
Possible values are `n`, `p` or `poly`.
- 2 `region=normal` Estimated operating region.
Possible values are `normal` or `breakdown`.
- 3 `i` (A) Current through the resistor.
- 4 `capd1` (F) Capacitance at the positive node.
- 5 `capd2` (F) Capacitance at the negative node.
- 6 `id1` (A) Current between nodes `t1` and `t0`.
- 7 `id2` (A) Current between nodes `t2` and `t0`.
- 8 `res` (Ω) Resistance between nodes `t1` and `t2`.
- 9 `resd1` (Ω) Resistance between nodes `t1` and `t0`.
- 10 `resd2` (Ω) Resistance between nodes `t2` and `t0`.
- 11 `pwr` (W) Power at op point.

Parameter Index

In the following index, `I` refers to instance parameters, `M` refers to the model parameters section, `O` refers to the output parameters section, and `OP` refers to the operating point

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parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of M-35 means the 35th model parameter.

af	M-43	i	OP-3	mjsw	M-28	tc1	I-6
bvj	M-23	idl	OP-6	n	M-15	tc1	M-7
c	I-2	id2	OP-7	pb	M-29	tc1c	I-8
c	M-24	imax	M-20	pbsw	M-30	tc1c	M-9
capd1	OP-4	imelt	M-18	polyarg	M-6	tc2	I-7
capd2	OP-5	is	M-13	pwr	OP-11	tc2	M-8
ceff	O-5	iseff	O-3	r	I-1	tc2c	I-9
cj	M-25	jmax	M-21	r	M-2	tc2c	M-10
cjsw	M-26	jmelt	M-19	reff	O-4	tnom	M-11
coeffs	M-5	js	M-14	region	I-5	trise	I-10
dskip	M-22	kf	M-42	region	OP-2	trise	M-12
eg	M-16	l	M-34	res	OP-8	tt	M-33
etch	M-36	l	I-3	resd1	OP-9	w	I-4
etchc	M-38	ldexp	M-45	resd2	OP-10	w	M-35
etchl	M-37	leexp	M-47	rsh	M-3	wdexp	M-44
etchlc	M-39	leff	O-1	scalec	M-41	weexp	M-46
fc	M-31	m	I-11	scaler	M-40	weff	O-2
fcsw	M-32	minr	M-4	subtype	M-1	xti	M-17
fexp	M-48	mj	M-27	subtype	OP-1		

Independent Resistive Source (port)

Description

A port is a resistive source that is tied between `pos` and `neg`. It is equivalent to a voltage source in series with a resistor, and the reference resistance of the port is the value of the resistor. The DC value given for the port voltage specifies the DC voltage across the port when it is terminated in its reference resistance (in other words, the DC voltage of the internal voltage source is double the user specified DC value, `dc`). The same is true for the values for the transient, AC and PAC signals of the port. However, the amplitude of the sine wave in the transient and PAC analyses can alternatively be specified as the power in dBm delivered by the port when terminated with the reference resistance.

While generally useful as a stimulus in high frequency circuits, the port has three unique capabilities. First, it acts to define the ports of the circuit to the S-parameter analysis. Second, it has an intrinsic noise source, and so allows the noise analysis to directly compute the noise figure of the circuit. And finally, it is the only source for which the amplitude can be specified in terms of power.

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This device is not supported within altergroup.

The value of the DC voltage as a function of the temperature is given by:

$$V(T) = V * [1 + tc1 * (T - tnom) + tc2 * (T - tnom)^2]$$

Sample Instance Statement

```
p20 (2 0) port num=2 r=50 type=pulse period=1e-9 rise=1e-10 fall=1e-10 val1=1
width=0.5n mag=1
```

Instance Definition

Name p n port parameter=value ...

Instance Parameters

1 dc=0 vDC value.

General Waveform parameters

2 type=dcWaveform type.

Possible values are dc, pulse, pwl, sine, or exp.

3 fundnameName of the fundamental frequency. Must be specified if the source is active during a pdisto analysis or it is the active clock during an envlp analysis.

4 delay=0 sWaveform delay time.

Pulse Waveform Parameters

5 val0=0 vZero value used in pulse and exponential waveforms.

6 val1=1 vOne value used in pulse and exponential waveforms.

7 period=∞ sPeriod of waveform.

8 rise (s)Rise time for pulse waveform (time for transition from val0 to val1).

9 fall (s)Fall time for pulse waveform (time for transition from val1 to val0).

10 width=∞ sPulse width (duration of val1).

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PWL Waveform Parameters

- 11 `fileName` of file containing waveform.
- 12 `wave=[...]` Vector of time/value pairs that defines waveform.
- 13 `offset=0` VDC offset for the PWL waveform.
- 14 `scale=1` Scale factor for the PWL waveform.
- 15 `stretch=1` Scale factor for time given for the PWL waveform.
- 16 `allbrkpts` All the points in the PWL waveform are breakpoints if set to yes. Default is yes if the number of points is less than 20.
Possible values are `no` or `yes`.
- 17 `pwlperiod (s)` Period of the periodic PWL waveform.
- 18 `twidth=pwlperiod/1000 s`
Transition width used when making PWL waveforms periodic.

Sinusoidal Waveform Parameters

- 19 `sinedc=dc` VDC level for sinusoidal waveforms.
- 20 `ampl=1` VPeak amplitude of sinusoidal waveform.
- 21 `dbm (dBm)` Amplitude of sinusoidal waveform in dBm (alternative to `ampl`).
- 22 `freq=0` HzFrequency of sinusoidal waveform.
- 23 `sinephase=0` °Phase of sinusoid when `t=delay`.
- 24 `ampl2=1` VPeak amplitude of second sinusoidal waveform.
- 25 `dbm2 (dBm)` Amplitude of second sinusoidal waveform in dBm (alternative to `ampl2`).
- 26 `freq2=0` HzFrequency of second sinusoidal waveform.
- 27 `sinephase2=0` °Phase of second sinusoid when `t=delay`.
- 28 `fundname2` Name of the fundamental frequency associated with `freq2`. Must be specified if `freq2` is used in a `pdisto` analysis.

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- 29 `fmodindex=0` FM index of modulation for sinusoidal waveform.
- 30 `fmodfreq=0` Hz FM modulation frequency for sinusoidal waveform.
- 31 `ammodindex=0` AM index of modulation for sinusoidal waveform.
- 32 `ammodfreq=0` Hz AM modulation frequency for sinusoidal waveform.
- 33 `ammodphase=0` ° AM phase of modulation for sinusoidal waveform.
- 34 `damp=0` 1/s Damping factor for sinusoidal waveform.

Exponential Waveform Parameters

- 35 `td1=0` s Rise start time for exponential wave.
- 36 `tau1 (s)` Rise time constant for exponential wave.
- 37 `td2 (s)` Fall start time for exponential wave.
- 38 `tau2 (s)` Fall time constant for exponential wave.

Noise Parameters

- 39 `noisefile` Name of file containing excess spot noise data in the form of frequency-noise pairs.
- 40 `noisevec=[...]` V^2/Hz Excess spot noise as a function of frequency in the form of frequency-noise pairs.
- 41 `noisetemp (C)` Noise temperature of port. If not specified, the noise temperature is taken to be the actual temperature of the port.

Port Parameters

- 42 `r=50` Ω Reference resistance.
- 43 `num` Port number.
- 44 `m=1` Multiplicity factor.

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Component Statements Part 2

Small signal parameters

- 45 `mag=0` `V`Small signal voltage.
- 46 `phase=0` `°`Small signal phase.
- 47 `xfmag=1` `V/V`Transfer function analysis magnitude.
- 48 `pacmag=0` `V`Periodic AC analysis magnitude.
- 49 `pacdbm` (`dBm`) Periodic AC analysis magnitude in `dBm` (alternative to `pacmag`).
- 50 `pacphase=0` `°`Periodic AC analysis phase.

Temperature Effects Parameters

- 51 `tc1=0` `1/C`First order temperature coefficient.
- 52 `tc2=0` `C-2`Second order temperature coefficient.
- 53 `tnom` (`C`) Parameters measurement temperature. Default set by options.

If you do not specify the DC value, it is assumed to be the `time=0` value of the waveform.

In DC analyses, the only active parameters are `dc`, `m`, and the temperature coefficient parameters. In AC analyses, the only active parameters are `m`, `mag` and `phase`. In transient analyses, all parameters are active except the small signal parameters and the noise parameters. The `type` parameter selects which type of waveform is generated. You may specify parameters for more than one waveform type, and use the `alter` statement to change the waveform type between analyses.

A vector of time-value pairs describes the piecewise linear waveform. As an alternative, you can read the waveform from a file. In this case, you give time-value pairs one pair per line with a space or tab between the time and the value.

If you set `allbrkpts` to `yes`, you force the simulator to place time points at each point specified in a PWL waveform during a transient analysis. This can be very expensive for waveforms with many points. If you set `allbrkpts` to `no`, Spectre inspects the waveform, looking for abrupt changes, and forces time points only at those changes.

The PWL waveform is periodic if you specify `pwlperiod`. If the value of the waveform specified is not exactly the same at both its beginning its end, then you must provide a nonzero value `twidth`. Before repeating, the waveform changes linearly in an interval of

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Component Statements Part 2

`twidth` from its value at (`period - twidth`) to its value at the beginning of the waveform. Thus `twidth` must always be less than `period`.

You can give the excess noise of the source as a file or specify it with a vector of frequency-noise pairs. For a file, give the frequency-noise pairs one pair per line with a space or tab between the frequency and noise values.

When computing the noise figure of a circuit driven at its input by a port, the noise temperature (`noisetemp`) of the port should be set to 16.85C (290K) in order to match the standard IEEE definition of noise figure. In addition, all other sources of noise in the port (`noisefile` and `noisevec`) should be disabled. If a noiseless port is desired, set the noise temperature to absolute zero or below, and do not specify a noise file or noise vector.

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

<code>allbrkpts</code>	I-16	<code>fmmodindex</code>	I-29	<code>pacphase</code>	I-50	<code>tc2</code>	I-52
<code>ammodfreq</code>	I-32	<code>freq</code>	I-22	<code>period</code>	I-7	<code>td1</code>	I-35
<code>ammodindex</code>	I-31	<code>freq2</code>	I-26	<code>phase</code>	I-46	<code>td2</code>	I-37
<code>ammodphase</code>	I-33	<code>fundname</code>	I-3	<code>pwlperiod</code>	I-17	<code>tnom</code>	I-53
<code>ampl</code>	I-20	<code>fundname2</code>	I-28	<code>r</code>	I-42	<code>twidth</code>	I-18
<code>ampl2</code>	I-24	<code>m</code>	I-44	<code>rise</code>	I-8	<code>type</code>	I-2
<code>damp</code>	I-34	<code>mag</code>	I-45	<code>scale</code>	I-14	<code>val0</code>	I-5
<code>dbm</code>	I-21	<code>noisefile</code>	I-39	<code>sinedc</code>	I-19	<code>val1</code>	I-6
<code>dbm2</code>	I-25	<code>noisetemp</code>	I-41	<code>sinephase</code>	I-23	<code>wave</code>	I-12
<code>dc</code>	I-1	<code>noisevec</code>	I-40	<code>sinephase2</code>	I-27	<code>width</code>	I-10
<code>delay</code>	I-4	<code>num</code>	I-43	<code>stretch</code>	I-15	<code>xfmag</code>	I-47
<code>fall</code>	I-9	<code>offset</code>	I-13	<code>taul</code>	I-36		
<code>file</code>	I-11	<code>pacdbm</code>	I-49	<code>tau2</code>	I-38		
<code>fmmodfreq</code>	I-30	<code>pacmag</code>	I-48	<code>tc1</code>	I-51		

Polynomial Voltage Controlled Current Source (pvccs)

Description

A vector of coefficients specifies the polynomial function that defines the relationship between the output current and the controlling voltages. You must specify at least one coefficient. Current exits the source node and enters the sink node.

This device is not supported within altergroup.

For a polynomial in M variables a_1, a_2, \dots, a_m , the polynomial function $F(a_0, a_1, \dots, a_m)$ is given by

$$\begin{aligned} F = & c_0 + c_1 * a_1 + c_2 * a_2 + \dots \\ & + c_{(m+1)} * a_1^2 + c_{(m+2)} * a_1 * a_2 + \dots \\ & + c_{(2m+1)} * a_2^2 + c_{(2m+2)} * a_2 * a_3 + \dots \end{aligned}$$

where the c s are coefficients of the polynomial terms.

Sample Instance Statement

```
v2 (net1 0 net2 0) pvccs coeffs=[0 -2e-3 - 10e-3] gain=2 m=1
```

Instance Definition

```
Name sink src ps1 ns1 ... pvccs parameter=value ...
```

Instance Parameters

- 1 `coeffs=[...]` Polynomial coefficients. At least one must be given.
- 2 `gain=1` Gain Parameter.
- 3 `m=1` Multiplicity factor.

Operating-Point Parameters

- 1 `i (A)` Output current.
- 2 `v (V)` Output voltage.

3 `pwr` (W) Power dissipation.

Polynomial Voltage Controlled Voltage Source (pvcvs)

Description

A vector of coefficients specifies the polynomial function that defines the relationship between the output voltage and the controlling voltages. You must specify at least one coefficient. Current through the voltage source is calculated and is defined to be positive if it flows from the positive terminal, through the source, to the negative terminal.

This device is not supported within altergroup.

For a polynomial in M variables a_1, a_2, \dots, a_m , the polynomial function $F(a_0, a_1, \dots, a_m)$ is given by

$$\begin{aligned} F = & c_0 + c_1 * a_1 + c_2 * a_2 + \dots \\ & + c_{(m+1)} * a_1^2 + c_{(m+2)} * a_1 * a_2 + \dots \\ & + c_{(2m+1)} * a_2^2 + c_{(2m+2)} * a_2 * a_3 + \dots \end{aligned}$$

where the c s are coefficients of the polynomial terms.

Sample Instance Statement

```
v1 (p 0 c1 0) pvcvs coeffs=[0 0 0 0.1 1 1] gain=1
```

Instance Definition

```
Name p n ps1 ns1 ... pvcvs parameter=value ...
```

Instance Parameters

- 1 `coeffs=[...]` Polynomial coefficients. At least one must be given.
- 2 `gain=1` Gain Parameter.
- 3 `m=1` Multiplicity factor.

Operating-Point Parameters

- 1 `i` (A) Output current.
- 2 `v` (V) Output voltage.
- 3 `pwr` (W) Power dissipation.

Quantity Information (quantity)

Description

Quantities are used to hold information about particular types of signals, such as their units, absolute tolerances, and maximum allowed change per Newton iteration. Two predefined quantities are voltage and current. A node indicates the type of its signals by keeping pointers to quantities, thus an electrical node points to the voltage quantity for its value, and to the current for its residue. Since many electrical nodes point to the same quantities, changing an attribute on the quantity, such as the absolute tolerance, changes it for many nodes. Use this statement to create new quantities or to redefine properties of an existing quantity. Use the `node` statement to set the quantities for a particular node.

The predefined quantities are as follows:

I: Electrical current in Amperes.

Units = A

Absolute tolerance = 1 pA

Huge value = 4..036 MA

Blowup value = 1 GA

MMF: Magnetomotive force in Ampere-Turns.

Units = A*turn

Absolute tolerance = 1 pA*turn

Huge value = 4..036 MA*turn

Blowup value = 1 GA*turn

Pwr: Power in Watts.

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Component Statements Part 2

Units = W

Absolute tolerance = 1 nW

Huge value = 4..036 MW

Blowup value = 1 GW

Temp: Temperatures in Celsius.

Units = C

Absolute tolerance = 100 uC

Huge value = 4..036 MC

Blowup value = 1 GC

U: Unitless signals scaled to unity.

Absolute tolerance = 1e-06

Huge value = 4..036e+06

Blowup value = 1e+09

V: Electromotive force in Volts.

Units = V

Absolute tolerance = 1 uV

Maximum change = 300 mV if limit=delta

Huge value = 1 kV

Blowup value = 1 GV

Wb: Magnetic flux in Webers.

Units = Wb

Absolute tolerance = 1 nWb

Huge value = 4..036 MWb

Blowup value = 1 GWb

This device is not supported within altergroup.

Spectre Circuit Simulator Reference

Component Statements Part 2

Sample Instance Statement

```
voltageQ quantity name="V" abstol=3u maxdelta=500m huge=10K blowup=1G
```

Instance Definition

```
Name quantity parameter=value ...
```

Instance Parameters

- 1 nameName.
- 2 descriptionDescription of quantity.
- 3 unitsUnits.

Newton Parameters

- 4 abstolAbsolute tolerance.
- 5 maxdelta= ∞
Maximum change allowed on a Newton iteration when
limit=delta.
- 6 huge=4..036e+06Maximum change allowed on a Newton iteration otherwise.
- 7 blowup=1e+09If a signal exceeds this value, the simulation will terminate with an error.
It is assumed that the circuit is unstable and is blowing up.

Diffusion Resistor Model (rdiff)

Description

The rdiff model is a diffusion resistor model, which accurately models the temperature, applied bias and back-bias dependencies of NWell, N+, and P+ resistors. It is described in the paper MODEL FOR DIFFUSION RESISTORS (NWell, N+, P+) USED IN CMOS IC DESIGNS by M.J.B.Bolt, FASELEC Process Development Group, PDG-93029, Modified 3rd May 1995.

Some extensions to that description are applied:

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Component Statements Part 2

Appropriate model and instance parameter default values are used.

No clipping of parameters is performed. Parameter values are checked for validity. If invalid parameter values occur, the job is aborted with an error message.

For exact inverse behavior of the model in case of V_h less than V_l , the setting of $V_{bl}=abs(V_b-V_l)$ is replaced by $V_{bl}=\min(abs(V_b-V_h),abs(V_b-V_l))$. Additionally, the direction of I_h is inverted in this case.

Note: In noise analysis, `rdiff` instances will not generate any contribution, since there are no noise sources included in the `rdiff` model.

(c) Philips Electronics N.V. 1993, 1995

This device is supported within altergroups.

This device is dynamically loaded from the shared object

`/vobs/spectre_dev/tools.sun4v/spectre/lib/cmi/2.0.doc/libphilips.so`

Sample Instance Statement

```
r2 (1 2 0) rdsn l=9u w=2u nb=0 m=1
```

Sample Model Statement

```
model rdsn rdiff level=1 tr=27 dta=0 rshr=2.5e3 wtol=0.22u rint=3.5u swvp=13.4u  
power=2 tcrl=1.5e-3 tcr2=1e-5 vpr=40
```

Instance Definition

```
Name h l [b] ModelName parameter=value ...
```

Instance Parameters

- 1 `l=1.0 scale mDrawn` length of resistor. Must be greater than zero. Scale set by option `scale`.
- 2 `w=1.0 scale mDrawn` width of resistor. Must be greater than zero. Scale set by option `scale`.
- 3 `nb=0.0` Number of bends in the resistor. Must be greater or equal zero.
- 4 `m=1.0` Multiplicity factor. Must be greater than zero.

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Component Statements Part 2

Model Definition

model modelName rdiff parameter=value ...

Model Parameters

- 1 level=1.0 Level of this model. Must be 1.
- 2 tr (C) Reference temperature. Default set by option tnom.
- 3 tref (C) Alias of tr. Default set by option tnom.
- 4 tnom (C) Alias of tr. Default set by option tnom.
- 5 dta=0 K Temperature offset of the device.
- 6 trise=0 K Alias of dta.
- 7 rshr=1.0e+3 Ω/sqr
Sheet resistance at reference temperature. Must be greater than zero.
- 8 wtol=0.0 m Offset between the drawn and effective resistor width.
- 9 tcr1=0.0 1/K Linear temperature coefficient of the resistor.
- 10 tcr2=0.0 1/K² Quadratic temperature coefficient of the resistor.
- 11 vpr=100.0 V Reference Pinch-off voltage.
- 12 swvp=0.0 V/m Coefficient of the width dependence of vp.
- 13 power=1.5 Voltage exponent. Must be greater than zero.
- 14 vdr=1.0 V Diffusion voltage at reference temperature.
- 15 rint=0.0 Ω m Interface resistance at reference temperature.
- 16 tcrint1=0.0 1/K Linear temperature coefficient of the interface resistor.

Output Parameters

- 1 vd (V) Diffusion voltage. Must be greater than zero.

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Component Statements Part 2

- 2 `rsh` (Ω/sqr) Sheet resistance. Must be greater than zero.
- 3 `vp` (V) Pinch-off voltage. Must be greater than zero.
- 4 `r0` (Ω) Zero bias resistance. Must be greater than zero.

Operating-Point Parameters

- 1 `vhl` (V) Absolute value of the applied bias across the resistor.
- 2 `vbl` (V) Absolute value of the back-bias across the resistor.
- 3 `ih` (A) DC current into the resistor.
- 4 `r` (Ω) Actual resistance value.
- 5 `pwr` (W) Power.

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

<code>dta</code> M-5	<code>r</code> OP-4	<code>tcrint1</code> M-16	<code>vhl</code> OP-1
<code>ih</code> OP-3	<code>r0</code> O-4	<code>tnom</code> M-4	<code>vp</code> O-3
<code>l</code> I-1	<code>rint</code> M-15	<code>tr</code> M-2	<code>vpr</code> M-11
<code>level</code> M-1	<code>rsh</code> O-2	<code>tref</code> M-3	<code>w</code> I-2
<code>m</code> I-4	<code>rshr</code> M-7	<code>trise</code> M-6	<code>wtol</code> M-8
<code>nb</code> I-3	<code>swvp</code> M-12	<code>vbl</code> OP-2	
<code>power</code> M-13	<code>tcr1</code> M-9	<code>vd</code> O-1	
<code>pwr</code> OP-5	<code>tcr2</code> M-10	<code>vdr</code> M-14	

Four Terminal Relay (relay)

Description

The four-terminal relay is a voltage controlled relay tied between terminals `t1` and `t2`. The voltage between terminals `ps` and `ns` controls the relay resistance. The relay resistance varies nonlinearly between `ropen` and `rclosed`, the open relay resistance and closed relay resistance, respectively. These resistance values correspond to control voltages of `vt1` and `vt2` respectively. The four parameters, `vt1`, `vt2`, `ropen`, and `rclosed`, can be instance or model parameters.

As an alternative, you can specify the threshold voltage `vth` and a transition width `trans` rather than specifying `vt1` and `vt2`. These two parameters are then calculated from `vth` and `trans`. If all four parameters are specified, `vth` and `trans` override `vt1` and `vt2`. However, `vt1` and `vt2` values you specify on the instance override any model parameter specifications.

The final model parameter, `hysteresis`, designates a hysteresis with the on voltage shifted from `vth` by an amount `hysteresis` and the off voltage shifted by the same amount in the opposite direction. The direction of shift depends on the sign of `trans` (or the relative magnitudes of `vt1` and `vt2`): if `trans` is positive, the on voltage shifts by `+hysteresis`; if `trans` is negative (implying that the relay is normally on), the on-voltage shifts by `-hysteresis`.

This device is not supported within `altergroup`.

Operating conductance is calculated from the instance parameters as follows:

When `Vc` lies between `vt1` and `vt2`,

$$G = G_{min} + (G_{min} - G_{max}) * [2 * (Vc - vt1)^3 - 3 * (vt2 - vt1) * (Vc - vt1)^2] / (vt2 - vt1)^3$$

Otherwise, if `vt1 < vt2`, then

$$G = G_{min} \quad \text{for } Vc < vt1 \text{ and}$$

$$G = G_{max} \quad \text{for } Vc > vt2.$$

If `vt1 > vt2`,

$$G = G_{min} \quad \text{for } Vc > vt1 \text{ and}$$

$$G = G_{max} \quad \text{for } Vc < vt2.$$

where $G_{min} = 1 / ropen$, $G_{max} = 1 / rclosed$, and $Vc = V(ps) - V(ns)$.

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Component Statements Part 2

Sample Instance Statement

```
rel1 (1 2 ps ns) my_relay ropen=1G rclosed=2
```

Sample Model Statement

```
model my_relay relay vt1=2.5 vt2=5 ropen=100M rclosed=0.1
```

Instance Definition

```
Name 1 2 ps ns modelName parameter=value ...
```

```
Name 1 2 ps ns relay parameter=value ...
```

Instance Parameters

- 1 vt1 (V) Relay resistance is ropen at this voltage.
- 2 vt2=vt1+1.0 V Relay resistance is rclosed at this voltage.
- 3 ropen= ∞ Ω
Resistance of a fully open relay.
- 4 rclosed=1.0 Ω Resistance of a fully closed relay.
- 5 m=1.0 Multiplicity factor.
- 6 region=off Estimated operating region.
Possible values are off or on.

Model Definition

```
model modelName relay parameter=value ...
```

Model Parameters

- 1 vt1 (V) Relay resistance is ropen at this voltage.
- 2 vt2=vt1+1.0 V Relay resistance is rclosed at this voltage.
- 3 ropen= ∞ Ω
Resistance of a fully open relay.
- 4 rclosed=1.0 Ω Resistance of a fully closed relay.

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Component Statements Part 2

5 `hysteresis=0.0` `vSwitching` Hysteresis.

6 `vth=0.0` `vThreshold` Voltage.

7 `trans=0.0` `vSwitch` Transition Region Width.

Operating-Point Parameters

1 `region=off` Estimated operating region.
Possible values are `off` or `on`.

2 `res` (Ω) Relay resistance.

Parameter Index

In the following index, `I` refers to instance parameters, `M` refers to the model parameters section, `O` refers to the output parameters section, and `OP` refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of `M-35` means the 35th model parameter.

<code>hysteresis</code>	<code>M-5</code>	<code>region</code>	<code>I-6</code>	<code>ropen</code>	<code>M-3</code>	<code>vt2</code>	<code>I-2</code>
<code>m</code>	<code>I-5</code>	<code>region</code>	<code>OP-1</code>	<code>trans</code>	<code>M-7</code>	<code>vt2</code>	<code>M-2</code>
<code>rclosed</code>	<code>I-4</code>	<code>res</code>	<code>OP-2</code>	<code>vt1</code>	<code>M-1</code>	<code>vth</code>	<code>M-6</code>
<code>rclosed</code>	<code>M-4</code>	<code>ropen</code>	<code>I-3</code>	<code>vt1</code>	<code>I-1</code>		

Two Terminal Resistor (resistor)

Description

You can give the resistance explicitly or allow it to be computed from the physical length and width of the resistor. In either case, the resistance can be a function of temperature or applied voltage.

This device is supported within `altergroups`.

If `R(inst)` is not given,

$$R(\text{inst}) = R(\text{model})$$

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Component Statements Part 2

if $R(\text{model})$ is given, and

$$R(\text{inst}) = R_{\text{sh}} * (L - 2 * \text{etchl}) / (W - 2 * \text{etch})$$

otherwise.

If the polynomial coefficients vector ($\text{coeffs}=[c1 \ c2 \ \dots]$) is specified, the resistor is nonlinear. When `nonlinform` is set to `g`, the resistance is

$$\begin{aligned} R(V) &= dV / dI \\ &= R(\text{inst}) / (1 + c1 * V + c2 * V^2 + \dots). \end{aligned}$$

The branch current as a function of applied voltage is

$$I(V) = (V / R(\text{inst})) * (1 + 1/2 * c1 * V + 1/3 * c2 * V^2 + \dots)$$

When `nonlinform` is set to `r`, the resistance is

$$\begin{aligned} R(V) &= dV / dI \\ &= R(\text{inst}) * (1 + c1 * V + c2 * V^2 + \dots). \end{aligned}$$

where c_k is the k th entry in the coefficient vector.

The value of the resistor as a function of the temperature is given by:

$$R(T) = R(\text{tnom}) * [1 + \text{tc1} * (T - \text{tnom}) + \text{tc2} * (T - \text{tnom})^2]$$

where

$$T = \text{trise}(\text{inst}) + \text{temp}$$

if $\text{trise}(\text{inst})$ is given, and

$$T = \text{trise}(\text{model}) + \text{temp}$$

otherwise.

Sample Instance Statement

Without model:

```
r1 (1 2) resistor r=1.2K m=2
```

With model:

```
r1 (1 2) resmod l=8u w=1u
```

Spectre Circuit Simulator Reference

Component Statements Part 2

Sample Model Statement

```
model resmod resistor rsh=150 l=2u w=2u etch=0.05u tc1=0.1 tnom=27 kf=1
```

Instance Definition

```
Name 1 2 ModelName parameter=value ...
```

```
Name 1 2 resistor parameter=value ...
```

Instance Parameters

- 1 `r` (Ω) Resistance.
- 2 `l` (m) Resistor length.
- 3 `w` (m) Resistor width.
- 4 `m=1` Multiplicity factor.
- 5 `scale=1` Scale factor.
- 6 `resform` Use the resistance form for this instance. Default is `yes` if `r < thresh`.
Possible values are `no` or `yes`.
- 7 `tc1=0` $1/C$ Linear temperature coefficient.
- 8 `tc2=0` C^{-2} Quadratic temperature coefficient.
- 9 `trise` (C) Temperature rise from ambient.
- 10 `isnoisy=yes` Should resistor generate noise.
Possible values are `no` or `yes`.

The instance parameter `scale`, if specified, overrides the value given by the `option` parameter `scale`. The `w` and `l` parameters are scaled by the resulting `scale`, and the `option` parameter `scalem`. The values of `w` and `l` printed out by `spectre` are those given in the input, and these values might not have the correct units if the scaling factors are not unity. The actual effective resistor dimensions are stored in the output parameters. You can obtain these dimensions by using the `info` statement.

Model Definition

```
model modelName resistor parameter=value ...
```


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Component Statements Part 2

Model Parameters

Resistance Parameters

- 1 $r=\infty \ \Omega$ Default resistance.
- 2 $rsh=\infty \ \Omega/\text{sqr}$
Sheet resistance.
- 3 $\text{thresh}=1.0\text{e-}3 \ \Omega$ Resistances smaller than this will use the resistance form, as opposed to the standard conductance form.

Resistor Size Parameters

- 4 $l=\infty \ \text{m}$ Default resistor length.
- 5 $w=1\text{e-}6 \ \text{m}$ Default resistor width.
- 6 $\text{etch}=0 \ \text{m}$ Width narrowing due to etching per side.
- 7 $\text{etchl}=0 \ \text{m}$ Length narrowing due to etching per side.
- 8 $\text{scaler}=1$ Resistance scaling factor.

Temperature Effects Parameters

- 9 $tc1=0 \ 1/^\circ\text{C}$ Linear temperature coefficient.
- 10 $tc2=0 \ ^\circ\text{C}^{-2}$ Quadratic temperature coefficient.
- 11 $t_{nom} \ (^\circ\text{C})$ Parameters measurement temperature. Default set by options.
- 12 $trise=0 \ ^\circ\text{C}$ Default temperature rise from ambient.

Nonlinearity Coefficients

- 13 $\text{coeffs}=[\dots]$ Vector of polynomial conductance coefficients.
- 14 $\text{nonlinform}=g$ The form of the nonlinear resistance.
Possible values are g or r .

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Component Statements Part 2

Noise Model Parameters

- 15 `kf=0`Flicker (1/f) noise coefficient.
- 16 `af=2`Flicker (1/f) noise exponent.
- 17 `wdexp=1`Flicker (1/f) noise W exponent.
- 18 `ldexp=1`Flicker (1/f) noise L exponent.
- 19 `weexp=0`Flicker (1/f) noise W effective exponent.
- 20 `leexp=0`Flicker (1/f) noise L effective exponent.
- 21 `fexp=1`Flicker (1/f) noise frequency exponent.

DC-Mismatch Model Parameters

- 22 `mr=0.0`Resistor mismatch dependence.
- 23 `mr1=0.0`Resistor mismatch length dependence.
- 24 `mr1p=0.0`Resistor mismatch length power dependence.
- 25 `mrw=0.0`Resistor mismatch width dependence.
- 26 `mrwp=0.0`Resistor mismatch width power dependence.
- 27 `mr1w1=0.0`Resistor mismatch area 1 dependence.
- 28 `mr1w1p=0.0`Resistor mismatch area 1 power dependence.
- 29 `mr1w2=0.0`Resistor mismatch area 2 dependence.
- 30 `mr1w2p=0.0`Resistor mismatch area 2 power dependence.

The instance parameter `resform` and the model parameter `thresh` control whether a resistor is formulated in the standard conductance form, or in the resistance form. If the value of the resistor is smaller than `thresh`, Spectre uses the resistance form; otherwise it uses the conductance form. If `resform` is set on an instance, it overrides the `thresh` parameter. The resistance form is appropriate for very small resistances and the conductance form is intended for larger resistances. Using the conductance form for very small resistances or the resistance form for very large resistances can cause convergence problems.

Spectre Circuit Simulator Reference

Component Statements Part 2

With the resistance form, the resistance can be zero; with the conductance form, the resistance can be infinite. The resistance form is less efficient than the conductance form. You cannot change the formulation of a resistor once it has been determined. Spectre makes this choice by comparing the initial value of the resistance to `thresh`.

Modeling AC Resistance

In certain situations, a part of a circuit that is required to calculate the DC operating point needs to be removed during a subsequent AC analysis or visa versa. An example of a situation in which this occurs is when measuring the loop gain of a feedback amplifier. In this case the feedback loop must be removed when computing the AC response of the amplifier. In Spectre, the most accurate method of doing this is to use an ideal switch component (see `spectre -h switch`), e.g.

```
Vin (pin 0) vsource mag=1
    OA1 (pin nin out) opamp
    Sw1 (nin out 0) switch position=1 ac_position=2
    LoopGain ac start=1 stop=1MHz
```

Another possibility is that the resistance of an instance changes from one analysis to another. The following subcircuit models a resistance whose value is given by the parameter `rac` during AC analyses, and `rdc` for all other analyses.

```
subckt ac_res (a b)
    parameters rdc=1 rac=2
    R1 (a i) resistor r=rdc
    Rac (i b) resistor r=rac-rdc
    Sw (i b) switch position=1 ac_position=0
ends ac_res
```

Output Parameters

- 1 `leff` (m) Effective resistor length.
- 2 `weff` (m) Effective resistor width.
- 3 `reff` (Ω) Effective resistance.

Operating-Point Parameters

- 1 `v` (V) Voltage at operating point.
- 2 `i` (A) Current through the resistor.

Spectre Circuit Simulator Reference

Component Statements Part 2

3 `res` (Ω) Resistance at op point.

4 `pwr` (W) Power dissipation.

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

<code>af</code> M-16	<code>leff</code> O-1	<code>pwr</code> OP-4	<code>tc2</code> I-8
<code>coeffs</code> M-13	<code>m</code> I-4	<code>r</code> M-1	<code>thresh</code> M-3
<code>etch</code> M-6	<code>mr</code> M-22	<code>r</code> I-1	<code>tnom</code> M-11
<code>etchl</code> M-7	<code>mrl</code> M-23	<code>reff</code> O-3	<code>trise</code> I-9
<code>fexp</code> M-21	<code>mrlp</code> M-24	<code>res</code> OP-3	<code>trise</code> M-12
<code>i</code> OP-2	<code>mrlw1</code> M-27	<code>resform</code> I-6	<code>v</code> OP-1
<code>isnoisy</code> I-10	<code>mrlwlp</code> M-28	<code>rsh</code> M-2	<code>w</code> M-5
<code>kf</code> M-15	<code>mrlw2</code> M-29	<code>scale</code> I-5	<code>w</code> I-3
<code>l</code> I-2	<code>mrlw2p</code> M-30	<code>scaler</code> M-8	<code>wdexp</code> M-17
<code>l</code> M-4	<code>mrw</code> M-25	<code>tc1</code> M-9	<code>weexp</code> M-19
<code>ldexp</code> M-18	<code>mrwp</code> M-26	<code>tc1</code> I-7	<code>weff</code> O-2
<code>leexp</code> M-20	<code>nonlinform</code> M-14	<code>tc2</code> M-10	

s-Domain Linear Current Controlled Current Source (scccs)

Description

The device output is defined through a transfer function given as a ratio of two polynomials in the complex variable s . Polynomials can be specified in terms of either coefficients or roots. The roots of the numerator are the zeros of the transfer function and the roots of the denominator are the poles.

To specify the polynomial in terms of the coefficients, you enter them as a vector in ascending order of the power of the variable s , starting from the constant term. For example, to specify a denominator of $3s^2 + 4s + 1$, use `denom=[1 4 3]`.

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To specify a polynomial in terms of its roots, you give the roots as a vector of complex frequencies (frequencies should be in radians/second). You must give both the real and imaginary parts of the root, even when the root is real. For the transfer function to be stable, all poles must have negative real values. When specifying a complex root, you should also specify its complex conjugate. However, if you omit the conjugate root, Spectre will supply the missing root and print a warning that a missing root was supplied. The order of the roots is not important. For example, to specify poles of $s = -1$, $s = 4j$, $s = -4j$, $s = -2 + 2j$, and $s = -2 - 2j$; use `poles=[-1 0 0 4 0 -4 -2 2 -2 -2]`.

Either the numerator or the denominator specification can be omitted. An omitted denominator or numerator is taken to be 1.

The parameter `gain` is interpreted either as the DC gain or, if the function has zeros or poles at the origin, as a constant factor.

This device is not supported within `altergroup`.

Sample Instance Statement

```
l1 (2 1) inductor l=15
scl (1 0) scccs probe=l1 zeros=[0 6 0 -6 2 -8 2 8] poles=[-1 0 0 64 0 -64 -2 8 -2 -8]
```

Instance Definition

```
Name sink src scccs parameter=value ...
```

Instance Parameters

- 1 `probeDevice` through which the controlling current flows.
- 2 `port=0` Index of the probe port through which the controlling current flows.
- 3 `gain=1` Transfer function gain.
- 4 `numer=[...]` Vector of numerator coefficients.
- 5 `denom=[...]` Vector of denominator coefficients.
- 6 `zeros=[...]` Vector of complex zeros.
- 7 `poles=[...]` Vector of complex poles.
- 8 `m=1` Multiplicity factor.

Operating-Point Parameters

- 1 `i` (A) Input current.
- 2 `v` (V) Output voltage.
- 3 `pwr` (W) Power dissipation.

Parameter Index

In the following index, `I` refers to instance parameters, `M` refers to the model parameters section, `O` refers to the output parameters section, and `OP` refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of `M-35` means the 35th model parameter.

<code>denom</code>	<code>I-5</code>	<code>m</code>	<code>I-8</code>	<code>port</code>	<code>I-2</code>	<code>v</code>	<code>OP-2</code>
<code>gain</code>	<code>I-3</code>	<code>numer</code>	<code>I-4</code>	<code>probe</code>	<code>I-1</code>	<code>zeros</code>	<code>I-6</code>
<code>i</code>	<code>OP-1</code>	<code>poles</code>	<code>I-7</code>	<code>pwr</code>	<code>OP-3</code>		

s-Domain Current Controlled Voltage Source (sccvs)

Description

The device output is defined through a transfer function given as a ratio of two polynomials in the complex variable s . Polynomials can be specified in terms of either coefficients or roots. The roots of the numerator are the zeros of the transfer function and the roots of the denominator are the poles.

To specify the polynomial in terms of the coefficients, you enter them as a vector in ascending order of the power of the variable s , starting from the constant term. For example, to specify a denominator of $3s^2 + 4s + 1$, use `denom=[1 4 3]`.

To specify a polynomial in terms of its roots, you give the roots as a vector of complex frequencies (frequencies should be in radians/second). You must give both the real and imaginary parts of the root, even when the root is real. For the transfer function to be stable, all poles must have negative real values. When specifying a complex root, you should also specify its complex conjugate. However, if you omit the conjugate root, Spectre will supply the missing root and print a warning that a missing root was supplied. The order of the roots is

Spectre Circuit Simulator Reference

Component Statements Part 2

not important. For example, to specify poles of $s = -1$, $s = 4j$, $s = -4j$, $s = -2 + 2j$, and $s = -2 - 2j$; use `poles=[-1 0 0 4 0 -4 -2 2 -2 -2]`.

Either the numerator or the denominator specification can be omitted. An omitted denominator or numerator is taken to be 1.

The parameter `gain` is interpreted either as the DC gain or, if the function has zeros or poles at the origin, as a constant factor.

This device is not supported within `altergroup`.

Sample Instance Statement

```
myv (1 0) vsource type=sine freq=10K
scc1 (2 0) sccvs probe=myv gain=0.5 numer=[2] denom=[5]
```

Instance Definition

Name p n sccvs parameter=value ...

Instance Parameters

- 1 `probeDevice` through which the controlling current flows.
- 2 `port=0` Index of the probe port through which the controlling current flows.
- 3 `gain=1` Transfer function gain.
- 4 `numer=[...]` Vector of numerator coefficients.
- 5 `denom=[...]` Vector of denominator coefficients.
- 6 `zeros=[...]` Vector of complex zeros.
- 7 `poles=[...]` Vector of complex poles.
- 8 `m=1` Multiplicity factor.

Operating-Point Parameters

- 1 `i (A)` Output current.

2 `v` (V) Output voltage.

3 `pwr` (W) Power dissipation.

Parameter Index

In the following index, `I` refers to instance parameters, `M` refers to the model parameters section, `O` refers to the output parameters section, and `OP` refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of `M-35` means the 35th model parameter.

<code>denom</code> <code>I-5</code>	<code>m</code> <code>I-8</code>	<code>port</code> <code>I-2</code>	<code>v</code> <code>OP-2</code>
<code>gain</code> <code>I-3</code>	<code>numer</code> <code>I-4</code>	<code>probe</code> <code>I-1</code>	<code>zeros</code> <code>I-6</code>
<code>i</code> <code>OP-1</code>	<code>poles</code> <code>I-7</code>	<code>pwr</code> <code>OP-3</code>	

s-Domain Linear Voltage Controlled Current Source (svccs)

Description

The device output is defined through a transfer function given as a ratio of two polynomials in the complex variable s . Polynomials can be specified in terms of either coefficients or roots. The roots of the numerator are the zeros of the transfer function and the roots of the denominator are the poles.

To specify the polynomial in terms of the coefficients, you enter them as a vector in ascending order of the power of the variable s , starting from the constant term. For example, to specify a denominator of $3s^2 + 4s + 1$, use `denom=[1 4 3]`.

To specify a polynomial in terms of its roots, you give the roots as a vector of complex frequencies (frequencies should be in radians/second). You must give both the real and imaginary parts of the root, even when the root is real. For the transfer function to be stable, all poles must have negative real values. When specifying a complex root, you should also specify its complex conjugate. However, if you omit the conjugate root, Spectre will supply the missing root and print a warning that a missing root was supplied. The order of the roots is not important. For example, to specify poles of $s = -1$, $s = 4j$, $s = -4j$, $s = -2 + 2j$, and $s = -2 - 2j$; use `poles=[-1 0 0 4 0 -4 -2 2 -2 -2]`.

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Component Statements Part 2

Either the numerator or the denominator specification can be omitted. An omitted denominator or numerator is taken to be 1.

The parameter `gain` is interpreted either as the DC gain or, if the function has zeros or poles at the origin, as a constant factor.

This device is not supported within `altergroup`.

Sample Instance Statement

```
s2 (1 0 control 0) svccs gain=0.4 numer=[2 3] denom=[4 5 1]
```

Instance Definition

```
Name sink src ps ns svccs parameter=value ...
```

Instance Parameters

- 1 `gain=1` Transfer function gain.
- 2 `numer=[...]` Vector of numerator coefficients.
- 3 `denom=[...]` Vector of denominator coefficients.
- 4 `zeros=[...]` Vector of complex zeros.
- 5 `poles=[...]` Vector of complex poles.
- 6 `m=1` Multiplicity factor.

Operating-Point Parameters

- 1 `i (A)` Output current.
- 2 `v (V)` Output voltage.
- 3 `pwr (W)` Power dissipation.

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point

Spectre Circuit Simulator Reference

Component Statements Part 2

parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of M-35 means the 35th model parameter.

denom	I-3	m	I-6	pwr	OP-3
gain	I-1	numer	I-2	v	OP-2
i	OP-1	poles	I-5	zeros	I-4

s-Domain Voltage Controlled Voltage Source (svcv)

Description

The device output is defined through a transfer function given as a ratio of two polynomials in the complex variable s . Polynomials can be specified in terms of either coefficients or roots. The roots of the numerator are the zeros of the transfer function and the roots of the denominator are the poles.

To specify the polynomial in terms of the coefficients, you enter them as a vector in ascending order of the power of the variable s , starting from the constant term. For example, to specify a denominator of $3s^2 + 4s + 1$, use `denom=[1 4 3]`.

To specify a polynomial in terms of its roots, you give the roots as a vector of complex frequencies (frequencies should be in radians/second). You must give both the real and imaginary parts of the root, even when the root is real. For the transfer function to be stable, all poles must have negative real values. When specifying a complex root, you should also specify its complex conjugate. However, if you omit the conjugate root, Spectre will supply the missing root and print a warning that a missing root was supplied. The order of the roots is not important. For example, to specify poles of $s = -1$, $s = 4j$, $s = -4j$, $s = -2 + 2j$, and $s = -2 - 2j$; use `poles=[-1 0 0 4 0 -4 -2 2 -2 -2]`.

Either the numerator or the denominator specification can be omitted. An omitted denominator or numerator is taken to be 1.

The parameter `gain` is interpreted either as the DC gain or, if the function has zeros or poles at the origin, as a constant factor.

This device is not supported within `altergroup`.

Sample Instance Statement

```
e1 (1 0 control 0) svccs gain=5 poles=[-1 0 1 0] zero=[0 0 1 0]
```

Spectre Circuit Simulator Reference

Component Statements Part 2

Instance Definition

Name p n ps ns svcvs parameter=value ...

Instance Parameters

- 1 gain=1 Transfer function gain.
- 2 numer=[...] Vector of numerator coefficients.
- 3 denom=[...] Vector of denominator coefficients.
- 4 zeros=[...] Vector of complex zeros.
- 5 poles=[...] Vector of complex poles.
- 6 m=1 Multiplicity factor.

Operating-Point Parameters

- 1 i (A) Output current.
- 2 v (V) Output voltage.
- 3 pwr (W) Power dissipation.

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

denom	I-3	m	I-6	pwr	OP-3
gain	I-1	numer	I-2	v	OP-2
i	OP-1	poles	I-5	zeros	I-4

Ideal Switch (switch)

Description

Ideal switch is a single-pole multiple-throw switch with infinite `off` resistance and zero `on` resistance. The switch is provided to allow you to reconfigure your circuit between analyses. You can only change the switch state between analyses (using the `alter` statement), not during an analysis.

When the switch is set to position 0 it is open. In other words, no terminal is connected to any other. When the switch is set to position 1, terminal 1 is connected to terminal 0, and all others are unconnected. When the position is set to 2, terminal 2 is connected to terminal 0, etc.

An offset voltage is supported. It is placed in series with the common terminal. The negative side of the source is connected to the common terminal.

The switch can change its position based on which analysis type is being performed using the `xxx_position` parameters. This feature should be used carefully. Careless use can generate discontinuities that result in convergence problems. Once an analysis specific position has been specified using `xxx_position`, it will always dominate over a position given with the `position` parameter. To disable an analysis specific position, alter it to its default value of unspecified.

This device is not supported within `altergroup`.

Sample Instance Statement

```
sw1 (t1 t2 t3) switch dc_position=0 ac_position=1 tran_position=2
```

Instance Definition

```
Name  t0  t1 ... switch parameter=value ...
```

Instance Parameters

- 1 `position=0` Switch position (0, 1, 2, ...).
- 2 `dc_position` Position to which switch is set at start of DC analysis.
- 3 `ac_position` Position to which switch is set at start of AC analysis.
- 4 `tran_position` Position to which switch is set at start of transient analysis.

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Component Statements Part 2

- 5 `ic_position` Position to which switch is set at start of IC analysis (precedes transient analysis).
- 6 `offset=0` Offset voltage in series with common terminal.
- 7 `m=1.0` Multiplicity factor.

Output Parameters

- 1 `present_position` Current switch position.

Parameter Index

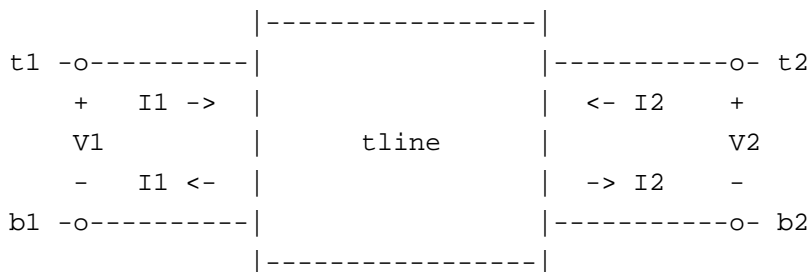
In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

<code>ac_position</code>	I-3	<code>ic_position</code>	I-5	<code>offset</code>	I-6	<code>present_position</code>	O-1
<code>dc_position</code>	I-2	<code>m</code>	I-7	<code>position</code>	I-1	<code>tran_position</code>	I-4

Transmission Line (tline)

Description

Lossy or lossless transmission line.



The lossy transmission line model includes dielectric and conductor loss effects. The conductor loss includes skin effect assuming finite or infinite conductor thickness.

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Component Statements Part 2

Only the odd mode is modeled, so only the voltage difference across each port is important. (The absolute voltage of each terminal is not significant.) Also, the current into one node of a port exactly equals the current leaving the other node of the port.

This device is supported within altergroups.

Sample Instance Statement

```
t1 (1 0 2 0) lmodel z0=100
```

Sample Model Statement

```
model lmodel tline f=10M z0=50 alphac=8501 fc=10M dcr=88
```

Instance Definition

```
Name t1 b1 t2 b2 ModelName parameter=value ...  
Name t1 b1 t2 b2 tline parameter=value ...
```

Instance Parameters

- 1 `z0=50` Ω Characteristic impedance of lossless line.
- 2 `td (s)` Time delay of a lossless line in seconds, a measure of the electrical length.
- 3 `f (Hz)` Reference frequency (used in conjunction to the normalized length to specify electrical length of line).
- 4 `nl=0.25` Normalized electrical length in wavelengths at `f` of a lossless line.
- 5 `vel=1` Propagation velocity of the line given as a multiple of `c`, the speed of light in free space. (`vel <= 1`).
- 6 `len=0 m` Physical length (used with `vel` to specify electrical length of line).
- 7 `m=1` Multiplicity factor.

Model Definition

```
model modelName tline parameter=value ...
```

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Component Statements Part 2

Model Parameters

- 1 $z0=50 \ \Omega$ Characteristic impedance of lossless line.
- 2 $f \text{ (Hz)}$ Reference frequency (used in conjunction to the normalized length to specify electrical length of line).
- 3 $vel=1$ Propagation velocity of the line given as a multiple of c , the speed of light in free space. ($vel \leq 1$).

Conductor Loss Parameters

- 4 $corner=0 \text{ Hz}$ Corner frequency for skin effect, frequency where skin depth equals the conductors wall thickness.
- 5 $dcr=0 \ \Omega/m$ DC series resistance per unit length.
- 6 $fc \text{ (Hz)}$ Conductor loss measurement frequency (use with r , qc , or $alphac$).
- 7 $r=0 \ \Omega/m$ Conductor (series) resistance per unit length at fc .
- 8 $alphac=0 \text{ dB/m}$ Conductor loss at fc (low loss approximation).
- 9 $qc=\infty$ Conductor loss quality factor at fc (low loss approximation).

Dielectric Loss Parameters

- 10 $fd \text{ (Hz)}$ Dielectric loss measurement frequency (use with qd).
- 11 $g=0 \text{ S/m}$ Dielectric (shunt) conductance per unit length.
- 12 $alphad=0 \text{ dB/m}$ Dielectric loss (low loss approximation).
- 13 $qd=\infty$ Dielectric loss quality factor at fd (low loss approximation).

Lossless Case

The lossless transmission line is specified with parameters $z0$ and td . The device behavior is then:

$$V1(t) - z0 \cdot I1(t) = V2(t-td) + z0 \cdot I2(t-td)$$

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Component Statements Part 2

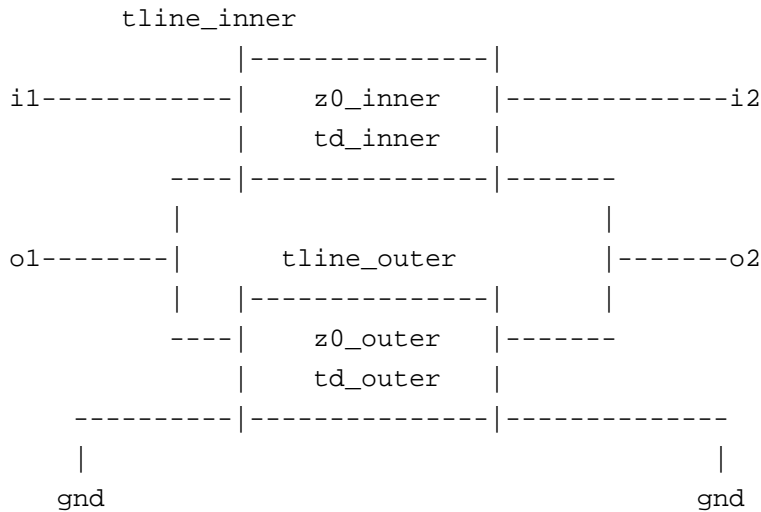
and

$$V2(t) - z0 \cdot I2(t) = V1(t-t_d) + z0 \cdot I1(t-t_d).$$

where t is time and t_d is the delay. Note, if the device is terminated by a matched impedance of $z0$ (across $t2$ and $b2$), then it becomes an ideal delay. i.e $V2(t) = V1(t-t_d)$.

To Model Both Even and Odd Modes

Use two lines as shown below:



This model is suitable for a coax where `tline_inner` models the inner/outer conductor line (or the odd mode) while `tline_outer` models the outer/ground line (or the even mode). Note that this model is non-symmetric.

Lossy Case

In the frequency-domain the device is modeled by

$$V1(jw) - Z(jw) \cdot I1(jw) = S12(jw) \cdot [V2(jw) + Z(jw) \cdot I2(jw)]$$

and

$$V2(jw) - Z(jw) \cdot I2(jw) = S21(jw) \cdot [V1(jw) + Z(jw) \cdot I1(jw)]$$

where $j = \sqrt{-1}$ and w is the angular frequency in radians/s.

The loss coefficient is computed from

$$S21(jw) = S12(jw) = \exp(-\text{Gamma}(jw) \cdot \text{len})$$

where

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Component Statements Part 2

$$\Gamma(j\omega) = \sqrt{Z_c(j\omega) * Y_d(j\omega)}$$

where Z_c represents the per-unit-length series impedance and Y_d represents the per-unit-length shunt admittance loss (as described below).

The characteristic impedance (Z) is computed from

$$Z(j\omega) = \sqrt{Z_c(j\omega) / Y_d(j\omega)}$$

The time-domain behavior of the lossy transmission line is computed through a recursive convolution algorithm.

The dielectric loss (Y_d) is computed from

$$Y_d(j\omega) = G + j * \omega / (z_0 * c * vel)$$

where G is the per-unit-length shunt conductance and can be specified in three ways.

- q $G = g$ { when g is given }
- q $G = 2/z_0 * \alpha$ { when α is given }
- q $G = 2/z_0 * f_d / (2 * q_d * c * vel)$ { when f_d and q_d are given }

m where c is the speed of light.

The series impedance (Z_c) is computed from

$$Z_c(j\omega) = Z_i + j * \omega * z_0 / (c * vel)$$

where Z_i represents the internal loss. When skin effect is not present then

$$Z_i = d_{cr}$$

where d_{cr} is the DC series per-unit-length resistance.

Skin Effect Assuming Finite Thickness

In this case the internal impedance (Z_i) is computed from

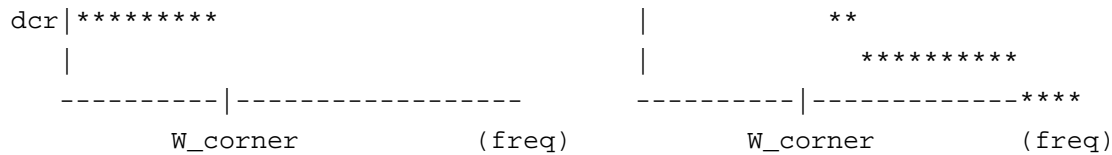
$$Z_i = R_i + j * \omega * L_i$$

where R_i and L_i exhibit the following behavior



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Component Statements Part 2



The expressions for R_i and L_i are

when $w \ll W_corner$: $R_i \sim dcr$ and $L_i \sim dcr/(1.5*W_corner)$

when $w \gg W_corner$: $R_i \sim dcr*\sqrt{w/W_corner}$

and

$L_i \sim dcr/(\sqrt{w*W_corner})$

Otherwise: $R_i = dcr * nt * (\sinh(2*nt) + \sin(2*nt)) / (\cosh(2*nt) - \cos(2*nt))$

and

$L_i = dcr * nt * (\sinh(2*nt) - \sin(2*nt)) / (\cosh(2*nt) - \cos(2*nt)) / w$

where $nt = \sqrt{w/W_corner}$ is the normalized thickness

The equations can be found in:

Ramo, Whinnery, Van Duzer. Fields and waves in communication electronics. 1965. See Section on "Impedance of thin-walled conductors". Pg 301.

The corner frequency (W_corner) results from skin effect on conductors of finite thickness. As frequency decreases, skin depth increases resulting in more conductor to pass the current, which results in lower loss. However, at the corner frequency, the skin depth equals the radius of the conductor. Decreasing the frequency below that point does not further reduce the loss.

The corner frequency (W_corner) can be specified in two ways.

q When dcr and $corner$ are given, then

$$W_corner = 2*\pi*corner$$

q When dcr , r , and fc are given, then

$$W_corner = 2*\pi*fc*(dcr/r)^2$$

In addition, there are two alternative ways to specify r .

q $r = 2*z0*alphac$ { when $alphac$ is given }

q $r = 2*z0*fc/(2*qc*c*vel)$ { when qc is given }

Spectre Circuit Simulator Reference

Component Statements Part 2

where c is the speed of light are defined below.

Skin Effect Assuming Infinite Thickness

In this case there is no corner frequency (and no d_{cr}), and the internal loss (Z_i) is computed from

$$Z_i = R_i + j\omega L_i$$

$$\text{where } R_i = r \sqrt{\omega / (2\pi f_c)} \text{ and } L_i = r \sqrt{\omega^2 \pi f_c}.$$

Again, r can be specified directly, or using `alphac` or `qc` as described above in the case of finite thickness.

Three Ways to Specify `vel`, `td`, and `len`

n When `vel` and `len` are given

$$td = len / (vel * c)$$

n When `td` and `vel` are given

$$len = td * vel * c$$

n When `f`, `nl` and `vel` are given

$$td = nl / f$$

$$len = (nl / f) * vel * c$$

The parameter `len` is the physical length, c is the speed of light and `vel` is the propagation velocity as a multiple of c . (Recall that $velocity = c / \sqrt{\text{relative dielectric constant}}$.) The parameter `f` is a reference frequency and `nl` is the normalized electrical length in wavelengths at `f`.

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

`alphac` **M-8**

`f` **M-2**

`m` **I-7**

`td` **I-2**

Spectre Circuit Simulator Reference

Component Statements Part 2

alphad	M-12	fc	M-6	nl	I-4	vel	M-3
corner	M-4	fd	M-10	qc	M-9	vel	I-5
dcr	M-5	g	M-11	qd	M-13	z0	M-1
f	I-3	len	I-6	r	M-7	z0	I-1

GaAs MESFET (tom2)

Description

TOM2 stands for Triquint Own Model version-2. It is an improved GaAs MESFET developed by David H. Smith. The charge model in TOM2 is similar to that of the Statz model and does not conserve charge. Therefore, this model should not be used to simulate circuits that requires charge conservation such as charge-pump circuits. TOM2 GaAs MESFET instances require that you use a model statement.

This device is supported within altergroups.

There are some convergence problems with this model because of C_{gs} going to zero beyond pinchoff. The problems occur when the gate is driven from an inductive source, and there is no other capacitance at the gate. To prevent these problems, avoid setting C_{gd} to zero and add side wall capacitance to the gate-source and gate-drain junctions. A good estimate for these capacitors is $C = \pi * \epsilon * w / 2$ where w is the gate width in microns and $\epsilon = 0.116 \text{ fF/micron}$.

Sample Instance Statement

```
mt1 (2 1 0) tom2mos area=1 region=fwd
```

Sample Model Statement

```
model tom2mos tom2 vto=-0.55 alpha=3.9 beta=0.001 gamma=0.075 delta=100 ng=1 rd=550  
rs=550 rg=1 is=0.295e-14 n=1.2 cgs=1.4e-15 cgd=2e-16 cds=3e-16
```

Instance Definition

```
Name d g s ModelName parameter=value ...
```

Instance Parameters

1 area=1 Junction area factor.

2 m=1 Multiplicity factor.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 3 `region=fwd` Estimated operating region.
Possible values are `off`, `triode`, `sat`, or `subth`.

Model Definition

`model modelName tom2 parameter=value ...`

Model Parameters

Device Type Parameters

- 1 `type=n` Transistor type.
Possible values are `n` or `p`.

Drain Current Parameters

- 2 `vto=-2.5` $V_{\text{Threshold}}$ voltage.
3 `alpha=2` $1/V_{\text{Knee}}$ -voltage parameter.
4 `beta=0.1` A/V^2 Transconductance parameter.
5 `gamma=0` $1/V_{\text{Threshold}}$ shifting parameter.
6 `delta=0.2` V_{Output} feedback parameter.
7 `q=2` Power-law parameter.

Subthreshold Parameters

- 8 `ng=0` Subthreshold slope gate parameter.
9 `nd=0` $1/V_{\text{Subthreshold}}$ slope drain pull parameter.

Parasitic Resistance Parameters

- 10 `rd=0` Ω Drain resistance (/area).
11 `rs=0` Ω Source resistance (/area).

Spectre Circuit Simulator Reference

Component Statements Part 2

12 `rg=0` Ω Gate resistance (/area).

13 `minr=0.1` Ω Minimum source/drain/gate resistance.

Junction Diode Model Parameters

14 `is=1e-14` AGate diode saturation current (*area).

15 `n=1`Emission coefficient for the gate junction.

16 `imelt='imaxA'` Explosion current (*area).

17 `dskip=yes`Use simple piece-wise linear model for diode currents below $0.1 \cdot i_{abstol}$.
Possible values are `no` or `yes`.

Junction Capacitance Model Parameters

18 `capmod=2`Charge model selector.

19 `cgs=0` FGate-source zero-bias junction capacitance (*area).

20 `cgd=0` FGate-drain zero-bias junction capacitance (*area).

21 `cds=0` FDrain-to-source capacitance.

22 `vbi=1` VGate diode built-in potential.

23 `vmax=0.95`Gate diode capacitance limiting voltage.

24 `vdelta=0.2` VCapacitance transition voltage.

25 `tau=0` sConduction current delay time.

Temperature Effects Parameters

26 `tnom (C)`Parameters measurement temperature. Default set by options.

27 `xTi=0`Temperature exponent for effect on `is`.

28 `eg=1.11` VEnergy band gap.

29 `vtotc=0` V/CTemperature coefficient for `vto`.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 30 `vbitc=0` V/C Temperature coefficient for `vbi`.
- 31 `alphatce=0` $1/C$ Temperature coefficient for `alpha`.
- 32 `betatce=0` $1/C$ Temperature coefficient for `beta`.
- 33 `gammatc=0` $1/C$ Temperature coefficient for `gamma`.
- 34 `trsl=0` $1/C$ Temperature parameter for source resistance.
- 35 `trdl=0` $1/C$ Temperature parameter for drain resistance.
- 36 `trgl=0` $1/C$ Temperature parameter for gate resistance.
- 37 `cgdtce=0` $1/C$ Drain junction capacitance temperature coefficient.
- 38 `cgstce=0` $1/C$ Source junction capacitance temperature coefficient.

Operating Region Warning Control Parameters

- 39 `imax=1` A Maximum allowable current (*area).
- 40 `bvj= ∞` V Junction reverse breakdown voltage.

Noise Model Parameters

- 41 `kf=0` Flicker (1/f) noise coefficient.
- 42 `af=1` Flicker (1/f) noise exponent.
- 43 `kfd=0` Flicker noise (1/f) coefficient for gate diodes.
- 44 `afg=1` Flicker noise (1/f) exponent for gate diodes.

The `imax` parameter aids convergence and prevents numerical overflow. The junction characteristics of the FET are accurately modeled for currents up to `imax`. For currents above `imax`, the junction is modeled as a linear resistor and a warning is printed. The `bv` parameter detects the junction breakdown only. The breakdown currents of the junctions are not modeled.

Spectre Circuit Simulator Reference

Component Statements Part 2

Operating-Point Parameters

- 1 `type=n` Transistor type.
Possible values are `n` or `p`.
- 2 `region=fwd` Estimated operating region.
Possible values are `off`, `triode`, `sat`, or `subth`.
- 3 `vgs (V)` Gate-source voltage.
- 4 `vds (V)` Drain-source voltage.
- 5 `id (A)` Drain current.
- 6 `ig (A)` Gate current.
- 7 `ids (A)` Drain-to-source current.
- 8 `gm (S)` Common-source transconductance.
- 9 `gds (S)` Common-source output conductance.
- 10 `vth (V)` Threshold voltage.
- 11 `cgs (F)` Gate-source capacitance.
- 12 `cgd (F)` Gate-drain capacitance.
- 13 `cds (F)` Drain-source capacitance.
- 14 `pwr (W)` Power at op point.

Parameter Index

In the following index, `I` refers to instance parameters, `M` refers to the model parameters section, `O` refers to the output parameters section, and `OP` refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of `M-35` means the 35th model parameter.

<code>af</code>	<code>M-42</code>	<code>cgstce</code>	<code>M-38</code>	<code>m</code>	<code>I-2</code>	<code>trsl</code>	<code>M-34</code>
<code>afg</code>	<code>M-44</code>	<code>delta</code>	<code>M-6</code>	<code>minr</code>	<code>M-13</code>	<code>type</code>	<code>OP-1</code>

Spectre Circuit Simulator Reference

Component Statements Part 2

alpha M-3	dskip M-17	n M-15	type M-1
alphatce M-31	eg M-28	nd M-9	vbi M-22
area I-1	gamma M-5	ng M-8	vbitc M-30
beta M-4	gammatc M-33	pwr OP-14	vdelta M-24
betatce M-32	gds OP-9	q M-7	vds OP-4
bvj M-40	gm OP-8	rd M-10	vgs OP-3
capmod M-18	id OP-5	region I-3	vmax M-23
cds OP-13	ids OP-7	region OP-2	vth OP-10
cds M-21	ig OP-6	rg M-12	vto M-2
cgd OP-12	imax M-39	rs M-11	vtotc M-29
cgd M-20	imelt M-16	tau M-25	xti M-27
cgdtce M-37	is M-14	tnom M-26	
cgs OP-11	kf M-41	trdl M-35	
cgs M-19	kfd M-43	trgl M-36	

Linear Two Winding Ideal Transformer (transformer)

Description

Winding 1 connects terminals t_1 and b_1 , and winding 2 connects t_2 and b_2 . The number of turns on windings 1 and 2 are given by n_1 and n_2 , respectively, and n_2 must not be zero. The absolute number of turns of each winding is not important, only the ratio of n_1 to n_2 . Current through winding 1 is computed.

This device is not supported within altergroup.

An ideal transformer is modeled, so it acts as a transformer at DC. Thus

$$\frac{v_1}{v_2} = \frac{t_1}{t_2} = \frac{i_2}{i_1}$$

To model a physical transformer with L_1 and L_2 as the inductance of the windings and k as the coupling coefficient, add an inductor $L_m = k \sqrt{L_1 L_2}$ in parallel with winding 1 and inductors $L_{e1} = L_1 (1 - k^2)$ and $L_{e2} = L_2 (1 - k^2)$ in series with windings 1 and 2, respectively. The turns ratio can be computed with

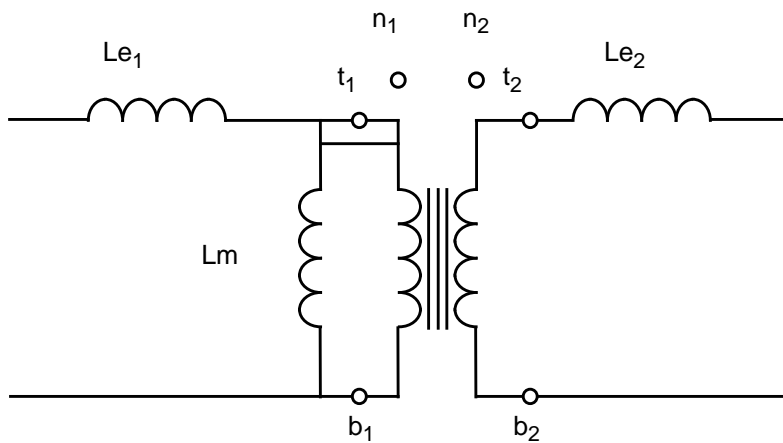
$$\frac{n_1}{n_2} = \sqrt{\frac{L_1}{L_2}}$$

Spectre Circuit Simulator Reference

Component Statements Part 2

k can be calculated from the L_1 (the inductance of winding 1 with winding 2 open) and L_s (the inductance of winding 1 with winding 2 shorted) with

$$k = \sqrt{1 - \frac{L_s}{L_1}}$$



Linear Two-Winding Ideal Transformer

Instance Definition

Name t1 b1 t2 b2 transformer parameter=value ...

Instance Parameters

- 1 n1=1 Number of turns on winding 1.
- 2 n2=1 Number of turns on winding 2.
- 3 m=1 Multiplicity factor.

VBIC Bipolar Transistor (vbic)

Description

The VBIC model was developed as a replacement for the SPICE G-P model. The model has four electrical terminals, two thermal terminals, and up to nine internal nodes, depending on the model parameters that the user specifies. Detailed description of the model and equations are given in the Spectre Circuit Simulator Device Model Equations manual.

This device is supported within altergroups.

Sample Instance Statement

```
q1 (1 2 0 0 0) vbjt area=1
```

Sample Model Statement

```
model vbjt vbic type=npn is=2e-16 iben=4.5e-15 isp=1e-15 gamm=1.55e-11 ikf=0.0021  
ikr=0.0021 vef=15 ver=7 rbi=35 rbx=7 re=3 rs=15 cje=1.5e-14 tf=15e-12 selft=yes  
rth=1K
```

Instance Definition

```
Name c b e [s] [dt] [tl] ModelName parameter=value ...
```

`tl` node is the local temperature and the `dt` node is the rise above the local temperature caused by the thermal power dissipated by the device being modeled by VBIC. Consequently, the `tl` node can be connected to a thermal network that models heat flow through the substrate and/or between devices. It is not necessary to specify the substrate and the two thermal terminals. If left unspecified, the substrate and the `tl` thermal terminal are connected to ground. But if the self-heating flag is turned on and `dt` is not given, an internal node is created for self-heating. You must specify the substrate terminal if you specify `dt` and both substrate and `dt` must be given if `tl` needs to be specified.

Instance Parameters

- 1 `area=1` Transistor area factor.
- 2 `m=1` Multiplicity factor.
- 3 `region=fwd` Estimated operating region.
Possible values are `off`, `fwd`, `rev`, `sat`, or `breakdown`.

Spectre Circuit Simulator Reference

Component Statements Part 2

4 `trise` Temperature rise from ambient.

Model Definition

`model modelName vbic parameter=value ...`

Model Parameters

Structural Parameters

1 `type=npn` Transistor type.
Possible values are `npn` or `pnP`.

Saturation Current Parameters

- 2 `is=1e-16` A Transport saturation current (*area).
- 3 `ibei=1e-18` A Ideal B-E saturation current. (*area).
- 4 `iben=0` A Nonideal B-E saturation current (*area).
- 5 `ibci=1e-16` A Ideal B-C saturation current. (*area).
- 6 `ibcn=0` A Nonideal B-C saturation current (*area).
- 7 `isp=0` A Parasitic transport saturation current. (*area).
- 8 `ibeip=0` A Ideal parasitic B-E saturation current (*area).
- 9 `ibenp=0` A Nonideal parasitic B-E saturation current (*area).
- 10 `ibcip=0` A Ideal parasitic B-C saturation current (*area).
- 11 `ibcnp=0` A Nonideal parasitic B-C saturation current (*area).
- 12 `vo=0` V Epi drift saturation voltage.
- 13 `gamm=0` V Epi doping parameter.
- 14 `hrcf=1` High current RC factor.
- 15 `wbe=1` Portion of `Ibei` from `Vbei`.

Spectre Circuit Simulator Reference

Component Statements Part 2

16 `wsp=1` Portion of I_{ccp} from V_{bep} .

Emission Coefficient Parameters

17 `nf=1` Forward emission coefficient.

18 `nr=1` Reverse emission coefficient.

19 `nei=1` Ideal B-E emission coefficient.

20 `nen=2` Nonideal B-E emission coefficient.

21 `nci=1` Ideal B-C emission coefficient.

22 `ncn=2` Nonideal B-C emission coefficient.

23 `nfp=1` Parasitic forward emission coefficient.

24 `ncip=1` Ideal parasitic B-C emission coefficient.

25 `ncnp=2` Nonideal parasitic B-C emission coefficient.

Current Gain Parameters

26 `ikf= ∞` A Forward knee current (*area).

27 `ikr= ∞` A Reverse knee current (*area).

28 `ikp= ∞` A Parasitic knee current (*area).

Early Voltage Parameters

29 `vef= ∞` V Forward Early voltage.

30 `ver= ∞` V Reverse Early voltage.

Breakdown Voltage Parameters

31 `avc1=0` B-C weak avalanche parameter.

32 `avc2=0` B-C weak avalanche parameter.

Spectre Circuit Simulator Reference

Component Statements Part 2

Parasitic Resistance Parameters

- 33 `rbi=0` Ω Intrinsic base resistance (/area).
- 34 `rbx=0` Ω Extrinsic base resistance (/area).
- 35 `re=0` Ω Emitter resistance (/area).
- 36 `rs=0` Ω Substrate resistance (/area).
- 37 `rbp=0` Ω Parasitic base resistance (/area).
- 38 `rcx=0` Ω Extrinsic collector resistance (/area).
- 39 `rci=0` Ω Intrinsic collector resistance (/area).

Junction Capacitance Parameters

- 40 `cje=0` F B-E zero-bias capacitance (*area).
- 41 `pe=0.75` V B-E built-in potential.
- 42 `me=0.33` B-E grading coefficient.
- 43 `aje=-0.5` B-E capacitance smoothing factor.
- 44 `fc=0.9` Forward-bias depletion capacitance limit.
- 45 `cbeo=0` F Extrinsic B-E overlap capacitance (*area).
- 46 `cjc=0` F B-C zero-bias capacitance (*area).
- 47 `cjep=0` F B-C extrinsic zero-bias capacitance (*area).
- 48 `pc=0.75` V B-C built-in potential.
- 49 `mc=0.33` B-C grading coefficient.
- 50 `ajc=-0.5` B-C capacitance smoothing factor.
- 51 `cbco=0` F Extrinsic B-C overlap capacitance (*area).
- 52 `qco=0` Coul Epi charge parameter.

Spectre Circuit Simulator Reference

Component Statements Part 2

53 `cjcp=0` F S-C zero-bias capacitance (*area).

54 `ps=0.75` V S-C built-in potential.

55 `ms=0.33` S-C grading coefficient.

56 `ajs=-0.5` S-C capacitance smoothing factor.

Transit Time and Excess Phase Parameters

57 `tf=0` s Forward transit time.

58 `tr=0` s Reverse transit time.

59 `td=0` s Forward excess-phase delay time.

60 `qtf=0` Variation of `tf` with base width modulation.

61 `xtf=0` Coefficient of `tf` with bias dependence.

62 `vtf=0` Coefficient of `tf` dependence on `Vbc`.

63 `itf=0` Coefficient of `tf` dependence on `Ic`.

Temperature Effects Parameters

64 `selft=0` Flag denoting self-heating.
Possible values are `no` or `yes`.

65 `tnom (C)` Parameters measurement temperature. Default set by options.

66 `trise=0` C Temperature rise from ambient.

67 `rth=0` Ω Thermal resistance, must be given for self-heating.

68 `cth=0` F Thermal capacitance.

69 `xis=3` V Temperature exponent of `Is`.

70 `xii=3` V Temperature exponent of `Ibei`, `Ibci`, `Ibeip`, and `Ibcip`.

71 `xin=3` V Temperature exponent of `Iben`, `Ibcn`, `Ibenp`, and `Ibcnp`.

Spectre Circuit Simulator Reference

Component Statements Part 2

- 72 `tnf=0` v Temperature coefficient of N_f .
- 73 `tavc=0` v Temperature coefficient of A_{vc2} .
- 74 `ea=1.12` v Activation energy for i_s .
- 75 `eaie=1.12` v Activation energy for I_{bei} .
- 76 `eaic=1.12` v Activation energy for I_{bci}/I_{beip} .
- 77 `eaic=1.12` v Activation energy for I_{bcip} .
- 78 `eane=1.12` v Activation energy for I_{ben} .
- 79 `eanc=1.12` v Activation energy for I_{bcn}/I_{benp} .
- 80 `eans=1.12` v Activation energy for I_{bcnp} .
- 81 `xre=0` Temperature exponent of r_e .
- 82 `xrb=0` Temperature exponent of r_b .
- 83 `xrc=0` Temperature exponent of r_c .
- 84 `xrs=0` Temperature exponent of r_s .
- 85 `xvo=0` Temperature exponent of v_o .
- 86 `dtmax=226.85` c Maximum expected device temperature. (500 K).

Noise Model Parameters

- 87 `kfn=0` B-E flicker (1/f) noise coefficient.
- 88 `afn=1` B-E flicker (1/f) noise exponent.
- 89 `bfn=1` B-E flicker (1/f) noise dependence.

Junction Diode Model Control Parameters

- 90 `dskip=yes` Skip junction calculations if they are reverse-saturated.
Possible values are `no` or `yes`.

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Component Statements Part 2

91 `imelt=10` AExplosion current (*area).

Operating Region Warning Control Parameters

92 `bvbe=∞` v_{B-E} breakdown voltage.

93 `bvbc=∞` v_{B-C} breakdown voltage.

94 `bvce=∞` v_{C-E} breakdown voltage.

95 `bvsub=∞` $v_{\text{Substrate}}$ junction breakdown voltage.

96 `vbefwd=0.2` v_{B-E} forward voltage.

97 `vbcfwd=0.2` v_{B-C} forward voltage.

98 `vsubfwd=0.2` $v_{\text{Substrate}}$ junction forward voltage.

99 `imax=1` AMaximum allowable base current (*area).

100 `imax1=imax` AMaximum allowable collector current (*area).

101 `alarm=none`Forbidden operating region.
Possible values are none, off, fwd, rev, or sat.

DC-Mismatch Model Parameters

102 `mvt0=0.0` $v_{\text{Threshold}}$ mismatch intercept.

Imax and Imelt

The `imax` parameter aids convergence and prevents numerical overflow. The junction characteristics of the device are accurately modeled for current up to `imax`. If `imax` is exceeded during iterations, the linear model is substituted until the current drops below `imax` or until convergence is achieved. If convergence is achieved with the current exceeding `imax`, the results are inaccurate, and Spectre prints a warning.

A separate model parameter, `imelt`, is used as a limit warning for the junction current. This parameter can be set to the maximum current rating of the device. When any component of the junction current exceeds `imelt`, note that base and collector currents are composed of many exponential terms, Spectre issues a warning and the results become inaccurate. The

Spectre Circuit Simulator Reference

Component Statements Part 2

junction current is linearized above the value of `imelt` to prevent arithmetic exception, with the exponential term replaced by a linear equation at `imelt`.

Operating-Point Parameters

- 1 `type=npn` Transistor type.
Possible values are `npn` or `pnP`.
- 2 `region=fwd` Estimated operating region.
Possible values are `off`, `fwd`, `rev`, `sat`, or `breakdown`.
- 3 `vbe (V)` Base-emitter voltage.
- 4 `vbc (V)` Base-collector voltage.
- 5 `vce (V)` Collector-emitter voltage.
- 6 `vcs (V)` Collector-substrate voltage.
- 7 `temp (C)` Device temperature.
- 8 `ith (A)` Thermal source.
- 9 `ic (A)` Intrinsic DC collector current. ($I_{CC} - I_{BC} + I_{GC}$).
- 10 `ib (A)` Intrinsic DC base current. ($I_{BE} + I_{BC} - I_{GC}$).
- 11 `icc (A)` C-E current.
- 12 `ibe (A)` Intrinsic B-E junction current.
- 13 `ibc (A)` Intrinsic B-C junction current.
- 14 `ibex (A)` BX-E junction current.
- 15 `igc (A)` Breakdown current.
- 16 `iccp (A)` Parasitic transistor C-E current.
- 17 `ibep (A)` Parasitic transistor B-E current.
- 18 `ibcp (A)` Parasitic transistor B-C current.

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Component Statements Part 2

- 19 `betadc` (A/A) Ratio of external collector current to external base current. (i_{c_ext}/i_{b_ext}).
- 20 `gm` (S) Intrinsic small-signal transconductance. ($gm = dlcc_dV_{be} + dlcc_dV_{bc}$).
- 21 `gpi` (S) Intrinsic small-signal input conductance. ($gpi = dlbe_dV_{be}$).
- 22 `go` (S) Intrinsic small-signal output conductance. ($go = -dlcc_dV_{bc}$).
- 23 `gmu` (S) Intrinsic small-signal Collector-Base conductance. ($gmu = dlbc_dV_{bc}$).
- 24 `cpi` (F) Intrinsic small-signal B-E capacitance. Same as `cje`.
- 25 `cmu` (F) Intrinsic small signal B-C capacitance. Same as `cjc`.
- 26 `betaac` (A/A) Small-signal common-emitter current gain. (gm/gpi).
- 27 `ft` (Hz) Unity small-signal current-gain frequency.
- 28 `dic_dvbe` (S) Intrinsic dlc/dV_{be} .
- 29 `dic_dvbc` (S) Intrinsic dlc_dV_{bc} .
- 30 `dib_dvbe` (S) Intrinsic dlb_dV_{be} .
- 31 `dib_dvbc` (S) Intrinsic dlb_dV_{bc} .
- 32 `rbi` (Ω) Intrinsic base resistance.
- 33 `rci` (Ω) Intrinsic collector resistance.
- 34 `rbp` (Ω) Parasitic transistor base resistance.
- 35 `cje` (F) Intrinsic B-E capacitance.
- 36 `cjc` (F) Intrinsic B-C capacitance.
- 37 `cbex` (F) B-X junction capacitance.
- 38 `cbcx` (F) B-CX junction capacitance.
- 39 `cbep` (F) Parasitic B-E junction capacitance.
- 40 `cbcp` (F) Parasitic B-C junction capacitance.

Spectre Circuit Simulator Reference

Component Statements Part 2

41 `pwr` (W) Power dissipation.

Parameter Index

In the following index, `I` refers to instance parameters, `M` refers to the model parameters section, `O` refers to the output parameters section, and `OP` refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of `M-35` means the 35th model parameter.

<code>afn</code>	<code>M-88</code>	<code>eaic</code>	<code>M-76</code>	<code>imelt</code>	<code>M-91</code>	<code>rs</code>	<code>M-36</code>
<code>ajc</code>	<code>M-50</code>	<code>eaie</code>	<code>M-75</code>	<code>is</code>	<code>M-2</code>	<code>rth</code>	<code>M-67</code>
<code>aje</code>	<code>M-43</code>	<code>eaie</code>	<code>M-77</code>	<code>isp</code>	<code>M-7</code>	<code>selft</code>	<code>M-64</code>
<code>ajs</code>	<code>M-56</code>	<code>eanc</code>	<code>M-79</code>	<code>itf</code>	<code>M-63</code>	<code>tavc</code>	<code>M-73</code>
<code>alarm</code>	<code>M-101</code>	<code>eane</code>	<code>M-78</code>	<code>ith</code>	<code>OP-8</code>	<code>td</code>	<code>M-59</code>
<code>area</code>	<code>I-1</code>	<code>eans</code>	<code>M-80</code>	<code>kfn</code>	<code>M-87</code>	<code>temp</code>	<code>OP-7</code>
<code>avc1</code>	<code>M-31</code>	<code>fc</code>	<code>M-44</code>	<code>m</code>	<code>I-2</code>	<code>tf</code>	<code>M-57</code>
<code>avc2</code>	<code>M-32</code>	<code>ft</code>	<code>OP-27</code>	<code>mc</code>	<code>M-49</code>	<code>tnf</code>	<code>M-72</code>
<code>betaac</code>	<code>OP-26</code>	<code>gamm</code>	<code>M-13</code>	<code>me</code>	<code>M-42</code>	<code>tnom</code>	<code>M-65</code>
<code>betadc</code>	<code>OP-19</code>	<code>gm</code>	<code>OP-20</code>	<code>ms</code>	<code>M-55</code>	<code>tr</code>	<code>M-58</code>
<code>bfn</code>	<code>M-89</code>	<code>gmu</code>	<code>OP-23</code>	<code>mvt0</code>	<code>M-102</code>	<code>trise</code>	<code>M-66</code>
<code>bvbc</code>	<code>M-93</code>	<code>go</code>	<code>OP-22z</code>	<code>nci</code>	<code>M-21</code>	<code>trise</code>	<code>I-4</code>
<code>bvbe</code>	<code>M-92</code>	<code>gpi</code>	<code>OP-21</code>	<code>ncip</code>	<code>M-24</code>	<code>type</code>	<code>OP-1</code>
<code>bvce</code>	<code>M-94</code>	<code>hrcf</code>	<code>M-14</code>	<code>ncn</code>	<code>M-22</code>	<code>type</code>	<code>M-1</code>
<code>bvsub</code>	<code>M-95</code>	<code>ib</code>	<code>OP-10</code>	<code>ncnp</code>	<code>M-25</code>	<code>vbc</code>	<code>OP-4</code>
<code>cbco</code>	<code>M-51</code>	<code>ibc</code>	<code>OP-13</code>	<code>nei</code>	<code>M-19</code>	<code>vbcfwd</code>	<code>M-97</code>
<code>cbcp</code>	<code>OP-40</code>	<code>ibci</code>	<code>M-5</code>	<code>nen</code>	<code>M-20</code>	<code>vbe</code>	<code>OP-3</code>
<code>cbcx</code>	<code>OP-38</code>	<code>ibcip</code>	<code>M-10</code>	<code>nf</code>	<code>M-17</code>	<code>vbefwd</code>	<code>M-96</code>
<code>cbeo</code>	<code>M-45</code>	<code>ibcn</code>	<code>M-6</code>	<code>nfp</code>	<code>M-23</code>	<code>vce</code>	<code>OP-5</code>
<code>cbep</code>	<code>OP-39</code>	<code>ibcnp</code>	<code>M-11</code>	<code>nr</code>	<code>M-18</code>	<code>vcs</code>	<code>OP-6</code>
<code>cbex</code>	<code>OP-37</code>	<code>ibcp</code>	<code>OP-18</code>	<code>pc</code>	<code>M-48</code>	<code>vef</code>	<code>M-29</code>
<code>cjc</code>	<code>OP-36</code>	<code>ibe</code>	<code>OP-12</code>	<code>pe</code>	<code>M-41</code>	<code>ver</code>	<code>M-30</code>
<code>cjc</code>	<code>M-46</code>	<code>ibei</code>	<code>M-3</code>	<code>ps</code>	<code>M-54</code>	<code>vo</code>	<code>M-12</code>
<code>cjcp</code>	<code>M-53</code>	<code>ibeip</code>	<code>M-8</code>	<code>pwr</code>	<code>OP-41</code>	<code>vsubfwd</code>	<code>M-98</code>
<code>cje</code>	<code>OP-35</code>	<code>iben</code>	<code>M-4</code>	<code>qco</code>	<code>M-52</code>	<code>vtf</code>	<code>M-62</code>
<code>cje</code>	<code>M-40</code>	<code>ibenp</code>	<code>M-9</code>	<code>qtf</code>	<code>M-60</code>	<code>wbe</code>	<code>M-15</code>
<code>cjep</code>	<code>M-47</code>	<code>ibep</code>	<code>OP-17</code>	<code>rbi</code>	<code>M-33</code>	<code>wsp</code>	<code>M-16</code>
<code>cmu</code>	<code>OP-25</code>	<code>ibex</code>	<code>OP-14</code>	<code>rbi</code>	<code>OP-32</code>	<code>xii</code>	<code>M-70</code>
<code>cpi</code>	<code>OP-24</code>	<code>ic</code>	<code>OP-9</code>	<code>rbp</code>	<code>M-37</code>	<code>xin</code>	<code>M-71</code>
<code>cth</code>	<code>M-68</code>	<code>icc</code>	<code>OP-11</code>	<code>rbp</code>	<code>OP-34</code>	<code>xis</code>	<code>M-69</code>
<code>dib_dvbc</code>	<code>OP-31</code>	<code>iccp</code>	<code>OP-16</code>	<code>rbx</code>	<code>M-34</code>	<code>xrb</code>	<code>M-82</code>
<code>dib_dvbe</code>	<code>OP-30</code>	<code>igc</code>	<code>OP-15</code>	<code>rci</code>	<code>OP-33</code>	<code>xrc</code>	<code>M-83</code>
<code>dic_dvbc</code>	<code>OP-29</code>	<code>ikf</code>	<code>M-26</code>	<code>rci</code>	<code>M-39</code>	<code>xre</code>	<code>M-81</code>
<code>dic_dvbe</code>	<code>OP-28</code>	<code>ikp</code>	<code>M-28</code>	<code>rcx</code>	<code>M-38</code>	<code>xrs</code>	<code>M-84</code>

Spectre Circuit Simulator Reference

Component Statements Part 2

dskip	M-90	ikr	M-27	re	M-35	xtf	M-61
dtmax	M-86	imax	M-99	region	OP-2	xvo	M-85
ea	M-74	imax1	M-100	region	I-3		

Linear Voltage Controlled Current Source (vccs)

Description

Positive current exits the source node and enters the sink node.

This device is supported within altergroups.

Sample Instance Statement

```
v1 (1 0 2 3) gm=-1 m=2
```

Instance Definition

```
Name sink src ps ns vccs parameter=value ...
```

Instance Parameters

1 gm=0 STransconductance.

2 m=1Multiplicity factor.

Operating-Point Parameters

1 i (A)Output current.

2 v (V)Output voltage.

3 pwr (W)Power dissipation.

Linear Voltage Controlled Voltage Source (vcvs)

Description

Current through the voltage source is calculated and is defined to be positive if it flows from the positive terminal, through the source, to the negative terminal.

This device is supported within altergroups.

Sample Instance Statement

```
e1 (out1 0 pos neg) vcvs gain=10
```

Instance Definition

```
Name p n ps ns vcvs parameter=value ...
```

Instance Parameters

1 `gain=0` V/V Voltage gain.

2 `m=1` Multiplicity factor.

Operating-Point Parameters

1 `i` (A) Output current.

2 `v` (V) Output voltage.

3 `pwr` (W) Power dissipation.

Independent Voltage Source (vsource)

Description

Current through the source is computed and is defined to be positive if it flows from the positive node, through the source, to the negative node.

This device is supported within altergroups.

Spectre Circuit Simulator Reference

Component Statements Part 2

The value of the DC voltage as a function of the temperature is given by:

$$V(T) = V(tnom) * [1 + tc1 * (T - tnom) + tc2 * (T - tnom)^2]$$

Sample Instance Statement

```
vpulse1 (1 0) vsource type=pulse val0=0 val1=5 period=100n rise=10n fall=10n
width=40n
vpwl1 (1 0) vsource type=pwl wave=[1n 0 1.1n 2 1.5n 0.5 2n 3 5n 5] pwlperiod=5n
```

Instance Definition

Name p n vsource parameter=value ...

Instance Parameters

1 dc=0 vDC value.

General Waveform Parameters

2 type=dcWaveform type.

Possible values are dc, pulse, pwl, sine, or exp.

3 fundnameName of the fundamental frequency. Must be specified if the source is active during a pdisto analysis or it is the active clock during an envlp analysis.

4 delay=0 sWaveform delay time.

Pulse Waveform Parameters

5 val0=0 vZero value used in pulse and exponential waveforms.

6 val1=1 vOne value used in pulse and exponential waveforms.

7 period=∞ sPeriod of waveform.

8 rise (s)Rise time for pulse waveform (time for transition from val0 to val1).

9 fall (s)Fall time for pulse waveform (time for transition from val1 to val0).

10 width=∞ sPulse width (duration of val1).

Spectre Circuit Simulator Reference

Component Statements Part 2

PWL Waveform Parameters

- 11 `fileName` of file containing waveform.
- 12 `wave=[...]` Vector of time/value pairs that defines waveform.
- 13 `offset=0` VDC offset for the PWL waveform.
- 14 `scale=1` Scale factor for the PWL waveform.
- 15 `stretch=1` Scale factor for time given for the PWL waveform.
- 16 `allbrkpts` All the points in the PWL waveform are breakpoints if set to yes. Default is yes if the number of points is less than 20.
Possible values are `no` or `yes`.
- 17 `pwlperiod (s)` Period of the periodic PWL waveform.
- 18 `twidth=pwlperiod/1000 s`
Transition width used when making PWL waveforms periodic.

Sinusoidal Waveform Parameters

- 19 `sinedc=dc` VDC level for sinusoidal waveforms.
- 20 `ampl=1` VPeak amplitude of sinusoidal waveform.
- 21 `freq=0` HzFrequency of sinusoidal waveform.
- 22 `sinephase=0` °Phase of sinusoid when `t=delay`.
- 23 `ampl2=1` VPeak amplitude of second sinusoidal waveform.
- 24 `freq2=0` HzFrequency of second sinusoidal waveform.
- 25 `sinephase2=0` °Phase of second sinusoid when `t=delay`.
- 26 `fundname2` Name of the fundamental frequency associated with `freq2`. Must be specified if `freq2` is used in a `pdisto` analysis.
- 27 `fmodindex=0` FM index of modulation for sinusoidal waveform.
- 28 `fmodfreq=0` HzFM modulation frequency for sinusoidal waveform.

Spectre Circuit Simulator Reference

Component Statements Part 2

29 `ammodindex=0` AM index of modulation for sinusoidal waveform.

30 `ammodfreq=0` Hz AM modulation frequency for sinusoidal waveform.

31 `ammodphase=0` ° AM phase of modulation for sinusoidal waveform.

32 `damp=0` 1/s Damping factor for sinusoidal waveform.

Exponential Waveform Parameters

33 `td1=0` s Rise start time for exponential wave.

34 `tau1 (s)` Rise time constant for exponential wave.

35 `td2 (s)` Fall start time for exponential wave.

36 `tau2 (s)` Fall time constant for exponential wave.

Noise Parameters

37 `noisefile` Name of file containing excess spot noise data in the form of frequency-noise pairs.

38 `noisevec=[...]` V²/Hz Excess spot noise as a function of frequency in the form of frequency-noise pairs.

Small Signal Parameters

39 `mag=0` V Small signal voltage.

40 `phase=0` ° Small signal phase.

41 `xfmag=1` V/V Transfer function analysis magnitude.

42 `pacmag=0` V Periodic AC analysis magnitude.

43 `pacphase=0` ° Periodic AC analysis phase.

Multiplication Factor Parameters

44 `m=1` Multiplicity factor.

Spectre Circuit Simulator Reference

Component Statements Part 2

Temperature Effects Parameters

45 `tc1=0 1/C` First order temperature coefficient.

46 `tc2=0 C-2` Second order temperature coefficient.

47 `tnom=27 C` Parameter measurement temperature. Default set by options.

If you do not specify the DC value, it is assumed to be the `time=0` value of the waveform.

In DC analyses, the only active parameters are `dc`, `m`, and the temperature coefficient parameters. In AC analyses, the only active parameters are `m`, `mag` and `phase`. In transient analyses, all parameters are active except the small signal parameters and the noise parameters. The `type` parameter selects which type of waveform is generated. You may specify parameters for more than one waveform type, and use the `alter` statement to change the waveform type between analyses.

A vector of time-value pairs describes the piecewise linear waveform. As an alternative, you can read the waveform from a file. In this case, you give time-value pairs one pair per line with a space or tab between the time and the value.

If you set `allbrkpts` to `yes`, you force the simulator to place time points at each point specified in a PWL waveform during a transient analysis. This can be very expensive for waveforms with many points. If you set `allbrkpts` to `no`, Spectre inspects the waveform, looking for abrupt changes, and forces time points only at those changes.

The PWL waveform is periodic if you specify `pwlperiod`. If the value of the waveform specified is not exactly the same at both its beginning and its end, then you must provide a nonzero value `twidth`. Before repeating, the waveform changes linearly in an interval of `twidth` from its value at `(period - twidth)` to its value at the beginning of the waveform. Thus `twidth` must always be less than `period`.

You can give the excess noise of the source as a file or specify it with a vector of frequency-noise pairs. For a file, give the frequency-noise pairs one pair per line with a space or tab between the frequency and noise values.

Operating-Point Parameters

1 `v (V)` Voltage across the source.

2 `i (A)` Current through the source.

3 `pwr (W)` Power dissipation.

Spectre Circuit Simulator Reference

Component Statements Part 2

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

allbrkpts	I-16	freq	I-21	phase	I-40	td1	I-33
ammodfreq	I-30	freq2	I-24	pwlperiod	I-17	td2	I-35
ammodindex	I-29	fundname	I-3	pwr	OP-3	tnom	I-47
ammodphase	I-31	fundname2	I-26	rise	I-8	twidht	I-18
ampl	I-20	i	OP-2	scale	I-14	type	I-2
ampl2	I-23	m	I-44	sinedc	I-19	v	OP-1
damp	I-32	mag	I-39	sinephase	I-22	val0	I-5
dc	I-1	noisefile	I-37	sinephase2	I-25	vall	I-6
delay	I-4	noisevec	I-38	stretch	I-15	wave	I-12
fall	I-9	offset	I-13	tau1	I-34	width	I-10
file	I-11	pacmag	I-42	tau2	I-36	xfmag	I-41
fmodfreq	I-28	pacphase	I-43	tc1	I-45		
fmodindex	I-27	period	I-7	tc2	I-46		

Winding for Magnetic Core (winding)

Description

This winding is used in conjunction with magnetic cores to model coils and transformers with hysteresis. Each winding must be associated with a single core, though a core may have any number of windings.

Winding connects terminals **t1** and **b1**. Current through the winding is computed.

This device is not supported within **altergroup**.

Sample Instance Statement

```
c1 (1 0) core_mod area=1.2 len=8.1 id=0.45 id=0.55 gap=0.25
y1 (2 0) winding turn=5 core=c1 resis=1m
```

Instance Definition

```
Name t b winding parameter=value ...
```

Instance Parameters

- 1 `turn=1` Number of turns on winding.
- 2 `resis (Ω)` Resistance of the winding.
- 3 `m=1` Multiplicity factor.
- 4 `core` Name of core around which winding is wrapped.

Initial Conditions

- 5 `ic=0.0` Initial condition on the winding.

z-Domain Linear Current Controlled Current Source (zcccs)

Description

The output is defined with a transfer function given as the ratio of two polynomials in the complex variable z . Each polynomial can be specified using either its coefficients or its roots. The roots of the numerator are the zeros of the transfer function and the roots of the denominator are the poles.

You may specify polynomials either in the complex variable z or $1/z$ by setting optional parameter `polyarg` to `z` or `inversez` respectively. By default, it is set to `inversez`. If you choose to provide the coefficients of a polynomial, enter them as a vector in ascending order of the power of the variable z or $1/z$, starting from the constant term. For example, to specify a denominator of $3z^{-2} + 4z^{-1} + 1$, use `denom=[1 4 3]`. Or to specify a denominator of $4z^2 + 3z - 2$, use `polyarg=z denom=[2 3 4]`.

To specify transfer function in terms of its zeros and poles in z -plane, give them as vectors of complex numbers. You must always give the real and imaginary portions of the root, even when the root is real. You may give either both roots of a complex-conjugate pair or only one. In the latter case the conjugate complex root will be generated automatically. The order of the roots is not important. For example, to specify poles of $z = 1$, $z = 4j$, $z = -4j$, $z = 2 + 2j$, and $z = 2 - 2j$, use `poles=[1 0 0 4 0 -4 2 2 2 -2]` or, omitting conjugate poles, `poles=[1 0 0 4 2 2]`.

Either the numerator or the denominator specification can be omitted. An omitted denominator or numerator is taken to be 1.

Spectre Circuit Simulator Reference

Component Statements Part 2

The parameter `gain` is interpreted either as the DC gain or, if the function has zeros or poles on the unit circle, as a constant factor.

Transition time (`tt`) is an optional parameter that at each sampling point forces linear transition of the output to a new value within the specified time range. By default, it is set to one percent of the sampling period.

The sampling delay (`td`) is another optional parameter, with the default value of 0, that lets you set asynchronous sampling rates.

To use the `s` to `z` transformation, set the optional `sxz` parameter to one of the transformation methods - forward differences, backward differences, or bilinear. When the `sxz` parameter is specified, the transfer function specification is assumed to be given in the complex variable `s` and it will be transformed to the complex variable `z` using the indicated method.

This device is not supported within `altergroup`.

Sample Instance Statement

```
va (1 0) vsource type=sine freq=10K
z2 (2 0) zcccs probe=va gain=1 ts=4.9e-5 tt=1e-5 polyarg=inservez
numer=[1 -1] denom=[1 0]
```

Instance Definition

```
Name sink src zcccs parameter=value ...
```

Instance Parameters

- 1 `probe` Device through which the controlling current flows.
- 2 `port=0` Index of the probe port through which the controlling current flows.
- 3 `ts=1 s` Sampling period.
- 4 `td=0 s` Sampling delay.
- 5 `tt=0.01 ts s` Transition time.
- 6 `gain=1` Transfer function gain.
- 7 `polyarg=inservez` Polynomial argument.
Possible values are `z` or `inservez`.

Spectre Circuit Simulator Reference

Component Statements Part 2

8 `sxz=none`s to z transformation.

Possible values are none, backward, forward, or bilinear.

9 `numer=[...]`Vector of numerator coefficients.

10 `denom=[...]`Vector of denominator coefficients.

11 `zeros=[...]`Vector of complex zeros.

12 `poles=[...]`Vector of complex poles.

13 `m=1`Multiplicity factor.

Operating-Point Parameters

1 `i` (A) Input current.

2 `v` (V) Output voltage.

3 `pwr` (W) Power dissipation.

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

<code>denom</code>	I-10	<code>numer</code>	I-9	<code>probe</code>	I-1	<code>ts</code>	I-3
<code>gain</code>	I-6	<code>poles</code>	I-12	<code>pwr</code>	OP-3	<code>tt</code>	I-5
<code>i</code>	OP-1	<code>polyarg</code>	I-7	<code>sxz</code>	I-8	<code>v</code>	OP-2
<code>m</code>	I-13	<code>port</code>	I-2	<code>td</code>	I-4	<code>zeros</code>	I-11

z-Domain Current Controlled Voltage Source (zccvs)

Description

The output is defined with a transfer function given as the ratio of two polynomials in the complex variable z . Each polynomial can be specified using either its coefficients or its roots. The roots of the numerator are the zeros of the transfer function and the roots of the denominator are the poles.

You may specify polynomials either in the complex variable z or $1/z$ by setting optional parameter `polyarg` to `z` or `inversez` respectively. By default, it is set to `inversez`. If you choose to provide the coefficients of a polynomial, enter them as a vector in ascending order of the power of the variable z or $1/z$, starting from the constant term. For example, to specify a denominator of $3z^{-2} + 4z^{-1} + 1$, use `denom=[1 4 3]`. Or to specify a denominator of $4z^2 + 3z - 2$, use `polyarg=z denom=[2 3 4]`.

To specify transfer function in terms of its zeros and poles in z -plane, give them as vectors of complex numbers. You must always give the real and imaginary portions of the root, even when the root is real. You may give either both roots of a complex-conjugate pair or only one. In the latter case the conjugate complex root will be generated automatically. The order of the roots is not important. For example, to specify poles of $z = 1$, $z = 4j$, $z = -4j$, $z = 2 + 2j$, and $z = 2 - 2j$, use `poles=[1 0 0 4 0 -4 2 2 2 -2]` or, omitting conjugate poles, `poles=[1 0 0 4 2 2]`.

Either the numerator or the denominator specification can be omitted. An omitted denominator or numerator is taken to be 1.

The parameter `gain` is interpreted either as the DC gain or, if the function has zeros or poles on the unit circle, as a constant factor.

Transition time (`tt`) is an optional parameter that at each sampling point forces linear transition of the output to a new value within the specified time range. By default, it is set to one percent of the sampling period.

The sampling delay (`td`) is another optional parameter, with the default value of 0, that lets you set asynchronous sampling rates.

To use the s to z transformation, set the optional `sxz` parameter to one of the transformation methods - forward differences, backward differences, or bilinear. When the `sxz` parameter is specified, the transfer function specification is assumed to be given in the complex variable s and it will be transformed to the complex variable z using the indicated method.

This device is not supported within altergroup.

Spectre Circuit Simulator Reference

Component Statements Part 2

Sample Instance Statement

```
va (1 0) vsource type=sine freq=10K
z2 2 0 zccvs probe=va gain=-2 ts=5e-5 tt=1.1e-5 numer=[1 -1]
```

Instance Definition

Name p n zccvs parameter=value ...

Instance Parameters

- 1 probeDevice through which the controlling current flows.
- 2 port=0 Index of the probe port through which the controlling current flows.
- 3 ts=1 s Sampling period.
- 4 td=0 s Sampling delay.
- 5 tt=0.01 ts s Transition time.
- 6 gain=1 Transfer function gain.
- 7 polyarg=inversez Polynomial argument.
Possible values are z or inversez.
- 8 sxz=none s to z transformation.
Possible values are none, backward, forward, or bilinear.
- 9 numer=[...] Vector of numerator coefficients.
- 10 denom=[...] Vector of denominator coefficients.
- 11 zeros=[...] Vector of complex zeros.
- 12 poles=[...] Vector of complex poles.
- 13 m=1 Multiplicity factor.

Operating-Point Parameters

- 1 i (A) Output current.

Spectre Circuit Simulator Reference

Component Statements Part 2

2 `v` (V) Output voltage.

3 `pwr` (W) Power dissipation.

Parameter Index

In the following index, `I` refers to instance parameters, `M` refers to the model parameters section, `O` refers to the output parameters section, and `OP` refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of `M-35` means the 35th model parameter.

<code>denom</code>	<code>I-10</code>	<code>numer</code>	<code>I-9</code>	<code>probe</code>	<code>I-1</code>	<code>ts</code>	<code>I-3</code>
<code>gain</code>	<code>I-6</code>	<code>poles</code>	<code>I-12</code>	<code>pwr</code>	<code>OP-3</code>	<code>tt</code>	<code>I-5</code>
<code>i</code>	<code>OP-1</code>	<code>polyarg</code>	<code>I-7</code>	<code>sxz</code>	<code>I-8</code>	<code>v</code>	<code>OP-2</code>
<code>m</code>	<code>I-13</code>	<code>port</code>	<code>I-2</code>	<code>td</code>	<code>I-4</code>	<code>zeros</code>	<code>I-11</code>

z-Domain Linear Voltage Controlled Current Source (zvccs)

Description

The output is defined with a transfer function given as the ratio of two polynomials in the complex variable z . Each polynomial can be specified using either its coefficients or its roots. The roots of the numerator are the zeros of the transfer function and the roots of the denominator are the poles.

You may specify polynomials either in the complex variable z or $1/z$ by setting optional parameter `polyarg` to `z` or `inversez` respectively. By default, it is set to `inversez`. If you choose to provide the coefficients of a polynomial, enter them as a vector in ascending order of the power of the variable z or $1/z$, starting from the constant term. For example, to specify a denominator of $3z^{-2} + 4z^{-1} + 1$, use `denom=[1 4 3]`. Or to specify a denominator of $4z^2 + 3z - 2$, use `polyarg=z denom=[2 3 4]`.

To specify transfer function in terms of its zeros and poles in z -plane, give them as vectors of complex numbers. You must always give the real and imaginary portions of the root, even when the root is real. You may give either both roots of a complex-conjugate pair or only one. In the latter case the conjugate complex root will be generated automatically. The order of the roots is not important. For example, to specify poles of $z = 1$, $z = 4j$, $z = -4j$, $z = 2 + 2j$, and z

Spectre Circuit Simulator Reference

Component Statements Part 2

= 2 - 2j, use `poles=[1 0 0 4 0 -4 2 2 2 -2]` or, omitting conjugate poles, `poles=[1 0 0 4 2 2]`.

Either the numerator or the denominator specification can be omitted. An omitted denominator or numerator is taken to be 1.

The parameter `gain` is interpreted either as the DC gain or, if the function has zeros or poles on the unit circle, as a constant factor.

Transition time (`tt`) is an optional parameter that at each sampling point forces linear transition of the output to a new value within the specified time range. By default, it is set to one percent of the sampling period.

The sampling delay (`td`) is another optional parameter, with the default value of 0, that lets you set asynchronous sampling rates.

To use the s to z transformation, set the optional `sxz` parameter to one of the transformation methods - forward differences, backward differences, or bilinear. When the `sxz` parameter is specified, the transfer function specification is assumed to be given in the complex variable s and it will be transformed to the complex variable z using the indicated method.

This device is not supported within `altergroup`.

Sample Instance Statement

```
va (1 0) vsource type=sine freq=10K
z1 (2 0 1 0) zvccs gain=2 ts=4.5e-5 tt=1e-5 zeros=[-1 0] poles=[0 0]
```

Instance Definition

```
Name sink src ps ns zvccs parameter=value ...
```

Instance Parameters

- 1 `ts=1` s Sampling period.
- 2 `td=0` s Sampling delay.
- 3 `tt=0.01` ts s Transition time.
- 4 `gain=1` Transfer function gain.
- 5 `polyarg=inversez` Polynomial argument.
Possible values are `z` or `inversez`.

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Component Statements Part 2

- 6 `szz=none` to z transformation.
Possible values are none, backward, forward, or bilinear.
- 7 `numer=[...]` Vector of numerator coefficients.
- 8 `denom=[...]` Vector of denominator coefficients.
- 9 `zeros=[...]` Vector of complex zeros.
- 10 `poles=[...]` Vector of complex poles.
- 11 `m=1` Multiplicity factor.

Operating-Point Parameters

- 1 `i` (A) Output current.
- 2 `v` (V) Output voltage.
- 3 `pwr` (W) Power dissipation.

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

<code>denom</code>	I-8	<code>numer</code>	I-7	<code>szz</code>	I-6	<code>v</code>	OP-2
<code>gain</code>	I-4	<code>poles</code>	I-10	<code>td</code>	I-2	<code>zeros</code>	I-9
<code>i</code>	OP-1	<code>polyarg</code>	I-5	<code>ts</code>	I-1		
<code>m</code>	I-11	<code>pwr</code>	OP-3	<code>tt</code>	I-3		

z-Domain Voltage Controlled Voltage Source (zvcvs)

Description

The output is defined with a transfer function given as the ratio of two polynomials in the complex variable z . Each polynomial can be specified using either its coefficients or its roots. The roots of the numerator are the zeros of the transfer function and the roots of the denominator are the poles.

You may specify polynomials either in the complex variable z or $1/z$ by setting optional parameter `polyarg` to `z` or `inversez` respectively. By default, it is set to `inversez`. If you choose to provide the coefficients of a polynomial, enter them as a vector in ascending order of the power of the variable z or $1/z$, starting from the constant term. For example, to specify a denominator of $3z^{-2} + 4z^{-1} + 1$, use `denom=[1 4 3]`. Or to specify a denominator of $4z^2 + 3z - 2$, use `polyarg=z denom=[2 3 4]`.

To specify transfer function in terms of its zeros and poles in z -plane, give them as vectors of complex numbers. You must always give the real and imaginary portions of the root, even when the root is real. You may give either both roots of a complex-conjugate pair or only one. In the latter case the conjugate complex root will be generated automatically. The order of the roots is not important. For example, to specify poles of $z = 1$, $z = 4j$, $z = -4j$, $z = 2 + 2j$, and $z = 2 - 2j$, use `poles=[1 0 0 4 0 -4 2 2 2 -2]` or, omitting conjugate poles, `poles=[1 0 0 4 2 2]`.

Either the numerator or the denominator specification can be omitted. An omitted denominator or numerator is taken to be 1.

The parameter `gain` is interpreted either as the DC gain or, if the function has zeros or poles on the unit circle, as a constant factor.

Transition time (`tt`) is an optional parameter that at each sampling point forces linear transition of the output to a new value within the specified time range. By default, it is set to one percent of the sampling period.

The sampling delay (`td`) is another optional parameter, with the default value of 0, that lets you set asynchronous sampling rates.

To use the s to z transformation, set the optional `sxz` parameter to one of the transformation methods - forward differences, backward differences, or bilinear. When the `sxz` parameter is specified, the transfer function specification is assumed to be given in the complex variable s and it will be transformed to the complex variable z using the indicated method.

This device is not supported within `altergroup`.

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Component Statements Part 2

Sample Instance Statement

```
va (1 0) vsource type=sine freq=10K
z3 (3 0 1 0) zvcvs gain=-1 ts=4e-5 tt=1e-5 numer=[-1 -1]
```

Instance Definition

Name p n ps ns zvcvs parameter=value ...

Instance Parameters

1 ts=1 sSampling period.

2 td=0 sSampling delay.

3 tt=0.01 ts sTransition time.

4 gain=1Transfer function gain.

5 polyarg=inversezPolynomial argument.

Possible values are z or inversez.

6 sxz=none to z transformation.

Possible values are none, backward, forward, or bilinear.

7 numer=[...]Vector of numerator coefficients.

8 denom=[...]Vector of denominator coefficients.

9 zeros=[...]Vector of complex zeros.

10 poles=[...]Vector of complex poles.

11 m=1Multiplicity factor.

Operating-Point Parameters

1 i (A)Output current.

2 v (V)Output voltage.

3 pwr (W)Power dissipation.

Spectre Circuit Simulator Reference

Component Statements Part 2

Parameter Index

In the following index, **I** refers to instance parameters, **M** refers to the model parameters section, **O** refers to the output parameters section, and **OP** refers to the operating point parameters section. The number indicates where to look in the appropriate section to find the description for that parameter. For example, a reference of **M-35** means the 35th model parameter.

denom	I-8	numer	I-7	szx	I-6	v	OP-2
gain	I-4	poles	I-10	td	I-2	zeros	I-9
i	OP-1	polyarg	I-5	ts	I-1		
m	I-11	pwr	OP-3	tt	I-3		

Analysis Statements

This chapter discusses the following topics:

- [AC Analysis \(ac\)](#) on page 553
- [Alter a Circuit, Component, or Netlist Parameter \(alter\)](#) on page 555
- [Alter Group \(altergroup\)](#) on page 556
- [Check Parameter Values \(check\)](#) on page 558
- [DC Analysis \(dc\)](#) on page 558
- [DC Device Matching Analysis \(dcmatch\)](#) on page 562
- [Envelope Following Analysis \(envlp\)](#) on page 566
- [Circuit Information \(info\)](#) on page 571
- [Monte Carlo Analysis \(montecarlo\)](#) on page 573
- [Noise Analysis \(noise\)](#) on page 583
- [Immediate Set Options \(options\)](#) on page 587
- [Periodic AC Analysis \(pac\)](#) on page 594
- [Periodic Distortion Analysis \(pdisto\)](#) on page 598
- [Periodic Noise Analysis \(pnoise\)](#) on page 605
- [Periodic S-Parameter Analysis \(psp\)](#) on page 610
- [Periodic Steady-State Analysis \(pss\)](#) on page 616
- [Periodic Transfer Function Analysis \(pxf\)](#) on page 628
- [Quasi-Periodic AC Analysis \(qpac\)](#) on page 633
- [Quasi-Periodic Noise Analysis \(qpnoise\)](#) on page 637
- [Quasi-Periodic S-Parameter Analysis \(qpssp\)](#) on page 643

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Analysis Statements

- [Quasi-Periodic Steady State Analysis \(qpss\)](#) on page 649
- [Quasi-Periodic Transfer Function Analysis \(qpxf\)](#) on page 656
- [Deferred Set Options \(set\)](#) on page 660
- [Shell Command \(shell\)](#) on page 664
- [S-Parameter Analysis \(sp\)](#) on page 664
- [Stability Analysis \(stb\)](#) on page 668
- [Sweep Analysis \(sweep\)](#) on page 673
- [Time-Domain Reflectometer Analysis \(tdr\)](#) on page 675
- [Transient Analysis \(tran\)](#) on page 677
- [Transfer Function Analysis \(xf\)](#) on page 685

AC Analysis (ac)

Description

The AC analysis linearizes the circuit about the DC operating point and computes the response to a given small sinusoidal stimulus.

Spectre can perform the analysis while sweeping a parameter. The parameter can be frequency, temperature, component instance parameter, component model parameter, or netlist parameter. If changing a parameter affects the DC operating point, the operating point is recomputed on each step. You can sweep the circuit temperature by giving the parameter name as `temp` with no `dev` or `mod` parameter. You can sweep a netlist parameter by giving the parameter name with no `dev`, or `mod` parameter. After the analysis has completed, the modified parameter returns to its original value.

Definition

Name `ac parameter=value ...`

Parameters

1	<code>prevoppoint=no</code>	Use operating point computed on the previous analysis. Possible values are <code>no</code> or <code>yes</code> .
---	-----------------------------	---------------------------------------------------------------------------------------------------------------------

Sweep interval parameters

2	<code>start=0</code>	Start sweep limit.
3	<code>stop</code>	Stop sweep limit.
4	<code>center</code>	Center of sweep.
5	<code>span=0</code>	Sweep limit span.
6	<code>step</code>	Step size, linear sweep.
7	<code>lin=50</code>	Number of steps, linear sweep.
8	<code>dec</code>	Points per decade.

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Analysis Statements

9 `log=50` Number of steps, log sweep.

10 `values=[...]` Array of sweep values.

Sweep variable parameters

11 `dev` Device instance whose parameter value is to be swept.

12 `mod` Model whose parameter value is to be swept.

13 `param` Name of parameter to sweep.

14 `freq (Hz)` Frequency when parameter other than frequency is being swept.

State-file parameters

15 `readns` File that contains estimate of DC solution (nodeset).

Output parameters

16 `save` Signals to output.
Possible values are `all`, `lvl`, `allpub`, `lvlpub`, `selected`, or `none`.

17 `nestlvl` Levels of subcircuits to output.

18 `oppoint=no` Should operating point information be computed, and if so, where should it be sent.
Possible values are `no`, `screen`, `logfile`, or `rawfile`.

Convergence parameters

19 `restart=yes` Do not use previous DC solution as initial guess.
Possible values are `no` or `yes`.

Annotation parameters

20 `annotate=sweep` Degree of annotation.
Possible values are `no`, `title`, `sweep`, `status`, or `steps`.

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Analysis Statements

21 `stats=no` Analysis statistics.
Possible values are `no` or `yes`.

22 `title` Analysis title.

You can specify sweep limits by giving the end points or by providing the center value and the span of the sweep. Steps can be linear or logarithmic, and you can specify the number of steps or the size of each step. You can give a step size parameter (`step`, `lin`, `log`, `dec`) to determine whether the sweep is linear or logarithmic. If you do not give a step size parameter, the sweep is linear when the ratio of stop to start values is less than 10, and logarithmic when this ratio is 10 or greater. All frequencies are in Hertz.

The small-signal analysis begins by linearizing the circuit about an operating-point. By default this analysis computes the operating-point if it is not known, or recomputes it if any significant component or circuit parameter has changed. However, if a previous analysis computed an operating point, you can set `prevoppoint=yes` to avoid recomputing it. For example, if you use this option when the previous analysis was a transient analysis, the operating point is the state of the circuit on the final time point.

Parameter Index

In the following index, the number following each parameter name indicates where to find the description of that parameter.

<code>annotate</code> 20	<code>log</code> 9	<code>readns</code> 15	<code>step</code> 6
<code>center</code> 4	<code>mod</code> 12	<code>restart</code> 19	<code>stop</code> 3
<code>dec</code> 8	<code>nestlvl</code> 17	<code>save</code> 16	<code>title</code> 22
<code>dev</code> 11	<code>oppoint</code> 18	<code>span</code> 5	<code>values</code> 10
<code>freq</code> 14	<code>param</code> 13	<code>start</code> 2	
<code>lin</code> 7	<code>prevoppoint</code> 1	<code>stats</code> 21	

Alter a Circuit, Component, or Netlist Parameter (`alter`)

Description

The `alter` statement changes the value of any modifiable component or netlist parameter for any analyses that follow. The parameter to be altered can be circuit temperature, a device instance parameter, a device model parameter, a netlist parameter, or a subcircuit parameter for a particular subcircuit instance. You can alter the circuit temperature by giving the parameter name as `param=temp` with no `dev`, `mod` or `sub` parameter. You can alter a top-

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Analysis Statements

level netlist parameter by giving the parameter name with no `dev`, `mod` or `sub` parameter. You can alter a subcircuit parameter for a particular subcircuit instance by specifying the subcircuit instance name with the `sub` parameter, and the subcircuit parameter name with the `param` parameter. Each `alter` statement can change only one parameter.

Definition

Name `alter parameter=value ...`

Parameters

1	<code>mod</code>	Device model.
2	<code>dev</code>	Device instance.
3	<code>sub</code>	Subcircuit instance.
4	<code>param</code>	Name of parameter to be altered.
5	<code>value</code>	New value for parameter.
6	<code>annotate</code>	Degree of annotation. Possible values are <code>no</code> or <code>title</code> .

Alter Group (`altergroup`)

Description

The `altergroup` statement changes the values of any modifiable model, instance or netlist parameter for any analyses that follow. Within an alter group, you can specify model statements, instance statements and parameter statements. These statements should be bound within braces. The opening brace is required at the end of the line defining the alter group. Alter groups cannot be nested or specified within subcircuits. The following statements are not allowed within altergroups (`analyses`, `export`, `ic`, `nodeset`, `paramset`, `save`, and `sens`).

Within an alter group, each device (instance or model) is first defaulted and then the device parameters are updated. For netlist parameters, the expressions are updated and evaluated.

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For subckt within altergroup, all instances of the subckts are modified during the altergroup. There are strict checks that do not allow changes to topology.

You can include files into the alter group and can use the `simulator lang=spice` directive. See `spectre -h include` for more details. A model defined in the netlist, has to have the same model name and primitive type (`bsim2`, `bsim3`, `bjt`) in the alter group. An instance defined in the netlist, has to have the same instance name, terminal connections and primitive type. For model groups you can change the number of models in the group. There is a restriction that you cannot change from a model to a model group and vice versa. See `spectre -h bsim3v3` for details on model groups.

Definition

Name altergroup parameter=value ...

Parameters

1 `annotate` Degree of annotation.
Possible values are `no` or `title`.

Example:

```
FastCorner altergroup {
    parameters p2=1 p3=p1+2
    model myres resistor r1=1e3 af=1
    model mybsim bsim3v3 lmax=p1 lmin=3.5e-7
    m1 (n1 n2 n3 n4) mybsim w=0.3u l=1.2u
}
```

The list of public devices supported by altergroup:

tline	vsource	vcvs	vccs
vbic	tom2	resistor	phy_res
msline	mos3	mos2	mos1
jfet	isource	inductor	hvmos
hbt	gaas	ekv	diode
ccvs	cccs	capacitor	btasoi
bsim4	bsim3v3	bsim3	bsim2
bsim1	bjt		

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The list of public devices not supported by altergroup:

cktrom	zvcvs	zvccs	zccvs
zcccs	winding	transformer	switch
svcvs	svccs	sccvs	scccs
relay	pvcvs	pvccs	port
pccvs	pcccs	nport	mtline
mos0	mutual_inductor	iprobe	delay
d2a	core	b3soipd	a2d

Check Parameter Values (check)

Description

The `check` analysis checks the values of component parameters to assure they are reasonable. This analysis reduces the cost of data entry errors. Various filters specify which parameters are checked. You can perform checks on input, output, or operating-point parameters. Use this analysis in conjunction with the `+param` command line argument, which specifies a file that contains component parameter soft limits.

Definition

Name `check parameter=value ...`

Parameters

- | | | |
|---|-----------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 1 | <code>what=all</code> | What parameters should be checked.
Possible values are <code>none</code> , <code>inst</code> , <code>models</code> , <code>input</code> , <code>output</code> , <code>all</code> ,
or <code>oppoint</code> . |
|---|-----------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|

DC Analysis (dc)

Description

The DC analysis finds the DC operating-point or DC transfer curves of the circuit. To generate transfer curves, specify a parameter and a sweep range. The swept parameter can be circuit temperature, a device instance parameter, a device model parameter, a netlist parameter, or

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a subcircuit parameter for a particular subcircuit instance. You can sweep the circuit temperature by giving the parameter name as `param=temp` with no `dev`, `mod` or `sub` parameter. You can sweep a top-level netlist parameter by giving the parameter name with no `dev`, `mod` or `sub` parameter. You can sweep a subcircuit parameter for a particular subcircuit instance by specifying the subcircuit instance name with the `sub` parameter, and the subcircuit parameter name with the `param` parameter. After the analysis has completed, the modified parameter returns to its original value.

Definition

Name `dc parameter=value ...`

Parameters

Sweep interval parameters

1	<code>start=0</code>	Start sweep limit.
2	<code>stop</code>	Stop sweep limit.
3	<code>center</code>	Center of sweep.
4	<code>span=0</code>	Sweep limit span.
5	<code>step</code>	Step size, linear sweep.
6	<code>lin=50</code>	Number of steps, linear sweep.
7	<code>dec</code>	Points per decade.
8	<code>log=50</code>	Number of steps, log sweep.
9	<code>values=[...]</code>	Array of sweep values.

Sweep variable parameters

10	<code>dev</code>	Device instance whose parameter value is to be swept.
11	<code>mod</code>	Model whose parameter value is to be swept.

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12 `param` Name of parameter to sweep.

State-file parameters

13 `force=none` What should be used to force values for DC. Uses the values from the device and node ICs.
Possible values are `none`, `node`, `dev`, or `all`.

14 `readns` File that contains estimate of DC solution (`nodeset`).

15 `readforce` File that contains force values.

16 `write` File to which solution at first step in sweep is written.

17 `writefinal` File to which solution at last step in sweep is written.

Output parameters

18 `save` Signals to output.
Possible values are `all`, `lvl`, `allpub`, `lvlpub`, `selected`, or `none`.

19 `nestlvl` Levels of subcircuits to output.

20 `print=no` Print node voltages.
Possible values are `no` or `yes`.

21 `oppoint=no` Should operating point information be computed, and if so, where should it be sent.
Possible values are `no`, `screen`, `logfile`, or `rawfile`.

22 `check=yes` Check operating point parameters against soft limits.
Possible values are `no` or `yes`.

Convergence parameters

23 `homotopy=all` Method used when no convergence on initial attempt of DC analysis.
Possible values are `none`, `gmin`, `source`, `dptran`, `ptran`, `arclength`, or `all`.

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Analysis Statements

- | | | |
|----|-----------------------------|-------------------------------------------------------------------------------------------------------------|
| 24 | <code>restart=yes</code> | Do not use previous solution as initial guess.
Possible values are <code>no</code> or <code>yes</code> . |
| 25 | <code>maxiters=150</code> | Maximum number of iterations. |
| 26 | <code>maxsteps=10000</code> | Maximum number of steps used in homotopy method. |

Annotation parameters

- | | | |
|----|-----------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------|
| 27 | <code>annotate=sweep</code> | Degree of annotation.
Possible values are <code>no</code> , <code>title</code> , <code>sweep</code> , <code>status</code> , or <code>steps</code> . |
| 28 | <code>title</code> | Analysis title. |

You can specify sweep limits by giving the end points or by providing the center value and the span of the sweep. Steps can be linear or logarithmic, and you can specify the number of steps or the size of each step. You can give a step size parameter (`step`, `lin`, `log`, `dec`) and determine whether the sweep is linear or logarithmic. If you do not give a step size parameter, the sweep is linear when the ratio of stop to start values is less than 10, and logarithmic when this ratio is 10 or greater. If you specify the `oppoint` parameter, Spectre computes and outputs the linearized model for each nonlinear component.

Nodesets help find the DC or initial transient solution. You can supply them in the circuit description file with `nodeset` statements, or in a separate file using the `readns` parameter. When nodesets are given, Spectre computes an initial guess of the solution by performing a DC analysis while forcing the specified values onto nodes by using a voltage source in series with a resistor whose resistance is `rforce`. Spectre then removes these voltage sources and resistors and computes the true solution from this initial guess.

Nodesets have two important uses. First, if a circuit has two or more solutions, nodesets can bias the simulator towards computing the desired one. Second, they are a convergence aid. By estimating the solution of the largest possible number of nodes, you might be able to eliminate a convergence problem or dramatically speed convergence.

When you simulate the same circuit many times, we suggest that you use both the `write` and `readns` parameters and give the same file name to both parameters. The DC analysis then converges quickly even if the circuit has changed somewhat since the last simulation, and the nodeset file is automatically updated.

You may specify values to force for the DC analysis by setting the parameter `force`. The values used to force signals are specified by using the `force` file, the `ic` statement, or the `ic` parameter on the capacitors and inductors. The `force` parameter controls the interaction of various methods of setting the force values. The effects of individual settings are:

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`force=none`: Any initial condition specifiers are ignored.

`force=node`: The `ic` statements are used, and the `ic` parameter on the capacitors and inductors are ignored.

`force=dev`: The `ic` parameters on the capacitors and inductors are used, and the `ic` statements are ignored.

`force=all`: Both the `ic` statements and the `ic` parameters are used, with the `ic` parameters overriding the `ic` statements.

If you specify a `force` file with the `readforce` parameter, force values read from the file are used, and any `ic` statements are ignored.

Once you specify the force conditions, Spectre computes the DC analysis with the specified nodes forced to the given value by using a voltage source in series with a resistor whose resistance is `rforce` (see `options`).

Parameter Index

In the following index, the number following each parameter name indicates where to find the description of that parameter.

<code>annotate</code> 27	<code>lin</code> 6	<code>param</code> 12	<code>start</code> 1
<code>center</code> 3	<code>log</code> 8	<code>print</code> 20	<code>step</code> 5
<code>check</code> 22	<code>maxiters</code> 25	<code>readforce</code> 15	<code>stop</code> 2
<code>dec</code> 7	<code>maxsteps</code> 26	<code>readns</code> 14	<code>title</code> 28
<code>dev</code> 10	<code>mod</code> 11	<code>restart</code> 24	<code>values</code> 9
<code>force</code> 13	<code>nestlvl</code> 19	<code>save</code> 18	<code>write</code> 16
<code>homotopy</code> 23	<code>oppoint</code> 21	<code>span</code> 4	<code>writefinal</code> 17

DC Device Matching Analysis (`dcmatch`)

Description

The `dcmatch` analysis performs DC device mis-matching analysis for a given output. It computes the deviation in the DC operating point of the circuit caused by mismatch in the devices. Users need to specify mismatch parameters in their model cards for each device contributing to the deviation. The analysis uses the device mismatch models to construct equivalent mismatch current sources to all the devices that have mismatch modeled. These current sources will have zero mean and some variance. The variance of the current sources

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are computed according to mismatch models. It then computes the 3-sigma variance of dc voltages or currents at user specified outputs due to the mismatch current sources. The simulation results displays the devices rank ordered by their contributions to the outputs. In addition, for mosfet devices, it displays threshold voltage mismatch, current factor mismatch, gate voltage mismatch, and drain current mismatch. For bipolar devices, it displays base-emitter junction voltage mismatch. For resistors, it displays resistor mismatches.

The analysis replaces multiple simulation runs by circuit designers for accuracy versus size analysis. It automatically identifies the set of critical matched components during circuit design. For example, when there are matched pairs in the circuit, the contribution of two matched transistors will be equal in magnitude and opposite in sign. Typical usage are to simulate the output offset voltage of operational amplifiers, estimate the variation in bandgap voltages, and predict the accuracy of current steering DACs.

Definition

Name ... dcmatch parameter=value ...

Parameters

- | | | |
|---|----------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 1 | <code> mth</code> | Relative mismatch contribution threshold value. |
| 2 | <code> where=screen</code> | Where DC-Mismatch analysis results should be printed.
Possible values are <code>screen</code> , <code>logfile</code> , <code>file</code> , or <code>rawfile</code> . |
| 3 | <code> file</code> | File name for results to be printed if <code>where=file</code> is used. |

Probe parameters

- | | | |
|---|----------------------|-----------------------------------------------------------|
| 4 | <code> oprobe</code> | Compute mismatch at the output defined by this component. |
|---|----------------------|-----------------------------------------------------------|

Port parameters

- | | | |
|---|---------------------|------------------------------------------------------------|
| 5 | <code> portv</code> | Voltage across this probe port is output of the analysis. |
| 6 | <code> porti</code> | Current through this probe port is output of the analysis. |

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Analysis Statements

Sweep interval parameters

7	<code>start=0</code>	Start sweep limit.
8	<code>stop</code>	Stop sweep limit.
9	<code>center</code>	Center of sweep.
10	<code>span=0</code>	Sweep limit span.
11	<code>step</code>	Step size, linear sweep.
12	<code>lin=50</code>	Number of steps, linear sweep.
13	<code>dec</code>	Points per decade.
14	<code>log=50</code>	Number of steps, log sweep.
15	<code>values=[...]</code>	Array of sweep values.

Sweep variable parameters

16	<code>dev</code>	Device instance whose parameter value is to be swept.
17	<code>mod</code>	Model whose parameter value is to be swept.
18	<code>param</code>	Name of parameter to sweep.

State-file parameters

19	<code>readns</code>	File that contains estimate of DC solution (nodeset).
----	---------------------	-------------------------------------------------------

Output parameters

20	<code>save</code>	Signals to output. Possible values are <code>all</code> , <code>lvl</code> , <code>allpub</code> , <code>lvlpub</code> , <code>selected</code> , or <code>none</code> .
21	<code>nestlvl</code>	Levels of subcircuits to output.

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22 `oppoint=no` Should operating point information be computed, and if so, where should it be sent.
Possible values are `no`, `screen`, `logfile`, or `rawfile`.

Convergence parameters

23 `prevoppoint=no` Use operating point computed on the previous analysis.
Possible values are `no` or `yes`.

24 `restart=yes` Do not use previous DC solution as initial guess.
Possible values are `no` or `yes`.

Annotation parameters

25 `annotate=sweep` Degree of annotation.
Possible values are `no`, `title`, `sweep`, `status`, or `steps`.

26 `stats=no` Analysis statistics.
Possible values are `no` or `yes`.

27 `title` Analysis title.

The `dcmatch` analysis will find a dc operating point first. If the dc analysis fails, then the `dcmatch` analysis will fail also. The parameter `mt h` is a threshold value relative to maximum contribution. Any device contribution less than ($mt h * maximum$) will not be reported where *maximum* is the maximum contribution among all the devices of a given type.

Example

```
dcmm1 dcmatch mth=1e-3 oprobe=vd porti=1
dcmm2 dcmatch mth=1e-3 oprobe=r3 portv=1
dcmm3 n1 n2 dcmatch mth=1e-3 where=rawfile stats=yes
dcmm4 n3 0 dcmatch mth=1e-3 where=file file="%C:r.info.what"
sweep1 sweep dev=mp6 param=w start=80e-6 stop=90e-6 step=2e-6 {
dcmm5 dcmatch oprobe=vd mth=1e-3 where=rawfile }
dcmm6 n3 0 dcmatch mth=0.01 dev=x1.mp2 param=w start=15e-6 stop=20e-6 step=1e-6
dcmm7 n3 0 dcmatch mth=0.01 param=temp start=25 stop=100 step=25
```

Note: `porti` allows users to select a current associated with a specific device given in `oprobe` as an output. This device, however, has to have its terminal currents as network variables, i.e. the device has to be an inductor, a `vsource`, a switch, a `tline`, a controlled voltage source, an `iprobe`, or other type of device which has current solution. Further, for inductor,

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vsource, switch, controlled voltage source and iprobe, `port i` can only be set to one, since these devices are two terminal devices (one port); and for `tline` `port i` can be set to one or two, since it is a four terminal device (two ports).

Parameter Index

In the following index, the number following each parameter name indicates where to find the description of that parameter.

annotate	25	mod	17	portv	5	stats	26
center	9	mth	1	prevoppoint	23	step	11
dec	13	nestlvl	21	readns	19	stop	8
dev	16	oppoint	22	restart	24	title	27
file	3	oprobe	4	save	20	values	15
lin	12	param	18	span	10	where	2
log	14	porti	6	start	7		

Envelope Following Analysis (envlp)

Description

This analysis computes the envelope response of a circuit. The user specifies the analysis `clockname`. The simulator automatically determines the clock period by looking through all the sources with the specified name. The envelope response is computed over the interval from `start` to `stop`. If the interval is not a multiple of the clock period, it is rounded off to the nearest multiple before the stop time. The initial condition is taken to be the DC steady-state solution if not otherwise given.

Envelope following analysis is most efficient for circuits where the modulation bandwidth is orders of magnitude lower than the clock frequency. This is typically the case, for example, in circuits where the clock is the only fast varying signal and other input signals have a spectrum whose frequency range is orders of magnitude lower than the clock frequency. For another example, the down conversion of two closely placed frequencies can also generate a slow-varying modulation envelope.

The analysis generates two types of output files, a voltage versus time (`td`) file, and an amplitude/phase versus time (`fd`) file for each of specified harmonic of the clock fundamental.

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Definition

Name envlp parameter=value ...

Parameters

Envelope fundamental parameters

- 1 `clockname` Name of the clock fundamental.
- 2 `modulationbw (Hz)` Modulation bandwidth.

Simulation interval parameters

- 3 `stop (s)` Stop time.
- 4 `start=0 s` Start time.
- 5 `tstab=0 s` Initial stabilization time.
- 6 `outputstart=start s` Output is saved only after this time is reached.

Time-step parameters

- 7 `maxstep (s)` Maximum time step for inner transient integration. Default derived from `errpreset`.
- 8 `envmaxstep (s)` Maximum outer envelope step size. Default derived from `errpreset`.

Initial-condition parameters

- 9 `ic=all` What should be used to set initial condition. Possible values are `dc`, `node`, `dev`, or `all`.
- 10 `skipdc=no` If yes, there will be no dc analysis for initial transient. Possible values are `no` or `yes`.
- 11 `readic` File that contains initial transient condition.

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Convergence parameters

- 12 `readns` File that contains estimate of initial DC solution.
- 13 `cmin=0 F` Minimum capacitance from each node to ground.

State-file parameters

- 14 `write` File to which initial transient solution is to be written.
- 15 `writefinal` File to which final transient solution is to be written.
- 16 `swapfile` Temporary file that holds the matrix information used by Newton's method. Tells Spectre to use a regular file rather than virtual memory to hold the matrix information. Use this option if Spectre complains about not having enough memory to complete this analysis.

Integration method parameters

- 17 `method` Integration method. Default derived from `errpreset`. Possible values are `euler`, `trap`, `traponly`, `gear2`, or `gear2only`.

Accuracy parameters

- 18 `errpreset=moderate` Selects a reasonable collection of parameter settings. Possible values are `conservative`, `moderate` or `liberal`.
- 19 `relref` Reference used for the relative convergence criteria. Default derived from `errpreset`. Possible values are `pointlocal`, `alllocal`, `sigglobal`, or `allglobal`.
- 20 `lteratio` Ratio used to compute LTE tolerances from Newton tolerance. Default derived from `errpreset`.
- 21 `steadyratio` Ratio used to compute steady state tolerances from LTE tolerance. Default derived from `errpreset`.

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Analysis Statements

22 `envlteratio` Ratio used to compute envelope LTE tolerances. Default derived from `errpreset`.

Annotation parameters

23 `stats=no` Analysis statistics.
Possible values are `no` or `yes`.

24 `annotate=sweep` Degree of annotation.
Possible values are `no`, `title`, `sweep`, `status`, or `steps`.

25 `title` Analysis title.

Output parameters

26 `harms=1` Number of clock harmonics to output.

27 `harmsvec=[...]` Array of desired clock harmonics. Alternate form of `harms` that allows selection of specific harmonics.

28 `outputtype=both` Output type.
Possible values are `both`, `envelope` or `spectrum`.

29 `save` Signals to output.
Possible values are `all`, `lvl`, `allpub`, `lvlpub`, `selected`, or `none`.

30 `nestlvl` Levels of subcircuits to output.

31 `compression=no` Do data compression on output.
Possible values are `no` or `yes`.

32 `strobeperiod (s)` The output strobe interval (in seconds of envelope following time). The actual strobe interval is rounded to an integer multiple of the clock period.

Newton parameters

33 `maxiters=5` Maximum number of Newton iterations per transient integration time step.

34 `envmaxiters=3` Maximum number of Newton iterations per envelope step.

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35 `restart=yes` Do not use previous DC solution as initial guess.
Possible values are `no` or `yes`.

Circuit age

36 `circuitage (Years)` Stress Time. Age of the circuit used to simulate hot-electron degradation of MOSFET and BSIM circuits.

The simulator examines all the sources whose name matches the clock name specified in the analysis line by the `clockname` parameter to determine the clock frequency. If more than one frequencies are found, the least common factor of these frequencies is used as the clock frequency.

The maximum envelope step size is affected by many parameters. It can be directly limited by `envmaxstep`. It is also limited by `modulationbw`. The user gives an estimate of the modulation bandwidth. The simulator will put at least eight points within the modulation period.

The `harms` and `harmsvec` parameters affect the simulation time in a significant way. The spectrum is calculated for all the specified harmonics for all sampled integration cycles as the envelope following analysis marches on. For each harmonic, a file is generated. The user should refrain from specifying unnecessary harmonics. Typically, `harms` is set to 1 or 2.

Most parameters of this analysis are inherited from either transient or PSS analysis and their meanings are consistent. However, a few of them need to be clarified. The effect of `errpreset` on some particular envelope following analysis parameters is shown in the following table.

Parameter defaults as a function of `errpreset`

<code>errpreset</code>	<code>envmaxstep</code>	<code>steayratio</code>	<code>envlteratio</code>
<code>liberal</code>	Interval/10	1.0	10.0
<code>moderate</code>	Interval/50	0.1	1.0
<code>conservative</code>	Interval/100	0.01	0.1

Its effect on parameters such as `reltol`, `relref`, `method`, `maxstep`, and `lteratio` is the same as defined for transient analysis, except for that the transient simulation interval is always a clock period.

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Parameter Index

In the following index, the number following each parameter name indicates where to find the description of that parameter.

annotate	24	harms	26	outputstart	6	stats	23
circuitage	36	harmsvec	27	outputtype	28	steadyratio	21
clockname	1	ic	9	readic	11	stop	3
cmin	13	lteratio	20	readns	12	strobeperiod	32
compression	31	maxiters	33	relref	19	swapfile	16
envlteratio	22	maxstep	7	restart	35	title	25
envmaxiters	34	method	17	save	29	tstab	5
envmaxstep	8	modulationbw	2	skipdc	10	write	14
errpreset	18	nestlvl	30	start	4	writefinal	15

Circuit Information (info)

Description

The circuit information analysis outputs several kinds of information about the circuit and its components. You can use various filters to specify what information is output. You can create a listing of model, instance, temperature-dependent, input, output, and operating point parameters. You can also generate a summary of the minimum and maximum parameter values (by using `extremes=yes` or `only`). Finally, you can request that Spectre provide a node-to-terminal map (by using `what=terminals`) or a terminal-to-node map (by using `what=nodes`).

The following are brief descriptions of the types of parameters you can request with the `info` statement:

Input parameters: Parameters that you specify in the netlist, such as the given length of a MOSFET or the saturation current of a bipolar transistor (use `what=inst, models, input, or all`)

Output parameters: Parameters that are computed by Spectre, such as temperature dependent parameters and the effective length of a MOSFET after scaling (use `what=output or all`)

Operating-point parameters: Parameters that depend on the actual solution computed (use `what=oppoint`)

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Analysis Statements

Definition

Name info parameter=value ...

Parameters

- | | | |
|---|------------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------|
| 1 | <code>what=oppoint</code> | What parameters should be printed.
Possible values are none, inst, models, input, output, nodes, all, terminals, oppoint, captab, or parameters. |
| 2 | <code>where=logfile</code> | Where parameters should be printed.
Possible values are nowhere, screen, file, logfile, or rawfile. |
| 3 | <code>file="%C:r.info.what"</code> | File name when where=file. |
| 4 | <code>save</code> | Signals to output.
Possible values are all, lvl, allpub, lvlpub, selected, or none. |
| 5 | <code>nestlvl</code> | Levels of subcircuits to output. |
| 6 | <code>extremes=yes</code> | Print minimum and maximum values.
Possible values are no, yes or only. |
| 7 | <code>title</code> | Analysis title. |

Captab parameters

- | | | |
|----|----------------------------|--------------------------------------------------------------------------------------------------------|
| 8 | <code>detail=node</code> | How detailed should the capacitance table be.
Possible values are node, nodetoground or nodetonode. |
| 9 | <code>sort=name</code> | How to sort the capacitance table.
Possible values are name or value. |
| 10 | <code>threshold=0 F</code> | Threshold value for printing capacitances (ignore capacitances smaller than this value). |

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Analysis Statements

Parameter Index

In the following index, the number following each parameter name indicates where to find the description of that parameter.

detail 8	nestlvl 5	threshold 10	where 2
extremes 6	save 4	title 7	
file 3	sort 9	what 1	

Monte Carlo Analysis (montecarlo)

Description

The `montecarlo` analysis is a swept analysis with associated child analyses similar to the sweep analysis (see `spectre -h sweep`.) The Monte Carlo analysis refers to statistics blocks" where statistical distributions and correlations of netlist parameters are specified. (Detailed information on statistics blocks is given below.) For each iteration of the Monte Carlo analysis, new pseudo-random values are generated for the specified netlist parameters (according to their specified distributions) and the list of child analyses are then executed.

"export" statements are associated with the child analysis. These export statements allow scalar calculator expressions to be specified which can be used to measure circuit output or performance values (such as for example the slew-rate of an op-amp). For more details see "spectre -h export". During a Monte Carlo analysis, these export statement values will vary as the netlist parameters vary for each Monte Carlo iteration, and are stored in a scalar data file for post processing. By varying netlist parameters and evaluating export statements, the Monte Carlo analysis becomes a tool that allows you to examine and predict circuit performance variations, which affect yield.

The statistics blocks allow you to specify batch-to-batch (process) and per-instance (mismatch) variations for netlist parameters. These statistically-varying netlist parameters can be referenced by models or instances in the main netlist and may represent IC manufacturing process variation, or component variations for board-level designs for example. The following description gives a simplified example of the Monte Carlo analysis flow:

```
perform nominal run if requested
if any errors in nominal run then stop
foreach Monte Carlo iteration {
    if process variations specified then
        apply "process" variation to parameters
```

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```
if mismatch variations specified then
  foreach subcircuit instance {
    apply "mismatch" variation to parameters
  }
foreach child analysis {
  run child analysis
  evaluate any export statements and
  store results in a scalar data file
}
}
```

Definition

Name montecarlo parameter=value ...

Parameters

Analysis parameters

- | | |
|---------------|----------------------------------------------------------------------|
| 1 numruns=100 | Number of Monte Carlo iterations to perform (not including nominal). |
| 2 seed | Optional starting seed for random number generator. |
| 3 scalarfile | Output file that will contain output scalar data. |
| 4 paramfile | Output file that will contain output scalar data labels. |

Saving Process Parameters

- | | |
|---------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------|
| 5 saveprocessparams | Whether or not to save scalar data for statistically varying process parameters which are subject to process variation. Possible values are no or yes. |
| 6 processscalarfile | Output file that will contain process parameter scalar data. |
| 7 processparamfile | Output file that will contain process parameter scalar data labels. |

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8 `saveprocessvec=[...]` Array of statistically varying process parameters (which are subject to process variation) to save as scalar data in `processscalarfile`.

9 `firstrun=1` Starting iteration number.

10 `variations=process`
Level of statistical variation to apply.
Possible values are `process`, `mismatch` or `all`.

Flags

11 `donominal=yes` Whether or not to perform nominal run.
Possible values are `no` or `yes`.

12 `appendsd=no` Whether or not to append scalar data.
Possible values are `no` or `yes`.

13 `savefamilyplots=no`
Whether or not to save data for family plots. If yes, this could require a lot of disk space.
Possible values are `no` or `yes`.

14 `saveprocessparams`
Whether or not to save scalar data for statistically varying process parameters which are subject to process variation.
Possible values are `no` or `yes`.

Annotation parameters

15 `annotate=sweep` Degree of annotation.
Possible values are `no`, `title`, `sweep`, or `status`.

16 `title` Analysis title.

Detailed Description and Examples

`numruns:(default=100)`

The number of Monte Carlo iterations to perform. The simulator will perform a loop, running the specified child analyses and evaluating any export statements `numruns` times.

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`seed:(no default)`

seed for the random number generator. By always specifying the same seed, you can reproduce a previous experiment. If you do not specify a seed, then each time you run the analysis, you will get different results i.e. a different stream of pseudo-random numbers will be generated.

`scalarfile="filename"`

This parameter allows an ASCII file to be specified in which scalar data (results of export expressions that resolve to scalar values) will be written. The data from this file can be read and plotted in histograms by Artist. For each iteration of each Monte Carlo child analyses, Spectre (through Artil) will write a line to this ASCII file which contains scalar data (one scalar expression per column e.g. slewrate or bandwidth.) The default name for this file will be of the form name.mcddata, where name is the name of the Monte Carlo analysis instance. This file contains only the matrix of numeric values. Artist Monte Carlo users will be more familiar with the term "mcddata" file for the scalar file. Additionally, when the Analog Artist Monte Carlo tool is used to generate the spectre netlist file, Spectre will merge the values of the statistically varying process parameters into this file containing the scalar data (results of export expressions). This means that Analog Artist can later read the data, and create scatterplots of the statistically varying process parameters against each other, or against the results of the export expressions. In this way, the user can see correlations between process parameter variations and circuit performances variations. This data merging will take place whenever the scalarfile and processscalarfile (see below) are written in the same directory.

`paramfile="filename"`

This file contains the titles, sweep variable values and the full expression for each of the columns in the scalarfile. Artist Monte Carlo users will be more familiar with the term "mcparam" file for the paramfile. This file will be created in the psf directory by default, unless you specify some path information in the filename.

`processscalarfile="filename"`

If saveprocessparams is set to yes, then the process (batch-to-batch) values of all statistically varying parameters are saved to this scalar data file. You can use the saveprocessvec to filter out a subset of parameters in which case Spectre will save only the parameters specified in saveprocessvec to the processscalarfile.) The processscalarfile is equivalent to the scalarfile, except that the data in the scalarfile contains the values of the scalar export statements, whereas the data in the processscalarfile contains the corresponding process parameter values. The default name for this file will be of the form instname.process.mcddata, where instname is the name of the Monte Carlo analysis instance. This file will be created in the psf directory by default, unless you specify some path information in the filename. You can load the processscalar file and processparamfile into the Artist statistical postprocessing

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environment to plot/verify the process parameter distributions. If you later merge the processparamfile with the data in the scalarfile, you can then plot export scalar values against the corresponding process parameters by loading this merged file into the Artist statistical postprocessing environment.

```
processparamfile="filename"
```

This file contains the titles, sweep variable values for each of the columns in the processscalarfile. These titles will be the names of the process parameters.

The processparamfile is equivalent to the paramfile, except that the paramfile contains the name of the export expressions, whereas the processparamfile contains the names of the process parameters. The default name for this file will be of the form instname.process.mcpam, where instname is the name of the Monte Carlo analysis instance. This file will be created in the psf directory by default, unless you specify some path information in the filename.

```
firstrun:(default=1)
```

index of first iteration. If the first iteration is specified as some number n greater than one, then the beginning n-1 iterations are "skipped" i.e. the Monte Carlo analysis behaves as if the first n-1 iterations were run, but without actually performing the child analyses for these iterations. The subsequent stream of random numbers generated for the remaining iterations will be the same as if the first n-1 iterations were actually run. By specifying the first iteration number and the same value for seed, you can reproduce a particular run or sequence of runs from a previous experiment (for example to examine an outlier case in more detail.)

```
variations={process,mismatch,all} (defaults to process).
```

Whether to apply process (batch-to-batch) variations only, or mismatch (per-instance) variations only, or both together. This assumes that you have specified appropriate statistical distributions in the statistics block. You cannot request that mismatch variations be applied unless you have specified mismatch statistics in the statistics block. You cannot request that process variations be applied unless you have specified process statistics in the statistics block. More details on statistics blocks are given below.

```
saveprocessvec=[rshsp TOX ...]
```

If saveprocessparams is specified as yes, then save the process (batch-to-batch) values of only those parameters listed in saveprocessvec in the processparamfile. This acts as a filter so that you do not save all process parameters to the file. If you do not want to filter the list of process parameters, then do not specify this parameter.

```
donominal={yes,no}(defaults to yes).
```

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This parameter controls whether or not Spectre should perform a nominal run before starting the main Monte Carlo loop of iterations. If any errors are encountered during the nominal run (e.g. convergence problems, incorrect export expressions, etc.) then Spectre will issue an appropriate error message and immediately abandon the Monte Carlo analysis.

If donominal is set to "no", then Spectre will run the Monte Carlo iterations only, and will not perform a nominal analysis. If any errors are encountered during the Monte Carlo iterations, Spectre will issue a warning and continue with the next iteration of the Monte Carlo loop.

`appendsd={yes,no}`(defaults to no).

Specifies whether to append scalar data to an existing scalarfile, or to overwrite the existing scalarfile. This flag applies to both the scalar file and the processscalarfile.

`savefamilyplots={yes,no}`.

If "yes", a data file (e.g. psf) is saved for each analysis for each Monte Carlo iteration, in addition to the export scalar results which are saved to the ASCII scalar data file at the end of each iteration. Saving the full data files between runs enables the cloud plotting feature (overlaid waveforms) in Artist. It also enables the user to define/evaluate new calculator measurements after the simulation has been run using the Artist calculator. This feature could result in a huge amount of data being stored to disk, and it is advised that you use this feature with care. If you do decide to use this feature, it is advisable to keep the number of saved quantities to a minimum. If this parameter is set to "no", then data files are overwritten by each Monte Carlo iteration.

`annotate={no,title,sweep,status}`

Degree of annotation. Use the maximum value of "status" to print a summary of which runs did not converge or had problems evaluating "export" statements, etc.

Examples

```
// do a Monte Carlo analysis, with process variations only
// useful for looking at absolute performance spreads
mcl montecarlo variations=process seed=1234 numruns=200 {
  dcop1 dc          // a child analysis
  tran1 tran start=0 stop=1u    // another child analysis
  // export calculations are sent to the scalardata file
  export slewrate=oceanEval("slewRate(v(\"vout\"),10n,t,30n,t,10,90 )")
}
// do a Monte Carlo analysis, with mismatch variations only
// useful for detecting spreads in differential circuit
// applications, etc. Do not perform a nominal run.
```

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```
mc2 montecarlo donominal=no variations=mismatch seed=1234 numruns=200 {
    dco2 dc
    tran2 tran start=0 stop=1u
    export slewrate=oceanEval("slewRate(v("vout"),10n,t,30n,t,10,90 )")
}
// do both together...
mc3 montecarlo saveprocessparams=yes variations=all numruns=200 {
    dco3 dc
    tran3 tran start=0 stop=1u
    export slewrate=oceanEval("slewRate(v("vout"),10n,t,30n,t,10,90 )")
}
```

Specifying Parameter Distributions using Statistics Blocks

The "statistics blocks" are used to specify the input statistical variations for a Monte Carlo analysis. A statistics block may contain one or more "process" blocks (which represents batch-to-batch type variations), and/or one or more "mismatch" blocks (which represents on-chip or device mismatch variations), in which the distributions for parameters are specified. Statistics blocks may also contain one or more correlation statements to specify the correlations between specified process parameters, and/or to specify correlated device instances (for example matched pairs). Statistics blocks may also contain a "truncate" statement which may be used for generating truncated distributions. The distributions specified in the process block will be sampled once per Monte Carlo iteration, and are typically used to represent batch-to-batch, or process variations, whereas the distributions specified in the mismatch block are sampled on a per subcircuit instance basis and are typically used to represent device-to-device mismatch for devices on the same chip. In the case where the same parameter is subject to both process and mismatch variations, then the sampled process value becomes the mean for the mismatch random number generator for that particular parameter.

Note: Multiple statistics blocks may exist, in which case they accumulate or overlay. Typically, process variations, mismatch variations and correlations between process parameters will be specified in one statistics block. A second statistics block would be specified where actual device instance correlations are specified (i.e. specification of matched pairs).

Statistics blocks can be specified using combinations of the Spectre keywords statistics, process, mismatch, vary, truncate and correlate. Braces {} are used to delimit blocks.

The following example shows some sample statistics blocks, which are discussed below along with syntax requirements.

```
// define some netlist parameters to represent process parameters
// such as sheet resistance and mismatch factors
parameters rshsp=200 rshpi=5k rshpi_std=0.4K xisp=1 xisp=1 xxx=20000 uuu=200
```

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Analysis Statements

```
// define statistical variations, to be used
// with a MonteCarlo analysis.
statistics {
    process {    // process: generate random number once per MC run
        vary rshsp dist=gauss std=12 percent=yes
        vary rshpi dist=gauss std=rshpi_std // rshpi_std is a parameter
        vary xxx dist=lnorm std=12
        vary uuu dist=unif N=10 percent=yes
        ...
    }
    mismatch { // mismatch: generate a random number per instance
        vary rshsp dist=gauss std=2
        vary xisn dist=gauss std=0.5
        vary xisp dist=gauss std=0.5
    }
    // some process parameters are correlated
    correlate param=[rshsp rshpi] cc=0.6
    // specify a global distribution truncation factor
    truncate tr=6.0    // +/- 6 sigma
}
// a separate statistics block to specify correlated (i.e. matched) components
// where m1 and m2 are subckt instances.
statistics {
    correlate dev=[m1 m2] param=[xisn xisp] cc=0.8
}
```

Specifying Distributions

Parameter variations are specified using the following syntax:

```
vary PAR_NAME dist=<type> {std=<value> | N=<value>} {percent=yes|no}
```

Three types of parameter distributions are available: gaussian, lognormal and uniform, corresponding to the *<type>* keywords *gauss*, *lnorm* and *unif* respectively. For both the *gauss* and the *lnorm* distributions, you specify a standard deviation using the *std* keyword.

Gaussian Distribution

For the gaussian distribution, the mean value is taken as the current value of the parameter being varied, giving a distribution denoted by Normal(mean,std). Using the example above, parameter *rshpi* is varied with a distribution of Normal(5k,0.4k)

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Lognormal Distribution

The lognormal distribution is denoted by

$\log(x) = \text{Normal}(\log(\text{mean}), \text{std})$

where x is the parameter being specified as having a lognormal distribution.

(NOTE: $\log()$ is the natural logarithm function.) For parameter xxx in the example above, the process variation is according to

$\log(\text{xxx}) = \text{Normal}(\log(20000), 12)$

Uniform Distribution

The uniform distribution for parameter x is generated according to

$x = \text{unif}(\text{mean}-N, \text{mean}+N)$

such that the mean value is the nominal value of the parameter x, and the parameter is varied about the mean with a range of $\pm N$. The standard deviation is not specified for the uniform distribution, but its value can be calculated from the formula: $\text{std}=N/\sqrt{3}$.

Values as percentages

The "percent" flag indicates whether the standard deviation std or uniform range N are specified in absolute terms (percent=no) or as a percentage of the mean value (percent=yes). For parameter uuu in the example above, the mean value is 200, and the variation is $200 \pm 10\% \times (200)$ i.e. 200 ± 20 . For parameter rshsp, the process variation is given by $\text{Normal}(200, 12\% \times (200))$ i.e. $\text{Normal}(200, 24)$. It is not advised that you use the percent=yes with the lognormal distribution.

Process and Mismatch Variations

The statistics specified in a process block are applied at global scope, and the distributions are sampled once per Monte Carlo iteration. The statistics specified in a mismatch block are applied on a per-subcircuit instance basis, and are sampled once per subcircuit instance. If you place model cards and/or device instances in subcircuits, and add a mismatch block to your statistics block you can effectively model device-to-device mismatch for these devices/models.

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Analysis Statements

Correlation Statements

There are two types of correlation statements that you can use: process parameter correlation statements, and instance correlation statements.

Process Parameter Correlation

The syntax of the process parameter correlation statement is:

```
correlate param=[list of parameters] cc=<value>
```

This allows you to specify a correlation coefficient between multiple process parameters. You can specify multiple process parameter correlation statements in a statistics block, to build a matrix of process parameter correlations. During a Monte Carlo analysis, process parameter values will be randomly generated according to the specified distributions and correlations.

Mismatch Correlation (Matched Devices)

The syntax of the instance or mismatch correlation statement is:

```
correlate dev=[list of subcircuit instances] {param=[list of parameters]} cc=<value>
```

where the device or subcircuit instances to be matched are listed in the list of subcircuit instances, and the list of parameters specifies exactly which parameters with mismatch variations are to be correlated.

The instance mismatch correlation statement is used to specify correlations for particular subcircuit instances. If a subcircuit contains a device, you can effectively use the instance correlation statements to specify that certain devices are correlated (i.e. matched) and give the correlation coefficient. You can optionally specify exactly which parameters are to be correlated by giving a list of parameters (each of which must have had distributions specified for it in a mismatch block), or specify no parameter list, in which case all parameters with mismatch statistics specified are correlated with the given correlation coefficient. The correlation coefficients are specified in the <value> field and must be between +/- 1.0, not including 1.0 or -1.0.

Note: correlation coefficients can be constants or expressions, as can "std" and "N" when specifying distributions.

Truncation Factor

The default truncation factor for gaussian distributions (and for the gaussian distribution underlying the lognormal distribution) is 4.0 sigma. Randomly generated values which are

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outside the range of mean +/- 4.0 sigma are automatically rejected and regenerated until they fall inside the range. You can change the truncation factor using the "truncate" statement. The syntax is:

```
truncate tr=<value>
```

Note: The value of the truncation factor can be a constant or an expression.

Note: Parameter correlations can be affected by using small truncation factors.

Parameter Index

In the following index, the number following each parameter name indicates where to find the description of that parameter.

annotate	15	numruns	1	savefamilyplots	13	scalarfile	3
appendsd	12	paramfile	4	saveprocessparams	5	seed	2
donominal	11	processparamfile	7	saveprocessparams	14	title	16
firstrun	9	processscalarfile	6	saveprocessvec	8	variations	10

Noise Analysis (noise)

Description

The `noise` analysis linearizes the circuit about the operating point and computes the noise spectral density at the output. If you identify an input source, the transfer function and the input-referred noise for an equivalent noise-free network is computed. In addition, if the input source is noisy, then the noise figure is computed.

The noise is computed at the output of the circuit. The output is specified with either a pair of nodes or a probe component. To specify the output of a circuit with a probe, specify it with the `oprobe` parameter. If the output is voltage (or potential), choose a `resistor` or a `port` as the output probe. If the output is current (or flow), choose a `vsource` or `iprobe` as the output probe.

If the input-referred noise is desired, specify the input source using the `iprobe` parameter. Currently, only a `vsource`, an `isource`, or a `port` may be used as an input probe. If the input source is noisy, as is a `port`, the noise analysis will compute the noise factor (F) and noise figure (NF). To match the IEEE definition of noise figure, the input probe must be a port with no excess noise and its `noisetemp` must be set to 16.85C (290K). In addition, the output load must be a `resistor` or `port` and must be identified as the `oprobe`.

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The noise analysis always computes the total noise at the output, which includes contributions from the input source and the output load. The amount of the output noise that is attributable to each noise source in the circuit is also computed and output individually. If the input source is identified, the input-referred noise is computed, which includes the noise from the input source itself. Finally, if the input source is identified and is noisy, the noise factor and noise figure are computed. Thus if

No = total output noise

Ns = noise at the output due to the input probe (the source)

NI = noise at the output due to the output probe (the load)

IRN = input referred noise

G = gain of the circuit

F = noise factor

NF = noise figure

then,

$$\text{IRN} = \sqrt{\text{No}^2 / \text{G}^2}$$

$$\text{F} = (\text{No}^2 - \text{NI}^2) / \text{Ns}^2$$

$$\text{NF} = 10 * \log_{10}(\text{F})$$

When the results are output, No is named `out`, IRN is named `in`, G is named `gain`, F is named `F`, and NF is named `NF`.

Spectre can perform the analysis while sweeping a parameter. The parameter can be frequency, temperature, component instance parameter, component model parameter, or netlist parameter. If changing a parameter affects the DC operating point, the operating point is recomputed on each step. You can sweep the circuit temperature by giving the parameter name as `temp` with no `dev` or `mod` parameter. You can sweep a netlist parameter by giving the parameter name with no `dev`, or `mod` parameter. After the analysis has completed, the modified parameter returns to its original value.

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Definition

Name [p] [n] noise parameter=value ...

The optional terminals (p and n) specify the output of the circuit. If you do not give the terminals, then you must specify the output with a probe component.

Parameters

1 prevoppoint=no Use operating point computed on the previous analysis.
Possible values are no or yes.

Sweep interval parameters

2 start=0 Start sweep limit.
3 stop Stop sweep limit.
4 center Center of sweep.
5 span=0 Sweep limit span.
6 step Step size, linear sweep.
7 lin=50 Number of steps, linear sweep.
8 dec Points per decade.
9 log=50 Number of steps, log sweep.
10 values=[...] Array of sweep values.

Sweep variable parameters

11 dev Device instance whose parameter value is to be swept.
12 mod Model whose parameter value is to be swept.
13 param Name of parameter to sweep.
14 freq (Hz) Frequency when parameter other than frequency is being swept.

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Probe parameters

- | | | |
|----|---------------------|--------------------------------------------------------------|
| 15 | <code>oprobe</code> | Compute total noise at the output defined by this component. |
| 16 | <code>iprobe</code> | Input probe. Refer the output noise to this component. |

State-file parameters

- | | | |
|----|---------------------|-------------------------------------------------------|
| 17 | <code>readns</code> | File that contains estimate of DC solution (nodeset). |
|----|---------------------|-------------------------------------------------------|

Output parameters

- | | | |
|----|-------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 18 | <code>save</code> | Signals to output.
Possible values are <code>all</code> , <code>lvl</code> , <code>allpub</code> , <code>lvlpub</code> , <code>selected</code> , or <code>none</code> . |
| 19 | <code>nestlvl</code> | Levels of subcircuits to output. |
| 20 | <code>oppoint=no</code> | Should operating point information be computed, and if so, where should it be sent.
Possible values are <code>no</code> , <code>screen</code> , <code>logfile</code> , or <code>rawfile</code> . |

Convergence parameters

- | | | |
|----|--------------------------|----------------------------------------------------------------------------------------------------------------|
| 21 | <code>restart=yes</code> | Do not use previous DC solution as initial guess.
Possible values are <code>no</code> or <code>yes</code> . |
|----|--------------------------|----------------------------------------------------------------------------------------------------------------|

Annotation parameters

- | | | |
|----|-----------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------|
| 22 | <code>annotate=sweep</code> | Degree of annotation.
Possible values are <code>no</code> , <code>title</code> , <code>sweep</code> , <code>status</code> , or <code>steps</code> . |
| 23 | <code>stats=no</code> | Analysis statistics.
Possible values are <code>no</code> or <code>yes</code> . |
| 24 | <code>title</code> | Analysis title. |

You can specify sweep limits by giving the end points or by providing the center value and the span of the sweep. Steps can be linear or logarithmic, and you can specify the number of steps or the size of each step. You can give a step size parameter (`step`, `lin`, `log`, `dec`) to determine whether the sweep is linear or logarithmic. If you do not give a step size parameter,

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the sweep is linear when the ratio of stop to start values is less than 10, and logarithmic when this ratio is 10 or greater. All frequencies are in Hertz.

The small-signal analysis begins by linearizing the circuit about an operating-point. By default this analysis computes the operating-point if it is not known, or recomputes it if any significant component or circuit parameter has changed. However, if a previous analysis computed an operating point, you can set `prevoppoint=yes` to avoid recomputing it. For example, if you use this option when the previous analysis was a transient analysis, the operating point is the state of the circuit on the final time point.

Parameter Index

In the following index, the number following each parameter name indicates where to find the description of that parameter.

annotate 22	lin 7	param 13	start 2
center 4	log 9	prevoppoint 1	stats 23
dec 8	mod 12	readns 17	step 6
dev 11	nestlvl 19	restart 21	stop 3
freq 14	oppoint 20	save 18	title 24
iprobe 16	oprobe 15	span 5	values 10

Immediate Set Options (options)

Description

The immediate set options statement sets or changes various program control options. These options take effect immediately and are set while the circuit is read. For further options, see the individual analyses.

Note: Options that are dependent on netlist parameter values, do not maintain their dependencies on those netlist parameters.

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Analysis Statements

Definition

Name options parameter=value ...

Parameters

Tolerance parameters

- | | | |
|---|------------------------------|---------------------------------------------------|
| 1 | <code>reltol=0.001</code> | Relative convergence criterion. |
| 2 | <code>vabstol=1e-06 V</code> | Voltage absolute tolerance convergence criterion. |
| 3 | <code>iabstol=1e-12 A</code> | Current absolute tolerance convergence criterion. |

Temperature parameters

- | | | |
|---|------------------------------|-------------------------------------------------------------------------------------------------------------|
| 4 | <code>temp=27 C</code> | Temperature. |
| 5 | <code>tnom=27 C</code> | Default component parameter measurement temperature. |
| 6 | <code>tempeffects=all</code> | Temperature effect selector.
Possible values are <code>vt</code> , <code>tc</code> or <code>all</code> . |

Output parameters

- | | | |
|----|------------------------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 7 | <code>save=selected</code> | Signals to output.
Possible values are <code>all</code> , <code>lvl</code> , <code>allpub</code> , <code>lvlpub</code> , <code>selected</code> , or <code>none</code> . |
| 8 | <code>nestlvl=∞</code> | Levels of subcircuits to output. |
| 9 | <code>subcktprobelvl=0</code> | Level up to which subcircuit terminal current probes are to be set up. |
| 10 | <code>currents=selected</code> | Terminal currents to output. (See important note below about saving currents by using probes).
Possible values are <code>all</code> , <code>nonlinear</code> or <code>selected</code> . |

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- 11 `useprobes=no` Use current probes when measuring terminal currents. (See important note below about saving currents by using probes). Possible values are `no` or `yes`.
- 12 `redundant_currents=no` If yes, save both currents through two terminal devices. Possible values are `no` or `yes`.
- 13 `pwr=none` Power signals to create. Possible values are `all`, `subckts`, `devices`, `total`, or `none`.
- 14 `saveahdlvars=selected` AHDL variables to output. Possible values are `all` or `selected`.
- 15 `rawfmt=psfbin` Output raw data file format. Possible values are `nutbin`, `nutascii`, `wsfbin`, `wsfascii`, `psfbin`, `psfascii`, `awb`, or `sst2`.
- 16 `rawfile="%C:r.raw"` Output raw data file name.

Convergence parameters

- 17 `homotopy=all` Method used when no convergence on initial attempt of DC analysis. Possible values are `none`, `gmin`, `source`, `dptran`, `ptran`, `arclength`, or `all`.
- 18 `limit=dev` Limiting algorithms to aid DC convergence. Possible values are `delta`, `log` or `dev`.

Component parameters

- 19 `scalem=1` Model scaling factor.
- 20 `scale=1` Device instance scaling factor.
- 21 `compatible=spectre` Encourage device equations to be compatible with a foreign simulator. This option does not affect input syntax. Possible values are `spectre`, `spice2`, `spice3`, `cdsspice`, `hspice`, or `spiceplus`.

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22 `approx=no` Use approximate models. Difference between approximate and exact models is generally very small.
Possible values are `no` or `yes`.

23 `macromodels=no` Circuit contains macromodels; sometimes helps performance.
Possible values are `no` or `yes`.

Error-checking parameters

24 `topcheck=full` Check circuit topology for errors.
Possible values are `no`, `min`, `full`, or `fixall`.

25 `ignshorts=no` Silently ignore shorted components.
Possible values are `no` or `yes`.

26 `diagnose=no` Print additional information that might help diagnose accuracy and convergence problems.
Possible values are `no` or `yes`.

27 `opptcheck=yes` Check operating point parameters against soft limits.
Possible values are `no` or `yes`.

Resistance parameters

28 `gmin=1e-12 S` Minimum conductance across each nonlinear device.

29 `gmin_check=max_v_only`
Specifies that effect of `gmin` should be reported if significant.
Possible values are `no`, `max_v_only`, `max_only`, or `all`.

30 `rforce=1 Ω` Resistance used when forcing nodesets and node-based initial conditions.

Quantity parameters

31 `value="V"` Default value quantity.

32 `flow="I"` Default flow quantity.

33 `quantities=no` Print quantities.
Possible values are `no`, `min` or `full`.

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Annotation parameters

- | | | |
|----|--------------------|-------------------------------------------------------------------------------------------------------------------|
| 34 | audit=detailed | Print time required by various parts of the simulator.
Possible values are no, brief, detailed, or full. |
| 35 | inventory=detailed | Print summary of components used.
Possible values are no, brief or detailed. |
| 36 | narrate=yes | Narrate the simulation.
Possible values are no or yes. |
| 37 | debug=no | Give debugging messages.
Possible values are no or yes. |
| 38 | info=yes | Give informational messages.
Possible values are no or yes. |
| 39 | note=yes | Give notice messages.
Possible values are no or yes. |
| 40 | maxnotes=5 | Maximum number of times any notice will be issued per analysis. |
| 41 | warn=yes | Give warning messages.
Possible values are no or yes. |
| 42 | maxwarns=5 | Maximum number of times any warning message will be issued per analysis. |
| 43 | error=yes | Give error messages.
Possible values are no or yes. |
| 44 | digits=5 | Number of digits used when printing numbers. |
| 45 | notation=eng | When printing real numbers to the screen, what notation should be used.
Possible values are eng, sci or float. |
| 46 | cols=80 | Width of screen in characters. |
| 47 | title | Circuit title. |

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Matrix parameters

- 48 `pivotdc=no` Use numeric pivoting on every iteration of DC analysis.
Possible values are `no` or `yes`.
- 49 `pivrel=0.001` Relative pivot threshold.
- 50 `pivabs=0` Absolute pivot threshold.

Miscellaneous parameters

- 51 `ckptclock=1800 s` Clock time checkpoint period.

Sensitivity parameters

- 52 `sensfile` Output sensitivity data file name.
- 53 `sensformat=tabular`
Format of sensitivity data.
Possible values are `tabular` or `list`.
- 54 `senstype=partial` Type of sensitivity being calculated.
Possible values are `partial` or `normalized`.



Important note about `currents` and `useprobes` options

Adding probes to circuits that are sensitive to numerical noise might affect the solution. In such cases accurate solution may be obtained by reducing `reltol`.

The following devices will always use probes to save currents (even with `useprobes=no`):
`port`, `delay`, `switch`, `hbt`, `transformer`, `core`, `winding`, `fourier`, `d2a`, `a2d`, `a2ao`, `a2ai`.

Sensitivity Definitions

When `senstype` is set to `partial`, the sensitivity being calculated is the partial derivative of a differentiable output variable F with respect to a design parameter p

$$dF$$

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$$D(F \text{ w.r.t. } p) = \frac{dF}{dp}$$

dp

This definition is not scale free. When `senstype` is set to `normalized`, the sensitivity being calculated is the normalized sensitivity

$$S(F \text{ w.r.t. } p) = \frac{d \ln F}{d \ln p} = \frac{p}{F} \frac{dF}{dp}$$

$$S(F \text{ w.r.t. } p) = \frac{d \ln F}{d \ln p} = \frac{p}{F} \frac{dF}{dp} = \frac{p}{F} D(F \text{ w.r.t. } p)$$

$$S(F \text{ w.r.t. } p) = \frac{d \ln F}{d \ln p} = \frac{p}{F} \frac{dF}{dp}$$

When either F or p takes a zero value, the above normalized definition no longer provides a useful measure, the following two seminormalized sensitivities are used instead:

$$S(F \text{ w.r.t. } p) = \frac{dF}{dp} \quad \text{if } F = 0$$

$$S(F \text{ w.r.t. } p) = \frac{dF}{dp} = \frac{dF}{dp} \quad \text{if } F = 0$$

$$S(F \text{ w.r.t. } p) = \frac{dF}{dp} \quad \text{if } F = 0$$

and

$$S(F \text{ w.r.t. } p) = \frac{dF}{dp} \quad \text{if } p = 0$$

$$S(F \text{ w.r.t. } p) = \frac{dF}{dp} = \frac{dF}{dp} \quad \text{if } p = 0$$

$$S(F \text{ w.r.t. } p) = \frac{dF}{dp} \quad \text{if } p = 0$$

When both F and p are zero, the partial sensitivity is used.

Topcheck Parameter

When `topcheck=full`, a topology check is performed and `gmin` is inserted between isolated nodes and ground. A heuristic topology check is also performed to find nodes that may be isolated due to the numerical nature of the circuit. For example, nodes isolated by reverse biased diodes in MOSFETS.

Use `topcheck=fixall` to attach `gmin` to all types of isolated nodes. Including the ones found by the heuristic topology check.

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Parameter Index

In the following index, the number following each parameter name indicates where to find the description of that parameter.

approx 22	iabstol 3	pivotdc 48	sensformat 53
audit 34	ignshorts 25	pivrel 49	senstype 54
ckptclock 51	info 38	pwr 13	subcktprobelvl 9
cols 46	inventory 35	quantities 33	temp 4
compatible 21	limit 18	rawfile 16	tempeffects 6
currents 10	macromodels 23	rawfmt 15	title 47
debug 37	maxnotes 40	redundant_currents 12	tnom 5
diagnose 26	maxwarns 42	reltol 1	topcheck 24
digits 44	narrate 36	rforce 30	useprobes 11
error 43	nestlvl 8	save 7	vabstol 2
flow 32	notation 45	saveahdlvars 14	value 31
gmin 28	note 39	scale 20	warn 41
gmin_check 29	opptcheck 27	scalem 19	
homotopy 17	pivabs 50	sensfile 52	

Periodic AC Analysis (pac)

Description

The periodic AC (PAC) analysis is used to compute transfer functions for circuits that exhibit frequency translation. Such circuits include mixers, switched-capacitor filters, samplers, phase-locked loops, and the like. It is a small-signal analysis like AC analysis, except the circuit is first linearized about a periodically varying operating point as opposed to a simple DC operating point. Linearizing about a periodically time-varying operating point allows transfer-functions that include frequency translation, whereas simply linearizing about a DC operating point could not because linear time-invariant circuits do not exhibit frequency translation. Also, the frequency of the sinusoidal stimulus is not constrained by the period of the large periodic solution.

Computing the small-signal response of a periodically varying circuit is a two step process. First, the small stimulus is ignored and the periodic steady-state response of the circuit to possibly large periodic stimulus is computed using PSS analysis. As a normal part of the PSS analysis, the periodically time-varying representation of the circuit is computed and saved for later use. The second step is applying the small stimulus to the periodically varying linear representation to compute the small signal response. This is done using the PAC analysis. A

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PAC analysis cannot be used alone, it must follow a PSS analysis. However, any number of periodic small-signal analyses such as PAC, PSP, PXF, PNoise, can follow a PSS analysis.

Unlike other analyses in Spectre, this analysis can only sweep frequency.

Definition

Name `pac parameter=value ...`

Parameters

Sweep interval parameters

1	<code>start=0</code>	Start sweep limit.
2	<code>stop</code>	Stop sweep limit.
3	<code>center</code>	Center of sweep.
4	<code>span=0</code>	Sweep limit span.
5	<code>step</code>	Step size, linear sweep.
6	<code>lin=50</code>	Number of steps, linear sweep.
7	<code>dec</code>	Points per decade.
8	<code>log=50</code>	Number of steps, log sweep.
9	<code>values=[...]</code>	Array of sweep values.
10	<code>sweepstype</code>	Specifies if the sweep frequency range is absolute frequency of input or if it is relative to the port harmonics. Possible values are <code>absolute</code> or <code>relative</code> .
11	<code>relharmnum=1</code>	Harmonic to which relative frequency sweep should be referenced.

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Output parameters

- 12 `sidebands=[...]` Array of relevant sidebands for the analysis.
- 13 `maxsideband=0` An alternative to the `sidebands` array specification, which automatically generates the array: `[-maxsideband ... 0 ... +maxsideband]`.
- 14 `freqaxis` Specifies whether the results should be output versus the input frequency, the output frequency, or the absolute value of the output frequency. Default is `in` for logarithmic frequency sweeps and `absout` otherwise.
Possible values are `absout`, `out` or `in`.
- 15 `save` Signals to output.
Possible values are `all`, `lvl`, `allpub`, `lvlpub`, `selected`, or `none`.
- 16 `nestlvl` Levels of subcircuits to output.
- 17 `outputperiod=0.0` (no output)
Time-domain output period. The time-domain small-signal response is computed for the period specified, rounded to the nearest integer multiple of the `pss` period.

Convergence parameters

- 18 `tolerance=1e-9` Relative tolerance for linear solver.
- 19 `gear_order=2` Gear order used for small-signal integration.
- 20 `solver=turbo` Solver type.
Possible values are `std` or `turbo`.
- 21 `oscsolver=turbo` Oscillator solver type.
Possible values are `std` or `turbo`.

Annotation parameters

- 22 `annotate=sweep` Degree of annotation.
Possible values are `no`, `title`, `sweep`, `status`, or `steps`.

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23 `stats=no` Analysis statistics.
Possible values are `no` or `yes`.

24 `title` Analysis title.

You can select the set of periodic small-signal output frequencies of interest by setting either the `maxsideband` or the `sidebands` parameters. For a given set of n integer numbers representing the sidebands K_1, K_2, \dots, K_n , the output frequency at each sideband is computed as $f(\text{out}) = f(\text{in}) + K_i * \text{fund}(\text{pss})$, where $f(\text{in})$ represent the (possibly swept) input frequency, and $\text{fund}(\text{pss})$ represents the fundamental frequency used in the corresponding PSS analysis. Thus, when analyzing a down-converting mixer, while sweeping the RF input frequency, the most relevant sideband for IF output is $K_i = -1$. When simulating an up-converting mixer, while sweeping IF input frequency, the most relevant sideband for RF output is $K_i = 1$. By setting the `maxsideband` value to K_{max} , all $2 * K_{\text{max}} + 1$ sidebands from $-K_{\text{max}}$ to $+K_{\text{max}}$ are generated.

The number of requested sidebands does not change substantially the simulation time. However, the `maxacfreq` of the corresponding PSS analysis should be set to guarantee that $| \max\{f(\text{out})\} |$ is less than `maxacfreq`, otherwise the computed solution might be contaminated by aliasing effects. The PAC simulation is not executed for $| f(\text{in}) |$ greater than `maxacfreq`. Diagnostic messages are printed for those extreme cases, indicating how `maxacfreq` should be set in the PSS analysis. In the majority of the simulations, however, this is not an issue, because `maxacfreq` is never allowed to be smaller than 40x the PSS fundamental.

With PAC the frequency of the stimulus and of the response are usually different (this is an important way in which PAC differs from AC). The `freqaxis` parameter is used to specify whether the results should be output versus the input frequency (`in`), the output frequency (`out`), or the absolute value of the output frequency (`absout`).

Unlike AC analysis, PAC analysis can output the time-domain simulation results, by specifying the `outputperiod` parameter.

You can specify sweep limits by giving the end points or by providing the center value and the span of the sweep. Steps can be linear or logarithmic, and you can specify the number of steps or the size of each step. You can give a step size parameter (`step, lin, log, dec`) to determine whether the sweep is linear or logarithmic. If you do not give a step size parameter, the sweep is linear when the ratio of stop to start values is less than 10, and logarithmic when this ratio is 10 or greater. Alternatively, you may specify the particular values that the sweep parameter should take using the `values` parameter. If you give both a specific set of values and a set specified using a sweep range, the two sets are merged and collated before being used. All frequencies are in Hertz.

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Parameter Index

In the following index, the number following each parameter name indicates where to find the description of that parameter.

annotate 22	log 8	save 15	step 5
center 3	maxsideband 13	sidebands 12	stop 2
dec 7	nestlvl 16	solver 20	sweeptype 10
freqaxis 14	oscsolver 21	span 4	title 24
gear_order 19	outputperiod 17	start 1	tolerance 18
lin 6	relharmnum 11	stats 23	values 9

Periodic Distortion Analysis (pdisto)

Description

Similar to PAC analysis, the periodic distortion (PDISTO) analysis calculates responses of a circuit that exhibits frequency translations. However, instead of having small signal linear behavior, it models the response as having components of a few harmonics of input signal frequencies. This allows computing responses to moderately large input signals. An example is intermodulation distortion with two moderate input signals. PDISTO treats one particular input signal (usually the one that causes the most nonlinearity or the largest response) as the large signal, and the others as moderate signals.

An initial transient analysis is carried out by first suppressing all moderate input signals. Then we run a number of (at least 2) stable iterations with all signals activated, which is followed by shooting Newton method. PDISTO employs the Mixed Frequency Time (MFT) algorithm extended to multiple fundamental frequencies. For details of MFT algorithm, please see *Steady-State Methods for Simulating Analog and Microwave Circuits*, by K. S. Kundert, J. K. White, and A. Sangiovanni-Vincentelli, Kluwer, Boston, 1990.

Like PSS, PDISTO uses shooting Newton method as its backbone. However, instead of doing a single transient integration, each Newton iteration does a number of transient integrations of one large signal period.

Each of the integrations differs by a phase-shift in each moderate input signal. The number of integrations is determined by the numbers of harmonics of moderate fundamentals specified by the user. Given $\text{maxharm}=[k_1 \ k_2 \ \dots \ k_n]$, the total number of integrations is $(2*k_2+1)*(2*k_3+1)*\dots*(2*k_n+1)$. As one consequence, the efficiency of the algorithm depends significantly on the number of harmonics required to model the responses of moderate fundamentals. As another consequence, the number of harmonics of the large fundamental

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does not significantly affect the efficiency of the shooting algorithm. The boundary conditions of a shooting interval are such that the time domain integrations are consistent with a frequency domain transformation with a shift of one large signal period.

PDISTO inherits a majority of PSS parameters. A few new parameters are added. The most important ones are `funds` and `maxharms`. They replace PSS parameters, `fund` (or `period`) and `harms`, respectively. The `funds` parameter accepts a list of names of fundamentals that are present in the sources. These names are specified in the sources by parameter `fundname`. The first fundamental is considered as the large signal. A few heuristics can be used for picking the large fundamental.

- (1) Pick the one which is not a sinusoidal.
- (2) Pick the one which causes the most nonlinearity.
- (3) Pick the one which causes the largest response.

The `maxharms` parameter accepts a list of numbers of harmonics that are required to sufficiently model responses due to different fundamentals.

Definition

Name `pdisto` parameter=value ...

Parameters

Distortion fundamental parameters

- | | |
|-------------------------------|------------------------------------------------------------------------------------|
| 1 <code>funds=[...]</code> | Array of fundamental frequency names for fundamentals to use in analysis. |
| 2 <code>maxharms=[...]</code> | Array of number of harmonics of each fundamental to consider for each fundamental. |

Simulation interval parameters

- | | |
|-----------------------------|----------------------------------------------------------------------------------|
| 3 <code>tstab=0.0 s</code> | Extra stabilization time after the onset of periodicity for independent sources. |
| 4 <code>tstart=0.0 s</code> | Initial transient analysis start time. |

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Analysis Statements

Time-step parameters

- 5 `maxstep (s)` Maximum time step. Default derived from `errpreset`.
- 6 `step=0.001 period s` Minimum time step that would be used solely to maintain the aesthetics of the results.

Initial-condition parameters

- 7 `ic=all` What should be used to set initial condition.
Possible values are `dc`, `node`, `dev`, or `all`.
- 8 `skipdc=no` If yes, there will be no dc analysis for transient.
Possible values are `no` or `yes`.
- 9 `readic` File that contains initial condition.

Convergence parameters

- 10 `readns` File that contains estimate of initial transient solution.
- 11 `cmin=0 F` Minimum capacitance from each node to ground.

Output parameters

- 12 `outputtype=time` for PSS, `freq` for PDISTO
Output type.
Possible values are `all`, `time` or `freq`.
- 13 `save` Signals to output.
Possible values are `all`, `lvl`, `allpub`, `lvlpub`, `selected`, or `none`.
- 14 `nestlvl` Levels of subcircuits to output.
- 15 `oppoint=no` Should operating point information be computed for initial timestep, and if so, where should it be sent.
Possible values are `no`, `screen`, `logfile`, or `rawfile`.
- 16 `skipstart=starttime s` The time to start skipping output data.

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- 17 `skipstop=stoptime s` The time to stop skipping output data.
- 18 `skipcount` Save only one of every skipcount points.
- 19 `strobeperiod (s)` The output strobe interval (in seconds of transient time).
- 20 `strobedelay=0 s` The delay (phase shift) between the skipstart time and the first strobe point.
- 21 `compression=no` Do data compression on output.
Possible values are `no` or `yes`.
- 22 `saveinit=no` If set, the waveforms for the initial transient before steady state are saved.
Possible values are `no` or `yes`.

State-file parameters

- 23 `write` File to which initial transient solution (before steady-state) is to be written.
- 24 `writefinal` File to which final transient solution in steady-state is to be written.
- 25 `swapfile` Temporary file that holds steady-state information. Tells Spectre to use a regular file rather than virtual memory to hold the periodic operating point. Use this option if Spectre complains about not having enough memory to complete this analysis.

Integration method parameters

- 26 `method` Integration method. Default derived from `errpreset`.
Possible values are `euler`, `trap`, `traonly`, `gear2`, or `gear2only`.

Accuracy parameters

- 27 `errpreset=moderate` Selects a reasonable collection of parameter settings.
Possible values are `liberal`, `moderate` or `conservative`.

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Analysis Statements

28	<code>relref</code>	Reference used for the relative convergence criteria. Default derived from <code>errpreset</code> . Possible values are <code>pointlocal</code> , <code>alllocal</code> , <code>sigglobal</code> , or <code>allglobal</code> .
29	<code>lteratio</code>	Ratio used to compute LTE tolerances from Newton tolerance. Default derived from <code>errpreset</code> .
30	<code>steadyratio</code>	Ratio used to compute steady state tolerances from LTE tolerance. Default derived from <code>errpreset</code> .
31	<code>maxperiods=20</code> for driven PSS, 50 for autonomous PSS	Maximum number of simulated periods to reach steady-state.
32	<code>tolerance=1e-4</code>	Relative tolerance for linear solver.
33	<code>finitediff</code>	Options for finite difference method refinement after quasi-periodic shooting method. <code>finitediff</code> is changed from <code>no</code> to <code>samegrid</code> automatically when <code>readqpss</code> and <code>writeqpss</code> are used to re-use QPSS results. Possible values are <code>no</code> , <code>yes</code> or <code>refine</code> .

Annotation parameters

34	<code>stats=no</code>	Analysis statistics. Possible values are <code>no</code> or <code>yes</code> .
35	<code>annotate=sweep</code>	Degree of annotation. Possible values are <code>no</code> , <code>title</code> , <code>sweep</code> , <code>status</code> , or <code>steps</code> .
36	<code>title</code>	Analysis title.

Newton parameters

37	<code>maxiters=5</code>	Maximum number of iterations per time step.
38	<code>restart=yes</code>	Do not use previous DC solution as initial guess. Possible values are <code>no</code> or <code>yes</code> .

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Analysis Statements

Circuit age

- 39 `circuitage (Years)` Stress Time. Age of the circuit used to simulate hot-electron degradation of MOSFET and BSIM circuits.
- 40 `writeqpss` File to which final quasi-periodic steady-state solution is to be written. Small signal analyses such as `qpac`, `qpxf` and `qpnoise` can read in the steady-state solution from this file directly instead of running the `qpss` analysis again.
- 41 `readqpss` File from which final quasi-periodic steady-state solution is to be read. Small signal analyses such as `qpac`, `qpxf` and `qpnoise` can read in the steady-state solution from this file directly instead of running the `qpss` analysis again.

Most of PDISTO analysis parameters are inherited from PSS analysis, and their meanings remain essentially unchanged. Two new important parameters are `funds` and `maxharms`. They replace and extend the role of `fund` and `harms` parameters of PSS analysis. One important difference is that `funds` accepts a list of fundamental names instead of actual frequencies. The frequencies associated with fundamentals are figured out automatically by the simulator. An important feature is that each input signal can be a composition of more than one sources. However, these sources must have the same fundamental name. For each fundamental name, its fundamental frequency is the greatest common factor of all frequencies associated with the name. Missing or not listing all fundamental names using the parameter `funds` will result in an amputation of the current simulation. However if `maxharms` is not given, a warning message will be issued, and the number of harmonics is defaulted to 1 for each of the fundamentals.

The role of some PSS parameters is extended. The parameter `maxperiods` that controls the maximum number of shooting iterations for PSS analysis also controls the number of the maximum number of shooting iterations for PDISTO analysis.

The `tstab` parameter controls both the length of the initial transient integration with only the clock tone activated and the number of stable iterations with moderate tones activated. The stable iterations are run before Newton iterations.

The `errpreset` parameter lets you adjust the simulator parameters to fit your needs quickly. In most cases, it should also be the only parameter you need to adjust. If you want a fast simulation with reasonable accuracy, you may set `errpreset` to `liberal`. If have some concern for accuracy, you may set `errpreset` to `moderate`. If accuracy is your main interest, you may set `errpreset` to `conservative`.

If users dont specify `steadyratio`, it is always 1.0 - not affected by `errpreset`. The effect of `errpreset` on other parameters is shown in the following table.

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Analysis Statements

Parameter defaults as a function of errpreset

errpreset	reltol	relref	method	Iteratio	maxstep
liberal	1e-3	sigglobal	gear2only	3.5	clock period/80
moderate	1e-4	sigglobal	gear2only	3.5	clock period/100
conservative	1e-5	sigglobal	gear2only	*	clock period/200

* : If user specified `reltol` $\leq 1e-5 \cdot 10.0/3.5$, `Iteratio`=3.5. Otherwise `Iteratio`=10.0.

The value of `reltol` can only decrease from its value in the options statement, when `reltol` in options statement is larger. Spectre sets the value of `maxstep` so that it is no larger than the value given in the table. Except for `reltol` and `maxstep`, `errpreset` does not change the value of any parameters you have explicitly set. The actual values used for the QPSS analysis are given in the log file.

Parameter Index

In the following index, the number following each parameter name indicates where to find the description of that parameter.

annotate 35	maxiters 37	restart 38	strobeperiod 19
circuitage 39	maxperiods 31	save 13	swapfile 25
cmin 11	maxstep 5	saveinit 22	title 36
compression 21	method 26	skipcount 18	tstab 3
errpreset 27	nestlvl 14	skipdc 8	tstart 4
finitediff 33	oppoint 15	skipstart 16	write 23
funds 1	outputtype 12	skipstop 17	writefinal 24
ic 7	readic 9	stats 34	writeqpss 40
itres 32	readns 10	steadyratio 30	
lteratio 29	readqpss 41	step 6	
maxharms 2	relref 28	strobedelay 20	

Periodic Noise Analysis (pnoise)

Description

The Periodic Noise, or PNoise analysis is similar to the conventional noise analysis, except that it includes frequency conversion effects. Hence is it useful for predicting the noise behavior of mixers, switched-capacitor filters, and other periodically driven circuits. It is particularly useful for predicting the phase noise of autonomous circuits, such as oscillators.

PNoise analysis linearizes the circuit about the periodic operating point computed in the prerequisite PSS analysis. It is the periodically time-varying nature of the linearized circuit that accounts for the frequency conversion. In addition, the affect of a periodically time-varying bias point on the noise generated by the various components in the circuit is also included.

The time-average of the noise at the output of the circuit is computed in the form of a spectral density versus frequency. The output of the circuit is specified with either a pair of nodes or a probe component. To specify the output of a circuit with a probe, specify it using the `oprobe` parameter. If the output is voltage (or potential), choose a `resistor` or a `port` as the output probe. If the output is current (or flow), choose a `vsource` or `iprobe` as the output probe.

If the input-referred noise or noise figure is desired, specify the input source using the `iprobe` parameter. For input-referred noise, use either a `vsource` or `isource` as the input probe; for noise figure, use a `port` as the probe. Currently, only a `vsource`, an `isource`, or a `port` may be used as an input probe. If the input source is noisy, as is a `port`, the noise analysis will compute the noise factor (F) and noise figure (NF). To match the IEEE definition of noise figure, the input probe must be a port with no excess noise and its `noisetemp` must be set to 16.85C (290K). In addition, the output load must be a `resistor` or `port` and must be identified as the `oprobe`.

The reference sideband (`refsideband`) specifies which conversion gain is used when computing input-referred noise, noise factor, and noise figure. The reference sideband specifies the input frequency relative to the output frequency with:

$$|f(\text{input})| = |f(\text{out}) + \text{refsideband} * \text{fund}(\text{pss})|$$

Use `refsideband=0` when the input and output of the circuit are at the same frequency (such as with amplifiers and filters). When `refsideband` differs from 0, the single side-band noise figure is computed.

The noise analysis always computes the total noise at the output, which includes contributions from the input source and the output load. The amount of the output noise that is attributable to each noise source in the circuit is also computed and output individually. If

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the input source is identified (using iprobe) and is a vsource or isourec, the input-referred noise is computed, which includes the noise from the input source itself. Finally, if the input source is identified (using iprobe) and is noisy, as is the case with ports, the noise factor and noise figure are computed. Thus if

No = total output noise

Ns = noise at the output due to the input probe (the source)

Nsi = noise at the output due to the image harmonic at the source

Nso = noise at the output due to harmonics other than input at the source

NI = noise at the output due to the output probe (the load)

IRN = input referred noise

G = gain of the circuit

F = noise factor

NF = noise figure

Fdsb = double sideband noise factor

NFdsb = double sideband noise figure

Fieee = IEEE single sideband noise factor

NFieee = IEEE single sideband noise figure

then,

$$IRN = \sqrt{No^2/G^2}$$

$$F = (No^2 - NI^2)/Ns^2$$

$$NF = 10 \cdot \log_{10}(F)$$

$$Fdsb = (No^2 - NI^2)/(Ns^2 + Nsi^2)$$

$$NFdsb = 10 \cdot \log_{10}(Fdsb)$$

$$Fieee = (No^2 - NI^2 - Nso^2)/Ns^2$$

$$NFieee = 10 \cdot \log_{10}(Fieee).$$

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When the results are output, No is named `out`, IRN is named `in`, G is named `gain`, F, NF, Fdsb, NFdsb, Fieee, and NFieee are named `F`, `NF`, `Fdsb`, `NFdsb`, `Fieee`, and `NFieee` respectively.

Unlike other analyses in Spectre, this analysis can only sweep frequency.

Definition

Name `[p] [n] pnoise parameter=value ...`

The optional terminals (p and n) specify the output of the circuit. If you do not give the terminals, then you must specify the output with a probe component.

Parameters

Sweep interval parameters

1	<code>start=0</code>	Start sweep limit.
2	<code>stop</code>	Stop sweep limit.
3	<code>center</code>	Center of sweep.
4	<code>span=0</code>	Sweep limit span.
5	<code>step</code>	Step size, linear sweep.
6	<code>lin=50</code>	Number of steps, linear sweep.
7	<code>dec</code>	Points per decade.
8	<code>log=50</code>	Number of steps, log sweep.
9	<code>values=[...]</code>	Array of sweep values.
10	<code>sweepstype</code>	Specifies if the sweep frequency range is absolute frequency of input or if it is relative to the port harmonics. Possible values are <code>absolute</code> or <code>relative</code> .
11	<code>relharmnum=1</code>	Harmonic to which relative frequency sweep should be referenced.

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Probe parameters

- 12 `oprobe` Compute total noise at the output defined by this component.
- 13 `iprobe` Refer the output noise to this component.
- 14 `refsideband` Conversion gain associated with this sideband is used when computing input-referred noise or noise figure.

Output parameters

- 15 `noisetype=sources` Specifies if the pnoise analysis should output cross-power densities or noise source information.
Possible values are `sources`, `correlations` or `timedomain`.
- 16 `maxsideband=7` Maximum sideband included when computing noise either up-converted or down-converted to the output by the periodic drive signal.
- 17 `sidebands=[...]` Array of relevant sidebands for the analysis.
- 18 `save` Signals to output.
Possible values are `all`, `lvl`, `allpub`, `lvlpub`, `selected`, or `none`.
- 19 `nestlvl` Levels of subcircuits to output.
- 20 `maxcycles=0` Maximum cycle correlation frequency included when computing noise either up-converted or down-converted to the output by the periodic drive signal.
- 21 `cycles=[...]` Array of relevant cycle frequencies. Valid only if `noisetype=correlations`.
- 22 `noiseskipcount=0` Calculate time-domain noise on only one of every `noiseskipcount` time points.
- 23 `noisetimepoints=[...]` Additional timepoints for time-domain noise analysis..
- 24 `saveallsidebands=no` Save noise contributors by sideband.
Possible values are `no` or `yes`.

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Convergence parameters

- | | | |
|----|------------------------------|-----------------------------------------------------------------------------------------|
| 25 | <code>tolerance=1e-9</code> | Relative tolerance for linear solver. |
| 26 | <code>gear_order=2</code> | Gear order used for small-signal integration. |
| 27 | <code>solver=turbo</code> | Solver type.
Possible values are <code>std</code> or <code>turbo</code> . |
| 28 | <code>oscsolver=turbo</code> | Oscillator solver type.
Possible values are <code>std</code> or <code>turbo</code> . |

Annotation parameters

- | | | |
|----|-----------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------|
| 29 | <code>annotate=sweep</code> | Degree of annotation.
Possible values are <code>no</code> , <code>title</code> , <code>sweep</code> , <code>status</code> , or <code>steps</code> . |
| 30 | <code>stats=no</code> | Analysis statistics.
Possible values are <code>no</code> or <code>yes</code> . |
| 31 | <code>title</code> | Analysis title. |

In practice, noise can mix with each of the harmonics of the periodic drive signal applied in the PSS analysis and end up at the output frequency. However, the PNoise analysis only includes the noise that mixes with a finite set of harmonics that are typically specified using the `maxsideband` parameter, but in special circumstances may be specified with the `sidebands` parameter. If K_i represents sideband i , then

$$f(\text{noise_source}) = f(\text{out}) + K_i * \text{fund}(\text{pss})$$

The `maxsideband` parameter specifies the maximum $|K_i|$ included in the PNoise calculation. Thus, noise at frequencies less than $f(\text{out}) - \text{maxsideband} * \text{fund}(\text{pss})$ and greater than $f(\text{out}) + \text{maxsideband} * \text{fund}(\text{pss})$ are ignored. If selected sidebands are specified using the `sidebands` parameter, then only those are included in the calculation. Care should be taken when specifying the sidebands because the results will be in error if you do not include a sideband that contributes significant noise to the output.

The number of requested sidebands does not change substantially the simulation time. However, the `maxacfreq` of the corresponding PSS analysis should be set to guarantee that $|\max\{f(\text{noise_source})\}|$ is less than `maxacfreq`, otherwise the computed solution might be contaminated by aliasing effects. The PNoise simulation is not executed for $|f(\text{out})|$ greater than `maxacfreq`. Diagnostic messages are printed for those extreme cases, indicating which `maxacfreq` should be set in the PSS analysis. In the majority of the simulations,

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however, this is not an issue, because `maxacfreq` is never allowed to be smaller than 40x the PSS fundamental.

You can specify sweep limits by giving the end points or by providing the center value and the span of the sweep. Steps can be linear or logarithmic, and you can specify the number of steps or the size of each step. You can give a step size parameter (`step`, `lin`, `log`, `dec`) to determine whether the sweep is linear or logarithmic. If you do not give a step size parameter, the sweep is linear when the ratio of stop to start values is less than 10, and logarithmic when this ratio is 10 or greater. Alternatively, you may specify the particular values that the sweep parameter should take using the `values` parameter. If you give both a specific set of values and a set specified using a sweep range, the two sets are merged and collated before being used. All frequencies are in Hertz.

Parameter Index

In the following index, the number following each parameter name indicates where to find the description of that parameter.

<code>annotate</code> 29	<code>maxcycles</code> 20	<code>refsideband</code> 14	<code>stats</code> 30
<code>center</code> 3	<code>maxsideband</code> 16	<code>relharmnum</code> 11	<code>step</code> 5
<code>cycles</code> 21	<code>nestlvl</code> 19	<code>save</code> 18	<code>stop</code> 2
<code>dec</code> 7	<code>noiseskipcount</code> 22	<code>saveallsidebands</code> 24	<code>sweeptype</code> 10
<code>gear_order</code> 26	<code>noisetimepoints</code> 23	<code>sidebands</code> 17	<code>title</code> 31
<code>iprobe</code> 13	<code>noisetype</code> 15	<code>solver</code> 27	<code>tolerance</code> 25
<code>lin</code> 6	<code>oprobe</code> 12	<code>span</code> 4	<code>values</code> 9
<code>log</code> 8	<code>oscsolver</code> 28	<code>start</code> 1	

Periodic S-Parameter Analysis (psp)

Description

The periodic SP (`psp`) analysis is used to compute scattering and noise parameters for n-port circuits that exhibit frequency translation, such as mixers. It is a small-signal analysis like SP analysis, except, as in PAC and PXF, the circuit is first linearized about a periodically varying operating point as opposed to a simple DC operating point. Linearizing about a periodically time-varying operating point allows the computation of S-parameters between circuit ports that convert signals from one frequency band to another. PSP can also calculate noise parameters in frequency-converting circuits. PSP computes noise figure (both single-sideband and double-sideband), input referred noise, equivalent noise parameters, and noise

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correlation matrices. As in PNoise, but unlike SP, the noise features of the PSP analysis include noise folding effects due to the periodically time-varying nature of the circuit.

Computing the n-port S-parameters and noise parameters of a periodically varying circuit is a two step process. First, the small stimulus is ignored and the periodic steady-state response of the circuit to possibly large periodic stimulus is computed using PSS analysis. As a normal part of the PSS analysis, the periodically time-varying representation of the circuit is computed and saved for later use. The second step is applying small-signal excitations to compute the n-port S-parameters and noise parameters. This is done using the PSP analysis. A PSP analysis cannot be used alone, it must follow a PSS analysis. However, any number of periodic small-signal analyses such as PAC, PSP, PXF, PNoise, can follow a single PSS analysis.

Unlike other analyses in Spectre, this analysis can only sweep frequency.

Definition

Name `psp parameter=value ...`

Parameters

Sweep interval parameters

1	<code>start=0</code>	Start sweep limit.
2	<code>stop</code>	Stop sweep limit.
3	<code>center</code>	Center of sweep.
4	<code>span=0</code>	Sweep limit span.
5	<code>step</code>	Step size, linear sweep.
6	<code>lin=50</code>	Number of steps, linear sweep.
7	<code>dec</code>	Points per decade.
8	<code>log=50</code>	Number of steps, log sweep.
9	<code>values=[...]</code>	Array of sweep values.

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Analysis Statements

10 `sweep` Specifies if the sweep frequency range is absolute frequency of input or if it is relative to the port harmonics. Possible values are `absolute` or `relative`.

Port parameters

11 `ports=[...]` List of active ports. Ports are numbered in the order given. For purposes of noise figure computation, the input is considered port 1 and the output is port 2.

12 `portharmsvec=[...]` List of harmonics active on specified list of ports. Must have a one-to-one correspondence with the ports vector.

13 `harmsvec=[...]` List of harmonics, in addition to ones associated with specific ports by `portharmsvec`, that are active.

Output parameters

14 `freqaxis` Specifies whether the results should be output versus the input frequency, the output frequency, or the absolute value of the input frequency. Default is `in`. Possible values are `absin`, `in` or `out`.

Noise parameters

15 `donoise=yes` Perform noise analysis. If `oprobe` is specified as a valid port, this is set to `yes`, and a detailed noise output is generated. Possible values are `no` or `yes`.

Probe parameters

16 `maxsideband=7` Maximum sideband included when computing noise either up-converted or down-converted to the output by the periodic drive signal.

Convergence parameters

17 `tolerance=1e-9` Relative tolerance for linear solver.

18 `gear_order=2` Gear order used for small-signal integration.

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19 `solver=turbo` Solver type.
Possible values are `std` or `turbo`.

20 `oscsolver=turbo` Oscillator solver type.
Possible values are `std` or `turbo`.

Annotation parameters

21 `annotate=sweep` Degree of annotation.
Possible values are `no`, `title`, `sweep`, `status`, or `steps`.

22 `stats=no` Analysis statistics.
Possible values are `no` or `yes`.

23 `title` Analysis title.

To specify the PSP analysis the port and port harmonic relations must be specified. You can select the ports of interest by setting the port parameter and the set of periodic small-signal output frequencies of interest by setting the `portharmsvec` or the `harmsvec` parameters. For a given set of n integer numbers representing the harmonics K_1, K_2, \dots, K_n , the scattering parameters at each port are computed at the frequencies $f(\text{scattered}) = f(\text{rel}) + K_i * \text{fund}(\text{pss})$, where $f(\text{rel})$ represents the relative frequency of a signal incident on a port, $f(\text{scattered})$ represents the frequency to which the relevant scattering parameter represents the conversion, and $\text{fund}(\text{pss})$ represents the fundamental frequency used in the corresponding PSS analysis.

Thus, when analyzing a down-converting mixer, with signal in the upper sideband, and sweeping the RF input frequency, the most relevant harmonic for RF input is $K_i = 1$ and for IF output $K_i = 0$. Hence we can associate $K_2 = 1$ with the IF port and $K_1 = 0$ with the RF port. S21 will represent the transmission of signal from the RF to IF, and S11 the reflection of signal back to the RF port. If the signal was in the lower sideband, then a choice of $K_1 = -1$ would be more appropriate.

Either the `portharmsvec` or the `harmsvec` parameters can be used to specify the harmonics of interest. If the `portharmsvec` is given, the harmonics must be in one-to-one correspondence with the ports, with each harmonic associated with a single port. If harmonics are specified in the optional `harmsvec` parameter, then all possible frequency-translating scattering parameters associated with the specified harmonics are computed.

With PSP the frequency of the input and of the response are usually different (this is an important way in which PSP differs from SP). Because the PSP computation involves inputs and outputs at frequencies that are relative to multiple harmonics, the `freqaxis` and `sweeptype` parameters behave somewhat differently in PSP than in PAC and PXF.

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The sweeptype parameter controls the way the frequencies in the PSP analysis are swept. Specifying a relative sweep indicates to sweep relative to the analysis harmonics (not the PSS fundamental) and an absolute sweep is a sweep of the absolute input source frequency. For example, with a PSS fundamental of 100MHz, the portharmsvec set to [9 1] to examine a downconverting mixer, sweeptype=relative, and a sweep range of f(rel)=0->50MHz, then S21 would represent the strength of signal transmitted from the input port in the range 900->950MHz to the output port at frequencies 100->150MHz. Using sweeptype=absolute and sweeping the frequency from 900->950MHz would calculate the same quantities, since f(abs)=900->950MHz, and f(rel) = f(abs) - K1 * fund(pss) = 0->50MHz, because K1=9 and fund(pss) = 100MHz.

Usually it is not necessary to sweep frequency in PSP over more than one fundamental PSS period.

The freqaxis parameter is used to specify whether the results should be output versus the scattered frequency at the input port (in), the scattered frequency at the output port (out), or the absolute value of the frequency swept at the input port (absin).

Unlike in PAC/PXF/PNoise, increasing the number of requested ports and harmonics will increase the simulation time substantially.

To insure accurate results in PSP, the maxacfreq of the corresponding PSS analysis should be set to guarantee that $|\max\{f(\text{scattered})\}|$ is less than maxacfreq, otherwise the computed solution might be contaminated by aliasing effects.

PSP analysis also computes noise figures, equivalent noise sources, and noise parameters. The noise computation, which is skipped only when donoise is set to no, requires additional simulation time. If

No = total output noise at frequency f

Ns = noise at the output due to the input probe (the source)

Nsi = noise at the output due to the image harmonic at the source

Nso = noise at the output due to harmonics other than input at the source

Nl = noise at the output due to the output probe (the load)

IRN = input referred noise

G = gain of the circuit

F = noise factor (single side band)

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NF = noise figure (single side band)

Fdsb = double sideband noise factor

NFdsb = double sideband noise figure

Fieee = IEEE single sideband noise factor

NFieee = IEEE single sideband noise figure

then,

$$\text{IRN} = \sqrt{\text{No}^2 / \text{G}^2}$$
$$\text{F} = (\text{No}^2 - \text{NI}^2) / \text{Ns}^2$$
$$\text{NF} = 10 \cdot \log_{10}(\text{F})$$
$$\text{Fdsb} = (\text{No}^2 - \text{NI}^2) / (\text{Ns}^2 + \text{Nsi}^2)$$
$$\text{NFdsb} = 10 \cdot \log_{10}(\text{Fdsb})$$
$$\text{Fieee} = (\text{No}^2 - \text{NI}^2 - \text{Nso}^2) / \text{Ns}^2$$
$$\text{NFieee} = 10 \cdot \log_{10}(\text{Fieee}).$$

When the results are output, IRN is named `in`, G is named `gain`, F, NF, Fdsb, NFdsb, Fieee, and NFieee are named `F`, `NF`, `Fdsb`, `NFdsb`, `Fieee`, and `NFieee` respectively. Note that the gain computed by PSP is the voltage gain from the actual circuit input to the circuit output, not the gain from the internal port voltage source to the output.

To insure accurate noise calculations, the `maxsideband` or `sidebands` parameters must be set to include the relevant noise folding effects. `maxsideband` is only relevant to the noise computation features of PSP.

You can specify sweep limits by giving the end points or by providing the center value and the span of the sweep. Steps can be linear or logarithmic, and you can specify the number of steps or the size of each step. You can give a step size parameter (`step`, `lin`, `log`, `dec`) to determine whether the sweep is linear or logarithmic. If you do not give a step size parameter, the sweep is linear when the ratio of stop to start values is less than 10, and logarithmic when this ratio is 10 or greater. Alternatively, you may specify the particular values that the sweep parameter should take using the `values` parameter. If you give both a specific set of values and a set specified using a sweep range, the two sets are merged and collated before being used. All frequencies are in Hertz.

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In the following index, the number following each parameter name indicates where to find the description of that parameter.

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Periodic Steady-State Analysis (pss)

Description

This analysis computes the periodic steady-state (PSS) response of a circuit, with a simulation time independent of the time-constants of the circuit. Also, it sets the circuits periodic operating point, which can then be used during a periodic time-varying small-signal analysis, such as PAC, PXF, and PNOISE.

PSS analysis is capable of handling both autonomous (non-driven) and driven (non-autonomous) circuits. Autonomous circuits are time-invariant circuits that have time-varying responses. Thus, autonomous circuits generate non-constant waveforms even though they are not driven by a time-varying stimulus. Driven circuits require some time-varying stimulus to generate a time-varying response. The most common example of an autonomous circuit is an oscillator. Common driven circuits include amplifiers, filters, mixers, etc.

With driven circuits the user specifies the analysis `period`, or its corresponding fundamental frequency `fund`. The `period` must be an integer multiple of the period of the drive signal or signals. Autonomous circuits have no drive signal and the actual period of oscillation is not known precisely by the user in advance. Instead, the user specifies an estimate of the oscillation period and PSS analysis computes the precise period along with the periodic solution waveforms.

When applied to autonomous circuits, PSS analysis requires the user to specify a pair of nodes, `p` and `n`. In fact this is how PSS analysis determines whether it is being applied to an autonomous or a driven circuit. If the pair of nodes is supplied, PSS assumes the circuit is autonomous; if not, the circuit is assumed to be driven.

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A PSS analysis consists of two phases, an initial transient phase, which allows the circuit to be initialized, and the shooting phase, which is where the periodic steady-state solution is computed. The transient phase consists of three intervals.

The first starts at `tstart`, which is normally 0, and continues through the onset of periodicity `tonset` for the independent sources. The onset of periodicity, which is automatically generated, is the minimum time for which all sources are periodic. The second is an optional user specified stabilization interval whose length is `tstab`. The final interval whose length is `period` for driven circuits, or `4x period` for autonomous circuits has a special use for the autonomous PSS analysis, i.e., the PSS analysis monitors the waveforms in the circuit and develops a better estimate of the oscillation period. Once the initial transient phase is complete, the shooting interval begins. In this phase, the circuit is repeatedly simulated over one period while adjusting the initial condition (and the period when applied to autonomous circuits) to find the periodic steady-state solution.

Typically the process takes three to five such iterations to reach steady-state. Upon completion, if requested by the user, the frequency-domain response is computed. For driven circuits, one can use `writpss` and `readpss` to reuse the results in a previous simulation.

Definition

Name `[p] [n] pss parameter=value ...`

Parameters

Simulation interval parameters

- | | | |
|---|---------------------------|-----------------------------------------------------------------------------------------------------------------------------|
| 1 | <code>period (s)</code> | Steady state analysis period (or its estimate for autonomous circuits). |
| 2 | <code>fund (Hz)</code> | Alternative to period specification. Steady state analysis fundamental frequency (or its estimate for autonomous circuits). |
| 3 | <code>tstab=0.0 s</code> | Extra stabilization time after the onset of periodicity for independent sources. |
| 4 | <code>tstart=0.0 s</code> | Initial transient analysis start time. |

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Time-step parameters

- 5 `maxstep (s)` Maximum time step. Default derived from `errpreset`.
- 6 `maxacfreq` Maximum frequency requested in a subsequent periodic small-signal analysis. Default derived from `errpreset` and `harms`.
- 7 `step=0.001 period s` Minimum time step that would be used solely to maintain the aesthetics of the results.

Initial-condition parameters

- 8 `ic=all` What should be used to set initial condition.
Possible values are `dc`, `node`, `dev`, or `all`.
- 9 `skipdc=no` If yes, there will be no dc analysis for transient.
Possible values are `no` or `yes`.
- 10 `readic` File that contains initial condition.

Convergence parameters

- 11 `readns` File that contains estimate of initial transient solution.
- 12 `cmin=0 F` Minimum capacitance from each node to ground.

Output parameters

- 13 `harms=9` Number of harmonics to output when `outputtype=freq` or `all`.
- 14 `harmsvec=[...]` Array of desired harmonics. Alternate form of `harms` that allows selection of specific harmonics.
- 15 `outputtype=time` for PSS, `freq` for PDISTO
Output type.
Possible values are `all`, `time` or `freq`.
- 16 `save` Signals to output.
Possible values are `all`, `lvl`, `allpub`, `lvlpub`, `selected`, or `none`.

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17	<code>nestlvl</code>	Levels of subcircuits to output.
18	<code>oppoint=no</code>	Should operating point information be computed for initial timestep, and if so, where should it be sent. Possible values are <code>no</code> , <code>screen</code> , <code>logfile</code> , or <code>rawfile</code> .
19	<code>skipstart=starttime s</code>	The time to start skipping output data.
20	<code>skipstop=stoptime s</code>	The time to stop skipping output data.
21	<code>skipcount</code>	Save only one of every skipcount points.
22	<code>strobeperiod (s)</code>	The output strobe interval (in seconds of transient time).
23	<code>strobedelay=0 s</code>	The delay (phase shift) between the skipstart time and the first strobe point.
24	<code>compression=no</code>	Do data compression on output. Possible values are <code>no</code> or <code>yes</code> .
25	<code>saveinit=no</code>	If set, the waveforms for the initial transient before steady state are saved. Possible values are <code>no</code> or <code>yes</code> .

State-file parameters

26	<code>write</code>	File to which initial transient solution (before steady-state) is to be written.
27	<code>writefinal</code>	File to which final transient solution in steady-state is to be written.
28	<code>swapfile</code>	Temporary file that holds steady-state information. Tells Spectre to use a regular file rather than virtual memory to hold the periodic operating point. Use this option if Spectre complains about not having enough memory to complete this analysis.
29	<code>writepss</code>	File to which the converged steady-state solution is to be written. <code>finitediff</code> is set to <code>yes</code> automatically to improve PSS results.

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30 `readpss` File from which a previously converged steady-state solution is to be read. PSS loads the solution and checks the residue of the circuit equations only. The solution is re-used if the residue is satisfying. Otherwise, the solution is re-converged using the finite difference method.

Integration method parameters

31 `method` Integration method. Default derived from `errpreset`. Possible values are `euler`, `trap`, `traponly`, `gear2`, or `gear2only`.

Accuracy parameters

32 `errpreset=moderate` Selects a reasonable collection of parameter settings. Possible values are `liberal`, `moderate` or `conservative`.

33 `relref` Reference used for the relative convergence criteria. Default derived from `errpreset`. Possible values are `pointlocal`, `alllocal`, `sigglobal`, or `allglobal`.

34 `lteratio` Ratio used to compute LTE tolerances from Newton tolerance. Default derived from `errpreset`.

35 `steadyratio` Ratio used to compute steady state tolerances from LTE tolerance. Default derived from `errpreset`.

36 `maxperiods=20` for driven PSS, 50 for autonomous PSS
Maximum number of simulated periods to reach steady-state.

37 `tolerance=1e-4` Relative tolerance for linear solver.

38 `finitediff` Options for finite difference method refinement after shooting method for driven circuits. Possible values are `no`, `yes` or `refine`.

39 `highorder` Perform a high-order refinement after low-order convergence. The Multi-Interval Chebyshev polynomial spectral algorithm will be used. Possible values are `no` or `yes`.

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- 40 `psratio=1` Ratio used to compute high-order polynomial spectral accuracy from Newton tolerance.
- 41 `maxorder=16` The maximum order of the Chebyshev polynomials used in waveform approximation. Possible values are from 2 to 16.
- 42 `fullpssvec` Use the full vector containing solutions at all PSS time steps in the linear solver. Default derived from the size of the equation and the property of the PSS time steps.
Possible values are `no` or `yes`.

Annotation parameters

- 43 `stats=no` Analysis statistics.
Possible values are `no` or `yes`.
- 44 `annotate=sweep` Degree of annotation.
Possible values are `no`, `title`, `sweep`, `status`, or `steps`.
- 45 `title` Analysis title.

Newton parameters

- 46 `maxiters=5` Maximum number of iterations per time step.
- 47 `restart=yes` Do not use previous DC solution as initial guess.
Possible values are `no` or `yes`.

Circuit age

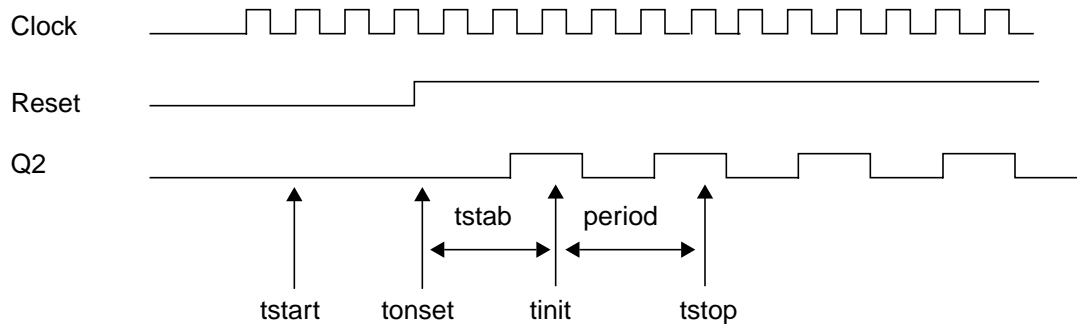
- 48 `circuitage (Years)` Stress Time. Age of the circuit used to simulate hot-electron degradation of MOSFET and BSIM circuits.

The initial transient analysis provides a flexible mechanism to direct the circuit to a particular steady-state solution of interest, and to avoid undesired solutions. Another usage of the initial transient simulation is helping convergence by eliminating large but fast decaying modes that

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are present in many circuits. For example, in case of driven circuits, consider the reset signal in the figure below.



In the figure above, the initial transient analysis is executed from `tstart` to `tstop`. If initial transient results are relevant, you can output them by setting `saveinit` to `yes`. The steady-state results are always computed for the specified `period`, from `tinit` to `tstop`. By default, `tstart` and `tstab` are set to zero, while `tinit`, `tonset` and `tstop` are always automatically generated.

It happens in some circuits that the linearity of the relationship between the initial and final state depends on when the shooting interval begins. Conceptually, when the shooting interval begins should not matter, as long as it is after the time when the stimuli have become periodic, because the periodic response repeats endlessly. However in practice, one can improve the convergence by starting at a good point, and degrade the convergence, which slows the analysis, by starting in a bad spot. In general, it is best to try to avoid starting the shooting interval at a point where the circuit is undergoing strong nonlinear behavior. For example, in switch-capacitor filters it is best if `tinit` falls at the beginning of a clock transition, preferably a transition that follows a relatively long period of settling. If instead `tinit` occurred during a clock transition or soon after one, then it is likely the opamps would be undergoing slew-rate limiting at the start of the shooting interval, which would act to slow convergence. Switching mixers follow similar rules.

When applying PSS analysis to oscillators, it is necessary to start the oscillator, just as you would if you were simulating the turn-on transient of the oscillator using transient analysis. The Designers Guide to Spice and Spectre [K. S. Kundert, Kluwer Academic Publishers, 1995] describes techniques for starting oscillators in some depth. In summary, there are two techniques for starting oscillators, using initial conditions, or using a brief impulsive stimulus. Initial conditions would be provided for the components of the oscillators resonator. If an impulsive stimulus is used, it should be applied so as to couple strongly into the oscillatory mode of the circuit, and poorly into any other long-lasting modes, such as those associated with bias circuitry. Either way, once the trigger is applied to start the oscillator, it is important to allow the oscillator to run for a while before the shooting methods

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are applied to compute the steady-state result. To do so, specify an additional stabilization interval using the `tstab` parameter. In practice, an additional stabilization interval often improves convergence.

By default, only the time-domain results are computed. If you specify either `harms` or `harmsvec` or set `outputtype` to `freq` or `all`, the frequency-domain results will also be computed. If frequency-domain results are requested, but the desired harmonics are not specified, its default value is 10. The time-domain output waveform generation can be inhibited by setting `outputtype` to `freq`.

The accuracy of the results does not depend on the number of harmonics that are requested, only on the accuracy parameters, which are set in the same fashion as in the transient analysis. Besides a few new parameters, like `steadyratio` and `maxacfreq`, all the others parameters work in PSS analysis in the exact same fashion as they work on transient analysis.

Several parameters determine the accuracy of the PSS analysis. `reltol` and `abstol` control the accuracy of the discretized equation solution. These parameters determine how well charge is conserved and how accurately steady-state or equilibrium points are computed. You can set the integration error, or the errors in the computation of the circuit dynamics (such as time constants), relative to `reltol` and `abstol` by setting the `lteratio` parameter.

The `steadyratio` parameter adjusts the maximum allowed mismatch in node voltages or current branches from the beginning to the end of the steady-state period. This value is multiplied by the `lteratio` and `reltol` to determine the convergence criterion. The relative convergence norm is printed out along with the actual mismatch value at the end of each iteration, thus indicating the progress of the steady-state iteration.

The `finitediff` parameter allows the use of finite difference (FD) after shooting. Usually this will eliminate the above mismatch in node voltages or current branches. It can also refine the grid of time steps. In some cases, numerical error of the linear solver still introduces a mismatch. One can set `steadyratio` to a smaller value to activate a tighter tolerance for the iterative linear solver. If `finitediff` is set to `no`, FD method is turned off. If it is set to `yes`, `pss` applies FD method and trying to improve the beginning small time steps if necessary. If it is set to `refine`, `pss` applies FD method and tries to refine the time steps. When the simulation uses 2nd-order method, uniform 2nd order gear is used. `finitediff` is changed from `no` to `yes` automatically when `readpss` and `writepss` are used to re-use PSS results

The `maxacfreq` parameter is used to automatically adjust the `maxstep` to reduce errors due to aliasing in frequency-domain results. By default, the `maxacfreq` is set to 4x the frequency of the largest requested harmonic, but is never set to less than 40x the fundamental.

The parameter `relref` determines how the relative error is treated. The `relref` options are:

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`relref=pointlocal`: Compares the relative errors in quantities at each node to that node alone.

`relref=alllocal`: Compares the relative errors at each node to the largest values found for that node alone for all past time.

`relref=sigglobal`: Compares relative errors in each of the circuit signals to the maximum for all signals at any previous point in time.

`relref=allglobal`: Same as `relref=sigglobal` except that it also compares the residues (KCL error) for each node to the maximum of that nodes past history.

The `errpreset` parameter lets you adjust the simulator parameters to fit your needs quickly. In most cases, it should also be the only parameter you need to adjust.

Guidelines for using `errpreset` in driven circuits are as follows. If the circuit contains only one periodic tone and you are only interested in obtaining the periodic operating point, you may set `errpreset` to `liberal`, which gives reasonably accurate result and the simulation speed is the fastest. If the circuit contains more than one periodic tone and you are interested in intermodulation results, you may set `errpreset` to `moderate`, which gives very accurate result. If you want a very low noise floor in your simulation result and accuracy is your main interest, you may set `errpreset` to `conservative`. Multi-interval Chebsyehv (MIC) is activated automatically for moderate and conservative settings, unless you set `highorder=no` explicitly. MIC falls back to the original method if it encounters convergence difficulty. If you set `highorder=yes`, MIC will try harder to converge.

The effect of `errpreset` on other parameters for driven circuits is shown in the following table.

Parameter Defaults and Estimated Numerical Noise Floor in Simulation Result as a Function of `errpreset`

<code>errpreset</code>	<code>reltol</code>	<code>relref</code>	<code>method</code>	<code>Iteratio</code>	<code>steadyratio</code>	<code>noisefloor</code>
<code>liberal</code>	1e-3	<code>sigglobal</code>	<code>gear2only</code>	3.5	0.001	-70dB
<code>moderate</code>	1e-3	<code>alllocal</code>	<code>gear2only+mic</code>	3.5	0.001	-120dB
<code>conservative</code>	1e-4	<code>alllocal</code>	<code>gear2only+mic</code>	*	0.01	-200dB

* : If user specified `reltol` $\leq 1e-4 \cdot 10.0/3.5$, `Iteratio`=3.5. Otherwise `Iteratio`=10.0.

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Estimated numerical noise floor is for weakly nonlinear circuit with successful MIC simulation. For linear circuit, the noise floor is even lower and for very nonlinear circuit, you may need to tighten `psratio` or increase `maxacfreq` to achieve this noise floor.

Guidelines for using `errpreset` in autonomous circuits are as follows. If you want a fast simulation with reasonable accuracy, you may set `errpreset` to `liberal`. If have some concern for accuracy, you may set `errpreset` to `moderate`. If accuracy is your main interest, you may set `errpreset` to `conservative`.

The effect of `errpreset` on other parameters for autonomous circuits is shown in the following table.

Parameter defaults as a function of `errpreset`

<code>errpreset</code>	<code>reltol</code>	<code>relref</code>	<code>method</code>	<code>lteratio</code>	<code>steadyratio</code>	<code>maxstep</code>
<code>liberal</code>	1e-3	<code>sigglobal</code>	<code>traponly</code>	3.5	0.001	<code>period/80</code>
<code>moderate</code>	1e-4	<code>alllocal</code>	<code>gear2only</code>	3.5	0.001	<code>period/200</code>
<code>conservative</code>	1e-5	<code>alllocal</code>	<code>gear2only</code>	*	0.001	<code>period/400</code>

* : If user specified `reltol` $\leq 1e-5 \cdot 10.0/3.5$, `lteratio`=3.5. Otherwise `lteratio`=10.0.

The value of `reltol` can only decrease from its value in the options statement, when `reltol` in options statement is larger. Spectre sets the value of `maxstep` so that it is no larger than the value given in the table. Except for `reltol` and `maxstep`, `errpreset` does not change the value of any parameters you have explicitly set. The actual values used for the PSS analysis are given in the log file.

If the circuit you are simulating can have infinitely fast transitions (for example, a circuit that contains nodes with no capacitance), Spectre might have convergence problems. To avoid this, you must prevent the circuit from responding instantaneously. You can accomplish this by setting `cmin`, the minimum capacitance to ground at each node, to a physically reasonable nonzero value. This often significantly improves Spectre convergence.

You may specify the initial condition for the transient analysis by using the `ic` statement or the `ic` parameter on the capacitors and inductors. If you do not specify the initial condition, the DC solution is used as the initial condition. The `ic` parameter on the transient analysis controls the interaction of various methods of setting the initial conditions. The effects of individual settings are:

`ic=dc`: Any initial condition specifiers are ignored, and the DC solution is used.

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`ic=node`: The `ic` statements are used, and the `ic` parameter on the capacitors and inductors are ignored.

`ic=dev`: The `ic` parameters on the capacitors and inductors are used, and the `ic` statements are ignored.

`ic=all`: Both the `ic` statements and the `ic` parameters are used, and the `ic` parameters override the `ic` statements.

If you specify an initial condition file with the `readic` parameter, initial conditions from the file are used, and any `ic` statements are ignored.

Once you specify the initial conditions, Spectre computes the actual initial state of the circuit by performing a DC analysis. During this analysis, Spectre forces the initial conditions on nodes by using a voltage source in series with a resistor whose resistance is `rforce` (see `options`).

With the `ic` statement it is possible to specify an inconsistent initial condition (one that cannot be sustained by the reactive elements). Examples of inconsistent initial conditions include setting the voltage on a node with no path of capacitors to ground or setting the current through a branch that is not an inductor. If you initialize Spectre inconsistently, its solution jumps; that is, it changes instantly at the beginning of the simulation interval. You should avoid such changes if possible because Spectre can have convergence problems while trying to make the jump.

You can skip the DC analysis entirely by using the parameter `skipdc`. If the DC analysis is skipped, the initial solution will be either trivial, or given in the file you specified by the `readic` parameter, or, if the `readic` parameter is not given, the values specified on the `ic` statements. Device based initial conditions are not used for `skipdc`. Nodes that you do not specify with the `ic` file or `ic` statements will start at zero. You should not use this parameter unless you are generating a nodeset file for circuits that have trouble in the DC solution; it usually takes longer to follow the initial transient spikes that occur when the DC analysis is skipped than it takes to find the real DC solution. The `skipdc` parameter might also cause convergence problems in the transient analysis.

Nodesets help find the DC or initial transient solution. You can supply them in the circuit description file with `nodeset` statements, or in a separate file using the `readns` parameter. When nodesets are given, Spectre computes an initial guess of the solution by performing a DC analysis while forcing the specified values onto nodes by using a voltage source in series with a resistor whose resistance is `rforce`. Spectre then removes these voltage sources and resistors and computes the true solution from this initial guess.

Nodesets have two important uses. First, if a circuit has two or more solutions, nodesets can bias the simulator towards computing the desired one. Second, they are a convergence aid.

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By estimating the solution of the largest possible number of nodes, you might be able to eliminate a convergence problem or dramatically speed convergence.

When you simulate the same circuit many times, we suggest that you use both the `write` and `readns` parameters and give the same file name to both parameters. The DC analysis then converges quickly even if the circuit has changed somewhat since the last simulation, and the nodeset file is automatically updated.

Nodesets and initial conditions have similar implementation but produce different effects. Initial conditions actually define the solution, whereas nodesets only influence it. When you simulate a circuit with a transient analysis, Spectre forms and solves a set of differential equations. However, differential equations have an infinite number of solutions, and a complete set of initial conditions must be specified in order to identify the desired solution. Any initial conditions you do not specify are computed by the simulator to be consistent. The transient waveforms then start from initial conditions. Nodesets are usually used as a convergence aid and do not affect the final results. However, in a circuit with more than one solution, such as a latch, nodesets bias the simulator towards finding the solution closest to the nodeset values.

The `method` parameter specifies the integration method. The possible settings and their meanings are:

`method=euler:` Backward-Euler is used exclusively.

`method=traponly:` Trapezoidal rule is used almost exclusively.

`method=trap:` Backward-Euler and the trapezoidal rule are used.

`method=gear2only:` Gears second-order backward-difference method is used almost exclusively.

`method=gear2:` Backward-Euler and second-order Gear are used.

The trapezoidal rule is usually the most efficient when you want high accuracy. This method can exhibit point-to-point ringing, but you can control this by tightening the error tolerances. For this reason, though, if you choose very loose tolerances to get a quick answer, either backward-Euler or second-order Gear will probably give better results than the trapezoidal rule. Second-order Gear and backward-Euler can make systems appear more stable than they really are. This effect is less pronounced with second-order Gear or when you request high accuracy.

Spectre provides two methods for reducing the number of output data points saved: `strobing`, based on the simulation time, and `skipping` time points, which saves only every Nth point.

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The parameters `strobeperiod` and `strobedelay` control the strobing method. `strobeperiod` sets the interval between points that you want to save, and `strobedelay` sets the offset within the period relative to `skipstart`. The simulator forces a time step on each point to be saved, so the data is computed, not interpolated.

The skipping method is controlled by `skipcount`. If this is set to N, then only every Nth point is saved.

The parameters `skipstart` and `skipstop` apply to both data reduction methods. Before `skipstart` and after `skipstop`, Spectre saves all computed data.

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<code>circuitage</code> 48	<code>lteratio</code> 34	<code>readic</code> 10	<code>steadyratio</code> 35
<code>cmin</code> 12	<code>maxacfreq</code> 6	<code>readns</code> 11	<code>step</code> 7
<code>compression</code> 24	<code>maxiters</code> 46	<code>readpss</code> 30	<code>strobedelay</code> 23
<code>errpreset</code> 32	<code>maxorder</code> 41	<code>relref</code> 33	<code>strobeperiod</code> 22
<code>finitediff</code> 38	<code>maxperiods</code> 36	<code>restart</code> 47	<code>swapfile</code> 28
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Periodic Transfer Function Analysis (pxf)

Description

A conventional transfer function analysis computes the transfer function from every source in the circuit to a single output. It differs from a conventional AC analysis in that the AC analysis

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computes the response from a single stimulus to every node in the circuit. The difference between `pac` and `pxf` analysis are similar. The Periodic Transfer Function (`pxf`) analysis computes the transfer functions from any source at any frequency to a single output at a single frequency. Thus, like `pac` analysis, `pxf` analysis includes frequency conversion effects.

The `pxf` analysis directly computes such useful quantities as conversion efficiency (transfer function from input to output at desired frequency), image and sideband rejection (input to output at undesired frequency), and LO feed-through and power supply rejection (undesired input to output at all frequencies).

As with a `pac`, `psp`, and `pnoise` analyses, a `pxf` analysis must follow a `PSS` analysis.

Unlike other analyses in Spectre, this analysis can only sweep frequency.

Definition

Name `[p] [n] pxf parameter=value ...`

The optional terminals (`p` and `n`) specify the output of the circuit. If you do not give the terminals, then you must specify the output with a probe component.

Parameters

Sweep interval parameters

1	<code>start=0</code>	Start sweep limit.
2	<code>stop</code>	Stop sweep limit.
3	<code>center</code>	Center of sweep.
4	<code>span=0</code>	Sweep limit span.
5	<code>step</code>	Step size, linear sweep.
6	<code>lin=50</code>	Number of steps, linear sweep.
7	<code>dec</code>	Points per decade.
8	<code>log=50</code>	Number of steps, log sweep.

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- 9 `values=[...]` Array of sweep values.
- 10 `sweepstype` Specifies if the sweep frequency range is absolute frequency of input or if it is relative to the port harmonics.
Possible values are `absolute` or `relative`.
- 11 `relharmnum=1` Harmonic to which relative frequency sweep should be referenced.

Probe parameters

- 12 `probe` Compute every transfer function to this probe component.

Output parameters

- 13 `stimuli=sources` Stimuli used for xf analysis.
Possible values are `sources` or `nodes_and_terminals`.
- 14 `sidebands=[...]` Array of relevant sidebands for the analysis.
- 15 `maxsideband=0` An alternative to the `sidebands` array specification, which automatically generates the array: `[-maxsideband ... 0 ... +maxsideband]`.
- 16 `freqaxis` Specifies whether the results should be output versus the input frequency, the output frequency, or the absolute value of the input frequency. Default is `out` for logarithmic frequency sweeps and `absin` otherwise.
Possible values are `absin`, `in` or `out`.
- 17 `save` Signals to output.
Possible values are `all`, `lvl`, `allpub`, `lvlpub`, `selected`, or `none`.
- 18 `nestlvl` Levels of subcircuits to output.

Convergence parameters

- 19 `tolerance=1e-9` Relative tolerance for linear solver.
- 20 `gear_order=2` Gear order used for small-signal integration.

Spectre Circuit Simulator Reference

Analysis Statements

21 `solver=turbo` Solver type.
Possible values are `std` or `turbo`.

22 `oscsolver=turbo` Oscillator solver type.
Possible values are `std` or `turbo`.

Annotation parameters

23 `annotate=sweep` Degree of annotation.
Possible values are `no`, `title`, `sweep`, `status`, or `steps`.

24 `stats=no` Analysis statistics.
Possible values are `no` or `yes`.

25 `title` Analysis title.

The variable of interest at the output can be voltage or current, and its frequency is not constrained by the period of the large periodic solution. While sweeping the selected output frequency, you can select the periodic small-signal input frequencies of interest by setting either the `maxsideband` or the `sidebands` parameters. For a given set of n integer numbers representing the sidebands K_1, K_2, \dots, K_n , the input signal frequency at each sideband is computed as $f(in) = f(out) + K_i * fund(pss)$, where $f(out)$ represent the (possibly swept) output signal frequency, and $fund(pss)$ represents the fundamental frequency used in the corresponding `pss` analysis. Thus, when analyzing a down-converting mixer, and sweeping the IF output frequency, $K_i = +1$ for the RF input represents the first upper-sideband, while $K_i = -1$ for the RF input represents the first lower-sideband. By setting the `maxsideband` value to K_{max} , all $2 * K_{max} + 1$ sidebands from $-K_{max}$ to $+K_{max}$ are be selected.

The number of requested sidebands does not change substantially the simulation time. However, the `maxacfreq` of the corresponding `pss` analysis should be set to guarantee that $| \max\{f(in)\} |$ is less than `maxacfreq`, otherwise the computed solution might be contaminated by aliasing effects. The `pxf` simulation is not executed for $| f(out) |$ greater than `maxacfreq`. Diagnostic messages are printed for those extreme cases, indicating how `maxacfreq` should be set in the `pss` analysis. In the majority of the simulations, however, this is not an issue, because `maxacfreq` is never allowed to be smaller than 40x the `pss` fundamental.

With `PXF` the frequency of the stimulus and of the response are usually different (this is an important way in which `pxf` differs from `XF`). The `freqaxis` parameter is used to specify whether the results should be output versus the input frequency (`in`), the output frequency (`out`), or the absolute value of the input frequency (`absin`).

Spectre Circuit Simulator Reference

Analysis Statements

You can specify the output with a pair of nodes or a probe component. Any component with two or more terminals can be a voltage probe. When there are more than two terminals, they are grouped in pairs; and you use the `portv` parameter to select the appropriate pair of terminals. Alternatively, you can simply specify a voltage to be the output by giving a pair of nodes on the `pxf` analysis statement.

Any component that naturally computes current as an internal variable can be a current probe. If the probe component computes more than one current, you use the `porti` parameter to select the appropriate current. It is an error to specify both `portv` and `porti`. If neither is specified, the probe component provides a reasonable default.

The `stimuli` parameter specifies what is used for the inputs for the transfer functions. There are two choices. `stimuli=sources` indicates that the sources present in the circuit should be used. The `xfmag` parameters provided by the sources may be used to adjust the computed gain to compensate for gains or losses in a test fixture. One can limit the number of sources in hierarchical netlists by using the `save` and `nestlvl` parameters. `stimuli=nodes_and_terminals` indicates that all possible transfer functions should be computed.

This is useful when it is not known in advance which transfer functions are interesting. Transfer functions for nodes are computed assuming that a unit magnitude flow (current) source is connected from the node to ground. Transfer functions for terminals are computed assuming that a unit magnitude value (voltage) source is connected in series with the terminal. By default, the transfer functions from a small set of terminals are computed. If transfer functions from specific terminals are desired, specify the terminals in the `save` statement. You must use the `:probe` modifier (for example, `Rout:1:probe`) or specify `useprobes=yes` on the options statement. If transfer functions from all terminals are desired, specify `currents=all` and `useprobes=yes` on the options statement.

You can specify sweep limits by giving the end points or by providing the center value and the span of the sweep. Steps can be linear or logarithmic, and you can specify the number of steps or the size of each step. You can give a step size parameter (`step, lin, log, dec`) to determine whether the sweep is linear or logarithmic. If you do not give a step size parameter, the sweep is linear when the ratio of stop to start values is less than 10, and logarithmic when this ratio is 10 or greater. Alternatively, you may specify the particular values that the sweep parameter should take using the `values` parameter. If you give both a specific set of values and a set specified using a sweep range, the two sets are merged and collated before being used. All frequencies are in Hertz.

Spectre Circuit Simulator Reference

Analysis Statements

Parameter Index

In the following index, the number following each parameter name indicates where to find the description of that parameter.

annotate	23	maxsideband	15	solver	21	sweeptype	10
center	3	nestlvl	18	span	4	title	25
dec	7	oscsolver	22	start	1	tolerance	19
freqaxis	16	probe	12	stats	24	values	9
gear_order	20	relharmnum	11	step	5		
lin	6	save	17	stimuli	13		
log	8	sidebands	14	stop	2		

Quasi-Periodic AC Analysis (qpac)

Description

The quasi periodic AC (QPAC) analysis is used to compute transfer functions for circuits that exhibit multitone frequency translation. Such circuits include mixers, switched-capacitor filters, samplers, phase-locked loops, and the like. It is a small-signal analysis like AC analysis, except the circuit is first linearized about a quasiperiodically varying operating point as opposed to a simple DC operating point. Linearizing about a quasiperiodically time-varying operating point allows transfer-functions that include frequency translation, whereas simply linearizing about a DC operating point could not because linear time-invariant circuits do not exhibit frequency translation. Also, the frequency of the sinusoidal stimulus is not constrained by the period of the large periodic solution.

Computing the small-signal response of a quasiperiodically varying circuit is a two step process. First, the small stimulus is ignored and the quasiperiodic steady-state response of the circuit to possibly large periodic stimuli is computed using QPSS analysis. As a normal part of the QPSS analysis, the quasiperiodically time-varying representation of the circuit is computed and saved for later use. The second step is to apply the small stimulus to the periodically varying linear representation to compute the small signal response. This is done using the QPAC analysis.

A QPAC analysis cannot be used alone, it must follow a QPSS analysis. However, any number of quasiperiodic small-signal analyses such as QPAC, QPSP, QPXF, QPNOISE, can follow a QPSS analysis.

Unlike other analyses in Spectre, this analysis can only sweep frequency.

Spectre Circuit Simulator Reference

Analysis Statements

Definition

Name **qpac** parameter=value ...

Parameters

Sweep interval parameters

- | | | |
|----|-------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 1 | <code>start=0</code> | Start sweep limit. |
| 2 | <code>stop</code> | Stop sweep limit. |
| 3 | <code>center</code> | Center of sweep. |
| 4 | <code>span=0</code> | Sweep limit span. |
| 5 | <code>step</code> | Step size, linear sweep. |
| 6 | <code>lin=50</code> | Number of steps, linear sweep. |
| 7 | <code>dec</code> | Points per decade. |
| 8 | <code>log=50</code> | Number of steps, log sweep. |
| 9 | <code>values=[...]</code> | Array of sweep values. |
| 10 | <code>sweepstype</code> | Specifies if the sweep frequency range is absolute frequency of input or if it is relative to the port harmonics.
Possible values are <code>absolute</code> or <code>relative</code> . |
| 11 | <code>relharmnum=[...]</code> | Harmonic to which relative frequency sweep should be referenced. |

Output parameters

- | | | |
|----|-----------------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 12 | <code>sidevec=[...]</code> | Array of relevant sidebands for the analysis. |
| 13 | <code>clockmaxharm=0</code> | An alternative to the <code>sidevec</code> array specification, which automatically generates the array: <code>[-clockmaxharm ... 0 ... +clockmaxharm][maxharms(QPSS)[2]...0...maxharms(QPSS)[2]][...]</code> . |

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Analysis Statements

- 14 `freqaxis` Specifies whether the results should be output versus the input frequency, the output frequency, or the absolute value of the output frequency. Default is `in` for logarithmic frequency sweeps and `absout` otherwise.
Possible values are `absout`, `out` or `in`.
- 15 `save` Signals to output.
Possible values are `all`, `lvl`, `allpub`, `lvlpub`, `selected`, or `none`.
- 16 `nestlvl` Levels of subcircuits to output.
- 17 `outputperiod=0.0` (no output)
Time-domain output period. The time-domain small-signal response is computed for the period specified, rounded to the nearest integer multiple of the `pss` period.

Convergence parameters

- 18 `tolerance=1e-9` Relative tolerance for linear solver.
- 19 `gear_order=2` Gear order used for small-signal integration, 1 or 2.
- 20 `solver=turbo` Solver type.
Possible values are `std` or `turbo`.

Annotation parameters

- 21 `annotate=sweep` Degree of annotation.
Possible values are `no`, `title`, `sweep`, `status`, or `steps`.
- 22 `stats=no` Analysis statistics.
Possible values are `no` or `yes`.
- 23 `title` Analysis title.

User can select the set of periodic small-signal output frequencies of interest by setting either the `clockmaxharm` or the `sidevec` parameters. Sidebands are vectors in QPAC. Assume we have one large tone and one moderate tone in QPSS. A sideband K1 will be represented as [K1_1 K1_2]. Corresponding frequency is

$$K1_1 * \text{fund}(\text{large tone of QPSS}) + K1_2 * \text{fund}(\text{moderate tone of QPSS})$$

Spectre Circuit Simulator Reference

Analysis Statements

We assume that there are L large and moderate tones in QPSS analysis and a given set of n integer vectors representing the sidebands

$K1 = \{ K1_1, \dots, K1_j, \dots, K1_L \}$, $K2, \dots, Kn$. The output frequency at each sideband is computed as

$$f(\text{out}) = f(\text{in}) + \text{SUM}_{j=1_to_L} \{ K1_j * \text{fund_j}(\text{qpss}) \},$$

where $f(\text{in})$ represents the (possibly swept) input frequency, and $\text{fund_j}(\text{qpss})$ represents the fundamental frequency used in the corresponding QPSS analysis. Thus, when analyzing a down-converting mixer, while sweeping the RF input frequency, the most relevant sideband for IF output is $\{-1, 0\}$. When simulating an up-converting mixer, while sweeping IF input frequency, the most relevant sideband for RF output is $\{1, 0\}$. User would enter `sidevec` as a sequence of integer numbers, separated by spaces. The set of vectors $\{1\ 1\ 0\} \{1\ -1\ 0\} \{1\ 1\ 1\}$ becomes `sidevec=[1 1 0 1 -1 0 1 1 1]`. For `clockmaxharm`, only the large tone - first fundamental will be affected by this entry, all the rest - moderate tones - will be limited by `maxharms`, specified for a QPSS analysis. Given `maxharms=[k1max k2max ... knmax]` in QPSS and `clockmaxharm=Kmax` all $(2*Kmax + 1)*(2*k2max+1)*(2*k3max+1)*\dots*(2*knmax+1)$ sidebands are generated.

The number of requested sidebands changes substantially the simulation time. In addition, the `maxacfreq` of the corresponding QPSS analysis should be set to guarantee that $|\max\{f(\text{out})\}|$ is less than `maxacfreq`, otherwise the computed solution might be contaminated by aliasing effects. The QPAC simulation is not executed for $|f(\text{in})|$ greater than `maxacfreq`. Diagnostic messages are printed for those extreme cases, indicating how `maxacfreq` should be set in the QPSS analysis. Usually, however, this is not an issue, because `maxacfreq` is never allowed to be smaller than 40x the QPSS fundamental.

With QPAC the frequency of the stimulus and of the response are usually different (this is an important way in which QPAC differs from AC). The `freqaxis` parameter is used to specify whether the results should be output versus the input frequency (`in`), the output frequency (`out`), or the absolute value of the output frequency (`absout`).

Unlike AC analysis, QPAC analysis can output the time-domain simulation results, by specifying the `outputperiod` parameter.

You can specify sweep limits by giving the end points or by providing the center value and the span of the sweep. Steps can be linear or logarithmic, and you can specify the number of steps or the size of each step. You can give a step size parameter (`step`, `lin`, `log`, `dec`) to determine whether the sweep is linear or logarithmic. If you do not give a step size parameter, the sweep is linear when the ratio of stop to start values is less than 10, and logarithmic when this ratio is 10 or greater. Alternatively, you may specify the particular values that the sweep parameter should take using the `values` parameter. If you give both a specific set of values

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Analysis Statements

and a set specified using a sweep range, the two sets are merged and collated before being used. All frequencies are in Hertz.

Parameter Index

In the following index, the number following each parameter name indicates where to find the description of that parameter.

annotate 21	lin 6	sidevec 12	stop 2
center 3	log 8	solver 20	sweeptype 10
clockmaxharm 13	nestlvl 16	span 4	title 23
dec 7	outputperiod 17	start 1	tolerance 18
freqaxis 14	relharmnum 11	stats 22	values 9
gear_order 19	save 15	step 5	

Quasi-Periodic Noise Analysis (qpnoise)

Description

The Quasi-Periodic Noise, or QPNOISE analysis is similar to the conventional noise analysis, except that it includes frequency conversion and intermodulation effects. Hence is it useful for predicting the noise behavior of mixers, switched-capacitor filters, and other periodically or quasi-periodically driven circuits.

QPNOISE analysis linearizes the circuit about the quasi-periodic operating point computed in the prerequisite QPSS analysis. It is the quasiperiodically time-varying nature of the linearized circuit that accounts for the frequency conversion and intermodulation. In addition, the affect of a quasi-periodically time-varying bias point on the noise generated by the various components in the circuit is also included.

The time-average of the noise at the output of the circuit is computed in the form of a spectral density versus frequency. The output of the circuit is specified with either a pair of nodes or a probe component. To specify the output of a circuit with a probe, specify it using the `oprobe` parameter. If the output is voltage (or potential), choose a `resistor` or a `port` as the output probe. If the output is current (or flow), choose a `vsource` or `iprobe` as the output probe.

If the input-referred noise is desired, specify the input source using the `iprobe` parameter. Currently, only a `vsource`, an `isource`, or a `port` may be used as an input probe. If the input source is noisy, as is a `port`, the noise analysis will compute the noise factor (F) and noise figure (NF). To match the IEEE definition of noise figure, the input probe must be a port

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Analysis Statements

with no excess noise and its `noisetemp` must be set to 16.85C (290K). In addition, the output load must be a `resistor` or `port` and must be identified as the `oprobe`.

The reference sideband (`refsideband`) specifies which conversion gain is used when computing input-referred noise, noise factor, and noise figure. The reference sideband satisfies:

$$|f(\text{input})| = |f(\text{out}) + \text{refsideband frequency shift}|.$$

The reference sideband option (`refsidebandoption`) specifies whether to consider the input at the frequency or the input at the individual quasi-periodic sideband specified. Note that Different sidebands can lead to the same frequency.

Sidebands are vectors in QPNOISE. Assume we have one large tone and one moderate tone in QPSS. A sideband K_i will be a vector $[K_{i_1} \ K_{i_2}]$. It gives the frequency at

$$K_{i_1} * \text{fund}(\text{large tone of QPSS}) + K_{i_2} * \text{fund}(\text{moderate tone of QPSS})$$

Use `refsideband=[0 0 ...]` when the input and output of the circuit are at the same frequency (such as with amplifiers and filters).

The noise analysis always computes the total noise at the output, which includes contributions from the input source and the output load. The amount of the output noise that is attributable to each noise source in the circuit is also computed and output individually. If the input source is identified (using `iprobe`) and is a `vsource` or `isourec`, the input-referred noise is computed, which includes the noise from the input source itself. Finally, if the input source is identified (using `iprobe`) and is noisy, as is the case with ports, the noise factor and noise figure are computed. Thus if

N_o = total output noise

N_s = noise at the output due to the input probe (the source)

N_{si} = noise at the output due to the image harmonic at the source

N_{so} = noise at the output due to harmonics other than input at the source

N_l = noise at the output due to the output probe (the load)

IRN = input referred noise

G = gain of the circuit

F = noise factor

NF = noise figure

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Fdsb = double sideband noise factor

NFdsb = double sideband noise figure

Fieee = IEEE single sideband noise factor

NFieee = IEEE single sideband noise figure

then,

$$\text{IRN} = \sqrt{\text{No}^2 / \text{G}^2}$$

$$\text{F} = (\text{No}^2 - \text{NI}^2) / \text{Ns}^2$$

$$\text{NF} = 10 * \log_{10}(\text{F})$$

$$\text{Fdsb} = (\text{No}^2 - \text{NI}^2) / (\text{Ns}^2 + \text{Nsi}^2)$$

$$\text{NFdsb} = 10 * \log_{10}(\text{Fdsb})$$

$$\text{Fieee} = (\text{No}^2 - \text{NI}^2 - \text{Nso}) / \text{Ns}^2$$

$$\text{NFieee} = 10 * \log_{10}(\text{Fieee}).$$

When the results are output, No is named `out`, IRN is named `in`, G is named `gain`, F, NF, Fdsb, NFdsb, Fieee, and NFieee are named `F`, `NF`, `Fdsb`, `NFdsb`, `Fieee`, and `NFieee` respectively.

The computation of gain and IRN in QPNOISE assumes that the circuit under test is impedance-matched to the input source. This can introduce inaccuracy into the gain and IRN computation.

Unlike other analyses in Spectre, this analysis can only sweep frequency.

Definition

Name [p] [n] **qpnoise** parameter=value ...

The optional terminals (p and n) specify the output of the circuit. If you do not give the terminals, then you must specify the output with a probe component.

Spectre Circuit Simulator Reference

Analysis Statements

Parameters

Sweep interval parameters

1	<code>start=0</code>	Start sweep limit.
2	<code>stop</code>	Stop sweep limit.
3	<code>center</code>	Center of sweep.
4	<code>span=0</code>	Sweep limit span.
5	<code>step</code>	Step size, linear sweep.
6	<code>lin=50</code>	Number of steps, linear sweep.
7	<code>dec</code>	Points per decade.
8	<code>log=50</code>	Number of steps, log sweep.
9	<code>values=[...]</code>	Array of sweep values.
10	<code>sweepstype</code>	Specifies if the sweep frequency range is absolute frequency of input or if it is relative to the port harmonics. Possible values are <code>absolute</code> or <code>relative</code> .
11	<code>relharmnum=[...]</code>	Harmonic to which relative frequency sweep should be referenced.

Probe parameters

12	<code>oprobe</code>	Compute total noise at the output defined by this component.
13	<code>iprobe</code>	Refer the output noise to this component.
14	<code>refsideband=[...]</code>	Conversion gain associated with this sideband is used when computing input-referred noise or noise figure.
15	<code>refsidebandoption=freq</code>	Whether to view the sideband as a specification of a frequency or a specification of an individual sideband. Possible values are <code>freq</code> or <code>individual</code> .

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Output parameters

- 16 `clockmaxharm=7` Maximum clock harmonics range included when computing noise either up-converted or down-converted to the output by the periodic drive signal..
- 17 `sidevec=[...]` Array of relevant sidebands for the analysis.
- 18 `save` Signals to output.
Possible values are `all`, `lvl`, `allpub`, `lvlpub`, `selected`, or `none`.
- 19 `nestlvl` Levels of subcircuits to output.
- 20 `saveallsidebands=no` Save noise contributors by sideband.
Possible values are `no` or `yes`.

Convergence parameters

- 21 `tolerance=1e-9` Relative tolerance for linear solver.
- 22 `gear_order=2` Gear order used for small-signal integration, 1 or 2.
- 23 `solver=turbo` Solver type.
Possible values are `std` or `turbo`.

Annotation parameters

- 24 `annotate=sweep` Degree of annotation.
Possible values are `no`, `title`, `sweep`, `status`, or `steps`.
- 25 `stats=no` Analysis statistics.
Possible values are `no` or `yes`.
- 26 `title` Analysis title.

In practice, noise can mix with each of the harmonics of the quasi-periodic drive signal applied in the QPSS analysis and end up at the output frequency. The QPNOISE analysis only includes the noise that mixes with a finite set of harmonics that are specified using the `clockmaxharm` and `sidevec` parameters. Sidebands are vectors in quasi-periodic analyses. For one large tone and one moderate tone in QPSS, a sideband K1 will be represented as [K1_1 K1_2]. Corresponding frequency shift is

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Analysis Statements

$K1_1 * \text{fund}(\text{large tone of QPSS}) + K1_2 * \text{fund}(\text{moderate tone of QPSS})$

We assume that there are L large and moderate tones in QPSS analysis and a given set of n integer vectors representing the sidebands

$K1 = \{ K1_1, \dots, K1_j, \dots, K1_L \},$

$K2, \dots, Kn.$

If K_i represents sideband i, then

$f(\text{noise_source}) = f(\text{out}) + \text{SUM}_{j=1_to_L} \{ K_{i_j} * \text{fund}_{j}(\text{qpss}) \},$

The `clockmaxharm` parameter only affects clock frequency. It can be less or more than `maxharms[1]` in QPSS. Moderate tones are limited by `maxharms` specified in QPSS. If selected sidebands are specified using the `sidevec` parameter, then only those are included in the calculation. Care should be taken when specifying the `sidevec` or `clockmaxharm` QPNOISE and `maxharms` in QPSS. Noise results will be in error if you do not include a sideband that contributes significant noise to the output.

The number of requested sidebands will change substantially the simulation time.

You can specify sweep limits by giving the end points or by providing the center value and the span of the sweep. Steps can be linear or logarithmic, and you can specify the number of steps or the size of each step. You can give a step size parameter (`step`, `lin`, `log`, `dec`) to determine whether the sweep is linear or logarithmic. If you do not give a step size parameter, the sweep is linear when the ratio of stop to start values is less than 10, and logarithmic when this ratio is 10 or greater. Alternatively, you may specify the particular values that the sweep parameter should take using the `values` parameter. If you give both a specific set of values and a set specified using a sweep range, the two sets are merged and collated before being used. All frequencies are in Hertz.

Parameter Index

In the following index, the number following each parameter name indicates where to find the description of that parameter.

<code>annotate</code> 24	<code>log</code> 8	<code>saveallsidebands</code> 20	<code>stop</code> 2
<code>center</code> 3	<code>nestlvl</code> 19	<code>sidevec</code> 17	<code>sweeptype</code> 10
<code>clockmaxharm</code> 16	<code>oprobe</code> 12	<code>solver</code> 23	<code>title</code> 26
<code>dec</code> 7	<code>refsideband</code> 14	<code>span</code> 4	<code>tolerance</code> 21
<code>gear_order</code> 22	<code>refsidebandoption</code> 15	<code>start</code> 1	<code>values</code> 9

iprobe	13	relharmnum	11	stats	25
lin	6	save	18	step	5

Quasi-Periodic S-Parameter Analysis (qpssp)

Description

The quasi periodic SP (QPSP) analysis is used to compute scattering and noise parameters for n-port circuits that exhibit frequency translation. Such circuits include mixers, switched-capacitor filters, samplers, phase-locked loops, and the like. It is a small-signal analysis like SP analysis, except, as in QPAC and QPXF, the circuit is first linearized about a quasiperiodically varying operating point as opposed to a simple DC operating point. Linearizing about a quasiperiodically time-varying operating point allows the computation of S-parameters between circuit ports that convert signals from one frequency band to another. QPSP can also calculate noise parameters in frequency-converting circuits. QPSP computes noise figure (both single-sideband and double-sideband), input referred noise, equivalent noise parameters, and noise correlation matrices. As in QPNOISE, but unlike SP, the noise features of the QPSP analysis include noise folding effects due to the periodically time-varying nature of the circuit.

Computing the n-port S-parameters and noise parameters of a quasiperiodically varying circuit is a two step process. First, the small stimulus is ignored and the quasiperiodic steady-state response of the circuit to possibly large periodic stimulus is computed using QPSS analysis. As a normal part of the QPSS analysis, the quasiperiodically time-varying representation of the circuit is computed and saved for later use. The second step is applying small-signal excitations to compute the n-port S-parameters and noise parameters. This is done using the QPSP analysis. A QPSP analysis cannot be used alone, it must follow a QPSS analysis. However, any number of periodic small-signal analyses such as QPAC, QPSP, QPXF, QPNOISE, can follow a single QPSS analysis.

Unlike other analyses in Spectre, this analysis can only sweep frequency.

Spectre Circuit Simulator Reference

Analysis Statements

Definition

Name `qpsp parameter=value ...`

Parameters

Sweep interval parameters

- | | | |
|----|---------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 1 | <code>start=0</code> | Start sweep limit. |
| 2 | <code>stop</code> | Stop sweep limit. |
| 3 | <code>center</code> | Center of sweep. |
| 4 | <code>span=0</code> | Sweep limit span. |
| 5 | <code>step</code> | Step size, linear sweep. |
| 6 | <code>lin=50</code> | Number of steps, linear sweep. |
| 7 | <code>dec</code> | Points per decade. |
| 8 | <code>log=50</code> | Number of steps, log sweep. |
| 9 | <code>values=[...]</code> | Array of sweep values. |
| 10 | <code>sweeptype</code> | Specifies if the sweep frequency range is absolute frequency of input or if it is relative to the port harmonics.
Possible values are <code>absolute</code> or <code>relative</code> . |

Port parameters

- | | | |
|----|---------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 11 | <code>ports=[...]</code> | List of active ports. Ports are numbered in the order given. For purposes of noise figure computation, the input is considered port 1 and the output is port 2. |
| 12 | <code>portharmsvec=[...]</code> | List of harmonics active on specified list of ports. Must have a one-to-one correspondence with the ports vector. |

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Analysis Statements

13 `harmsvec=[...]` List of harmonics combinations, in addition to ones associated with specific ports by `portharmsvec`, that are active. Call them secondary..

Output parameters

14 `freqaxis` Specifies whether the results should be output versus the input frequency, the output frequency, or the absolute value of the input frequency. Default is `in`.
Possible values are `absin`, `in` or `out`.

Noise parameters

15 `donoise=yes` Perform noise analysis. If `oprobe` is specified as a valid port, this is set to `yes`, and a detailed noise output is generated.
Possible values are `no` or `yes`.

Probe parameters

16 `clockmaxharm=7` Maximum clock harmonics range included when computing noise either up-converted or down-converted to the output by the periodic drive signal..

Convergence parameters

17 `tolerance=1e-9` Relative tolerance for linear solver.

18 `gear_order=2` Gear order used for small-signal integration, 1 or 2.

19 `solver=turbo` Solver type.
Possible values are `std` or `turbo`.

Annotation parameters

20 `annotate=sweep` Degree of annotation.
Possible values are `no`, `title`, `sweep`, `status`, or `steps`.

21 `stats=no` Analysis statistics.
Possible values are `no` or `yes`.

22 `title` Analysis title.

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To specify the QPSP analysis the port and port harmonics combinations must be specified. You can select the ports of interest by setting the port parameter and the set of periodic small-signal output frequencies of interest by setting the `portharmsvec` or the `harmsvec` parameters. Sidebands are vectors in QPSP. Assume we have one large tone and one moderate tone in QPSS. A sideband K1 will be represented as [K1_1 K1_2]. Corresponding frequency is

$$K1_1 * \text{fund}(\text{large tone of QPSS}) + K1_2 * \text{fund}(\text{moderate tone of QPSS})$$

We assume that there are L (1 large plus L-1 moderate) tones in QPSS analysis and a given set of n integer vectors representing the sidebands

$$K1 = \{ K1_1, \dots, K1_j, \dots, K1_L \}, K2, \dots, Kn.$$

integer numbers representing the harmonics K_1, K_2, \dots, K_n , the scattering parameters at each port are computed at the frequencies

$$f(\text{scattered}) = f(\text{rel}) + \text{SUM}_{j=1_to_L} \{ K1_j * \text{fund}_j(\text{qpss}) \},$$

where $f(\text{rel})$ represents the relative frequency of a signal incident on a port, $f(\text{scattered})$ represents the frequency to which the relevant scattering parameter represents the conversion, and $\text{fund}_j(\text{qpss})$ represents the fundamental frequency used in the corresponding QPSS analysis.

Thus, when analyzing a down-converting mixer, with signal in the upper sideband, and sweeping the RF input frequency, the most relevant harmonic for RF input is $K1 = \{ 1, 0 \}$ and for IF output $K2 = \{ 0, 0 \}$. Hence we can associate $K2 = \{ 1, 0 \}$ with the IF port and $K1 = \{ 0, 0 \}$ with the RF port. S21 will represent the transmission of signal from the RF to IF, and S11 the reflection of signal back to the RF port. If the signal was in the lower sideband, then a choice of $K1 = \{ -1, 0 \}$ would be more appropriate.

Either the `portharmsvec` or the `harmsvec` parameters can be used to specify the harmonics of interest. If `portharmsvec` is given, the harmonics must be in one-to-one correspondence with the ports, with each harmonic associated with a single port. If harmonics are specified in the optional `harmsvec` parameter, then all possible frequency-translating scattering parameters associated with the specified harmonics are computed.

With QPSP the frequency of the input and of the response are usually different (this is an important way in which QPSP differs from SP). Because the QPSP computation involves inputs and outputs at frequencies that are relative to multiple harmonics, the `freqaxis` and `sweep_type` parameters behave somewhat differently in QPSP than in QPAC and QPXF.

The `sweep_type` parameter controls the way the frequencies in the QPSP analysis are swept. Specifying a relative sweep indicates to sweep relative to the analysis harmonics (not the QPSS fundamental) and an absolute sweep is a sweep of the absolute input source

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frequency. For example, with a QPSS fundamentals of 1000MHz and 900MHz, the `portharmsvec` set to `[0 1 1 -1]` to examine a downconverting mixer, `sweeptype=relative`, and a sweep range of `f(rel)=0->50MHz`, then `S21` would represent the strength of signal transmitted from the input port in the range 900->950MHz to the output port at frequencies 100->150MHz. Using `sweeptype=absolute` and sweeping the frequency from 900->950MHz would calculate the same quantities, since `f(abs)=900->950MHz`, and `f(rel) = f(abs) - (K1 * fund_1(qpss) + K2 * fund_2(qpss)) = 0->50MHz`, because `K1=0`, `K2=1` and `fund_1(qpss) = 1000MHz`, `fund_2(qpss) = 900MHz`.

Usually it is not necessary to sweep frequency in QPSP over more than one period of large (clock) fundamental in QPSS.

The `freqaxis` parameter is used to specify whether the results should be output versus the scattered frequency at the input port (`in`), the scattered frequency at the output port (`out`), or the absolute value of the frequency swept at the input port (`absin`).

An increase in the number of requested ports and harmonics will increase the simulation time substantially.

To insure accurate results in QPSP, the `maxacfreq` of the corresponding PSS analysis should be set to guarantee that $|\max\{f(\text{scattered})\}|$ is less than `maxacfreq`, otherwise the computed solution might be contaminated by aliasing effects.

QPSP analysis also computes noise figures, equivalent noise sources, and noise parameters. The noise computation, which is skipped only when `donoise` is set to `no`, requires additional simulation time. If

`No` = total output noise at frequency `f`

`Ns` = noise at the output due to the input probe (the source)

`Nsi` = noise at the output due to the image harmonic at the source

`Nso` = noise at the output due to harmonics other than input at the source

`NI` = noise at the output due to the output probe (the load)

`IRN` = input referred noise

`G` = gain of the circuit

`F` = noise factor (single side band)

`NF` = noise figure (single side band)

`Fdsb` = double sideband noise factor

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NFdsb = double sideband noise figure

Fieee = IEEE single sideband noise factor

NFieee = IEEE single sideband noise figure

then,

$$\text{IRN} = \sqrt{\text{No}^2 / \text{G}^2}$$
$$\text{F} = (\text{No}^2 - \text{NI}^2) / \text{Ns}^2$$
$$\text{NF} = 10 * \log_{10}(\text{F})$$
$$\text{Fdsb} = (\text{No}^2 - \text{NI}^2) / (\text{Ns}^2 + \text{Nsi}^2)$$
$$\text{NFdsb} = 10 * \log_{10}(\text{Fdsb})$$
$$\text{Fieee} = (\text{No}^2 - \text{NI}^2 - \text{Nso}) / \text{Ns}^2$$
$$\text{NFieee} = 10 * \log_{10}(\text{Fieee}).$$

When the results are output, IRN is named `in`, G is named `gain`, F, NF, Fdsb, NFdsb, Fieee, and NFieee are named `F`, `NF`, `Fdsb`, `NFdsb`, `Fieee`, and `NFieee` respectively. Note that the gain computed by QPSP is the voltage gain from the actual circuit input to the circuit output, not the gain from the internal port voltage source to the output.

To insure accurate noise calculations, the `clockmaxharm` parameters must be set to include the relevant noise folding effects. `clockmaxharm` is only relevant to the noise computation features of QPSP.

You can specify sweep limits by giving the end points or by providing the center value and the span of the sweep. Steps can be linear or logarithmic, and you can specify the number of steps or the size of each step. You can give a step size parameter (`step`, `lin`, `log`, `dec`) to determine whether the sweep is linear or logarithmic. If you do not give a step size parameter, the sweep is linear when the ratio of stop to start values is less than 10, and logarithmic when this ratio is 10 or greater. Alternatively, you may specify the particular values that the sweep parameter should take using the `values` parameter. If you give both a specific set of values and a set specified using a sweep range, the two sets are merged and collated before being used. All frequencies are in Hertz.

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center	3	harmsvec	13	span	4	title	22
clockmaxharm	16	lin	6	start	1	tolerance	17
dec	7	log	8	stats	21	values	9
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freqaxis	14	ports	11	stop	2		

Quasi-Periodic Steady State Analysis (qpss)

Description

Similar to PAC analysis, the periodic distortion (PDISTO) analysis calculates responses of a circuit that exhibits frequency translations. However, instead of having small signal linear behavior, it models the response as having components of a few harmonics of input signal frequencies. This allows computing responses to moderately large input signals. An example is intermodulation distortion with two moderate input signals. PDISTO treats one particular input signal (usually the one that causes the most nonlinearity or the largest response) as the large signal, and the others as moderate signals.

An initial transient analysis is carried out by first suppressing all moderate input signals. Then we run a number of (at least 2) stable iterations with all signals activated, which is followed by shooting Newton method. PDISTO employs the Mixed Frequency Time (MFT) algorithm extended to multiple fundamental frequencies. For details of MFT algorithm, please see *Steady-State Methods for Simulating Analog and Microwave Circuits*, by K. S. Kundert, J. K. White, and A. Sangiovanni-Vincentelli, Kluwer, Boston, 1990.

Like PSS, PDISTO uses shooting Newton method as its backbone. However, instead of doing a single transient integration, each Newton iteration does a number of transient integrations of one large signal period.

Each of the integrations differs by a phase-shift in each moderate input signal. The number of integrations is determined by the numbers of harmonics of moderate fundamentals specified by the user. Given $\text{maxharm}=[k_1 \ k_2 \ \dots \ k_n]$, the total number of integrations is $(2*k_2+1)*(2*k_3+1)*\dots*(2*k_n+1)$. As one consequence, the efficiency of the algorithm depends significantly on the number of harmonics required to model the responses of moderate fundamentals. As another consequence, the number of harmonics of the large fundamental

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does not significantly affect the efficiency of the shooting algorithm. The boundary conditions of a shooting interval are such that the time domain integrations are consistent with a frequency domain transformation with a shift of one large signal period.

PDISTO inherits a majority of PSS parameters. A few new parameters are added. The most important ones are `funds` and `maxharms`. They replace PSS parameters, `fund(or period)` and `harms`, respectively. The `funds` parameter accepts a list of names of fundamentals that are present in the sources. These names are specified in the sources by parameter `fundname`. The first fundamental is considered as the large signal. A few heuristics can be used for picking the large fundamental.

- (1) Pick the one which is not a sinusoidal.
- (2) Pick the one which causes the most nonlinearity.
- (3) Pick the one which causes the largest response.

The `maxharms` parameter accepts a list of numbers of harmonics that are required to sufficiently model responses due to different fundamentals.

Definition

Name `qpss parameter=value ...`

Parameters

Distortion fundamental parameters

- | | |
|-------------------------------|------------------------------------------------------------------------------------|
| 1 <code>funds=[...]</code> | Array of fundamental frequency names for fundamentals to use in analysis. |
| 2 <code>maxharms=[...]</code> | Array of number of harmonics of each fundamental to consider for each fundamental. |

Simulation interval parameters

- | | |
|-----------------------------|----------------------------------------------------------------------------------|
| 3 <code>tstab=0.0 s</code> | Extra stabilization time after the onset of periodicity for independent sources. |
| 4 <code>tstart=0.0 s</code> | Initial transient analysis start time. |

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Time-step parameters

- 5 `maxstep (s)` Maximum time step. Default derived from `errpreset`.
- 6 `step=0.001 period s` Minimum time step that would be used solely to maintain the aesthetics of the results.

Initial-condition parameters

- 7 `ic=all` What should be used to set initial condition.
Possible values are `dc`, `node`, `dev`, or `all`.
- 8 `skipdc=no` If yes, there will be no dc analysis for transient.
Possible values are `no` or `yes`.
- 9 `readic` File that contains initial condition.

Convergence parameters

- 10 `readns` File that contains estimate of initial transient solution.
- 11 `cmin=0 F` Minimum capacitance from each node to ground.

Output parameters

- 12 `outputtype=time` for PSS, `freq` for PDISTO
Output type.
Possible values are `all`, `time` or `freq`.
- 13 `save` Signals to output.
Possible values are `all`, `lvl`, `allpub`, `lvlpub`, `selected`, or `none`.
- 14 `nestlvl` Levels of subcircuits to output.
- 15 `oppoint=no` Should operating point information be computed for initial timestep, and if so, where should it be sent.
Possible values are `no`, `screen`, `logfile`, or `rawfile`.
- 16 `skipstart=starttime s` The time to start skipping output data.

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- 17 `skipstop=stoptime s` The time to stop skipping output data.
- 18 `skipcount` Save only one of every skipcount points.
- 19 `strobeperiod (s)` The output strobe interval (in seconds of transient time).
- 20 `strobedelay=0 s` The delay (phase shift) between the skipstart time and the first strobe point.
- 21 `compression=no` Do data compression on output.
Possible values are `no` or `yes`.
- 22 `saveinit=no` If set, the waveforms for the initial transient before steady state are saved.
Possible values are `no` or `yes`.

State-file parameters

- 23 `write` File to which initial transient solution (before steady-state) is to be written.
- 24 `writefinal` File to which final transient solution in steady-state is to be written.
- 25 `swapfile` Temporary file that holds steady-state information. Tells Spectre to use a regular file rather than virtual memory to hold the periodic operating point. Use this option if Spectre complains about not having enough memory to complete this analysis.

Integration method parameters

- 26 `method` Integration method. Default derived from `errpreset`.
Possible values are `euler`, `trap`, `traponly`, `gear2`, or `gear2only`.

Accuracy parameters

- 27 `errpreset=moderate` Selects a reasonable collection of parameter settings.
Possible values are `liberal`, `moderate` or `conservative`.

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28	<code>relref</code>	Reference used for the relative convergence criteria. Default derived from <code>errpreset</code> . Possible values are <code>pointlocal</code> , <code>alllocal</code> , <code>sigglobal</code> , or <code>allglobal</code> .
29	<code>lteratio</code>	Ratio used to compute LTE tolerances from Newton tolerance. Default derived from <code>errpreset</code> .
30	<code>steadyratio</code>	Ratio used to compute steady state tolerances from LTE tolerance. Default derived from <code>errpreset</code> .
31	<code>maxperiods=20</code> for driven PSS, 50 for autonomous PSS	Maximum number of simulated periods to reach steady-state.
32	<code>tolerance=1e-4</code>	Relative tolerance for linear solver.
33	<code>finitediff</code>	Options for finite difference method refinement after quasi-periodic shooting method. <code>finitediff</code> is changed from <code>no</code> to <code>samegrid</code> automatically when <code>readqpss</code> and <code>writeqpss</code> are used to re-use QPSS results. Possible values are <code>no</code> , <code>yes</code> or <code>refine</code> .

Annotation parameters

34	<code>stats=no</code>	Analysis statistics. Possible values are <code>no</code> or <code>yes</code> .
35	<code>annotate=sweep</code>	Degree of annotation. Possible values are <code>no</code> , <code>title</code> , <code>sweep</code> , <code>status</code> , or <code>steps</code> .
36	<code>title</code>	Analysis title.

Newton parameters

37	<code>maxiters=5</code>	Maximum number of iterations per time step.
38	<code>restart=yes</code>	Do not use previous DC solution as initial guess. Possible values are <code>no</code> or <code>yes</code> .

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Circuit age

- 39 `circuitage (Years)` Stress Time. Age of the circuit used to simulate hot-electron degradation of MOSFET and BSIM circuits.
- 40 `writeqpss` File to which final quasi-periodic steady-state solution is to be written. Small signal analyses such as `qpac`, `qpxf` and `qpnoise` can read in the steady-state solution from this file directly instead of running the `qpss` analysis again.
- 41 `readqpss` File from which final quasi-periodic steady-state solution is to be read. Small signal analyses such as `qpac`, `qpxf` and `qpnoise` can read in the steady-state solution from this file directly instead of running the `qpss` analysis again.

Most of PDISTO analysis parameters are inherited from PSS analysis, and their meanings remain essentially unchanged. Two new important parameters are `funds` and `maxharms`. They replace and extend the role of `fund` and `harms` parameters of PSS analysis. One important difference is that `funds` accepts a list of fundamental names instead of actual frequencies. The frequencies associated with fundamentals are figured out automatically by the simulator. An important feature is that each input signal can be a composition of more than one sources. However, these sources must have the same fundamental name. For each fundamental name, its fundamental frequency is the greatest common factor of all frequencies associated with the name. Missing or not listing all fundamental names using the parameter `funds` will result in an amputation of the current simulation. However if `maxharms` is not given, a warning message will be issued, and the number of harmonics is defaulted to 1 for each of the fundamentals.

The role of some PSS parameters is extended. The parameter `maxperiods` that controls the maximum number of shooting iterations for PSS analysis also controls the number of the maximum number of shooting iterations for PDISTO analysis.

The `tstab` parameter controls both the length of the initial transient integration with only the clock tone activated and the number of stable iterations with moderate tones activated. The stable iterations are run before Newton iterations.

The `errpreset` parameter lets you adjust the simulator parameters to fit your needs quickly. In most cases, it should also be the only parameter you need to adjust. If you want a fast simulation with reasonable accuracy, you may set `errpreset` to `liberal`. If have some concern for accuracy, you may set `errpreset` to `moderate`. If accuracy is your main interest, you may set `errpreset` to `conservative`.

If users don't specify `steadyratio`, it is always 1.0, not affected by `errpreset`. The effect of `errpreset` on other parameters is shown in the following table.

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Parameter defaults as a function of errpreset

check siggloaal??

errpreset	reltol	relref	method	lteratio	maxstep
liberal	1e-3	sigglobal	gear2only	3.5	clock period/80
moderate	1e-4	siggloaal	gear2only	3.5	clock period/100
conservative	1e-5	sigglobal	gear2only	*	clock period/200

* : If user specified `reltol` $\leq 1e-5 \cdot 10.0/3.5$, `lteratio`=3.5. Otherwise `lteratio`=10.0.

The value of `reltol` can only decrease from its value in the options statement, when `reltol` in options statement is larger. Spectre sets the value of `maxstep` so that it is no larger than the value given in the table. Except for `reltol` and `maxstep`, `errpreset` does not change the value of any parameters you have explicitly set. The actual values used for the QPSS analysis are given in the log file.

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In the following index, the number following each parameter name indicates where to find the description of that parameter.

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compression 21	method 26	skipcount 18	tstab 3
errpreset 27	nestlvl 14	skipdc 8	tstart 4
finitediff 33	oppoint 15	skipstart 16	write 23
funds 1	outputtype 12	skipstop 17	writefinal 24
ic 7	readic 9	stats 34	writeqpss 40
itres 32	readns 10	steadyratio 30	
lteratio 29	readqpss 41	step 6	
maxharms 2	relref 28	strobedelay 20	

Quasi-Periodic Transfer Function Analysis (qpxf)

Description

A conventional transfer function analysis computes the transfer function from every source in the circuit to a single output. It differs from a conventional AC analysis in that the AC analysis computes the response from a single stimulus to every node in the circuit. The difference between QPAC and QPXF analysis are similar. The Quasi Periodic Transfer Function or QPXF analysis computes the transfer functions from any source at any frequency to a single output at a single frequency. Thus, like QPAC analysis, QPXF analysis includes frequency conversion effects.

The QPXF analysis directly computes such useful quantities as conversion efficiency (transfer function from input to output at desired frequency), image and sideband rejection (input to output at undesired frequency), and LO feed-through and power supply rejection (undesired input to output at all frequencies).

As with a QPAC, QPSP, and QPNOISE analyses, a QPXF analysis must follow a QPSS analysis.

Unlike other analyses in Spectre, this analysis can only sweep frequency.

Definition

Name [p] [n] qpxf parameter=value ...

The optional terminals (p and n) specify the output of the circuit. If you do not give the terminals, then you must specify the output with a probe component.

Parameters

Sweep interval parameters

- | | | |
|---|----------------------|--------------------|
| 1 | <code>start=0</code> | Start sweep limit. |
| 2 | <code>stop</code> | Stop sweep limit. |
| 3 | <code>center</code> | Center of sweep. |
| 4 | <code>span=0</code> | Sweep limit span. |

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5	<code>step</code>	Step size, linear sweep.
6	<code>lin=50</code>	Number of steps, linear sweep.
7	<code>dec</code>	Points per decade.
8	<code>log=50</code>	Number of steps, log sweep.
9	<code>values=[...]</code>	Array of sweep values.
10	<code>sweepstype</code>	Specifies if the sweep frequency range is absolute frequency of input or if it is relative to the port harmonics. Possible values are <code>absolute</code> or <code>relative</code> .
11	<code>relharmnum=[...]</code>	Harmonic to which relative frequency sweep should be referenced.

Probe parameters

12	<code>probe</code>	Compute every transfer function to this probe component.
----	--------------------	----------------------------------------------------------

Output parameters

13	<code>stimuli=sources</code>	Stimuli used for xf analysis. Possible values are <code>sources</code> or <code>nodes_and_terminals</code> .
14	<code>sidevec=[...]</code>	Array of relevant sidebands for the analysis.
15	<code>clockmaxharm=0</code>	An alternative to the <code>sidevec</code> array specification, which automatically generates the array: <code>[-clockmaxharm ... 0 ... +clockmaxharm][maxharm(QPSS)[2]...0...maxharm(QPSS)[2]][...]</code> .
16	<code>freqaxis</code>	Specifies whether the results should be output versus the input frequency, the output frequency, or the absolute value of the input frequency. Default is <code>out</code> for logarithmic frequency sweeps and <code>absin</code> otherwise. Possible values are <code>absin</code> , <code>in</code> or <code>out</code> .
17	<code>save</code>	Signals to output. Possible values are <code>all</code> , <code>lvl</code> , <code>allpub</code> , <code>lvlpub</code> , <code>selected</code> , or <code>none</code> .

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Analysis Statements

18 `nestlvl` Levels of subcircuits to output.

Convergence parameters

19 `tolerance=1e-9` Relative tolerance for linear solver.

20 `gear_order=2` Gear order used for small-signal integration, 1 or 2.

21 `solver=turbo` Solver type.
Possible values are `std` or `turbo`.

Annotation parameters

22 `annotate=sweep` Degree of annotation.
Possible values are `no`, `title`, `sweep`, `status`, or `steps`.

23 `stats=no` Analysis statistics.
Possible values are `no` or `yes`.

24 `title` Analysis title.

The variable of interest at the output can be voltage or current, and its frequency is not constrained by the period of the large periodic solution. While sweeping the selected output frequency, you can select the periodic small-signal input frequencies of interest by setting either the `clockmaxharm` or the `sidevec` parameters. Sidebands are vectors in QPXF. Assume we have one large tone and one moderate tone in QPSS. A sideband K1 will be represented as [K1_1 K1_2]. Corresponding frequency is

$$K1_1 * \text{fund}(\text{large tone of QPSS}) + K1_2 * \text{fund}(\text{moderate tone of QPSS})$$

We assume that there are L (1 large plus L-1 moderate) tones in QPSS analysis and a given set of n integer vectors representing the sidebands

$$K1 = \{ K1_1, \dots, K1_j, \dots, K1_L \}, K2, \dots, Kn.$$

The input signal frequency at each sideband is computed as

$$f(\text{in}) = f(\text{out}) + \text{SUM}_{j=1_to_L} \{ K1_j * \text{fund}_j(\text{qpss}) \},$$

where $f(\text{out})$ represent the (possibly swept) output signal frequency, and $\text{fund}_j(\text{pss})$ represents the fundamental frequency used in the corresponding QPSS analysis. Thus, when analyzing a down-converting mixer, and sweeping the IF output frequency, $Ki = \{1, 0\}$ for

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the RF input represents the first upper-sideband, while $K_i = \{-1, 0\}$ for the RF input represents the first lower-sideband.

User would enter `sidevec` as a sequence of integer numbers, separated by spaces. The set of vectors $\{1\ 1\ 0\}$ $\{1\ -1\ 0\}$ $\{1\ 1\ 1\}$ becomes `sidevec=[1 1 0 1 -1 0 1 1 1]`. For `clockmaxharm`, only the large tone - first fundamental will be affected by this entry, all the rest - moderate tones - will be limited by `maxharms`, specified for a QPSS analysis. Given `maxharms=[k1max k2max ... knmax]` in QPSS and `clockmaxharm=Kmax` all $(2 \cdot K_{\max} + 1) \cdot (2 \cdot k_{2\max} + 1) \cdot (2 \cdot k_{3\max} + 1) \cdot \dots \cdot (2 \cdot k_{n\max} + 1)$ sidebands are generated.

The number of requested sidebands changes substantially the simulation time. In addition, the `maxacfreq` of the corresponding QPSS analysis should be set to guarantee that $| \max\{f(\text{in})\} |$ is less than `maxacfreq`, otherwise the computed solution might be contaminated by aliasing effects. The QPXF simulation is not executed for $| f(\text{out}) |$ greater than `maxacfreq`. Diagnostic messages are printed for those extreme cases, indicating how `maxacfreq` should be set in the QPSS analysis. In the majority of the simulations, however, this is not an issue, because `maxacfreq` is never allowed to be smaller than 40x the QPSS fundamental.

With QPXF the frequency of the stimulus and of the response are usually different (this is an important way in which QPXF differs from XF). The `freqaxis` parameter is used to specify whether the results should be output versus the input frequency (`in`), the output frequency (`out`), or the absolute value of the input frequency (`absin`).

You can specify the output with a pair of nodes or a probe component. Any component with two or more terminals can be a voltage probe. When there are more than two terminals, they are grouped in pairs; and you use the `portv` parameter to select the appropriate pair of terminals. Alternatively, you can simply specify a voltage to be the output by giving a pair of nodes on the QPXF analysis statement.

Any component that naturally computes current as an internal variable can be a current probe. If the probe component computes more than one current, you use the `porti` parameter to select the appropriate current. It is an error to specify both `portv` and `porti`. If neither is specified, the probe component provides a reasonable default.

The `stimuli` parameter specifies what is used for the inputs for the transfer functions. There are two choices. `stimuli=sources` indicates that the sources present in the circuit should be used. The `xfmag` parameters provided by the sources may be used to adjust the computed gain to compensate for gains or losses in a test fixture. One can limit the number of sources in hierarchical netlists by using the `save` and `nestlvl` parameters. `stimuli=nodes_and_terminals` indicates that all possible transfer functions should be computed.

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This is useful when it is not known in advance which transfer functions are interesting. Transfer functions for nodes are computed assuming that a unit magnitude flow (current) source is connected from the node to ground. Transfer functions for terminals are computed assuming that a unit magnitude value (voltage) source is connected in series with the terminal. By default, the transfer functions from a small set of terminals are computed. If transfer functions from specific terminals are desired, specify the terminals in the save statement. You must use the `:probe` modifier (for example, `Rout:1:probe`) or specify `useprobes=yes` on the options statement. If transfer functions from all terminals are desired, specify `currents=all` and `useprobes=yes` on the options statement.

You can specify sweep limits by giving the end points or by providing the center value and the span of the sweep. Steps can be linear or logarithmic, and you can specify the number of steps or the size of each step. You can give a step size parameter (`step`, `lin`, `log`, `dec`) to determine whether the sweep is linear or logarithmic. If you do not give a step size parameter, the sweep is linear when the ratio of stop to start values is less than 10, and logarithmic when this ratio is 10 or greater. Alternatively, you may specify the particular values that the sweep parameter should take using the `values` parameter. If you give both a specific set of values and a set specified using a sweep range, the two sets are merged and collated before being used. All frequencies are in Hertz.

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In the following index, the number following each parameter name indicates where to find the description of that parameter.

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<code>clockmaxharm</code> 15	<code>nestlvl</code> 18	<code>span</code> 4	<code>sweeptype</code> 10
<code>dec</code> 7	<code>probe</code> 12	<code>start</code> 1	<code>title</code> 24
<code>freqaxis</code> 16	<code>relharmnum</code> 11	<code>stats</code> 23	<code>tolerance</code> 19
<code>gear_order</code> 20	<code>save</code> 17	<code>step</code> 5	<code>values</code> 9

Deferred Set Options (set)

Description

The deferred set options statement sets or changes various program control options. You can set the options in any order and, once set, the options retain their value until reset. The set statement is queued with all analyses and is executed sequentially (The changes made to these options are deferred until the statement setting them is encountered). To set `temp`,

Spectre Circuit Simulator Reference

Analysis Statements

`tnom`, `scalem`, or `scale`, use the `alter` statement. For further options, see individual analyses.

Definition

Name set parameter=value ...

Parameters

Tolerance parameters

- | | |
|--------------------------------|---------------------------------------------------|
| 1 <code>reltol=0.001</code> | Relative convergence criterion. |
| 2 <code>vabstol=1e-06 V</code> | Voltage absolute tolerance convergence criterion. |
| 3 <code>iabstol=1e-12 A</code> | Current absolute tolerance convergence criterion. |

Temperature parameters

- | | |
|--------------------------------|-------------------------------------------------------------------------------------------------------------|
| 4 <code>tempeffects=all</code> | Temperature effect selector.
Possible values are <code>vt</code> , <code>tc</code> or <code>all</code> . |
|--------------------------------|-------------------------------------------------------------------------------------------------------------|

Convergence parameters

- | | |
|-----------------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 5 <code>homotopy=all</code> | Method used when no convergence on initial attempt of DC analysis.
Possible values are <code>none</code> , <code>gmin</code> , <code>source</code> , <code>dptran</code> , <code>ptran</code> , <code>arclength</code> , or <code>all</code> . |
| 6 <code>limit=dev</code> | Limiting algorithms to aid DC convergence.
Possible values are <code>delta</code> , <code>log</code> or <code>dev</code> . |

Component parameters

- | | |
|-----------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 7 <code>compatible=spectre</code> | Encourage device equations to be compatible with a foreign simulator. This option does not affect input syntax.
Possible values are <code>spectre</code> , <code>spice2</code> , <code>spice3</code> , <code>cdsspice</code> , <code>hspice</code> , or <code>spiceplus</code> . |
|-----------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|

Spectre Circuit Simulator Reference

Analysis Statements

- 8 `approx=no` Use approximate models. Difference between approximate and exact models is generally very small.
Possible values are `no` or `yes`.

Error-checking parameters

- 9 `diagnose=no` Print additional information that might help diagnose accuracy and convergence problems.
Possible values are `no` or `yes`.
- 10 `opptcheck=yes` Check operating point parameters against soft limits.
Possible values are `no` or `yes`.

Resistance parameters

- 11 `gmin=1e-12 S` Minimum conductance across each nonlinear device.
- 12 `gmin_check=max_v_only`
Specifies that effect of `gmin` should be reported if significant.
Possible values are `no`, `max_v_only`, `max_only`, or `all`.
- 13 `rforce=1 Ω` Resistance used when forcing nodesets and node-based initial conditions.

Quantity parameters

- 14 `quantities=no` Print quantities.
Possible values are `no`, `min` or `full`.

Annotation parameters

- 15 `narrate=yes` Narrate the simulation.
Possible values are `no` or `yes`.
- 16 `debug=no` Give debugging messages.
Possible values are `no` or `yes`.
- 17 `info=yes` Give informational messages.
Possible values are `no` or `yes`.

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Analysis Statements

18	<code>note=yes</code>	Give notice messages. Possible values are <code>no</code> or <code>yes</code> .
19	<code>maxnotes=5</code>	Maximum number of times any notice will be issued per analysis.
20	<code>warn=yes</code>	Give warning messages. Possible values are <code>no</code> or <code>yes</code> .
21	<code>maxwarns=5</code>	Maximum number of times any warning message will be issued per analysis.
22	<code>error=yes</code>	Give error messages. Possible values are <code>no</code> or <code>yes</code> .
23	<code>digits=5</code>	Number of digits used when printing numbers.
24	<code>notation=eng</code>	When printing real numbers to the screen, what notation should be used. Possible values are <code>eng</code> , <code>sci</code> or <code>float</code> .
25	<code>annotate=no</code>	Degree of annotation. Possible values are <code>no</code> or <code>title</code> .

Matrix parameters

26	<code>pivotdc=no</code>	Use numeric pivoting on every iteration of DC analysis. Possible values are <code>no</code> or <code>yes</code> .
27	<code>pivrel=0.001</code>	Relative pivot threshold.
28	<code>pivabs=0</code>	Absolute pivot threshold.

Parameter Index

In the following index, the number following each parameter name indicates where to find the description of that parameter.

<code>annotate</code> 25	<code>gmin</code> 11	<code>maxwarns</code> 21	<code>pivrel</code> 27
<code>approx</code> 8	<code>gmin_check</code> 12	<code>narrate</code> 15	<code>quantities</code> 14
<code>compatible</code> 7	<code>homotopy</code> 5	<code>notation</code> 24	<code>reltol</code> 1
<code>debug</code> 16	<code>iabstol</code> 3	<code>note</code> 18	<code>rforce</code> 13

Spectre Circuit Simulator Reference

Analysis Statements

diagnose 9	info 17	opptcheck 10	tempeffects 4
digits 23	limit 6	pivabs 28	vabstol 2
error 22	maxnotes 19	pivotdc 26	warn 20

Shell Command (shell)

Description

The shell analysis passes a command to the operating system command interpreter given in the SHELL environment variable. The command behaves as if it were typed into the command interpreter, except that any %X codes in the command are expanded first.

The default action of the shell analysis is to terminate the simulation.

Definition

Name **shell parameter=value ...**

Parameters

- | | | |
|---|----------------------------|-------------------------------------------------------------------------------------------------------------------------|
| 1 | <code>cmd="kill %P"</code> | Shell command. |
| 2 | <code>iferror=quit</code> | What to do if command returns nonzero error status.
Possible values are <code>quit</code> or <code>continue</code> . |
| 3 | <code>annotate</code> | Degree of annotation.
Possible values are <code>no</code> , <code>title</code> or <code>yes</code> . |

S-Parameter Analysis (sp)

Description

The S-parameter analysis linearizes the circuit about the DC operating point and computes S-parameters of the circuit taken as an N-port. The port statements define the ports of the circuit. Each active port is turned on sequentially, and a linear small-signal analysis is performed. Spectre converts the response of the circuit at each active port into S-parameters and outputs these parameters. There must be at least one active port statement in the circuit.

Spectre Circuit Simulator Reference

Analysis Statements

If a filename is specified using the `file` parameter, the S-parameter analysis generates an ASCII file containing the S-parameters of the circuit that can later be read-in by the `nport` component.

Spectre can perform the analysis while sweeping a parameter. The parameter can be frequency, temperature, component instance parameter, component model parameter, or netlist parameter. If changing a parameter affects the DC operating point, the operating point is recomputed on each step. You can sweep the circuit temperature by giving the parameter name as `temp` with no `dev` or `mod` parameter. You can sweep a netlist parameter by giving the parameter name with no `dev`, or `mod` parameter. After the analysis has completed, the modified parameter returns to its original value.

Definition

Name `sp parameter=value ...`

Parameters

1	<code>prevoppoint=no</code>	Use operating point computed on the previous analysis. Possible values are <code>no</code> or <code>yes</code> .
---	-----------------------------	---------------------------------------------------------------------------------------------------------------------

Sweep interval parameters

2	<code>start=0</code>	Start sweep limit.
3	<code>stop</code>	Stop sweep limit.
4	<code>center</code>	Center of sweep.
5	<code>span=0</code>	Sweep limit span.
6	<code>step</code>	Step size, linear sweep.
7	<code>lin=50</code>	Number of steps, linear sweep.
8	<code>dec</code>	Points per decade.
9	<code>log=50</code>	Number of steps, log sweep.
10	<code>values=[...]</code>	Array of sweep values.

Spectre Circuit Simulator Reference

Analysis Statements

Sweep variable parameters

11	<code>dev</code>	Device instance whose parameter value is to be swept.
12	<code>mod</code>	Model whose parameter value is to be swept.
13	<code>param</code>	Name of parameter to sweep.
14	<code>freq (Hz)</code>	Frequency when parameter other than frequency is being swept.

Port parameters

15	<code>ports=[...]</code>	List of active ports. Ports are numbered in the order given.
----	--------------------------	--------------------------------------------------------------

State-file parameters

16	<code>readns</code>	File that contains estimate of DC solution (nodeset).
----	---------------------	-------------------------------------------------------

Output parameters

17	<code>file</code>	S-parameters output file name.
18	<code>oppoint=no</code>	Should operating point information be computed, and if so, where should it be sent. Possible values are <code>no</code> , <code>screen</code> , <code>logfile</code> , or <code>rawfile</code> .

Noise parameters

19	<code>donoise=no</code>	Perform noise analysis. If <code>oprobe</code> is specified as a valid port, this is set to <code>yes</code> , and a detailed noise output is generated. Possible values are <code>no</code> or <code>yes</code> .
20	<code>oprobe</code>	Compute total noise at the output defined by this component.
21	<code>iprobe</code>	Input probe. Refer the output noise to this component.

Convergence parameters

22	<code>restart=yes</code>	Do not use previous DC solution as initial guess. Possible values are <code>no</code> or <code>yes</code> .
----	--------------------------	----------------------------------------------------------------------------------------------------------------

Spectre Circuit Simulator Reference

Analysis Statements

Annotation parameters

- 23 `annotate=sweep` Degree of annotation.
Possible values are `no`, `title`, `sweep`, `status`, or `steps`.
- 24 `stats=no` Analysis statistics.
Possible values are `no` or `yes`.
- 25 `title` Analysis title.

If the list of active ports is specified with the `ports` parameter, then the ports are numbered sequentially from one in the order given. Otherwise, all ports present in the circuit are active, and the port numbers used are those that were assigned on the port statements. If `donoise=yes` is specified, then the noise correlation matrix is computed. If in addition, the output is specified using `oprobe`, the amount that each noise source contributes to the output is computed. Finally, if an input is also specified (using `iprobe`), the two-port noise parameters are computed (`F`, `Fmin`, `NF`, `NFmin`, `Gopt`, `Bopt`, and `Rn`).

You can specify sweep limits by giving the end points or by providing the center value and the span of the sweep. Steps can be linear or logarithmic, and you can specify the number of steps or the size of each step. You can give a step size parameter (`step`, `lin`, `log`, `dec`) to determine whether the sweep is linear or logarithmic. If you do not give a step size parameter, the sweep is linear when the ratio of stop to start values is less than 10, and logarithmic when this ratio is 10 or greater. All frequencies are in Hertz.

The small-signal analysis begins by linearizing the circuit about an operating-point. By default this analysis computes the operating-point if it is not known, or recomputes it if any significant component or circuit parameter has changed. However, if a previous analysis computed an operating point, you can set `prevoppoint=yes` to avoid recomputing it. For example, if you use this option when the previous analysis was a transient analysis, the operating point is the state of the circuit on the final time point.

Parameter Index

In the following index, the number following each parameter name indicates where to find the description of that parameter.

<code>annotate</code>	23	<code>iprobe</code>	21	<code>ports</code>	15	<code>step</code>	6
<code>center</code>	4	<code>lin</code>	7	<code>prevoppoint</code>	1	<code>stop</code>	3
<code>dec</code>	8	<code>log</code>	9	<code>readns</code>	16	<code>title</code>	25
<code>dev</code>	11	<code>mod</code>	12	<code>restart</code>	22	<code>values</code>	10
<code>donoise</code>	19	<code>oppoint</code>	18	<code>span</code>	5		

Spectre Circuit Simulator Reference

Analysis Statements

file	17	oprobe	20	start	2
freq	14	param	13	stats	24

Stability Analysis (stb)

Description

The STB analysis linearizes the circuit about the DC operating point and computes the loop gain, gain and phase margins (if the sweep variable is frequency), for a feedback loop or a gain device.

Spectre can perform the analysis while sweeping a parameter. The parameter can be frequency, temperature, component instance parameter, component model parameter, or netlist parameter. If changing a parameter affects the DC operating point, the operating point is recomputed on each step. You can sweep the circuit temperature by giving the parameter name as `temp` with no `dev` or `mod` parameter. You can sweep a netlist parameter by giving the parameter name with no `dev`, or `mod` parameter. After the analysis has completed, the modified parameter returns to its original value.

Definition

Name `stb parameter=value ...`

Parameters

1	<code>prevoppoint=no</code>	Use operating point computed on the previous analysis. Possible values are <code>no</code> or <code>yes</code> .
---	-----------------------------	------------------------------------------------------------------------------------------------------------------

Sweep interval parameters

2	<code>start=0</code>	Start sweep limit.
3	<code>stop</code>	Stop sweep limit.
4	<code>center</code>	Center of sweep.
5	<code>span=0</code>	Sweep limit span.
6	<code>step</code>	Step size, linear sweep.

Spectre Circuit Simulator Reference

Analysis Statements

7 `lin=50` Number of steps, linear sweep.

8 `dec` Points per decade.

9 `log=50` Number of steps, log sweep.

10 `values=[...]` Array of sweep values.

Sweep variable parameters

11 `dev` Device instance whose parameter value is to be swept.

12 `mod` Model whose parameter value is to be swept.

13 `param` Name of parameter to sweep.

14 `freq (Hz)` Frequency when parameter other than frequency is being swept.

Probe parameters

15 `probe` Probe instance around which the loop gain is calculated.

State-file parameters

16 `readns` File that contains estimate of DC solution (nodeset).

Output parameters

17 `save` Signals to output.
Possible values are `all`, `lvl`, `allpub`, `lvlpub`, `selected`, or `none`.

18 `nestlvl` Levels of subcircuits to output.

19 `oppoint=no` Should operating point information be computed, and if so, where should it be sent.
Possible values are `no`, `screen`, `logfile`, or `rawfile`.

Spectre Circuit Simulator Reference

Analysis Statements

Convergence parameters

20 `restart=yes` Do not use previous DC solution as initial guess.
Possible values are `no` or `yes`.

Annotation parameters

21 `annotate=sweep` Degree of annotation.
Possible values are `no`, `title`, `sweep`, `status`, or `steps`.

22 `stats=no` Analysis statistics.
Possible values are `no` or `yes`.

23 `title` Analysis title.

You can specify sweep limits by giving the end points or by providing the center value and the span of the sweep. Steps can be linear or logarithmic, and you can specify the number of steps or the size of each step. You can give a step size parameter (`step`, `lin`, `log`, `dec`) to determine whether the sweep is linear or logarithmic. If you do not give a step size parameter, the sweep is linear when the ratio of stop to start values is less than 10, and logarithmic when this ratio is 10 or greater. All frequencies are in Hertz.

The small-signal analysis begins by linearizing the circuit about an operating-point. By default this analysis computes the operating-point if it is not known, or recomputes it if any significant component or circuit parameter has changed. However, if a previous analysis computed an operating point, you can set `prevoppoint=yes` to avoid recomputing it. For example, if you use this option when the previous analysis was a transient analysis, the operating point is the state of the circuit on the final time point.

Loop based and Device Based Algorithms

Two algorithms--the loop based and the device based, are available for small-signal stability analysis. Both algorithms are based on the calculation of Bodes return ratio. Loop gain waveform, gain margin, and phase margin are the analysis output.

The `probe` parameter must be specified to perform stability analysis. When it points to a current probe or voltage source instance, the loop based algorithm will be invoked; when it points to a supported active device instance, the device based algorithm will be invoked.

Spectre Circuit Simulator Reference

Analysis Statements

Loop Based Algorithm

The loop based algorithm calculates the true loop gain that consists of normal loop gain and reverse loop gain. The loop based algorithm requires the `probe` being placed on the feedback loop to identify and characterize the particular loop of interest. The introduction of the probe component should not change any of the circuit characteristics.

The loop based algorithm provides accurate stability information for single loop circuits, and multiloop circuits in which a `probe` component can be placed on a critical wire to break all loops. For a general multiloop circuit, such a critical wire may not be available. The loop based algorithm can only be performed on individual feedback loops to ensure they are stable. Although the stability of all feedback loops is only a necessary condition for the whole circuit to be stable, the multiloop circuit tends to be stable if all individual loops are associated with reasonable stability margins.

Device Based Algorithm

The device based algorithm calculates the loop gain around a particular active device. This algorithm is often applied to assess the stability of circuit design in which local feedback loops cannot be neglected; the loop based algorithm cannot be performed for these applications since the local feedback loops are inside the devices, they are not accessible from the schematic level or netlist level to insert the `probe` component.

With the `probe` parameter points to a particular active device, the dominant controlled source in the device will be nulled during the analysis. The dominant controlled source is defined as by nulling this source renders the active device to be passive. The device based algorithm produces accurate stability information for a circuit in which a critical active device can be identified such that nulling the dominant gain source of this device renders the whole network to be passive.

Stability Analysis of Differential Feedback Circuits

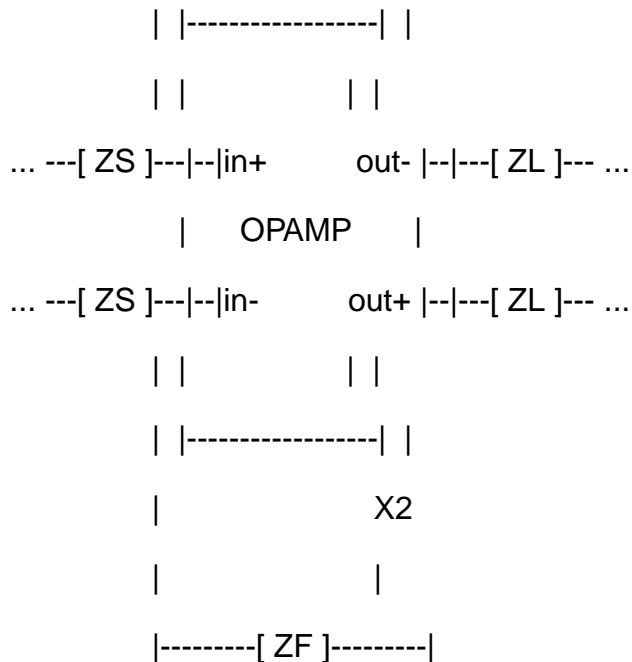
A balanced fully differential feedback circuit is illustrated below:

check???

```
|-----[ ZF ]-----|
|                       |
|                       X1
```

Spectre Circuit Simulator Reference

Analysis Statements



The feedback loops are broken at X1 and X2, with x1in and x2in being the input side nodes, x1out and x2out being the output side nodes. The following subcircuit connects these four nodes together:

```
subckt diffprobe xlin x2in xlout x2out
  ibranch inout xlout iprobe
  vinj inout xlin iprobe
  evinj x2in x2out xlin xlout vcvs gain=0
  fiinj 0 x2out pcccs probes=[ibranch vinj] coeffs=[0 1 1] gain=0
ends diffprobe
```

Let `diffprobe_inst` be the instance of subcircuit `diffprobe`, the following analysis measures the differential-mode loop gain:

```
DMalterv alter dev=diffprobe_inst.evinj param=gain value=-1
DMalteri alter dev=diffprobe_inst.fiinj param=gain value=-1
DMloopgain stb probe=diffprobe_inst.vinj
```

and the following analysis measures the common-mode loop gain:

```
CMalterv alter dev=diffprobe_inst.evinj param=gain value=1
CMalteri alter dev=diffprobe_inst.fiinj param=gain value=1
CMloopgain stb probe=diffprobe_inst.vinj
```


Spectre Circuit Simulator Reference

Analysis Statements

Parameter Index

In the following index, the number following each parameter name indicates where to find the description of that parameter.

annotate	21	log	9	probe	15	stats	22
center	4	mod	12	readns	16	step	6
dec	8	nestlvl	18	restart	20	stop	3
dev	11	oppoint	19	save	17	title	23
freq	14	param	13	span	5	values	10
lin	7	prevoppoint	1	start	2		

Sweep Analysis (sweep)

Description

The `sweep` analysis sweeps a parameter executing the list of analyses (or multiple analyses) for each value of the parameter. The swept parameter can be circuit temperature, a device instance parameter, a device model parameter, a netlist parameter, or a subcircuit parameter for a particular subcircuit instance.

A set of parameters can be swept simultaneously, using the `paramset` parameter. The other sweep interval or variable parameters cannot be specified with the `paramset` parameter. Do `spectre -h paramset` for information on defining a `paramset`.

Within a sweep statement, you can specify analyses statements. These statements should be bound within braces. The opening brace is required at the end of the line defining the sweep. Sweep statements can be nested.

You can sweep the circuit temperature by giving the parameter name as `param=temp` with no `dev`, `mod`, or `sub` parameter. You can sweep a top-level netlist parameter by giving the parameter name with no `dev`, `mod`, or `sub` parameter. You can sweep a subcircuit parameter for a particular subcircuit instance by specifying the subcircuit instance name with the `sub` parameter and the subcircuit parameter name with the `param` parameter. The same can be done using `dev` for the device instance name or `mod` for the device model name.

After the analysis has completed, the modified parameter returns to its original value.

Spectre Circuit Simulator Reference

Analysis Statements

Definition

Name sweep parameter=value ...

Parameters

Sweep interval parameters

1	<code>start=0</code>	Start sweep limit.
2	<code>stop</code>	Stop sweep limit.
3	<code>center</code>	Center of sweep.
4	<code>span=0</code>	Sweep limit span.
5	<code>step</code>	Step size, linear sweep.
6	<code>lin=50</code>	Number of steps, linear sweep.
7	<code>dec</code>	Points per decade.
8	<code>log=50</code>	Number of steps, log sweep.
9	<code>values=[...]</code>	Array of sweep values.

Sweep variable parameters

10	<code>dev</code>	Device instance whose parameter value is to be swept.
11	<code>sub</code>	Subcircuit instance whose parameter value is to be swept.
12	<code>mod</code>	Model whose parameter value is to be swept.
13	<code>param</code>	Name of parameter to sweep.
14	<code>paramset</code>	Name of parameter set to sweep.

Spectre Circuit Simulator Reference

Analysis Statements

Annotation parameters

- 15 `annotate=sweep` Degree of annotation.
Possible values are `no`, `title` or `sweep`.
- 16 `title` Analysis title.

You can specify sweep limits by giving the end points or by providing the center value and the span of the sweep. Steps can be linear or logarithmic, and you can specify the number of steps or the size of each step. You can give a step size parameter (`step`, `lin`, `log`, or `dec`) and determine whether the sweep is linear or logarithmic. If you do not give a step size parameter, the sweep is linear when the ratio of the stop-to-start values is less than 10 and logarithmic when this ratio is 10 or greater.

Example

```
swp sweep param=temp values=[-50 0 50 100 125] {  
    oppoint dc oppoint=logfile  
}
```

Parameter Index

In the following index, the number following each parameter name indicates where to find the description of that parameter.

<code>annotate</code> 15	<code>lin</code> 6	<code>paramset</code> 14	<code>stop</code> 2
<code>center</code> 3	<code>log</code> 8	<code>span</code> 4	<code>sub</code> 11
<code>dec</code> 7	<code>mod</code> 12	<code>start</code> 1	<code>title</code> 16
<code>dev</code> 10	<code>param</code> 13	<code>step</code> 5	<code>values</code> 9

Time-Domain Reflectometer Analysis (tdr)

Description

The time-domain reflectometer analysis linearizes the circuit about the DC operating point and computes the reflection coefficients versus time, looking from the active ports into the circuit.

Spectre Circuit Simulator Reference

Analysis Statements

Definition

Name `tdr parameter=value ...`

Parameters

1	<code>stop</code>	Stop time.
2	<code>settling=stop</code>	Time required for circuit to settle.
3	<code>start=-0.1 stop</code>	Time output waveforms begin.
4	<code>smoothing=2</code>	Window smoothing parameter (useful range is 0 to 15).
5	<code>vel=1</code>	Propagation velocity of medium normalized to c.
6	<code>points=64</code>	Number of time points.
7	<code>ports=[...]</code>	List of active ports. If not given, all ports are used.
8	<code>readns</code>	File that contains estimate of DC solution (nodeset).
9	<code>restart=yes</code>	Do not use previous DC solution as initial guess. Possible values are <code>no</code> or <code>yes</code> .
10	<code>annotate=sweep</code>	Degree of annotation. Possible values are <code>no</code> , <code>title</code> , <code>sweep</code> , <code>status</code> , or <code>steps</code> .
11	<code>title</code>	Analysis title.
12	<code>oppoint=no</code>	Should operating point information be computed, and if so, where should it be sent. Possible values are <code>no</code> , <code>screen</code> , <code>logfile</code> , or <code>rawfile</code> .
13	<code>prevoppoint=yes</code>	Use operating point computed on the previous analysis. Possible values are <code>no</code> or <code>yes</code> .

Such a small-signal analysis begins by linearizing the circuit about an operating point. By default, this analysis computes the operating point, if it is not yet known, or recomputes it, if any significant component or circuit parameter has changed. However, if a previous analysis computed an operating point, you can set `prevoppoint=yes` to avoid recomputing it. For

Spectre Circuit Simulator Reference

Analysis Statements

example, if you use this command when the previous analysis was a transient analysis, the operating point is the state of the circuit on the final time point.

Parameter Index

In the following index, the number following each parameter name indicates where to find the description of that parameter.

annotate 10	prevoppoint 13	smoothing 4	vel 5
oppoint 12	readns 8	start 3	
points 6	restart 9	stop 1	
ports 7	settling 2	title 11	

Transient Analysis (tran)

Description

This analysis computes the transient response of a circuit over the interval from `start` to `stop`. The initial condition is taken to be the DC steady-state solution if not otherwise given.

Definition

Name `tran parameter=value ...`

Parameters

Simulation interval parameters

- | | | |
|---|----------------------------------|--------------------------------------------------|
| 1 | <code>stop (s)</code> | Stop time. |
| 2 | <code>start=0 s</code> | Start time. |
| 3 | <code>outputstart=start s</code> | Output is saved only after this time is reached. |

Spectre Circuit Simulator Reference

Analysis Statements

- 4 `autostop=no` Enable early termination of the analysis, when all measurement expressions have been evaluated.
Possible values are `no` or `yes`.

Time-step parameters

- 5 `maxstep (s)` Maximum time step. Default derived from `errpreset`.
- 6 `step=0.001 (stop-start) s` Minimum time step used by the simulator solely to maintain the aesthetics of the computed waveforms.

Initial-condition parameters

- 7 `ic=all` What should be used to set initial condition.
Possible values are `dc`, `node`, `dev`, or `all`.
- 8 `skipdc=no` If yes, there will be no dc analysis for transient.
Possible values are `no`, `yes`, `waveless`, `rampup`, `autodc`, or `sigrampup`.
- 9 `readic` File that contains initial condition.

Convergence parameters

- 10 `readns` File that contains estimate of initial transient solution.
- 11 `cmin=0 F` Minimum capacitance from each node to ground.

State-file parameters

- 12 `write` File to which initial transient solution is to be written.
- 13 `writefinal` File to which final transient solution is to be written.
- 14 `ckptperiod` Checkpoint the analysis periodically using the specified period.

Spectre Circuit Simulator Reference

Analysis Statements

Integration method parameters

15 `method` Integration method. Default derived from `errpreset`. Possible values are `euler`, `trap`, `traponly`, `gear2`, `gear2only`, or `trapgear2`.

Accuracy parameters

16 `errpreset=moderate` Selects a reasonable collection of parameter settings. Possible values are `liberal`, `moderate` or `conservative`.

17 `relref` Reference used for the relative convergence criteria. Default derived from `errpreset`. Possible values are `pointlocal`, `alllocal`, `sigglobal`, or `allglobal`.

18 `lteratio` Ratio used to compute LTE tolerances from Newton tolerance. Default derived from `errpreset`.

Annotation parameters

19 `stats=no` Analysis statistics. Possible values are `no` or `yes`.

20 `annotate=sweep` Degree of annotation. Possible values are `no`, `title`, `sweep`, `status`, or `steps`.

21 `title` Analysis title.

Output parameters

22 `save` Signals to output. Possible values are `all`, `lvl`, `allpub`, `lvlpub`, `selected`, or `none`.

23 `nestlvl` Levels of subcircuits to output.

24 `oppoint=no` Should operating point information be computed for initial timestep, and if so, where should it be sent. Possible values are `no`, `screen`, `logfile`, or `rawfile`.

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- 25 `skipstart=starttime s` The time to start skipping output data.
- 26 `skipstop=stoptime s` The time to stop skipping output data.
- 27 `skipcount` Save only one of every skipcount points.
- 28 `strobeperiod (s)` The output strobe interval (in seconds of transient time).
- 29 `strobedelay=0 s` The delay (phase shift) between the skipstart time and the first strobe point.
- 30 `compression=no` Do data compression on output.
Possible values are `no` or `yes`.
- 31 `flushpoints` Flush outputs after number of calculated points.
- 32 `flushtime (s)` Flush outputs after real time has elapsed.
- 33 `flushofftime (s)` Time to stop flushing outputs.
- 34 `infoname` Name of info analysis to save operating point.
- 35 `infotimes=[...] s` Times when operating points should be saved.

Newton parameters

- 36 `maxiters=5` Maximum number of iterations per time step.
- 37 `restart=yes` Do not use previous DC solution as initial guess.
Possible values are `no` or `yes`.

Circuit age

- 38 `circuitage (Years)` Stress Time. Age of the circuit used to simulate hot-electron degradation of MOSFET and BSIM circuits.

You may specify the initial condition for the transient analysis by using the `ic` statement or the `ic` parameter on the capacitors and inductors. If you do not specify the initial condition, the DC solution is used as the initial condition. The `ic` parameter on the transient analysis

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controls the interaction of various methods of setting the initial conditions. The effects of individual settings are:

`ic=dc`: Any initial condition specifiers are ignored, and the DC solution is used.

`ic=node`: The `ic` statements are used, and the `ic` parameter on the capacitors and inductors are ignored.

`ic=dev`: The `ic` parameters on the capacitors and inductors are used, and the `ic` statements are ignored.

`ic=all`: Both the `ic` statements and the `ic` parameters are used, and the `ic` parameters override the `ic` statements.

If you specify an initial condition file with the `readic` parameter, initial conditions from the file are used, and any `ic` statements are ignored.

Once you specify the initial conditions, Spectre computes the actual initial state of the circuit by performing a DC analysis. During this analysis, Spectre forces the initial conditions on nodes by using a voltage source in series with a resistor whose resistance is `rforce` (see `options`).

With the `ic` statement it is possible to specify an inconsistent initial condition (one that cannot be sustained by the reactive elements). Examples of inconsistent initial conditions include setting the voltage on a node with no path of capacitors to ground or setting the current through a branch that is not an inductor. If you initialize Spectre inconsistently, its solution jumps; that is, it changes instantly at the beginning of the simulation interval. You should avoid such changes if possible because Spectre can have convergence problems while trying to make the jump.

You can skip the DC analysis entirely by using the parameter `skipdc`. If the DC analysis is skipped, the initial solution will be either trivial, or given in the file you specified by the `readic` parameter, or, if the `readic` parameter is not given, the values specified on the `ic` statements. Device based initial conditions are not used for `skipdc`. Nodes that you do not specify with the `ic` file or `ic` statements will start at zero. You should not use this parameter unless you are generating a `nodeset` file for circuits that have trouble in the DC solution; it usually takes longer to follow the initial transient spikes that occur when the DC analysis is skipped than it takes to find the real DC solution. The `skipdc` parameter might also cause convergence problems in the transient analysis.

The possible settings of parameter `skipdc` and their meanings are:

`skipdc=no`: Initial solution is calculated using the normal DC analysis (default).

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`skipdc=yes`: Initial solution is given in the file specified by the `readic` parameter or the values specified on the `ic` statements.

`skipdc=wireless`: Same initial solution as `skipdc=yes`. But, the waveform production in the time-varying independent sources is disabled during the transient analysis. The independent source values are fixed to their initial values (not their DC values).

`skipdc=ramup`: The independent source values start at 0 and ramp up to their initial values in the first 10% of the analysis interval. After that their values remain constant. Zero initial solution is used.

`skipdc=autodc`: Same as `skipdc=wireless` if a nonzero initial condition is specified. Otherwise, same as `skipdc=ramup`.

`skipdc=sigrampup`: The independent source values start at 0 and ramp up to their initial values in the first phase of the simulation. Unlike `skipdc=rampup`, the waveform production in the time-varying independent source is enable after the rampup phase. The rampup simulation is from `start` to `time=0` s, and the main simulation is from `time=0` s to `stop`. If the `start` parameter is not specified, the default start time is set to $-0.1 * stop$.

Nodesets help find the DC or initial transient solution. You can supply them in the circuit description file with `nodeset` statements, or in a separate file using the `readns` parameter. When nodesets are given, Spectre computes an initial guess of the solution by performing a DC analysis while forcing the specified values onto nodes by using a voltage source in series with a resistor whose resistance is `rforce`. Spectre then removes these voltage sources and resistors and computes the true solution from this initial guess.

Nodesets have two important uses. First, if a circuit has two or more solutions, nodesets can bias the simulator towards computing the desired one. Second, they are a convergence aid. By estimating the solution of the largest possible number of nodes, you might be able to eliminate a convergence problem or dramatically speed convergence.

When you simulate the same circuit many times, we suggest that you use both the `write` and `readns` parameters and give the same file name to both parameters. The DC analysis then converges quickly even if the circuit has changed somewhat since the last simulation, and the nodeset file is automatically updated.

Nodesets and initial conditions have similar implementation but produce different effects. Initial conditions actually define the solution, whereas nodesets only influence it. When you simulate a circuit with a transient analysis, Spectre forms and solves a set of differential equations. However, differential equations have an infinite number of solutions, and a complete set of initial conditions must be specified in order to identify the desired solution. Any initial conditions you do not specify are computed by the simulator to be consistent. The transient waveforms then start from initial conditions. Nodesets are usually used as a convergence aid and do not affect the final results. However, in a circuit with more than one

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solution, such as a latch, nodesets bias the simulator towards finding the solution closest to the nodeset values.

The `method` parameter specifies the integration method. The possible settings and their meanings are:

`method=euler:` Backward-Euler is used exclusively.

`method=traponly:` Trapezoidal rule is used almost exclusively.

`method=trap:` Backward-Euler and the trapezoidal rule are used.

`method=gear2only:` Gears second-order backward-difference method is used almost exclusively.

`method=gear2:` Backward-Euler and second-order Gear are used.

`method=trapgear2:` Allows all three integration methods to be used.

The trapezoidal rule is usually the most efficient when you want high accuracy. This method can exhibit point-to-point ringing, but you can control this by tightening the error tolerances. For this reason, though, if you choose very loose tolerances to get a quick answer, either backward-Euler or second-order Gear will probably give better results than the trapezoidal rule. Second-order Gear and backward-Euler can make systems appear more stable than they really are. This effect is less pronounced with second-order Gear or when you request high accuracy.

Several parameters determine the accuracy of the transient analysis. `reltol` and `abstol` control the accuracy of the discretized equation solution. These parameters determine how well charge is conserved and how accurately steady-state or equilibrium points are computed. You can set the integration error, or the errors in the computation of the circuit dynamics (such as time constants), relative to `reltol` and `abstol` by setting the `iteration` parameter.

The parameter `relref` determines how the relative error is treated. The `relref` options are:

`relref=pointlocal:` Compares the relative errors in quantities at each node to that node alone.

`relref=alllocal:` Compares the relative errors at each node to the largest values found for that node alone for all past time.

`relref=sigglobal:` Compares relative errors in each of the circuit signals to the maximum for all signals at any previous point in time.

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`relref=allglobal`: Same as `relref=sigglobal` except that it also compares the residues (KCL error) for each node to the maximum of that nodes past history.

The `errpreset` parameter lets you adjust the simulator parameters to fit your needs quickly. You can set `errpreset` to `conservative` if the circuit is very sensitive, or you can set it to `liberal` for a fast, but possibly inaccurate, simulation. The setting `errpreset=moderate` suits most needs.

The effect of `errpreset` on other parameters is shown in the following table. In this table, $T = \text{stop} - \text{start}$.
`check???`

errpreset	reltol	relref	method	maxstep	Iteratio
liberal	10	allglobal	gear2	Interval/10	3.5
moderate		sigglobal	traonly	Interval/50	3.5
conservative	0.1	alllocal	gear2only	Interval/100	10.0

The value of `reltol` is increased or decreased from its value in the options statement, but it is not allowed to be larger than 0.01. Spectre sets the value of `maxstep` so that it is no larger than the value given in the table. Except for `reltol` and `maxstep`, `errpreset` does not change the value of any parameters you have explicitly set. The actual values used for the transient analysis are given in the log file.

If the circuit you are simulating can have infinitely fast transitions (for example, a circuit that contains nodes with no capacitance), Spectre might have convergence problems. To avoid this, you must prevent the circuit from responding instantaneously. You can accomplish this by setting `cmin`, the minimum capacitance to ground at each node, to a physically reasonable nonzero value. This often significantly improves Spectre convergence.

Spectre provides two methods for reducing the number of output data points saved: `strobing`, based on the simulation time, and `skipping` time points, which saves only every Nth point.

The parameters `strobeperiod` and `strobedelay` control the strobing method. `strobeperiod` sets the interval between points that you want to save, and `strobedelay` sets the offset within the period relative to `skipstart`. The simulator forces a time step on each point to be saved, so the data is computed, not interpolated.

The skipping method is controlled by `skipcount`. If this is set to N, then only every Nth point is saved.

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The parameters `skipstart` and `skipstop` apply to both data reduction methods. Before `skipstart` and after `skipstop`, Spectre saves all computed data.

If you do not want any data saved before a given time, use `outputstart`. If you do not want any data saved after a given time, change the `stop` time.

Parameter Index

In the following index, the number following each parameter name indicates where to find the description of that parameter.

<code>annotate</code> 20	<code>ic</code> 7	<code>readic</code> 9	<code>stats</code> 19
<code>autostop</code> 4	<code>infoname</code> 34	<code>readns</code> 10	<code>step</code> 6
<code>circuitage</code> 38	<code>infotimes</code> 35	<code>relref</code> 17	<code>stop</code> 1
<code>ckptperiod</code> 14	<code>lteratio</code> 18	<code>restart</code> 37	<code>strobedelay</code> 29
<code>cmin</code> 11	<code>maxiters</code> 36	<code>save</code> 22	<code>strobeperiod</code> 28
<code>compression</code> 30	<code>maxstep</code> 5	<code>skipcount</code> 27	<code>title</code> 21
<code>errpreset</code> 16	<code>method</code> 15	<code>skipdc</code> 8	<code>write</code> 12
<code>flushofftime</code> 33	<code>nestlvl</code> 23	<code>skipstart</code> 25	<code>writefinal</code> 13
<code>flushpoints</code> 31	<code>oppoint</code> 24	<code>skipstop</code> 26	
<code>flushtime</code> 32	<code>outputstart</code> 3	<code>start</code> 2	

Transfer Function Analysis (xf)

Description

The transfer function analysis linearizes the circuit about the DC operating point and performs a small-signal analysis that calculates the transfer function from every independent source in the circuit to a designated output. The variable of interest at the output can be voltage or current.

You can specify the output with a pair of nodes or a probe component. Any component with two or more terminals can be a voltage probe. When there are more than two terminals, they are grouped in pairs; and you use the `portv` parameter to select the appropriate pair of terminals. Alternatively, you can simply specify a voltage to be the output by giving a pair of nodes on the `xf` analysis statement.

Any component that naturally computes current as an internal variable can be a current probe. If the probe component computes more than one current (as transmission lines, microstrip lines, and N-ports do), you use the `porti` parameter to select the appropriate

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current. It is an error to specify both `portv` and `porti`. If neither is specified, the probe component provides a reasonable default.

The `stimuli` parameter specifies what is used for the inputs for the transfer functions. There are two choices. `stimuli=sources` indicates that the sources present in the circuit should be used. The `xfmag` parameters provided by the sources may be used to adjust the computed gain to compensate for gains or losses in a test fixture. One can limit the number of sources in hierarchical netlists by using the `save` and `nestlvl` parameters.

The transfer functions computed versus output and source types are:

check??

Source	Output Type		Source
Type	voltage	current	Amplitude
-----+-----+-----			
vsource	$V(out)/V(src)$	$I(out)/V(src)$	$V(src)=xfmag$
isource	$V(out)/I(src)$	$I(out)/I(src)$	$I(src)=xfmag$
port	$2*V(out)/V(src)$	$2*I(out)/V(src)$	$V(src)=2*xfmag$

where `xfmag` defaults to 1 for each source type. For the `port`, $V(src)$ is the internal source voltage.

Specifying `stimuli=nodes_and_terminals` indicates that all possible transfer functions should be computed. This is useful when it is not known in advance which transfer functions are interesting. Transfer functions for nodes are computed assuming that a unit magnitude flow (current) source is connected from the node to ground. Transfer functions for terminals are computed assuming that a unit magnitude potential (voltage) source is connected in series with the terminal. By default, the transfer functions from a small set of terminals are computed. If transfer functions from specific terminals are desired, specify the terminals in the `save` statement. You must use the `:probe` modifier (for example, `Rout:1:probe`) or specify `useprobes=yes` on the options statement. If transfer functions from all terminals are desired, specify `currents=all` and `useprobes=yes` on the options statement.

Spectre can perform the analysis while sweeping a parameter. The parameter can be frequency, temperature, component instance parameter, component model parameter, or netlist parameter. If changing a parameter affects the DC operating point, the operating point is recomputed on each step. You can sweep the circuit temperature by giving the parameter name as `temp` with no `dev` or `mod` parameter. You can sweep a netlist parameter by giving

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the parameter name with no `dev`, or `mod` parameter. After the analysis has completed, the modified parameter returns to its original value.

Definition

Name `[p] [n] xf parameter=value ...`

The optional terminals (`p` and `n`) specify the output of the circuit. If you do not give the terminals, then you must specify the output with a probe component.

Parameters

1 `prevoppoint=no` Use operating point computed on the previous analysis.
Possible values are `no` or `yes`.

Sweep interval parameters

2 `start=0` Start sweep limit.
3 `stop` Stop sweep limit.
4 `center` Center of sweep.
5 `span=0` Sweep limit span.
6 `step` Step size, linear sweep.
7 `lin=50` Number of steps, linear sweep.
8 `dec` Points per decade.
9 `log=50` Number of steps, log sweep.
10 `values=[...]` Array of sweep values.

Sweep variable parameters

11 `dev` Device instance whose parameter value is to be swept.
12 `mod` Model whose parameter value is to be swept.

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- 13 `param` Name of parameter to sweep.
- 14 `freq (Hz)` Frequency when parameter other than frequency is being swept.

Probe parameters

- 15 `probe` Compute every transfer function to this probe component.

State-file parameters

- 16 `readns` File that contains estimate of DC solution (nodeset).

Output parameters

- 17 `stimuli=sources` Stimuli used for xf analysis.
Possible values are `sources` or `nodes_and_terminals`.
- 18 `save` Signals to output.
Possible values are `all`, `lvl`, `allpub`, `lvlpub`, `selected`, or `none`.
- 19 `nestlvl` Levels of subcircuits to output.
- 20 `oppoint=no` Should operating point information be computed, and if so, where should it be sent.
Possible values are `no`, `screen`, `logfile`, or `rawfile`.

Convergence parameters

- 21 `restart=yes` Do not use previous DC solution as initial guess.
Possible values are `no` or `yes`.

Annotation parameters

- 22 `annotate=sweep` Degree of annotation.
Possible values are `no`, `title`, `sweep`, `status`, or `steps`.
- 23 `stats=no` Analysis statistics.
Possible values are `no` or `yes`.
- 24 `title` Analysis title.

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You can specify sweep limits by giving the end points or by providing the center value and the span of the sweep. Steps can be linear or logarithmic, and you can specify the number of steps or the size of each step. You can give a step size parameter (`step`, `lin`, `log`, `dec`) to determine whether the sweep is linear or logarithmic. If you do not give a step size parameter, the sweep is linear when the ratio of stop to start values is less than 10, and logarithmic when this ratio is 10 or greater. All frequencies are in Hertz.

The small-signal analysis begins by linearizing the circuit about an operating-point. By default this analysis computes the operating-point if it is not known, or recomputes it if any significant component or circuit parameter has changed. However, if a previous analysis computed an operating point, you can set `prevoppoint=yes` to avoid recomputing it. For example, if you use this option when the previous analysis was a transient analysis, the operating point is the state of the circuit on the final time point.

Parameter Index

In the following index, the number following each parameter name indicates where to find the description of that parameter.

<code>annotate</code> 22	<code>log</code> 9	<code>probe</code> 15	<code>stats</code> 23
<code>center</code> 4	<code>mod</code> 12	<code>readns</code> 16	<code>step</code> 6
<code>dec</code> 8	<code>nestlvl</code> 19	<code>restart</code> 21	<code>stimuli</code> 17
<code>dev</code> 11	<code>oppoint</code> 20	<code>save</code> 18	<code>stop</code> 3
<code>freq</code> 14	<code>param</code> 13	<code>span</code> 5	<code>title</code> 24
<code>lin</code> 7	<code>prevoppoint</code> 1	<code>start</code> 2	<code>values</code> 10

Spectre Syntax

This chapter discusses the following topics:

- [Using Analogmodel for Model Passing \(analogmodel\)](#) on page 692
- [Checkpoint - Restart \(checkpoint\)](#) on page 693
- [Configuring CMI Shared Objects \(cmiconfig\)](#) on page 694
- [Built-in Mathematical and Physical Constants \(constants\)](#) on page 695
- [Convergence Difficulties \(convergence\)](#) on page 697
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- [SpectreHDL Usage and Language Summary \(spectrehdl\)](#) on page 720
- [SpectreRF Summary \(spectrerf\)](#) on page 728
- [Subcircuit Definitions \(subckt\)](#) on page 729
- [Verilog-A Usage and Language Summary \(veriloga\)](#) on page 733

Using Analogmodel for Model Passing (analogmodel)

`analogmodel` is a reserved word in Spectre that allows you to bind an instance to different masters based on the value of a special instance parameter called `modelname`. An instance of `analogmodel` must have a parameter named `modelname` whose string value will be the name of the master this instance will be bound to. The value of `modelname` can be passed into subcircuits.

The `analogmodel` keyword is used by the Cadence® Analog Design Environment to enable model name passing through the schematic hierarchy.

The syntax for an instance statement with a `modelname` parameter is given below:

```
name [(]node1 ... nodeN[)] analogmodel modelname=mastername [[param1=value1]
...[paramN=valueN]]
```

`name` Name of the statement or instance label.

`[(]node1...nodeN[)]` Names of the nodes that connect to the component.

`analogmodel` Special device name to indicate that this instance will have its master name specified by the value of the `modelname` parameter on the instance.

`modelname` Parameter to specify the master of this instance indicated by `mastername`. The `mastername` must either be a valid string identifier or a netlist parameter that must resolve to a valid master name – a primitive, model, subckt, or an AHDL module.

`param1` Parameter values for the component. Depending on the master type, these can either be device parameters or netlist parameters. This is an optional field.

An Example

```
//example spectre netlist to illustrate modelname parameter
simulator lang=spectre
parameters a="low" b="bottom" modelname=l0
ahdl_include "VerilogASTuff.va"
topInst1 (out in) top
topInst2 analogmodel modelname="VAMaster" //VAMaster is defined in
"VerilogASTuff.va"
    topInst3 (out in) analogmodel modelname="resistor" //topInst3 binds to a
primitive
```

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Spectre Syntax

```
topInst4 (out in) analogmodel modelname="myOwnRes" //topInst4 binds to a
modelcard "myOwnRes" defined below
topinst5 (out in) weiredRes modelname=modelname //modelname is just another
netlist param
model myOwnRes resistor r=100
subckt (out in) top
    parameters a="mid"
    x1 (out in) analogmodel modelname=a //topInst1.x1 binds to "mid"
ends top
subckt (out in) mid
    parameters c="low"
    x1 (out in) analogmodel modelname=b //topInst1.x1.x1 binds to "bottom"
    x2 (out in) analogmodel modelname=c //topInst1.x1.x1.x2 binds to "low"
ends mid
subckt (out in) low
    x1 (out in) analogmodel modelname="bottom" //topInst1.x1.x1.x2.x1 binds to
"bottom"
ends low
subckt (out in) bottom
    x1 (out in) analogmodel modelname="resistor" //x1 binds to primitive
"resistor"
ends bottom
dcl dc
```

Checkpoint - Restart (checkpoint)

Description

Spectre has the ability to save checkpoint files while the analyses are running, and to restart an analysis from its checkpoint file. Checkpoint files can be generated in several ways:

- 1) Periodically based on real time (wall clock time).
- 2) Asynchronous UNIX signals.
- 3) By other methods unique to the analyses.

To generate checkpoint files periodically based on real time, set the Spectre option `ckptclock` to the time interval in seconds that you want checkpoints. This option is turned on by default with a value of 1800 seconds (30 minutes). Spectre will delete the checkpoint file if the simulation completes normally. If the simulation terminates abnormally, the checkpoint file will not be deleted.

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Spectre Syntax

If Spectre receives the UNIX signal USR2, then Spectre will immediately write a checkpoint file. If Spectre receives interrupt signals like QUIT, TERM, INT, or HUP, Spectre will attempt to write a checkpoint file and then exit. After other fatal signals, it may not be possible for Spectre to write a checkpoint file.

The name of the checkpoint file is a combination of the circuit name, the analysis name, and the extension `.ckpt`. For example, if the circuit is named `test1` and the transient analysis is named `timeSweep`, then the checkpoint file will be named `test1.timeSweep.tran.ckpt`.

Spectre keeps only the latest checkpoint file. When a new checkpoint is created, it creates the file under a temporary name. After the file has been successfully written, it deletes the previous checkpoint file and renames the new file.

Currently only the transient analyses supports checkpoint and restart.

Checkpoint for Transient Analyses

The transient analysis can generate checkpoint files by using the above methods, or by generating a checkpoint file periodically based on the transient simulation time. This is accessed by a transient analysis parameter called `ckptperiod`, which is turned off by default.

Restart

To restart an analysis from a checkpoint file, use the `+recover` option on the Spectre command line. Spectre will look through the requested analyses to see if a checkpoint file exists for any of them. If a checkpoint file for a given analysis does exist, Spectre will skip over any analyses previous to that one, and start the analysis using the information from the file.

Configuring CMI Shared Objects (cmiconfig)

Description

Spectre supports the ability to install devices dynamically from shared objects at run time. CMI Configuration files are used to determine and locate the set of shared objects to be installed. Spectre first reads the default CMI configuration file which specifies the default shared objects provided by Cadence. The configuration file specified by the value of the `CMI_CONFIG` environment variable is then read. The third configuration file that Spectre reads is `~/cmiconfig`. Finally, the configuration file specified in Spectre's `-cmiconfig` command

line argument is read. Each CMI configuration file modifies the existing configuration established by the configuration files read before.

The following commands can be used in a CMI configuration file.

setpath Specifies and resets the search path.

```
setpath <path> or setpath ( <path1> <path2> ...  
                             <pathN> )
```

prepend Adds a path before the current search path.

```
prepend <path> or prepend ( <path1> <path2> ...  
                             <pathN> )
```

append Adds a path after the current search path.

```
append <path> or append ( <path1> <path2> ...  
                             <pathN> )
```

load Add a shared object to the list of shared objects to load.

```
loads [path/]<shared_object_name>
```

unload Removes a shared object to the list of shared objects to load.

```
unload <shared_object_name>
```

For example, given the following CMI configuration file

```
append /hm/spectre_dev/tools.sun4v/spectrecmi/lib/cmi/1.0  
load libbjtx+tfet.so  
load libmosx.so
```

The shared objects `libbjtx+tfet.so` and `libmosx.so` are loaded from `/hm/spectre_dev/tools.sun4v/spectrecmi/lib/cmi/1.0` in addition to the default shared objects provided by Cadence.

Built-in Mathematical and Physical Constants (constants)

Description

Spectre supports the following list of built-in mathematical and physical constants:

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Spectre Syntax

M_ is a mathematical constant

M_E	2.7182818284590452354	1 / pi
M_LOG2E	1.4426950408889634074	log2(e)
M_LOG10E	0.43429448190325182765	log10(e)
M_LN2	0.69314718055994530942	ln(2)
M_LN10	2.30258509299404568402	ln(10)
M_PI	3.14159265358979323846	pi
M_TWO_PI	6.28318530717958647652	2 * pi
M_PI_2	1.57079632679489661923	pi/2
M_PI_4	0.78539816339744830962	pi/4
M_1_PI	0.31830988618379067154	1/pi
M_2_PI	0.63661977236758134308	2/pi
M_2_SQRTPI	1.12837916709551257390	2/sqrt(pi)
M_SQRT2	1.41421356237309504880	sqrt(2)
M_SQRT1_2	0.70710678118654752440	sqrt(1/2)
M_DEGPERRAD	57.2957795130823208772	number of degrees per radian

P_ is a physical constant

P_Q	1.6021918e-19	charge of electron in coulombs
P_C	2.997924562e8	speed of light in vacuum in meters/sec
P_K	1.3806226e-23	Boltzman's constant in joules/kelvin
P_H	6.6260755e-34	Planck's constant in joules*sec
P_EPS0	8.85418792394420013968e-12	permittivity of vacuum in farads/meter
P_U0	(4.0e-7 * M_PI)	permeability of vacuum in henrys/meter
P_CELS IUS0	273.15	zero celsius in kelvin

These constants can be used in expressions, or anywhere that a numeric value of expression is expected.

Convergence Difficulties (convergence)

Description

If you are having convergence difficulties, try the following suggestions:

1. Carefully evaluate and resolve any notice, warning or error messages.
2. Assure topology checker is used (set `topcheck=full` on options statement) and heed any warnings it gives.
3. Perform sanity checking on the parameter values using the parameter range checker (use `+param param-limits-file` as a command line argument) and heed any warnings. Print the minimum and maximum parameter value using the `info` analysis. Assure that the bounds given for instance, model, output, temperature-dependent, and operating-point (if possible) parameters are reasonable.
4. Small floating resistors connected to high impedance nodes can cause convergence difficulties. Avoid very small floating resistors, particularly small parasitic resistors in semiconductors. Use voltage sources or iprobes to measure current rather than small resistors.
5. Use realistic device models. Check all component parameters, particularly nonlinear device model parameters, to assure that they are reasonable.
6. Increase the value of `gmin` (on options statement).
7. Loosen tolerances, particularly absolute tolerances like `iabstol` (on options statement). If tolerances are set too tight, they might preclude convergence.
8. Try to simplify the nonlinear component models in order to avoid regions in the model that may contribute to convergence problems.

DC Convergence Suggestions

Once you have a solution, write it to a nodeset file using the `write` parameter, and read it back in on subsequent simulations using the `readns` parameter.

1. If you have an estimate of what the solution should be, use nodeset statements or a nodeset file and set as many nodes as possible.

Spectre Circuit Simulator Reference

Spectre Syntax

2. If convergence difficulties occur when using nodesets or initial conditions, try increasing `rforce` (on options statement).
3. If this is not the first analysis, perhaps the solution from the previous analysis is far from the solution for this analysis. If so, set `restart=yes`.
4. If simulating a bipolar analog circuit, assure the region parameter on all transistors and diodes is set correctly.
5. If analysis fails at an extreme temperature, but succeeds at room temperature, then try adding a DC analysis that sweeps temperature. Start at room temperature, sweep to the extreme temperature, and write the final solution to a nodeset file.
6. Use numeric pivoting in the sparse matrix factorization by setting `pivotdc=yes` (on options statement). Sometimes it is also necessary to increase the pivot threshold to somewhere in the range of 0.1 to 0.5 using `pivrel` (on options statement).
7. Divide the circuit into smaller pieces and simulate them individually, but be careful to assure that the results will be close to what they would be if the rest of the circuit was present. Use the results to generate nodesets for the whole circuit.
8. If all else fails, replace the DC analysis with a transient analysis and modify all the independent sources to start at zero and ramp to their DC values. Run the transient analysis well beyond the time when all the sources have reached their final value (remember that transient analysis is very cheap when all of the signals in the circuit are not changing) and write the final point to a nodeset file. To make the transient analysis more efficient set the integration method to backward Euler (`method=euler`) and loosen the local truncation error criteria by increasing `lteratio`, say to 50. Occasionally, this approach will fail or be very slow because the circuit contains an oscillator. Often times the oscillation can be eliminated for the sake of finding the dc solution by setting the minimum capacitance from each node to ground (`cmin`) to a large value.

Transient Convergence Suggestions

1. Assure that a complete set of parasitic capacitors is used on nonlinear devices to avoid jumps in the solution waveforms. On MOS models, specify nonzero source and drain areas.
2. Use the `cmin` parameter to install a small capacitor from every node in the circuit to ground. This usually eliminates any jumps in the solution.

Export a Measurement to Be Evaluated (export)

The export feature is not supported. It is designated for internal use only.

Expressions (expressions)

Description

An expression is a construct that combines operands with operators to produce a result that is a function of the values of the operands and the semantic meaning of the operators. Any legal operand is also an expression in itself. Legal operands include numeric constants and references to top-level netlist parameters or subcircuit parameters. Calls to algebraic and trigonometric functions are also supported. The complete lists of operators, algebraic, and trigonometric functions are given after some examples.

Examples

```
simulator lang=spectre
parameters p1=1 p2=2           // declare some top-level parameters
r1 (1 0) resistor r=p1         // the simplest type of expression
r2 (1 0) resistor r=p1+p2      // a binary (+) expression
r3 (1 0) resistor r=5+6/2      // expression of constants, = 8
x1 s1 p4=8                     // instantiate a subcircuit, defined in the following lines
subckt s1
parameters p1=4 p3=5 p4=6      // subcircuit parameters
    r1 (1 0) resistor r=p1      // another simple expression
    r2 (1 0) resistor r=p2*p2   // a binary multiply expression
    r3 (1 0) resistor r=(p1+p2)/p3 // a more complex expression
    r4 (1 0) resistor r=sqrt(p1+p2) // an algebraic function call
    r5 (1 0) resistor r=3+atan(p1/p2) // a trigonometric function call
    r6 (1 0) RESMOD r=(p1 ? p4+1 : p3) // the ternary operator
ends
// a model card, containing expressions
model RESMOD resistor tc1=p1+p2 tc2=sqrt(p1*p2)
// some expressions used with analysis parameters
time_sweep tran start=0 stop=(p1+p2)*50e-6 // use 5*50e-6 = 150 us
// a vector of expressions (see notes on vectors below)
dc_sweep dc param=p1 values=[0.5 1 +p2 (sqrt(p2*p2))] // sweep p1
```

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Spectre Syntax

Where Expressions Can Be Used

The Spectre native netlist language allows expressions to be used where numeric values are expected on the right-hand side of an "=" sign, or within a vector, where the vector itself is on the right-hand side of an "=" sign. Expressions can be used when specifying device or analysis instance parameter values (for example specifying the resistance of a resistor or the stop time of a transient analysis, as outlined in the preceding example), when specifying model parameter values in model cards (for example specifying "bf=p1*0.8" for a bipolar model parameter, bf), or when specifying initial conditions and nodesets for individual circuit nodes.

Operators

The following operators are supported, listed in order of decreasing precedence. Parentheses can be used to change the order of evaluation. For a binary expression like "a+b", "a" is the first operand and "b" is the second operand. All operators are left associative, with the exceptions of the "to the power of" operator (**) and the ternary operator (? :), which are right associative. For logical operands, any nonzero value is considered true. The relational and equality operators return a value of 1 to indicate true or 0 to indicate false. There is no short circuiting of logical expressions involving && and ||.

Operator	Symbol(s)	Value
Unary +, Unary -	+, -	Value of operand, negative of operand.
To the power of	**	First operand to raised to power of second operand.
Multiply, Divide	*, /	Product, Quotient of operands.
Binary Plus/Minus	+, -	Sum, Difference of operands.
Shift	<<, >>	First operand shifted left (, right) by number of bits specified by second operand.
Relational	<, <=, >, >=	greater than, greater than or equal.
Equality	==, !=	True if operands are equal, not equal.
Bitwise AND	&	Bitwise AND (of integer operands).
Bitwise Exclusive NOR	~^ (or ^~)	Bitwise Exclusive NOR (of integer operands).
Bitwise OR		Bitwise OR (of integer operands).
Logical AND	&&	True only if both operands true.

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Spectre Syntax

Operator	Symbol(s)	Value
Logical OR		True if either operand is true.
Ternary Operator	(<i>cond</i>) ? <i>x</i> : <i>y</i>	Returns <i>x</i> if <i>cond</i> is true, <i>y</i> if not where <i>x</i> and <i>y</i> are expressions.

Algebraic and Trigonometric Functions:

The trigonometric and hyperbolic functions expect their operands to be specified in radians. The atan2() and hypot() functions are useful for converting from Cartesian to polar form.

Function	Description	Domain
log(<i>x</i>)	Natural logarithm	$x > 0$
log10(<i>x</i>)	Decimal logarithm	$x > 0$
exp(<i>x</i>)	Exponential	$x < 80$
sqrt(<i>x</i>)	Square Root	$x > 0$
min(<i>x</i> , <i>y</i>)	Minimum value	All <i>x</i> , all <i>y</i>
max(<i>x</i> , <i>y</i>)	Maximum value	All <i>x</i> , all <i>y</i>
abs(<i>x</i>)	Absolute value	All <i>x</i>
pow(<i>x</i> , <i>y</i>)	<i>x</i> to the power of <i>y</i>	All <i>x</i> , all <i>y</i>
int(<i>x</i>)	integer value of <i>x</i>	All <i>x</i>
floor(<i>x</i>)	largest integer $\leq x$	All <i>x</i>
ceil(<i>x</i>)	smallest integer $\geq x$	
fmod(<i>x</i> , <i>y</i>)	floating-point modulus	All <i>x</i> , all <i>y</i> , except <i>y</i> =0
sin(<i>x</i>)	Sine	All <i>x</i>
cos(<i>x</i>)	Cosine	All <i>x</i>
tan(<i>x</i>)	Tangent	All <i>x</i> , except $x = n \cdot (\pi/2)$, where <i>n</i> odd
asin(<i>x</i>)	Arc-sine	$-1 \leq x \leq 1$
acos(<i>x</i>)	Arc-cosine	$-1 \leq x \leq 1$
atan(<i>x</i>)	Arc-tangent	All <i>x</i>
atan2(<i>x</i> , <i>y</i>)	Arc-tangent of <i>x</i> / <i>y</i>	All <i>x</i> , all <i>y</i>

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Spectre Syntax

Function	Description	Domain
<code>hypot(x,y)</code>	<code>sqrt(x*x + y*y)</code>	All x , all y
<code>sinh(x)</code>	Hyperbolic sine	All x
<code>cosh(x)</code>	Hyperbolic cosine	All x
<code>tanh(x)</code>	Hyperbolic tangent	All x
<code>asinh(x)</code>	Arc-hyperbolic sine	All x
<code>acosh(x)</code>	Arc-hyperbolic cosine	x
<code>atanh(x)</code>	Arc-hyperbolic tangent	$-1 \leq x \leq 1$

User-defined functions are also supported. See `spectre -h functions` for a description of user-defined functions.

A large number of built-in mathematical and physical constants are available for use in expressions. See `spectre -h constants` for the list of these constants

Using Expressions in Vectors

Expressions can be used as vector elements, as in the following example:

```
dc_sweep dc param=p1 values=[0.5 1 +p2 (sqrt(p2*p2)) ] // sweep p1
```

Note that when expressions are used within vectors, anything other than constants, parameters, or unary expressions (unary +, unary -) must be surrounded by parentheses. Vector elements should be space separated for clarity, though this is not mandatory. The preceding `dc_sweep` example shows a vector of four elements, namely 0.5, 1, +p2, and `sqrt(p2*p2)`. Note that the square root expression is surrounded by parentheses.

User Defined Functions (functions)

Description

Spectre's user-defined function capability allows you to build upon the provided set of built-in mathematical and trigonometric functions. You can write your own functions, and call these functions from within any expression. The syntax for calling a user-defined function is the same as the syntax for calling a built-in algebraic or trigonometric function. Note that user-defined functions must be defined before they are referenced (called). Arguments to user-defined functions will be taken as real values, and the functions will return real values. A user-

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Spectre Syntax

defined function may contain only a single statement in braces and this statement must return an expression (which will typically be an expression involving the function arguments). The return expression may reference the built in parameters "temp" and "tnom". User-defined functions must be declared at the top level only, and must not be declared within subcircuits. User-defined functions may be called from anywhere that an expressions can be currently used in Spectre. User-defined functions may call other functions (both user-defined and built-in), however any user-defined function will need to be declared before it can be called. User-defined functions can override built-in mathematical and trigonometric functions.

Note: Only real values for arguments and return values are supported in this release.

See `spectre -h expressions` for a list of built-in algebraic and trigonometric functions.

Definition

```
real myfunc( [real arg1, ...real argn] ) {
```

Examples

```
real myfunc( real a, real b ) {  
    return a+b*2+sqrt(a*sin(b));  
}
```

An example of a function calling a previously defined function is given below.

```
real yourfunc( real a, real b ) {  
    return a+b*myfunc(a,b);    // call "myfunc"  
}
```

The final example shows how a user-defined function may be called from an expression in the Spectre netlist.

```
r1 (1 0) resistor r=myfunc(2.0, 4.5)
```

Global Nodes (global)

Description

The global statement allows a set of nodes to be designated as common to the main circuit and all subcircuits. Thus, components inside subcircuits can be attached to global nodes even though the subcircuits terminals are not attached to these nodes.

Any number of global nodes may be specified using the global statement. To do this, follow the keyword `global` with a list of the node names that you wish to declare as global. The first

node name that appears in this list is taken to be the name of the ground node. Ground is also known as the datum or reference node. If a global statement is not used, 0 is taken to be the name of the ground node.

At most one global statement is allowed and, if present, it must be the first statement in the file (however, you can have `simulator lang=spectre` statement, before the global statement so that you can use mixed case names for the node names). Ground is always treated as global even if a global statement is not used.

Definition

```
global <ground> <node> ...
```

Initial Conditions (ic)

Description

The `ic` statement is used to provide initial conditions for nodes in the transient analysis. It can occur multiple times in the input and the information provided in all the occurrences is collected. Initial conditions will only be accepted for inductor currents and node voltages where the nodes have a path of capacitors to ground. For more information, read the description of transient analysis. It should be noted that specifying `cmin` for a transient analysis, will not satisfy the condition that a node has a capacitive path to ground.

Definition

```
ic <node=value> ...
```

This statement takes a list of signals as an argument. The concept of nodes has been generalized to signals where a signal is a value associated with a topological node of the circuit or some other unknown that is solved by the simulator, such as the current through an inductor or the voltage of the internal node in a diode. Topological nodes can be either at the top-level or in a subcircuit.

For example,

```
ic 7=0 out=1 OpAmp1.comp=5 L1:l=1.0u
```

where `7=0` implies that node 7 should start at 0V, node `out` should start at 1V, node `comp` in subcircuit `OpAmp1` should start at 5V, and the current through the first terminal of `L1` should start at 1uA.

The Structural if-statement (if)

Description

The structural if-statement can be used to conditionally instantiate other instance statements.

Definition

```
if <condition> <statement1> [ else <statement2> ]
```

The condition is a boolean expression based on the comparisons of various arithmetic expressions which are evaluated during circuit hierarchy flattening. The statement1 and statement2 fields can be ordinary instance statements, if-statements, or a list of these within braces ({}). (Note that ordinary instance statements need a newline to terminate them.) The else part is optional. When if-statements are nested without braces, an else matches the closest previous unmatched if at the same level.

It is possible to have duplicate instance names within the if statement under strict topological conditions: These are:

- references to instance with duplicate names is only possible within a structural if statement which has both an "if" part and an "else" part.
- both the "if" part and the "else" part must be either a simple one-statement block, or another structural if statement to which these same rules apply.
- The duplicate instances must have the same number of terminals and be bound to the same list of nodes.
- The duplicate instances must refer to the same primitive or model.
- Where duplicate instances refer to a model, the underlying primitive must be the same.

This feature allows automatic model selection based on any netlist or subcircuit parameter. As an example, consider using Spectre's inline subcircuits and structural if statement to implement automatic model selection based on bipolar device area. Here, the duplicate instances are the inline components.

```
model npn_default bjt is=3.2e-16 va=59.8
model npn10x10 bjt is=3.5e-16 va=61.5
model npn20x20 bjt is=3.77e-16 va=60.5
// npn_mod choses scaled models binned on area!
// if ( area < 100e-12 ) use model npn10x10
// else if ( area < 400e-12 ) use model npn20x20
// else use model npn_default
```

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Spectre Syntax

```
inline subckt npn_mod (c b e s)
  parameters area=5e-12
  if ( area < 100e-12 ) {
    npn_mod (c b e s) npn10x10 // 10u * 10u, inline device
  } else if ( area < 400e-12 ) {
    npn_mod (c b e s) npn20x20 // 20u * 20u, inline device
  } else {
    npn_mod (c b e s) npn_default // 5u * 5u, inline device
  }
ends npn_mod
q1 (1 2 0 0) npn_mod area=350e-12 // gets 20x20 model
q2 (1 3 0 0) npn_mod area=25e-12 // gets 10x10 model
q3 (1 3 0 0) npn_mod area=1000e-12 // gets default model
```

Include File (include)

Description

File inclusion allows the circuit description to be spread over several files. The `include` statement itself is replaced by the contents of the file named. An included file may also contain include statements. If the name given is not an absolute path specification, then it is taken relative to the directory of the file currently being read.

In order to read existing SPICE library and model files, Spectre automatically switches to SPICE input mode when it opens an include file. Thus, all files that use the Spectre native language must begin with a `simulator lang=spectre` statement. The one exception is files that end with a ".scs" file extension which are treated specially and are read in Spectre input mode. This language mode treatment applies to files included by both Spectre's `include` statement, and CPP's `#include` statement.

After reading the include file, Spectre restores the language processing mode to what it was before the file was included, and continues reading the original file starting at the line after the include statement. Lines cannot be continued across file boundaries.

The CPP `#include` statement differs from Spectre's `include` statement in that CPP macro processing will not be performed on files included by Spectre, but will be performed on files included by CPP. If your netlist contains a `#include` statement, you must run CPP to perform this inclusion, otherwise an error will occur.

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Spectre Syntax

If the file to be included cannot be found in the same directory as the including file, both Spectre's `include` and CPP's `#include` will search for the file to be included along the search path specified by the `-I` command line arguments.

Spectre's `include` statement allows you to embed special characters in the name of the file to be included. Spectre's `include` statement will automatically expand the `"~"` character to the users home directory, and will expand environment variables and `%` codes, such as

```
include "~/models/${SIMULATOR}_pd/npn.scs"
```

which will look in the directory given by the environment variable `SIMULATOR` followed by `_pd`, which is under the `models` directory, in the users home directory. Note: These special character features are not available using CPP's `#include` statement.

Definition

```
include "filename"
```

Spectre Netlist Keywords (keywords)

Description

The following lists the reserved Spectre keywords, including netlist keywords, built-in algebraic and trigonometric functions, and built-in mathematical and physical constants. When creating a netlist, you should avoid using any of the keywords from this list in any context other than that in which it was intended. Creating node names, parameter names, instance names, or model names from any of these keywords will result in an error.

Keyword	Keyword Type
M_1_PI	Mathematical Constant
M_2_PI	Mathematical Constant
M_2_SQRTPI	Mathematical Constant
M_DEGPERRAD	Mathematical Constant
M_E	Mathematical Constant
M_LN10	Mathematical Constant
M_LN2	Mathematical Constant

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Spectre Syntax

Keyword	Keyword Type
M_LOG10E	Mathematical Constant
M_LOG2E	Mathematical Constant
M_PI	Mathematical Constant
M_PI_2	Mathematical Constant
M_PI_4	Mathematical Constant
M_SQRT1_2	Mathematical Constant
M_SQRT2	Mathematical Constant
M_TWO_PI	Mathematical Constant
P_C	Mathematical Constant
P_CELSIUS0	Mathematical Constant
P_EPS0	Mathematical Constant
P_H	Mathematical Constant
P_K	Mathematical Constant
P_Q	Mathematical Constant
P_U0	Mathematical Constant
abs	Algebraic Function
acos	Trigonometric Function
acosh	Trigonometric Function
altergroup	Netlist Keyword
asin	Trigonometric Function
asinh	Trigonometric Function
atan	Trigonometric Function
atan2	Trigonometric Function
atanh	Trigonometric Function
ceil	Algebraic Function
correlate	Netlist Keyword
cos	Trigonometric Function

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Spectre Syntax

Keyword	Keyword Type
cosh	Trigonometric Function
else	Netlist Keyword
end	Netlist Keyword
ends	Netlist Keyword
exp	Algebraic Function
export	Netlist Keyword
floor	Algebraic Function
fmod	Algebraic Function
for	Netlist Keyword
function	Netlist Keyword
global	Netlist Keyword
hypot	Algebraic Function
ic	Netlist Keyword
if	Netlist Keyword
inline	Netlist Keyword
int	Algebraic Function
library	Netlist Keyword
local	Netlist Keyword
log	Algebraic Function
log10	Algebraic Function
march	Netlist Keyword
max	Algebraic Function
min	Algebraic Function
model	Netlist Keyword
nodeset	Netlist Keyword
parameters	Netlist Keyword
paramset	Netlist Keyword

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Spectre Syntax

Keyword	Keyword Type
plot	Netlist Keyword
pow	Algebraic Function
print	Netlist Keyword
pwr	Netlist Keyword
real	Netlist Keyword
return	Netlist Keyword
save	Netlist Keyword
sens	Netlist Keyword
sin	Trigonometric Function
sinh	Trigonometric Function
sqrt	Algebraic Function
statistics	Netlist Keyword
subckt	Netlist Keyword
tan	Trigonometric Function
tanh	Trigonometric Function
to	Netlist Keyword
truncate	Netlist Keyword
vary	Netlist Keyword

Library - Sectional Include (library)

Description

Library inclusion allows the circuit description to be spread over several files. The library statement itself is replaced by the contents of the section of the library file. A library section may also contain library reference statements. If the file name given is not an absolute path specification, then it is taken relative to the directory of the file currently being read.

There are two kinds of library statements. One that references a library section, and one that defines a library section. The definition of a library section is prohibited in the netlist.

Spectre Circuit Simulator Reference

Spectre Syntax

In order to read existing SPICE library and model files, Spectre automatically switches to SPICE input mode when it opens a library file. Thus, all files that use the Spectre native language must contain a `simulator lang=spectre` statement within each section of the library or the file can have a `scs` filename extension. After reading the library section, Spectre restores the language processing mode and continues reading the original file starting at the line after the library statement. Lines cannot be continued across file boundaries.

Spectre allows only one library per file, but a library may contain multiple sections (typically one section per process corner for example.)

Definition

Inside netlist (reference library section)

Sample Library

```
library corner_lib
section tt
    model nch bsim3v3 type=n mobmod=1 capmod=2 version=3.1
    + xj=1.7e-7 vsat=7.99e4 at=3.6e4 a0=0.799 ags=0.4
    + a1=0 a2=1 keta=-0.05 nch=2.8e17 ngate=1.31e20 kl=0.74
    model pch bsim3v3 type=p mobmod=1 capmod=2 version=3.1
    + xj=1.7e-7 vsat=1.38e5 at=1e5 a0=1.3 ags=0.3
    + a1=1.1e-4 a2=1 keta=0 nch=4.1e17 ngate=7.6e19 kl=0.88
    model knpn bjt is=10e-13 bf=170 va=58.7 ik=5.63e-3 rb=rbn rbm=86
    + re=3.2 cje=0.25e-12 pe=0.76 me=0.34 tf=249e-12 cjc=0.34e-12 pc=0.55
    + mc=0.35 ccs=2.4e-12 ms=0.35 ps=0.53 rc=169
    model kpnp bjt type=pnp is=10e-13 bf=60 va=43.1 ik=0.206e-3 rb=rbp rbm=64.3
    + re=33.8 cje=0.16e-12 pe=0.5 me=0.26 tf=36e-9 cjc=0.72e-12 pc=0.58
    + mc=0.34 ccs=2.5e-12 ps=0.53 ms=0.35 rc=276
endsection
section ss
    model nch bsim3v3 type=n mobmod=1 capmod=2 version=3.1
    + xj=1.7e-7 vsat=7.99e4 at=3.6e4 a0=0.799 ags=0.4
    + a1=0 a2=1 keta=-0.05 nch=2.8e17 ngate=1.31e20 kl=0.74
    model pch bsim3v3 type=p mobmod=1 capmod=2 version=3.1
    + xj=1.7e-7 vsat=1.38e5 at=1e5 a0=1.3 ags=0.3
    + a1=1.1e-4 a2=1 keta=0 nch=4.1e17 ngate=7.6e19 kl=0.88
    model knpn bjt is=10e-13 bf=70 va=58.7 ik=5.63e-3 rb=rbn rbm=86
    + re=3.2 cje=0.25e-12 pe=0.76 me=0.34 tf=249e-12 cjc=0.34e-12 pc=0.55
    + mc=0.35 ccs=2.4e-12 ms=0.35 ps=0.53 rc=169
    model kpnp bjt type=pnp is=10e-13 bf=30 va=43.1 ik=0.206e-3 rb=rbp rbm=64.3
```

Spectre Circuit Simulator Reference

Spectre Syntax

```
+ re=33.8 cje=0.16e-12 pe=0.5 me=0.26 tf=36e-9 cjc=0.72e-12 pc=0.58
+ mc=0.34 ccs=2.5e-12 ps=0.53 ms=0.35 rc=276
endsection
section ff
  model nch bsim3v3 type=n mobmod=1 capmod=2 version=3.1
  + xj=1.7e-7 vsat=7.99e4 at=3.6e4 a0=0.799 ags=0.4
  + a1=0 a2=1 keta=-0.05 nch=2.8e17 ngate=1.31e20 k1=0.74
  model pch bsim3v3 type=p mobmod=1 capmod=2 version=3.1
  + xj=1.7e-7 vsat=1.38e5 at=1e5 a0=1.3 ags=0.3
  + a1=1.1e-4 a2=1 keta=0 nch=4.1e17 ngate=7.6e19 k1=0.88
  model knpn bjt is=10e-13 bf=220 va=58.7 ik=5.63e-3 rb=rbn rbm=86
  + re=3.2 cje=0.25e-12 pe=0.76 me=0.34 tf=249e-12 cjc=0.34e-12 pc=0.55
  + mc=0.35 ccs=2.4e-12 ms=0.35 ps=0.53 rc=169
  model kpnp bjt type=PNP is=10e-13 bf=90 va=43.1 ik=0.206e-3 rb=rbp rbm=64.3
  + re=33.8 cje=0.16e-12 pe=0.5 me=0.26 tf=36e-9 cjc=0.72e-12 pc=0.58
  + mc=0.34 ccs=2.5e-12 ps=0.53 ms=0.35 rc=276
endsection
endlibrary
```

Node Sets (nodeset)

Description

The `nodeset` statement is used to provide an initial guess for nodes in any DC analysis or the initial condition calculation for the transient analysis. It can occur multiple times in the input, the information provided in all the occurrences is collected. For more information, read the description of DC analysis.

Definition

```
nodeset <node=value> ...
```

This statement takes a list of signals as an argument. The concept of nodes has been generalized to signals where a signal is a value associated with a topological node of the circuit or some other unknown that is solved by the simulator, such as the current through an inductor or the voltage of the internal node in a diode. Topological nodes can be either at the top-level or in a subcircuit.

For example,

```
nodeset 7=0 out=1 OpAmp1.comp=5 L1:1=1.0u
```


where 7=0 implies that node 7 should be about 0V, node `out` should be about 1V, node `comp` in subcircuit `OpAmp1` should be about 5V, and the current through the first terminal of `L1` should be about 1uA.

Parameter Soft Limits (param_limits)

Description

The parameter values passed to Spectre components and analysis are subject to both hard and soft limits. If you set a parameter to a value that violates a hard limit, such as giving `z0=0` to a transmission line, Spectre issues an error message and quits. If the given parameter value violates a soft limit, only a warning is issued, but Spectre uses the value of the component as given. Hard limits are used to prevent you from using values that would cause Spectre to fail or put a model in an invalid region. Soft limits are used to call attention to unusual parameter values that might have been given mistakenly. If a parameter value violates a soft limit, a message similar to one of the following sample messages is printed:

```
Parameter rb has the unusually small value of 1uOhms.
```

or

```
Parameter rb has the unusually large value of 1MOhms.
```

Spectre has built-in soft limits on a few parameter values. However, it is possible for you to override these limits, or provide limits on parameters that do not have built-in limits. To do so, create a parameter range limits file, and invoke Spectre giving the name of the file after the `+param` command line option. For example,

```
spectre +param limits-file input-file
```

Limits are given using the following syntax:

```
[PrimitiveName] [model] [LowerLimit <[=]] [Param] [<[=] UpperLimit]
```

The limits can be given as strict (using `<=`) or nonstrict (using `<`). If the limits are strict, there can be no space between `<` and `=`. The limits for one parameter are given on one line. There is no way of continuing the specification of the limits for a parameter over more than one line. If a parameter is given more than once, the limits given the last time override earlier limits. The primitive name must be a Spectre primitive name, not a name used for SPICE compatibility. So, for example, `mos3` must be used rather than `mos`. Parameter limits can be written using Spectre native mode metric scale factors. Thus a limit of `f <= 1.0e6` could also be written as `f <= 1M`.

Here are some examples.

```
mos3      0.5u <= l <= 100u
          0.5u <= w
```

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```
0 < as <= 1e-8
0 < ad <= 1e-8
model |vto| <= 3
```

Notice that it is not necessary to give the primitive name each time. If not given, it is assumed to be the same as the previous parameter. Upper and lower limits may be given, and if not given there will be no limit on the parameter value. Thus, in the example, if *w* is less than 0.5um, a warning will be issued, but there is no limit on how large *w* can be. If a parameter is mentioned, but no limits given, then all limits are disabled for that parameter. Limits are placed on model parameters by giving the model keyword. If the model keyword is not given, the limits are applied to instance parameters. Notice that you can also place upper or lower limits on the absolute value of a parameter. For example,

```
resistor 0.1 < |r| < 1M
```

indicates that the absolute value of *r* should be greater than 0.1 Ohm and less than 1 MOhm. There can be no spaces between the absolute value symbols and the parameter name.

Here are some more examples.

```
1 <= x < 0.5
1 <= y <= 1
1 < z < 1
```

In the first case the lower bound is larger than the upper bound, which indicates that the range of *x* is all real numbers except those from 0.5 to 1 and 0.5 itself. The limits are applied separately, thus *x* must be both greater than or equal to 1 ($1 \leq x$) and less than 0.5 ($x < 0.5$). The second case specifies that *y* should be 1 and the third case specifies that *z* should not be 1.

It is possible to specify limits for any scalar parameter that takes either a real number, an integer, or an enumeration. To specify the limits of a parameter that takes enumerations use the indices associated with the enumerations. For example, consider the region parameter of the bjt. There are four possible regions: off, fwd, rev, or sat (see `spectre -help bjt`). Each enumeration is assigned a number starting at 0 and counting up. Thus, off=0, fwd=1, rev=2, and sat=3. The specification

```
bjt          3 <= region <= 1
```

indicates that a warning should be printed if region=rev because the conditions ($3 \leq \text{region}$) and ($\text{region} \leq 1$) exclude only (region=2) and region 2 is rev.

It is possible to read a parameter limits file from within another file. To do so, use an include statement. For example,

```
include "filename"
```

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will temporarily suspend the reading of the current file until the contents of `filename` have been read. Include statements may be nested arbitrarily deep with the condition that the operating system may limit the number of files that Spectre may have open at once. Paths in file names are taken to be relative to the directory that contains the current file, not from the directory in which Spectre was invoked.

Spectre can be instructed to always read a parameter limits file by using the `SPECTRE_DEFAULTS` environment variable. For example, if you put the following in your shell initialization file (`.profile` for `sh`, `.cshrc` for `csh`)

```
setenv SPECTRE_DEFAULTS "+param /cds/etc/spectre/param.lmts"
```

Spectre would always read the specified limits file.

Netlist Parameters (parameters)

Description

The Spectre native netlist language allows parameters to be specified and referenced in the netlist, both at the top-level scope and within subcircuit declarations (run `spectre -h subckt` for more details on parameters within subcircuits).

Definition

```
parameters <param=value> [param=value] ...
```

Examples

```
simulator lang=spectre
parameters p1=1 p2=2           // declare some parameters
r1 (1 0) resistor r=p1         // use a parameter, value=1
r2 (1 0) resistor r=p1+p2      // use parameters in an expression, value=3
x1 s1 p4=8                     // "s1" is defined below, pass in value 8 for "p4"
subckt s1
parameters p1=4 p3=5 p4=6      // note: no "p2" here, p1 "redefined"
r1 (1 0) resistor r=p1         // local definition used: value=4
r2 (1 0) resistor r=p2         // inherit from parent(top-level) value=2
r3 (1 0) resistor r=p3         // use local definition, value=5
r4 (1 0) resistor r=p4         // use passed-in value, value=8
r5 (1 0) resistor r=p1+p2/p3   // use local+inherited/local = (4+2/5) = 4.4
ends
time_sweep tran start=0 stop=(p1+p2)*50e-6 // use 5*50e-6 = 150 us
```

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```
dc_sweep dc param=p1 values=[0.5 1 +p2 (sqrt(p2*p2)) ] // sweep p1
```

Parameter Declaration

Parameters can be declared anywhere in the top-level circuit description or on the first line of a subcircuit definition. Parameters must be declared before they are used (referenced). Multiple parameters can be declared on a single line. When parameters are declared in the top-level, their values must be specified. When parameters are declared within subcircuits, their default values are optionally specified.

Parameter Inheritance

Subcircuit definitions inherit parameters from their parent (enclosing subcircuit definition, or top-level definition). This inheritance continues across all levels of nesting of subcircuit definitions, that is, if a subcircuit s1 is defined, which itself contains a nested subcircuit definition s2, then any parameters accessible within the scope of s1 are also accessible from within s2. Also, any parameters declared within the top-level circuit description are also accessible within both s1 and s2. However, any subcircuit definition can redefine a parameter that it has inherited. In this case, if no value is specified for the redefined parameter when the subcircuit is instantiated, then the redefined parameter uses the locally defined default value, rather than inheriting the actual parameter value from the parent.

Parameter Namespace

Parameter names must not conflict with device or analysis instance names, that is, it is not possible to reference a parameter called "r1" if there is an instance of a resistor (or other device or analysis) called "r1". Parameter names must also not be used where a node name is expected.

Parameter Referencing

Spectre netlist parameters can be referenced anywhere that a numeric value is normally specified on the right-hand side of an "=" sign or within a vector, where the vector itself is on the right-hand side of an "=" sign. This includes referencing of parameters in expressions (run `spectre -h expressions` for more details on netlist expression handling), as indicated in the preceding examples. You can use expressions containing parameter references when specifying device or analysis instance parameter values (for example specifying the resistance of a resistor or the stop time of a transient analysis, as outlined in the preceding example), when specifying model parameter values in model cards (for example specifying "bf=p1*0.8" for a bipolar model parameter, bf), or when specifying initial conditions and nodesets for individual circuit nodes.

Altering/Sweeping Parameters

Just as certain Spectre analyses (for example `sweep`, `alter`, `ac`, `dc`, `noise`, `sp`, `xf`) can sweep device instance or model parameters, they can also sweep netlist parameters. Run `spectre -h <analysis>` to see the particular details for any of these analyses, where `<analysis>` is the analysis of interest.

Temperature as a parameter

You can use the reserved parameters `temp` and `tnom` anywhere that an expression can be used, including within expressions and user-defined functions. The "temp" parameter always represents the simulator (circuit) temperature, and "tnom" always represents the measurement temperature. All expressions involving "temp" or "tnom" are re-evaluated any time the circuit temperature or measurement temperature changes.

You can also alter or sweep the "temp" and "tnom" parameters using any of the techniques available for altering or sweeping netlist or subcircuit parameters (with the exception of `altergroups`).

This capability allows you to write temperature dependent models for example, by using "temp" in an equation for a model or instance parameter. For example

```
r1 1 0 res r=(temp-tnom)*15+10k // temp is temperature
o1 options temp=55           // causes a change in above resistor r1
```

Reserved Parameters

The following parameters are reserved, and may not be declared as either top-level parameters or subcircuit parameters: `temp`, `tnom`, `scale`, `scalem`, `freq`, `time`.

Parameter Set - Block of Data (paramset)

Description

A parameter set is a block of data, which can be referenced by a sweep analysis. Within a paramset the first row contains an array of top-level netlist parameters. All other rows contain numbers which are used to alter the value of the parameters during the sweep. Each row represents an iteration of the sweep. This data should be bound within braces. The opening brace is required at the end of the line defining the paramset. The paramset cannot be defined within subcircuits or cannot be nested.

Definition

```
<Name> paramset {  
    <list of parameter names>  
    <list of number>  
    [more rows of numbers]  
}
```

Example

```
data paramset {  
    p1  p2  p3  
    1.1 2.2 3.3  
    4.4 5.5 6.6  
}
```

Output Selections (save)

Description

The `save` statement indicates that the values of specific nodes or signals should be saved in the output file. It works in conjunction with the `save` parameter on most analyses. The output file is written in Cadence Waveform Storage Format (WSF), Cadence Parameter Storage Format (PSF) or in Nutmeg/SPICE3 format as controlled by a a command line argument or a global option (see the options statement). The proper postprocessor should be used to view the output, generate plots, or do any further processing.

Definition

```
save <node|component|terminal> ...
```

This statement takes a list of signals as an argument. The concept of nodes has been generalized to signals where a signal is a value associated with a topological node of the circuit or some other unknown that is solved by the simulator, such as the current through an inductor or the voltage of the internal node in a diode. Topological nodes can be either at the top-level or in a subcircuit.

For example,

```
save 7 out OpAmp1.comp M1:currents D3:oppoint L1:l R4:pwr
```

which tells that node 7, node `out`, node `comp` in subcircuit `OpAmp1`, the currents through the terminals of `M1`, the `oppoint` information for diode `D3`, the current through the first terminal of

L1, and the instantaneous power dissipated by R4 should be saved. These outputs are saved in addition to any outputs specified with the `save` parameter on the analysis.

To specify a component terminal current, give the name of the component and the name or the index of the terminal separated by a colon. If `currents` is specified after the component and the colon, then all the terminal currents for the component are saved unless the component has only two terminals, in which case only the current through the first terminal is saved. Current is positive if it enters the terminal flowing into the component.

If a component name is followed by a colon and `oppoint`, then the operating point information associated with the component is computed and saved. If the colon is followed by an operating point parameter name (see each component for list of operating point parameters), then the value of that parameter is output.

If only a component name is given, all available information about the component, including the terminal currents and the operating point parameter values, is saved.

Sensitivity Analyses (sens)

Description

Use the `sens` control statement to find sensitivities of the output variables with respect to component and instance parameters for the list of the analyses performed. Currently DC and AC sensitivity analyses are supported. The results of the sensitivity analyses are stored in the output files written in Cadence Parameter Storage Format (PSF). In addition, you can use `+sensdata filename` command line argument or a global option (see the options statement) to direct sensitivity analyses results into a specified ASCII file.

Definition

```
sens (output_variables_list) to (design_parameters_list) for (analyses_list)
```

where

```
output_variables_list = ovar1 ovar2 ...
design_parameters_list = dpar1 dpar2 ...
analyses_list = anal1 anal2 ...
```

The list of the design parameters may include valid instance and model parameters. You can also specify device instances or device models without a modifier. In this case Spectre will attempt to compute sensitivities with respect to all corresponding instance or model parameters. Caution should be exercised in using this option as warnings or errors may be generated since many instance and model parameters cannot be modified. If no design

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parameters are specified then all the instance and model parameters are added. The list of the output variables for both AC and DC analyses may include node voltages and branch currents. For DC analyses, it also may include device instance operating point parameters.

Examples

```
sens (q1:betadc 2 Out) to (vcc:dc nbjt1:rb) for (analDC)
```

For this statement DC sensitivities of betadc operating point parameter of transistor `q1` and of nodes `2` and `Out` will be computed with respect to the `dc` voltage level of voltage source `vcc` and the model parameter `rb` for the DC analysis `analDC`. The results will be stored in the output file `analDC.sens.dc`.

```
sens (1 n2 7) to (q1:area nbjt1:rb) for (analAC)
```

For this statement AC sensitivities of nodes `1`, `n2`, `7` will be computed with respect to the area parameter of transistor `q1` and the model parameter `rb` for each frequency of the AC analysis `analAC`. The results will be stored in the output file `analAC.sens.ac`.

```
sens (1 n2 7) for (analAC)
```

For this statement AC sensitivities of nodes `1`, `n2`, `7` will be computed with respect to all instance and model parameters of all devices in the design for each frequency of the AC analysis `analAC`. The results will be stored in the output file `analAC.sens.ac`.

```
sens (vbb:p q1:int_c q1:gm 7) to (q1:area nbjt1:rb) for (analDC1)
```

For this statement DC sensitivities of the branch current `vbb:p`, the operating point parameter `gm` of the transistor `q1`, the internal collector voltage `q1:int_c` and the node `7` voltage will be computed with respect to the instance parameter `area` for instance `q1` and the model parameter `rb` for model `nbt1`.

SpectreHDL Usage and Language Summary (spectrehdl)

Description

SpectreHDL is a proprietary analog hardware description language. It allows analog circuit behavior to be described at a high level of abstraction, using a language which is similar to Verilog-A (run `spectre -h veriloga` for some details on the Verilog-A modeling language supported by spectre). Behavioral descriptions of modules/components may be instantiated in a Spectre netlist along with regular Spectre primitives.

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SpectreHDL descriptions are written in file(s) separate from the Spectre netlist file. These descriptions are written in modules (see the module `alpha` below). To include a module in the Spectre netlist, first add the line

```
ahdl_include "Ahdlfile.def"
```

to the Spectre netlist file (where `Ahdlfile.def` is the name of the file in which the required module is defined). The module is instantiated in the Spectre netlist in the same manner as Spectre primitives. For example,

```
name (node1 node2) alpha arg1=4.0 arg2=2 arg3="parameterized resistor"
```

This instantiates an element `alpha`, having two nodes and three parameters.

SpectreHDL modules can be debugged using `hdldebug`. `hdldebug` has a GUI and a command line mode. Please refer to the *Verilog-A Debugging Tool User Guide* for more information.

Module Template

The following is a SpectreHDL module template

```
module alpha( n1, n2 ) ( arg1, arg2, arg3 )
node [V,I] n1, n2;
parameter real arg1 = 2.0;
parameter integer arg2;
parameter string arg3;
{
    real local;
    // this is a comment
    initial {
        // initializations performed before the
        // start of an analysis.
    }
    analog {
        // module behavioral description
        V(n1, n2) <- I(n1, n2) * arg1;
    }
    final {
        // tasks performed at the end of an analysis
    }
}
```

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Language Summary

The following provides a summary of the SpectreHDL analog hardware description language. For more information refer to the *SpectreHDL Reference Manual*.

Derivative and Integral Operators

`dot(x)`

Differentiate x wrt time.

`integ(x <, ic <, assert>>)`

Integrate x wrt time. Output = ic during DC analysis. assert causes the integration to be reset.

`idtmmod(x, <ic <, modulus <, offset> > >)`

Circular Integration of x wrt time. Output = ic during DC analysis. Integration is performed with given offset and modulus if specified.

Built-In Mathematical Functions

<code>abs(x)</code>	Absolute value
<code>floor(x)</code>	Largest integer $< x$
<code>ceil(x)</code>	Smallest integer $> x$
<code>ln(x)</code>	Natural logarithm
<code>log(x)</code>	Decimal logarithm
<code>exp(x)</code>	Exponential
<code>sqrt(x)</code>	Square root
<code>min(x, y)</code>	Minimum
<code>max(x, y)</code>	Maximum
<code>pow(x, y)</code>	x to the power of y

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Simulator Time-Step Control Functions

`$threshold(x, direction <, abstol <, reltol_factor>>)`

Set breakpoint when x crosses zero.

`$bound_step(max_step)`

Limit time step, (time step \leq max_step).

`$break_point(target <, period>)`

Set breakpoints at time = target and at times = $N \cdot \text{period} + \text{target}$ if period is specified.

`$last_crossing(x, direction)`

Return time when expression last crossed zero in a given direction.

Waveform Filter Functions

`$transition(x <, delay <, trise <, tfall>>>)`

Specify details of signal transitions. For efficient simulation, it is recommended that x not be a continuous signal, i.e. a function of a probe. See the [*SpectreHDL Reference*](#) manual for further explanation of this issue.

`$slew(x <, SRpos <, SRneg>>)`

Model slew rate behavior.

`$tdelay(x, time_delay, max_delay)`

Response(t) = x(t - time_delay).

`$zdelay(x <, period <, ttransition <, sample offset time <, ic>>>>)`

Fixed period sample and hold function.

`$zi_nd(x, numer, denom, period <, ttransition <, sample offset time>>)`

z-domain filter function, numerator-denominator form.

`$zi_zd(x, zeros, denom, period <, ttransition <, sample offset time>>)`

z-domain filter function, zero-denominator form.

`$zi_np(x, numer, poles, period <, ttransition <, sample offset time>>)`

z-domain filter function, numerator-pole form.

`$zi_zp(x, zeros, poles, period <, ttransition <, sample offset time>>)`

z-domain filter function, zero-pole form.

`$laplace_nd(x, numer, denom, <, abstol >)`

s-domain filter function, numerator-denominator form.

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`$laplace_zd(x, zeros, denom, <, abstol >)`

s-domain filter function, zero-denominator form.

`$laplace_np(x, numer, poles, <, abstol >)`

s-domain filter function, numerator-pole form.

`$laplace_zp(x, zeros, poles, <, abstol >)`

s-domain filter function, zero-pole form.

Noise Functions

`$white_noise(power <, tag >)`

Generates white noise with given power. Noise contributions with the same tag are combined for a module.

`$flicker_noise(power, exp <, tag >)`

Generates pink noise with given power at 1 Hz that varies in proportion to $1/f^{\text{exp}}$. Noise contributions with the same tag are combined for a module.

`$noise_table(vector <, tag >)`

Generates noise where power is determined by linear interpolation from the given vector of frequency-power pairs. Noise contributions with the same tag are combined for a module.

AC Analysis Stimuli

`$ac_stim(<analysis_name <, mag > >)`

Small signal source of specified magnitude, active for given analysis.

Interpolation Functions

`$build_table(type, response, inVec1, sizeVec1 <, inVec2, sizeVec2 ...>)`

Build a table for B-Spline interpolation.

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<code>\$interpolate(interp_table, v1<, v2 <, v3 <, v4 >>>)</code>	Perform interpolation at given point.
-------------------------------------------------------------------------------------	---------------------------------------

Simulator IO Functions

<code>\$strobe("format string" <, arguments>)</code>	Print data to stdout every time step.
<code>\$debug("format string" <, arguments>)</code>	Print data to stdout every iteration.
<code>\$fstrobe(fp_ptr, "format string" <, arguments>)</code>	Print data to a file every time step.
<code>\$fdebug(fp_ptr, "format string" <, arguments>)</code>	Print data to a file every iteration.
<code>\$fread(fp_ptr, "format string" <, arguments>)</code>	Read data from a file.
<code>\$warning("format string" <, arguments>)</code>	Warning message.
<code>\$error("format string" <, arguments>)</code>	Error message. Abort analysis.
<code>\$fatal("format string" <, arguments>)</code>	Fatal message. Abort simulation.
<code>\$fopen("filename", mode)</code>	Open a file.
<code>\$fflush(fp_ptr)</code>	Flush a file to disk.
<code>\$fclose(fp_ptr)</code>	Close a file.
<code>\$popen("command", "mode")</code>	Open a pipe with given command in given mode.
<code>\$pclose(fp_ptr)</code>	Close a pipe.
<code>\$read_table("filename", table_id <, comment_str<, delim_str> >)</code>	Read from a file into a 2-D real array.
<code>\$write_table("filename", table_id <,delim_str>)</code>	Write from a 2-D real array to a file.
<code>\$halt("string")</code>	Halt the simulation, printing given string.
<code>\$system("system command")</code>	Sends a command to the operating system.
<code>\$str("format_string" < ,arg1 < ,arg2 < ..etc> > >)</code>	Create a string from arguments in given format.
<code>\$strcmp(str1, str2)</code>	Compares two strings lexicographically.
<code>\$strtoint(int_as_str)</code>	Converts a string, int_as_str, to an integer.
<code>\$strtoreal(real_as_str)</code>	Converts a string, real_as_str, to a real.

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<code>\$strcpy(des_str, src_str)</code>	Copies <code>src_str</code> to <code>des_src</code> .
<code>\$strcat(des_str, src_str)</code>	Appends <code>src_str</code> to <code>des_src</code> .
<code>\$strlen(str)</code>	Returns the number of characters in <code>str</code> .
<code>\$substr(input_str, start_pos, end_pos)</code>	Returns the substring of <code>input_str</code> between <code>start_pos</code> and <code>end_pos</code> .
<code>\$strstr(input_str, sub_str)</code>	Returns the first position where <code>sub_str</code> is found in <code>input_str</code> .
<code>\$strchr(input_str, character)</code>	Returns the first position where <code>character</code> is found in <code>input_str</code> .
<code>\$strrchr(input_str, character)</code>	Returns the last position where <code>character</code> is found in <code>input_str</code> .
<code>\$strspn(input_str, span_set)</code>	Returns the number of continuous characters from the start of <code>input_str</code> that are in <code>span_set</code> .
<code>\$strcspn(input_str, span_set)</code>	Returns the number of continuous characters from the start of <code>input_str</code> that are not in <code>span_set</code> .
<code>\$ascii(character)</code>	Returns the ascii code of <code>character</code> .

Simulator Environment Functions

<code>\$time()</code>	Returns current simulation time.
<code>\$temp()</code>	Returns ambient simulation temperature.
<code>\$vt(<temp>)</code>	Returns thermal voltage. If <code>temp</code> is defined, returns the thermal voltage at <code>temp</code> .
<code>\$analysis(analysis_string1<, analysis_string2 <, ...>)</code>	Returns true(1) if the current analysis phase matches one of the given analyses strings. Valid analyses strings are <code>dc</code> , <code>tran</code> , <code>ac</code> , <code>pss</code> , <code>noise</code> , <code>pdisto</code> , <code>pac</code> , <code>pnoise</code> , <code>pxf</code> , <code>sp</code> , <code>tdr</code> , <code>xf</code> , <code>static</code> , or <code>ic</code> .

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Spectre Syntax

Simulator Tolerance Functions

<code>\$reltol()</code>	Returns relative tolerance.
<code>\$abstol(<i>name</i>)</code>	Returns absolute tolerance of quantity <i>name</i> .

Parameter Functions

<code>\$param_given(<i>param</i>)</code>	Returns 1 if <i>param</i> was set. <i>param</i> can be a model parameter or an instance parameter.
<code>\$pwr(<i>x</i>)</code>	Assignment of model power consumption. Adds the expression <i>x</i> to the <code>pwr</code> parameter of a module.

Data Types

<code>integer</code>	Discrete numerical type.
<code>real</code>	Continuous numerical type.
<code>string</code>	Text string type.
<code>stream</code>	File pointer and text stream type.
<code>enum { name1 <, name2 <, name3 <, ... >>> }</code>	Discrete name type.
<code>void</code>	Null or empty type.
<code>table</code>	Interpolation table type.
<code>node [PotentialName, FlowName]</code>	Interconnection point type.

Data Qualifiers

<code>parameter</code>	Indicates that a variable is a parameter and so may be given a different value when the module is instantiated and may have a range specifier.
<code>const</code>	Indicates that a variable must be given a constant value when declared that can never be changed.

Subcircuit Definitions (subckt)

Description

Hierarchical Circuit Description

The `subckt` statement is used to define a subcircuit. Subcircuit definitions are simply circuit macros that can be expanded anywhere in the circuit any number of times. When an instance in your input file refers to a subcircuit definition, the instances specified within the subcircuit are inserted into the circuit. Subcircuits may be nested. Thus a subcircuit definition may contain instances of other subcircuits. Subcircuits may also contain component, analysis or model statements. Subcircuit definitions may also be nested, in which case the innermost subcircuit definition can only be referenced from within the subcircuit in which it is defined, and cannot be referenced from elsewhere.

Instances that instantiate a subcircuit definition are referred to as subcircuit calls. The node names (or numbers) specified in the subcircuit call are substituted, in order, for the node names given in the subcircuit definition. All instances that refer to a subcircuit definition must have the same number of nodes as are specified in the subcircuit definition and in the same order. Node names inside the subcircuit definition are strictly local unless declared otherwise in the input file with a global statement.

Subcircuit Parameters

Parameter specification in subcircuit definitions is optional. In the case of nested subcircuit definitions, any parameters which have been declared for the outer subcircuit definition are also available within the inner subcircuit definition. Any parameters that are specified are referred to by name optionally followed by an equals sign and a default value. If, when making a subcircuit call, you do not specify a particular parameter, this default value is used in the macro expansion. Subcircuit parameters can be used in expressions within the subcircuit consisting of subcircuit parameters,

constants, and various mathematical operators. Run `spectre -h expressions` for more details on Spectre expression handling capability. Run `spectre -h parameters` for more details on how Spectre handles netlist parameters, including subcircuit parameters, and how they inherit within nested subcircuit definitions.

Subcircuits always have an implicitly defined parameter `m`. This parameter is passed to all components in the subcircuit and each component is expected to multiply it by its own multiplicity factor. In this way, it is possible to efficiently model several copies of the subcircuit in parallel. It is an error to attempt to explicitly define `m` on a `parameters` line. Also, because `m` is only implicitly defined, it is not available for use in expressions in the subcircuit.

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Inline Subcircuits

An inline subckt is a special case of a subckt where one of the devices or models instantiated within this subckt does not get its full hierarchical name, but rather inherits the subckt call name itself. An inline subckt is syntactically denoted by the presence of the keyword `inline` before the `subckt`. It is called in the same manner as a regular subcircuit. The body of the inline subcircuit can typically contain one of the following, corresponding to different use models:

- multiple device instances (one of which is the "inline" component)
- multiple device instances, (one of which is "inline") and one or more parameterized models
- a single "inline" device instance and a parameterized model to which the device instance refers

The `inline` component is denoted by giving it the same name as the inline subcircuit itself. When the subcircuit is flattened, the `inline` component does not take on a hierarchical name such as X1.M1, but rather takes on the name of the subckt call itself, such as X1. Any non-inline components in the subckt take on the regular hierarchical name, just as if the subcircuit were a regular subckt (i.e. not an inline subckt).

Probing the Inline Device

Spectre allows the following list of items to be saved or probed for primitive devices. These would also apply to devices modeled as the inline components of inline subcircuits:

1. all terminal currents e.g. `save q1:currents`
2. specific (index) terminal current e.g. `save q1:1 (#1=collector)`
3. specific (named) terminal current e.g. `save q1:b ("b"=base)`
4. save all operating point info e.g. `save q1:oppoint`
5. save specific operating point info e.g. `save q1:vbe`
6. save all currents and oppoint info e.g. `save q1`

Parameterized Models and Inline Subckts

Inline subckts can be used in the same way as regular subcircuits to implement parameterized models, however inline subckts provide some powerful new options. When an inline subcircuit contains both a parameterized model and an inline device which references

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that model, then the user can create instances of the device, and each device will automatically get an appropriately scaled model assigned to it. For example, the instance parameters to an inline subckt could represent something like emitter width and length of a BJT device and within the subckt a model card could be created which is parameterized for emitter width and length and scales accordingly. When the designer instantiates the macro, he/she supplies the values for the emitter width and length, and a device is instantiated with an appropriate geometrically scaled model. Again, the inline device does not get a hierarchical name, and can be probed in the same manner as the inline device in the previous section on modeling parasitics, that is, it can be probed just as if it was a simple device, and not actually embedded in a subckt.

Automatic Model Selection using Inline Subckts

See `spectre -h if` for a description on how to combine Spectre's "structural if" statement with inline subckts to perform automatic model selection based on **any** netlist/subckt parameter.

Definition

```
[inline] subckt <Name> (<node1> ... <nodeN>)
    [parameters <name1>=<value1> ... <nameN>=<valueN>]
    ...
    <component, analysis, and/or model statements>
    ...
```

ends [Name] **Example 1: subckt**

```
subckt coax (i1 o1 i2 o2)
    parameters zin=50 zout=50 vin=1 vout=1 len=0
    inner i1 o1 i2 o2  tline z0=zin vel=vin len=len
    outer o1 0  o2 0  tline z0=zout vel=vout len=len
ends coax
```

defines a parameterized coaxial transmission line macro from two ideal transmission lines. To instantiate this subcircuit, one could use an instance statement such as:

```
Coax1 pin nin out gnd coax zin=75 zout=150 len=35m
```

Example 2: inline subckt - Parasitics

Consider the following example of an inline subcircuit, which contains a mosfet instance, and two parasitic capacitances:

```
inline subckt s1 (a b)                // "s1" is name of subckt
    parameters p1=1u p2=2u
```

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```
s1 (a b 0 0) mos_mod l=p1 w=p2          // "s1" is "inline" component
cap1 (a 0) capacitor c=1n
cap2 (b 0) capacitor c=1n
ends s1
```

The following circuit creates a simple mos device instance M1, and calls the inline subcircuit s1 twice (M2 & M3)

```
M1 (2 1 0 0) mos_mod
M2 (5 6) s1 p1=6u p2=7u
M3 (6 7) s1
```

This expands/flattens to:

```
M1 (2 1 0 0) mos_mod
M2 (5 6 0 0) mos_mod l=6u w=7u // the "inline" component, inherits
call name
M2.cap1 (5 0) capacitor c=1n    // a regular hierarchical name
M2.cap2 (6 0) capacitor c=1n
M3 (6 7 0 0) mos_mod l=1u w=2u // the "inline" component, inherits
call name
M3.cap1 (6 0) capacitor c=1n
M3.cap2 (7 0) capacitor c=1n
```

Here the final flattened names of the three mosfets (one for each instance) are M1, M2 and M3, rather than M1, M2.s1 and M3.s1 as they would be if s1 was a regular subcircuit. The parasitic capacitors (which the user is not really interested in, or perhaps even aware of, if the inline subckt definition was written by a separate modeling engineer) have full hierarchical names however.

Example 3: inline subckt - Scaled Models

Consider the following example, in which a parameterized model is declared within an inline subcircuit for a bipolar transistor. The model parameters are the emitter width, length, and area, and also the temperature delta (trise) of the device above nominal. Ninety nine instances of a 4*4 transistor are then placed, and one instance of a transistor with area=50 is placed. Each transistor gets an appropriately scaled model.

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```
* declare a subckt, which instantiates a transistor with
* a parameterized model. The parameters are emitter width
*and length.
inline subckt bjtmod (c b e s)
parameters le=1u we=2u area=le*we trise=0
model mod1 bjt type=npn bf=100+(le+we)/2*(area/le-12)
+      is=1e-12*(le/we)*(area/le-12)
bjtmod (c b e s) mod1 trise=trise      // "inline" component
ends bjtmod
* some instances of this subckt
q1 (2 3 1 0) bjtmod le=4u we=4u      // trise defaults to zero
q2 (2 3 2 0) bjtmod le=4u we=4u trise=2
q3 (2 3 3 0) bjtmod le=4u we=4u
.
.
q99 (2 3 99 0) bjtmod le=4u we=4u
q100 (2 3 100 0) bjtmod le=1u area=50e-12
```

Verilog-A Usage and Language Summary (veriloga)

Description

Verilog-A is an analog hardware description language standard from Open Verilog International. It allows analog circuit behavior to be described at a high level of abstraction, using a language which is similar to SpectreHDL (run `spectre -h spectrehdl` for some details on the SpectreHDL modeling language). Behavioral descriptions of modules/components may be instantiated in a Spectre netlist along with regular Spectre primitives. For more information about using the SpectreHDL product, see the [*SpectreHDL Reference*](#) manual. For more information about using Verilog-A, see the *Cadence Verilog-A Language Reference* manual.

Verilog-A descriptions are written in file(s) separate from the Spectre netlist file. These descriptions are written in modules (see the module alpha below). To include a module in the Spectre netlist, first add the line

```
ahdl_include "VerilogAfile.va"
```

to the Spectre netlist file (where VerilogAfile.va is the name of the file in which the required module is defined). The module is instantiated in the Spectre netlist in the same manner as Spectre primitives for example,

```
name (node1 node2) alpha arg1=4.0 arg2=2
```

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Spectre Syntax

This instantiates an element `alpha`, having two nodes and two parameters.

Verilog-A modules can be debugged using *hdldebug*. *hdldebug* has a GUI and a command line mode. Please refer to the *Verilog-A Debugging Tool User Guide* for more information.

Module Template

The following is a Verilog-A module template

```
include "discipline.h"
include "constants.h"
module alpha( n1, n2 );
electrical n1, n2;
parameter real arg1 = 2.0;
parameter integer arg2 = 0;
real local;
// this is a comment
analog begin
  @ ( initial_step ) begin
    // performed at the first timestep of an analysis
  end
  // module behavioral description
  V(n1, n2) <+ I(n1, n2) * arg1;
  @ ( final_step ) begin
    // performed at the last time step of an analysis
  end
end
endmodule
```

Language Summary

The following provides a summary of the Verilog-A analog hardware description language. For more information refer to the *Verilog-A Language Reference* manual.

Analog Operators/Waveform Filters

`ddt(x <,abstol>)`

Differentiate x wrt time.

`idt(x, ic <, assert <, abstol> >)`

Integrate x wrt time. Output = ic during dc analysis and when assert is 1.

`idtmod(x, <ic <, modulus <, offset> > >)`

Circular Integration of x wrt time. Output = ic during DC analysis. Integration is performed with given offset and modulus if specified.

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<code>transition(x <, delay <, trise <, tfall>>>)</code>	Specify details of signal transitions. For efficient simulation, it is recommended that x not be a continuous signal, i.e. a function of a probe. See the <i>Cadence Verilog-A Language Reference</i> manual for further explanation of this issue.
<code>slew(x <, SRpos <, SRneg>>)</code>	Model slew rate behavior.
<code>delay(x, time_delay, max_delay)</code>	$\text{Response}(t) = x(t - \text{time_delay})$.
<code>zi_nd(x, numer, denom, period, < ttransition <,sample offset time >)</code>	z-domain filter function, numerator-denominator form.
<code>zi_zd(x, zeros, denom, period, < ttransition <,sample offset time >)</code>	z-domain filter function, zero-denominator form.
<code>zi_np(x, numer, poles, period, < ttransition <,sample offset time >)</code>	z-domain filter function, numerator-pole form.
<code>zi_zp(x, zeros, poles, period, < ttransition <,sample offset time >)</code>	z-domain filter function, zero-pole form.
<code>laplace_nd(x, numer, denom, <, abstol >)</code>	s-domain filter function, numerator-denominator form.
<code>laplace_zd(x, zeros, denom, <, abstol >)</code>	s-domain filter function, zero-denominator form.
<code>laplace_np(x, numer, poles, <, abstol >)</code>	s-domain filter function, numerator-pole form.
<code>laplace_zp(x, zeros, poles, <, abstol >)</code>	s-domain filter function, zero-pole form.

Built-In Mathematical Functions

<code>abs(x)</code>	Absolute value
<code>exp(x)</code>	Exponential if $x < 80$
<code>ln(x)</code>	Natural logarithm
<code>log(x)</code>	Log base 10
<code>sqrt(x)</code>	Square root
<code>min(x,y)</code>	Minimum
<code>max(x,y)</code>	Maximum

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Spectre Syntax

`pow(x,y)` x to the power of y

Noise Functions

<code>white_noise(power <, tag >)</code>	Generates white noise with given power. Noise contributions with the same tag are combined for a module.
<code>flicker_noise(power, exp <, tag >)</code>	Generates pink noise with given power at 1 Hz that varies in proportion to $1/f^{\text{exp}}$. Noise contributions with the same tag are combined for a module.
<code>noise_table(vector <, tag >)</code>	Generates noise where power is determined by linear interpolation from the given vector of frequency-power pairs. Noise contributions with the same tag are combined for a module.

AC Analysis Stimuli

<code>ac_stim(<analysis_name <, mag > >)</code>	Small signal source of specified magnitude, active for given analysis.
---------------------------------------------------------------	------------------------------------------------------------------------

Analog Events

Analog events must be contained in an analog event detection statement; `@(analog_event) statement`.

<code>cross(x, direction <, timetol <, abstol >>)</code>	Generates an event when x crosses zero.
<code>timer(start_time <, period>)</code>	Set (optionally periodic) breakpoint event at time = start_time.
<code>initial_step< (arg1 <, arg2 <, etc... > >)</code>	Generate an event at the initial step of an analysis. arg1, arg2, etc. may be any of: dc, tran, ac, pss, noise, pdisto, pac, pnoise, pxf, sp, tdr, xf, static, or ic.

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Spectre Syntax

<code>final_step< (arg1 <, arg2 <, etc... > >)</code>	Generate an event at the final step of an analysis. arg1, arg2, etc. may be any of:dc, tran, ac, pss, noise, pdisto, pac, pnoise, pxf, sp, tdr, xf, static, or ic.
------------------------------------------------------------------------	--------------------------------------------------------------------------------------------------------------------------------------------------------------------

Timestep Control

<code>bound_step(max_step)</code>	Limit timestep, (timestep <= max_step).
<code>last_crossing(x, direction)</code>	Return time when expression last crossed zero in a given direction.
<code>discontinuity(n)</code>	Hint to simulator that discontinuity is present in nth derivative.

Simulator IO Functions

<code>\$display(argument_list)</code>	Print data to stdout. Formatting strings may be interspersed between arguments/data.
<code>\$fdisplay(fptr, argument_list)</code>	Print data to a file. Formatting strings may be interspersed between arguments/data.
<code>\$strobe(argument_list)</code>	Print data to stdout. Formatting strings may be interspersed between arguments/data.
<code>\$fstrobe(fptr, argument_list)</code>	Print data to a file. Formatting strings may be interspersed between arguments/data.
<code>\$fopen("filename")</code>	Open a file for writing.
<code>\$fclose(fptr)</code>	Close a file.
<code>\$finish<(n)></code>	Finish the simulation.
<code>\$stop<(n)></code>	Stop the simulation.

Simulator Environment Functions

<code>\$realtime</code>	Returns current simulation time.
<code>\$temperature</code>	Returns ambient simulation temperature.

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<code>\$vt</code>	Returns thermal voltage.
<code>\$vt(temp)</code>	Returns thermal voltage at given temp.
<code>\$analysis(analysis_string1<, analysis_string2 <, ...>>)</code>	Returns true(1) if the current analysis phase matches one of the given analyses strings. Valid analyses strings are; dc, tran, ac, pss, noise, pdisto, pac, pnoise, pxf, sp, tdr, xf, static, or ic.

Parameter Functions

`$pwr(x)` Assignment of model power consumption. Adds the expression x to the pwr parameter of a module.

Data Types

<code>integer</code>	Discrete numerical type.
<code>real</code>	Continuous numerical type.

Data Qualifiers

`parameter` Indicates that a variable is a parameter and so may be given a different value when the module is instantiated, and that it may not be assigned a different value inside the module.

Structural Statements

<code>module_or_primitive #(<.param1(expr1)<,...>>) inst_name (<nodel <, ..>>);</code>	Creates a new instance of module_or_primitive called inst_name.
---------------------------------------------------------------------------------------------------------------------	-----------------------------------------------------------------

References

This section gives additional details about the source documents referred to in the text.

[antognetti88] Paolo Antognetti, Giuseppe Massobrio. *Semiconductor Device Modeling with SPICE*. McGraw-Hill, New York, 1988.

[gear71] C. William Gear. *Numerical Initial Value Problems in Ordinary Differential Equations*. Prentice-Hall, 1971.

[hammerstad80] E. Hammerstad, O. Jensen. "Accurate models for microstrip computer-aided design." *IEEE MTT-S 1980 International Microwave Symposium Digest*, pages 407-409.

[jansen83] Rolf H. Jansen, Martin Kirschning. "Arguments and an accurate model for the power-current formulation of microstrip characteristic impedance." *Arch. Elek. Ubertragung (AEU)*, vol. 37, 1983, pages 108-112.

[kirschning82] M. Kirschning, R. H. Jansen. "Accurate model for effective dielectric constant of microstrip with validity up to millimetre-wave frequencies." *Electronic Letters*, vol. 18, no. 6, 18 March 1982, pages 272-273.

[kundert90] Kenneth S. Kundert, Jacob K. White, Alberto Sangiovanni-Vincentelli. *Steady-State Methods for Simulating Analog and Microwave Circuits*. Kluwer Academic Publishers, 1990.

[nagel75] Laurence W. Nagel. *SPICE2: A Computer Program to Simulate Semiconductor Circuits*. Ph. D. dissertation, University of California at Berkeley, May 1975. Available through Electronics Research Laboratory Publications, U. C. B., 94720; Memorandum No. UCB/ERL M520.

[quarles89] Thomas L. Quarles. *Analysis of Performance and Convergence Issues for Circuit Simulation*. Ph. D. dissertation, University of California

Spectre Circuit Simulator Reference

References

at Berkeley, April 1989. Extensively documents the Spice3 program. Available through Electronics Research Laboratory Publications, U. C. B., 94720; Memorandum No. UCB/ERL M89/42.

[statz87]Hermann Statz, Paul Newman, Irl W. Smith, Robert A. Pucel, Hermann A. Haus. "GaAs FET device and circuit simulation in SPICE." *IEEE Transactions on Electron Devices*, vol. ED-34, no. 2, pages 160-169, February 1987.

[vladimirescu81]A. Vladimirescu, Kaihe Zhang, A. R. Newton, D. O. Pederson, A. Sangiovanni-Vincentelli. *SPICE Version 2G User's Guide*, August 1981. Available through Industrial Liaison Program Software Distribution office, Department of Electrical Engineering and Computer Sciences, University of California at Berkeley, 94720.

[yang82]Ping Yang, Pallab K. Chatterjee. "SPICE modeling for small geometry MOSFET circuits." *IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems*, vol. CAD-1, no. 4, pages 169-182, October 1982.