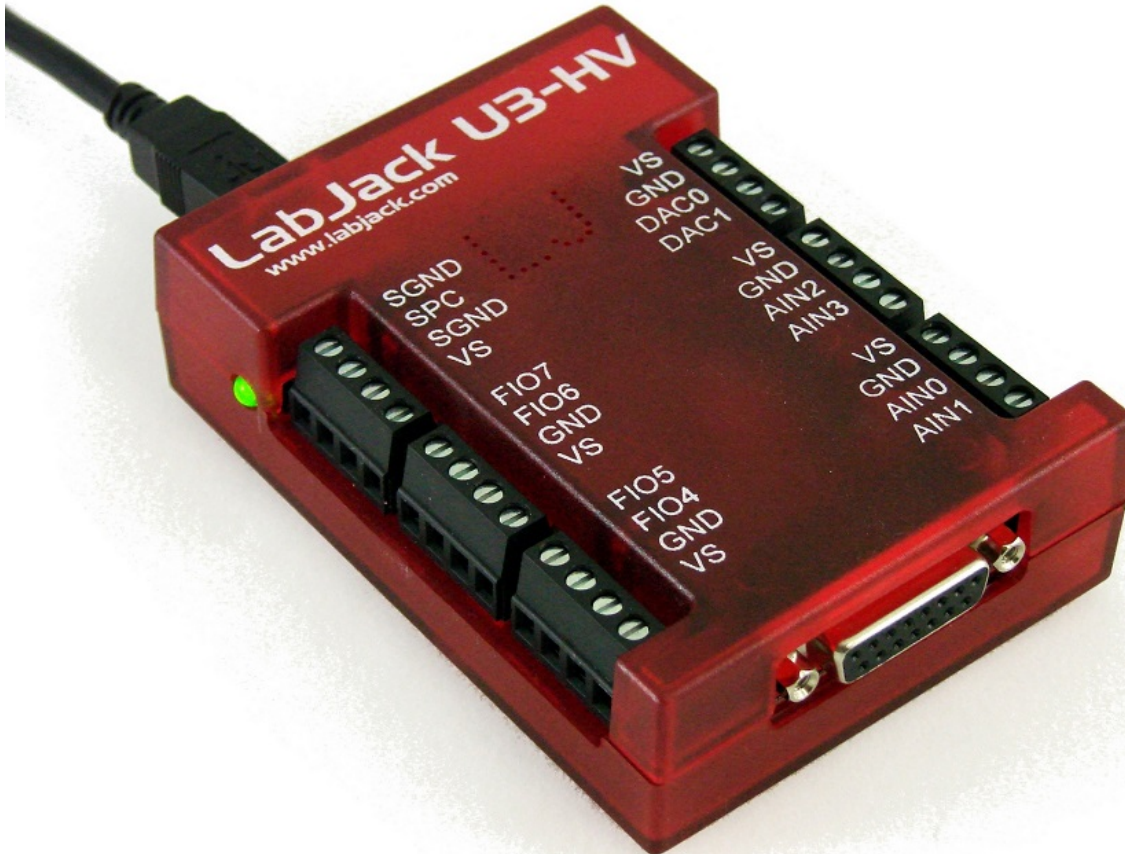


U3 Datasheet

U3

Stock: In Stock

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[Click here to order!](#)[Log in](#) or [register](#) to post comments**Affordable multifunction DAQ with USB.****This datasheet covers all U3 variants: U3-LV, U3-HV, U3-LV-OEM, and U3-HV-OEM.**

These HTML pages form the complete datasheet, manual, and user's guide for the U3. Most information in this datasheet applies to all U3 variants. There is an [OEM section](#) with information specific to the build of OEM versions. There is a [Hardware Revision section](#) that describes variations due to hardware revisions 1.20, 1.21 and 1.30. Specifications are in [Appendix A](#). UD library documentation is in [Section 4](#).

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Preface [U3 Datasheet]

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Package Contents:

The normal retail packaged U3 (-LV or -HV):

- U3 unit itself in red enclosure
- USB cable (6 ft / 1.8 m)
- Screwdriver

Warranty

Warranty:

All LabJack hardware is covered by a 5-year limited warranty, covering products and parts against defects in material or workmanship. We will troubleshoot, repair, or replace with like product to make sure you have a device that is operating to specifications.

LabJack products are very robust, but subject to the influence of user connections. The warranty does not apply if damaging mistakes were made in connecting our device, or if inspection reveals obvious signs of improper use, however we will still make a reasonable attempt to fix such devices for free if possible.

The warranty cannot be honored on discontinued products if replacements are unavailable and repair is not reasonably possible.

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U3 Compliance

Conformity Information (FCC, CE, RoHS):

See the [Conformity Page](#) and the text below:

FCC PART 15 STATEMENTS:

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense. The end user of this product should be aware that any changes or modifications made to this equipment without the approval of the manufacturer could result in the product not meeting the Class A limits, in which case the FCC could void the user's authority to operate the equipment.

Declaration of Conformity:

Manufacturers Name: LabJack Corporation
Manufacturers Address: 3232 S Vance St STE 200, Lakewood, CO 80227, USA

Declares that the product

Product Name: LabJack U3 (-LV or -HV)
Model Number: LJU3 (-LV or -HV)

conforms to the following Product Specifications:

EN 55011 Class A
EN 61326-1: 2002 General Requirements

and is marked with CE

RoHS:

The U3 is RoHS compliant per the requirements of Directive 2002/95/EC.

1 - Installation [U3 Datasheet]

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Windows

The LJUD driver requires a PC running Windows. For other operating systems, go to labjack.com for available support. Software will be installed to the LabJack directory which defaults to c:\Program Files\LabJack\.

Install the software first by going to labjack.com/support/u3.

Connect the USB cable: The USB cable provides data and power. After the UD software installation is complete, connect the hardware and Windows should prompt with "Found New Hardware" and shortly after the Found New Hardware Wizard will open. When the Wizard appears allow Windows to install automatically by accepting all defaults.

Run LJControlPanel: From the Windows Start Menu, go to the LabJack group and run LJControlPanel. Click the “Find Devices” button, and an entry should appear for the connected U3 showing the serial number. Click on the “USB – 1” entry below the serial number to bring up the U3 configuration panel. Click on “Test” in the configuration panel to bring up the test panel where you can view and control the various I/O on the U3.

If LJControlPanel does not find the U3, check Windows Device Manager to see if the U3 installed correctly. One way to get to the Device Manager is:

Start => Control Panel => System => Hardware => Device Manager

The entry for the U3 should appear as in the following figure. If it has a yellow caution symbol or exclamation point symbol, right-click and select “Uninstall” or “Remove”. Then disconnect and reconnect the U3 and repeat the Found New Hardware Wizard as described above.



Figure 1-1. Correctly Functioning U3 in Windows Device Manager

Linux and Mac OS X

The Exodriver is the native USB driver for Linux and Mac OS X. With it you can use low-level functions to interact with your U3 over USB. The LJUD driver, LJControlPanel and LJSelfUpgrade applications are not available for Linux or Mac OS X.

Download the Exodriver at labjack.com/support/software or labjack.com/support/linux-and-mac-os-x-drivers. For Mac OS X you can use the Mac Installer for installation, otherwise use the source code and install script.

Mac OS X Installer

Unzip the contents of Exodriver_NativeUSB_Setup.zip and run Exodriver_NativeUSB_Setup.pkg. Then follow the installer’s instructions to install the driver.

Source Code

Mac OS X Requirements

- OS X 10.5 or newer
- Xcode developer tools
- libusb-1.0 library available at libusb.info

Linux Requirements

- Linux kernel 2.6.28 or newer.
- GNU C Compiler
- libusb-1.0 library and development files (header files)

Installation

To install the driver from source code, first unzip the contents of the Exodriver source code. Then run the following commands in a terminal (replace <Exodriver-Source-Directory> with the directory you unzipped the Exodriver source code to):

```
cd <Exodriver-Source-Directory>
sudo ./install.sh
```

Follow the install script’s instructions to install the driver.

For more Exodriver installation information go to the Exodriver page at labjack.com/support/linux-and-mac-os-x-drivers. The source code download's README, INSTALL.Linux and INSTALL.MacOSX also provides more information. If you run into problems, first take a look at the comments section of the Exodriver page as the issue may have been helped with previously.

After installation, to test your U3 connect it to your computer with aUSB cable. The USB cable provides data and power. Build and run one of the examples from the source code download. Alternatively, install LabJackPython (at labjack.com/support/labjackpython) and run one of its examples.

1.1 - Control Panel Application (LJControlPanel) [U3 Datasheet]

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The application LJControlPanel is included with the [Windows installation package](#).

The LabJack Control Panel application (LJCP) handles configuration and testing of the UD series hardware (U3, U6 and UE9). Click on the "Find Devices" button to search for connected devices.

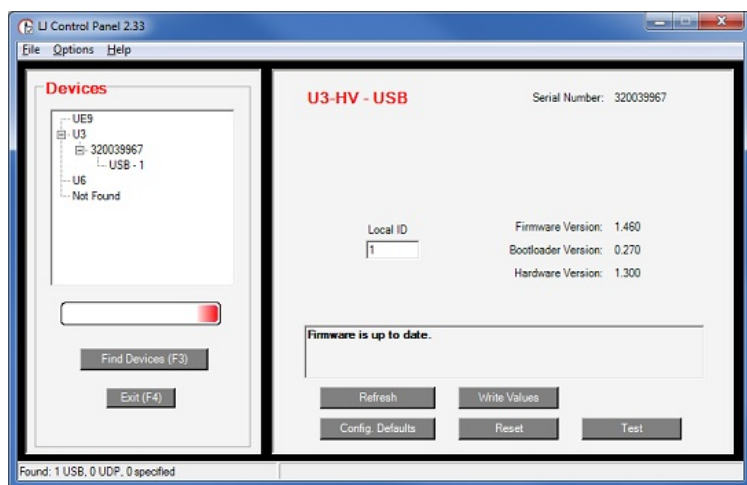


Figure 1-1. LJControlPanel Main Window

Figure 1-1 shows the results from a typical search. The application found two devices. The USB connection for a U3 has been selected in Figure 1-1, bringing up the configuration window on the right side.

- Refresh: Reload the window using values read from the device.
- Write Values: Write the Local ID from the window to the device.
- Config Defaults: Opens the window shown in Figure 1-2.
- Reset: Click to reset the selected device.
- Test: Opens the window shown in Figure 1-3. Starts with Factory Default settings.

Config Defaults:

This option provides control over the condition of the device at power-up or reset. Figure 1-2 shows a U3-HV with the factory default power-up configuration, which means AIN0-AIN3 set to analog input, FIO4 to CIO3 set to digital input, analog outputs set to minimum voltage (near 0), and timers/counters/watchdog disabled.

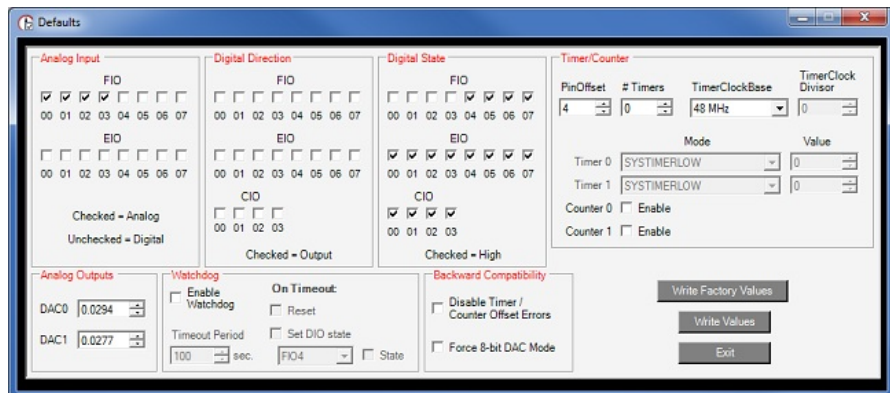


Figure 1-2. LJControlPanel U3 Configure Defaults Window

- Write Factory Values: Clicking this will set everything back to the factory defaults and write those factory defaults to nonvolatile memory.
- Write Values: Change any desired settings, and then click this to write the new settings to nonvolatile memory.

Test Panel:

Figure 1-3 shows the test window for a U3 device. This window continuously (once per second) writes to and reads from the selected LabJack.

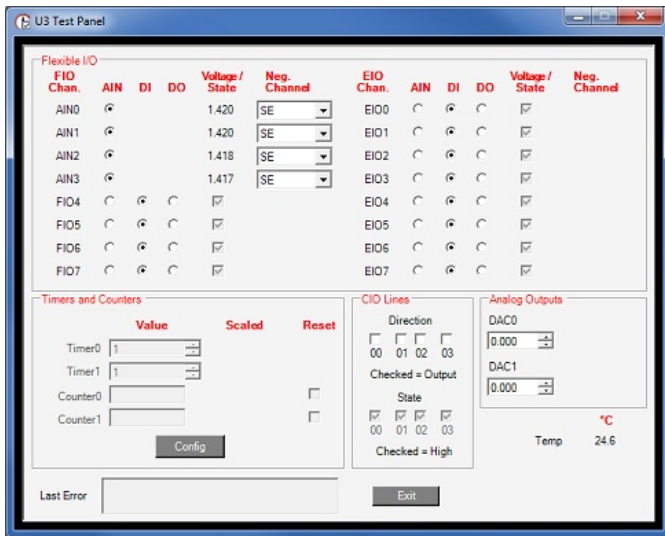


Figure 1-3. LJControlPanel U3 Test Window

Any configuration done on this screen is not written to nonvolatile memory. These settings just affect the current condition of the device, not the reset/power-up condition.

When the test panel first loads it sets everything to factory default, so previous settings (or reset/power-up settings) will not be shown.

LJCP Settings:

Selecting Options=>Settings from the main LJControlPanel menu brings up the window shown in Figure 1-4. This window allows some features to of the LJControlPanel application to be customized.

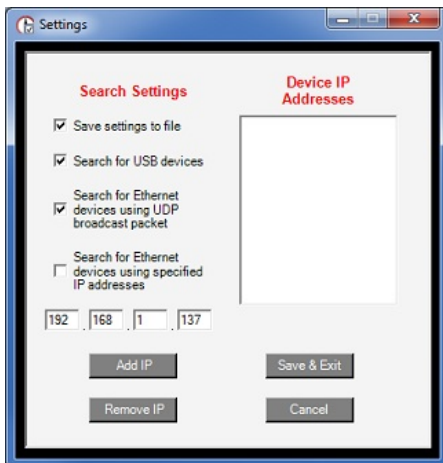


Figure 1-4. LJControlPanel Settings Window

Search for USB devices: If selected, LJControlPanel will include USB when searching for devices.

Search for Ethernet devices using UDP broadcast packet: Only applies to UE9 device.

Search for Ethernet devices using specified IP addresses: Only applies to UE9 device.

1.2 - Self-Upgrade Application (LJSelfUpgrade) [U3 Datasheet]

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The processor in the U3 has field upgradeable flash memory. The self-upgrade application shown in Figure 1-5 programs the latest firmware onto the processor.

USB is the only interface on the U3, and first found is the only option for self-upgrading the U3, so no changes are needed in the "Connect by" box. There must only be one U3 connected to the PC when running LJSelfUpgrade.

Click on "Get Version Numbers" to find out the current firmware versions on the device. Then use the provided [Firmware Upgrade File](#) internet link to go to labjack.com, go to the U3 Firmware subsection, and check for more recent firmware. Download firmware files to the any location on your computer.

Click the Browse button and select the firmware file to program. Click the Program button to begin the self-upgrade process.

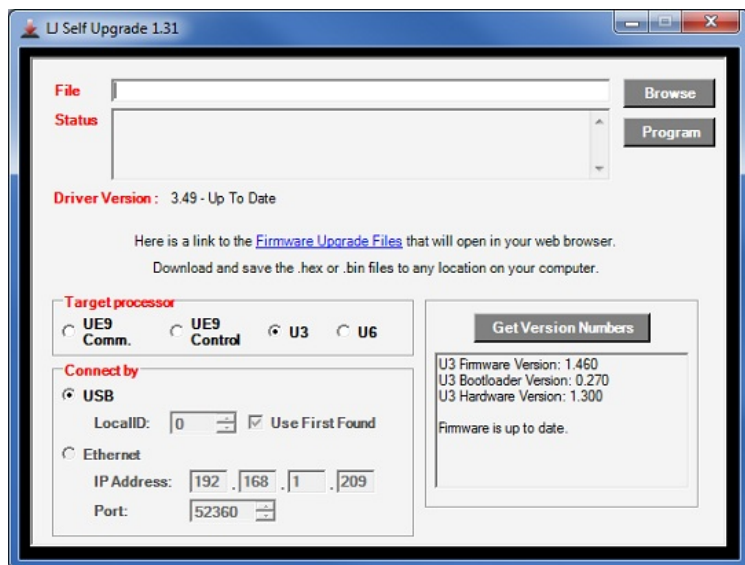


Figure 1.2-1. Self-Upgrade Application

If problems are encountered during programming, try the following:

1. Unplug the U3, wait 5 seconds then reconnect the U3. Click OK then press program again.
2. If step 1 does not fix the problem, unplug the U3 and watch the LED while plugging the U3 back in. Follow the following steps based on the LED's activity:
 - A. **If the LED is blinking continuously (flash mode)** connect a jumper between FIO4 and SPC (FIO0 to SCL on U3 1.20/1.21), then unplug the U3, wait 5 seconds and plug the U3 back in. Try programming again (disconnect the jumper before programming).
 - B. **If the LED blinks several times and stays on** connect a jumper between FIO5 and SPC (FIO1 to SCL on U3 1.20/1.21), then unplug the U3, wait 5 seconds and plug the U3 back in. Try programming again (disconnect the jumper before programming).
 - C. **If the LED blinks several times and stays off** the U3 is not enumerating. Please restart your computer and try to program again.
 - D. **If there is no LED activity**, connect a jumper between FIO5 and SPC (FIO1 to SCL on U3 1.20/1.21), then unplug the U3, wait 5 seconds and plug the U3 back in. If the LED is blinking continuously click OK and program again (after removing the jumper). If the LED does not blink connect a jumper between FIO4 and SPC (FIO0 to SCL on U3 1.20/1.21), then unplug the U3, wait 5 seconds and plug the U3 back in.
 - E. **If the LED does a repeating pattern of 3 blinks then pause** the U3 has detected internal memory corruption and you will have to contact LabJack Support.
3. If there is no activity from the U3's LED after following the above steps, please contact support.

2 - Hardware Description [U3 Datasheet]

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Hardware Description Overview

The U3 has 3 different I/O areas:

- Communication Edge,
- Screw Terminal Edge,
- DB Edge.

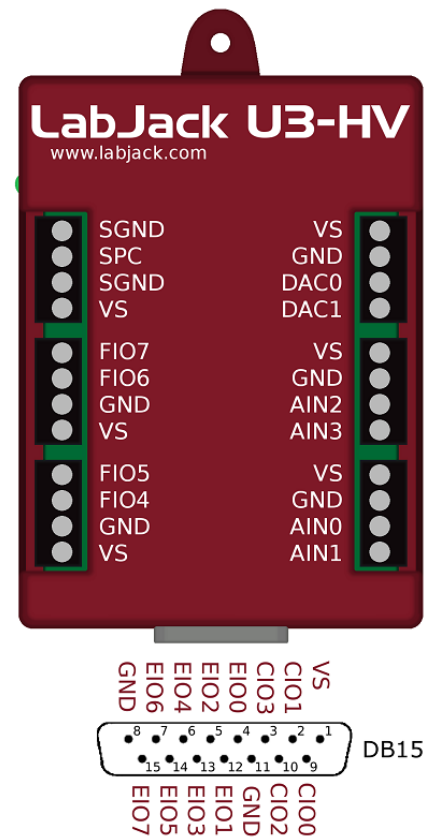
The communication edge has a USB type B connector (with black cable connected in Figure 2-1). All power and communication is handled by the USB interface.

The screw terminal edge has convenient connections for the analog outputs and 8 flexible I/O (digital I/O, analog inputs, timers, or counters). The screw terminals are arranged in blocks of 4, with each block consisting of Vs, GND, and two I/O. There is also a status LED located on the left edge.

The DB Edge has a D-sub type connectors called DB15 which has the 8 EIO lines and 4 CIO lines. The EIO lines are flexible like the FIO lines, while the CIO are dedicated digital I/O.



Figure 2-1. LabJack U3



2.1 - USB [U3 Datasheet]

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For information about USB installation, see [Section 1](#).

The U3 has a full-speed USB connection compatible with USB version 1.1 or 2.0. This connection provides communication and power (Vusb). USB ground is connected to the U3 ground (GND), and USB ground is generally the same as the ground of the PC chassis and AC mains.

The details of the U3USB interface are handled by the high level drivers (Windows LabJackUDDLL), so the following information is really only needed when developing low-level drivers.

The LabJack vendor ID is 0x0CD5. The product ID for the U3 is 0x0003.

The USB interface consists of the normal bidirectional control endpoint (0OUT & IN), 3 used bulk endpoints (1OUT, 2 IN, 3 IN), and 1 dummy endpoint (3 OUT). Endpoint 1 consists of a 64 byte OUT endpoint (address = 0x01). Endpoint 2 consists of a 64 byte IN endpoint (address = 0x82). Endpoint 3 consists of a dummy OUT endpoint (address = 0x03) and a 64 byte IN endpoint (address = 0x83). Endpoint 3 OUT is not supported by the firmware, and should never be used.

All commands should always be sent on Endpoint 1, and the responses to commands will always be on Endpoint 2. Endpoint 3 is only used to send stream data from the U3 to the host.

2.2 - Status LED [U3 Datasheet]

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There is a green status LED on the LabJack U3. This LED blinks on reset, and then remains steadily lit. Other LED behavior is generally related to flash upgrade modes ([Section 1.2](#)).

Normal power-up Status LED behavior: When the USB cable is connected to the U3, the Status LED blinks 5-6 times over 2 seconds and then remains solid on.

Normal power-up Status LED behavior but device keeps rebooting, even with no connections besides USB: You might notice this because you see the normal power-up LED behavior happening repeatedly and perhaps also hear the host making noises as the device disconnects and reconnects. Likely indicates that the Watchdog is resetting the device. To reset to factory default values (i.e. disable watchdog), power up the U3 with a jumper securely installed from FIO6 to SPC (FIO2 to SCL on U3 1.20/1.21), then remove the jumper and power cycle the device again.

Status LED blinking continuously at about 4 Hz, even with no software running: This indicates that the U3 is in flash mode. See [Section 1.2](#) and reprogram the device.

Other or no Status LED behavior, even with no connections besides USB : Try using the different jumpers and reprogramming as described in [Section 1.2](#). If you get no LED activity with any jumpers, but know the USB port is good and providing 5 volts, the device is likely damaged beyond field repair possibility ([returns, exchanges, and repairs](#)). Also see item F in the [USB Communication Failure App Note](#).

2.3 - GND and SGND [U3 Datasheet]

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The GND connections available at the screw-terminals and DB connectors provide a common ground for all LabJack functions. All GND terminals are the same and connect to the same ground plane. This ground is the same as the ground line on the USB connection, which is often the same as ground on the PC chassis and therefore AC mains ground.

SGND is located near the upper-left of the device. This terminal has a self-resetting thermal fuse in series with GND. This is often a good terminal to use when connecting the ground from another separately powered system that could unknowingly already share a common ground with the U3.

See the AIN, DAC, and Digital I/O Sections for more information about grounding.

2.4 - VS [U3 Datasheet]

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The VS terminals are designed as outputs for the internal supply voltage (nominally 5 volts). This will be the voltage provided from the USB cable. The VS connections are outputs, not inputs. Do not connect a power source to VS in normal situations. All VS terminals are the same.

Related specifications can be found in [Appendix A](#).

For information about powering the U3 from a source other than USB, see the OEM information in [Section 2.12](#).

What if some VS terminals are not working? We hear this every so often, but since the terminals are all simply soldered into the same VS plane on the PCB it is almost impossible for some to work and some not. Almost every time the actual problem is improper use of the screw terminals. The only guaranteed connection with the screw terminals is when a conductor is securely clamped inside the terminal. For example, touching DMM probes to the screw head of a loose terminal rarely provides a valid contact.

2.5 - Flexible I/O (FIO/EIO) [U3 Datasheet]

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The FIO and EIO ports on the LabJack U3 can be individually configured as digital input, digital output, or analog input. This is FIO0-EIO7 on the U3-LV (16 lines), or FIO4-EIO7 on the U3-HV (12 lines). In addition, up to 2 of these lines can be configured as timers, and up to 2 of these lines can be configured as counters. If a line is configured as analog, it is called AINx according to the following table:

Table 2.5-1. Analog Input Pin Locations

AIN0	FIO0	AIN8	EIO0
AIN1	FIO1	AIN9	EIO1
AIN2	FIO2	AIN10	EIO2
AIN3	FIO3	AIN11	EIO3
AIN4	FIO4	AIN12	EIO4
AIN5	FIO5	AIN13	EIO5
AIN6	FIO6	AIN14	EIO6
AIN7	FIO7	AIN15	EIO7

On the U3-HV, compared to the -LV, the first four flexible I/O are fixed as analog inputs (AIN0-AIN3) with a nominal ± 10 volt input range. All digital operations, including analog/digital configuration, are ignored on these 4 fixed analog inputs.

Timers and counters can appear on various pins, but other I/O lines never move. For example, Timer1 can appear anywhere from FIO4 to EIO1, depending on TimerCounterPinOffset and whether Timer0 is enabled. On the other hand, FIO5 (for example), is always on the screw terminal labeled FIO5, and AIN5 (if enabled) is always on that same screw terminal.

The first 8 flexible I/O lines (FIO0-FIO7) appear on built-in screw terminals. The other 8 flexible I/O lines (EIO0-EIO7) are available on the DB15 connector.

Many software applications will need to initialize the flexible I/O to a known pin configuration. That requires calls to the low-level functions ConfigIO and ConfigTimerClock. Following are the values to set the pin configuration to the factory default state:

Table 2.5-2. ConfigIO Factory Default Values

Byte #			
6	WriteMask	15	Write all parameters
8	TimerCounterConfig	0	No Timers/Counters. Offset = 4
9	DAC1 Enable	0	DAC1 Disabled. (Ignored on HW 1.3)

10	FIOAnalog	0	FIO all digital.
11	EIOAnalog	0	EIO all digital.

Table 2.5-3. ConfigTimerClock Factory Default Values

Byte #			
8	TimerClockConfig	130	Set clock to 48MHz.
9	TimerClockDivisor	0	Divisor = 0.

When using the high-level LabJackUD driver, this could be done with the following requests:

```
ePut (InqHandle, LJ_ioPUT_CONFIG, LJ_chNUMBER_TIMERS_ENABLED, 0, 0);
ePut (InqHandle, LJ_ioPUT_CONFIG, LJ_chTIMER_COUNTER_PIN_OFFSET, 4, 0);
ePut (InqHandle, LJ_ioPUT_CONFIG, LJ_chTIMER_CLOCK_BASE, LJ_tc48MHZ, 0);
ePut (InqHandle, LJ_ioPUT_CONFIG, LJ_chTIMER_CLOCK_DIVISOR, 0, 0);
ePut (InqHandle, LJ_ioPUT_COUNTER_ENABLE, 0, 0, 0);
ePut (InqHandle, LJ_ioPUT_COUNTER_ENABLE, 1, 0, 0);
ePut (InqHandle, LJ_ioPUT_DAC_ENABLE, 1, 0, 0); //Ignored on hardware rev 1.30+.
ePut (InqHandle, LJ_ioPUT_ANALOG_ENABLE_PORT, 0, 0, 16);
```

... or with a single request to the following IOType created exactly for this purpose:

```
ePut (InqHandle, LJ_ioPIN_CONFIGURATION_RESET, 0, 0, 0);
```

2.6 - AIN [U3 Datasheet]

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AIN Overview

The LabJack U3 has up to 16 analog inputs available on the flexible I/O lines (FIO0-FIO7 and EIO0-EIO7). Single-ended measurements can be taken of any line compared to ground, or differential measurements can be taken of any line to any other line.

Analog input resolution is 12-bits. The range of single-ended analog inputs is normally about 0-2.44, and there is a “special” 0-3.6 volt range available. The range of differential analog inputs is typically ± 2.4 volts, but is pseudobipolar, not true bipolar. The difference (positive channel minus negative channel) can be -2.4 volts, but neither input can have a voltage less than -0.3 volts to ground. For valid measurements, the voltage on every low-voltage analog input pin, with respect to ground, must be within -0.3 to +3.6 volts. See [Appendix A](#) for voltage limits to avoid damage.

On the U3-HV, compared to the -LV, the first four flexible I/O are fixed as analog inputs (AIN0-AIN3), and have scaling such that the input range is a true bipolar ± 10 volts normally, and -10 to +20 volts when using the “special” range. The input impedance of these four lines is roughly 1 M Ω , which is good, but less than the normal low voltage analog inputs. Analog/digital configuration and all other digital operations on these pins are ignored. FIO4-EIO7 are still available as flexible I/O, same as the U3-LV.

To get the special 0-3.6 volt or -10/+20 volt range, you do a differential reading request with the negative channel set to 32. The U3 will then do a special range single-ended reading on the specified positive channel. The U3 is using differential ADC techniques internally, but the external reading is single-ended.

Because the scaling on the high-voltage inputs on the U3-HV (AIN0-AIN3) is inherently single-ended, a factory calibration is not possible for differential readings. If a differential reading is requested where either channel is a high-voltage channel, the driver will return the raw binary reading and the user must handle calibration/conversion. Note that 0 counts is about -20V and 65520 counts is about +20V, and no high-voltage channel can be less than -12.8V or more than +20.1V.

The analog inputs have a QuickSample option where each conversion is done faster at the expense of increased noise. This is enabled by passing a nonzero value for put_config special channel *LJ_chAIN_RESOLUTION*. There is also a LongSettling option where additional settling time is added between the internal multiplexer configuration and the analog to digital conversion. This allows signals with more source impedance, and is enabled by passing a nonzero value for put_config special channel *LJ_chAIN_SETTLING_TIME*. Both of these options are disabled by default. This applies to command/response mode only, and the resulting typical data rates are discussed in [Section 3.1](#). For stream mode, see [Section 3.2](#).

Note that sinking excessive current into digital outputs can cause substantial errors in analog input readings. See [Section 2.8.1.4](#) for more info.

2.6.1 - Channel Numbers [U3 Datasheet]

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The LabJack U3 has up to 16 external analog inputs, plus a few internal channels. The low-level functions specify a positive and negative channel for each analog input conversion. With the LabJackUD driver, the IOType *LJ_ioGET_AIN* is used for single-ended channels only, and thus the negative channel is internally set to 31. There is an additional IOType called *LJ_ioGET_AIN_DIFF* that allows the user to specify the positive and negative channel.

Table 2.6.1-1. Positive Channel Numbers

Positive Channel #	
0-7	AIN0-AIN7 (FIO0-FIO7)
8-15	AIN8-AIN15 (EIO0-EIO7)
30	Temp Sensor
31	Vreg
193-241	DI, Timers, Counters

Table 2.6.1-2. Negative Channel Numbers

Negative Channel #	
0-7	AIN0-AIN7 (FIO0-FIO7)
8-15	AIN8-AIN15 (EIO0-EIO7)
30	Vref
31 or 199	Single-Ended
32	Special 0-3.6 or -10/+20 (UD Only)

Positive channel 31 puts the internal Vreg (~3.3 volts) on the positive input of the ADC. See [Section 2.6.4](#) for information about the internal temperature sensor.

If the negative channel is set to anything besides 31/199, the U3 does a differential conversion and returns a pseudobipolar value. If the negative channel is set to 31/199, the U3 does a single-ended conversion and returns a unipolar value. Channel 30 puts the internal voltage reference Vref (~2.44 volts) on the negative input of the ADC.

Channel 32 is a special negative channel supported by the LabJack UD driver. When used, the driver will actually pass 30 as the negative channel to the U3, and when the result is returned the driver adds Vref to the value. For a low-voltage analog input this results in a full span on the positive channel of about 0 to 4.88 volts (versus ground), but since the voltage on any analog input cannot exceed 3.6 volts, only 75% of the converter's range is used and the span is about 0 to 3.6 volts. For a high-voltage analog input, channel 32 (special range) results in a span of about -10 to +20 volts.

In the U3 examples that accompany the [Exodriver](#), u3.c also supports channel 32 in calls to `eAIN()`.

Channel 32 is also supported in [LabJackPython](#):

```
# On the U3, wire a jumper from DAC0 to FIO0, then run:
>>> import u3
>>> d = u3.U3()
>>> d.configIO(FIOAnalog = 1) # Set FIO0 to analog
>>> d.writeRegister(5000, 3) # Set DAC0 to 3 V
>>> d.getAIN(0, 32)
3.0141140941996127
```

For the four high-voltage channels on the U3-HV, the special channel negative channel also puts Vref on the negative. This results in an overall range of about -10 to +20 volts on the positive input.

Channels 193 to 241 are special analog input channel numbers that read digital inputs, timers or counters. See [Section 3.2.1](#) for more information.

2.6.2 - Converting Binary Readings to Voltages [U3 Datasheet]

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Converting Binary Readings to Voltages Overview

This information is only needed when using low-level functions and other ways of getting binary readings. Readings in volts already have the calibration constants applied. The UD driver, for example, normally returns voltage readings unless binary readings are specifically requested.

Following are the nominal input voltage ranges for the low-voltage analog inputs. This is all analog inputs on the U3-LV, and AIN4-AIN15 on the U3-HV.

Table 2.6.2-1. Nominal Analog Input Voltage Ranges for Low-Voltage Channels

	Max V	Min V
Single-Ended	2.44	0
Differential	2.44	-2.44

Table 2.6.2-2. Nominal Analog Input Voltage Ranges for High-Voltage Channels

	Max V	Min V
Single-Ended	10.3	-10.3
Differential	N/A	N/A
Special - 10/+20	20.1	-10.3

Note that the minimum differential input voltage of -2.44 volts means that the positive channel can be as much as 2.44 volts less than the negative channel, not that a channel can measure 2.44 volts less than ground. The voltage of any low-voltage analog input pin, compared to ground, must be in the range -0.3 to +3.6 volts.

The “special” range (0-3.6 on low-voltage channels and -10/+20 volts on high-voltage channels) is obtained by doing a differential measurement where the negative channel is set to the internal Vref (2.44 volts). For low-voltage channels, simply do the low-voltage differential conversion as described below, then add the stored Vref value. For high-voltage channels, do the same thing, then multiply by the proper high-voltage slope, divide by the single-ended low-voltage slope, and add the proper high-voltage offset. The UD driver handles these conversions automatically.

Although the binary readings have 12-bit resolution, they are returned justified as 16-bit values, so the approximate nominal conversion from binary to voltage is:

$$\text{Volts(uncalibrated)} = (\text{Bits}/65536) * \text{Span (Single-Ended)}$$

$$\text{Volts(uncalibrated)} = (\text{Bits}/65536) * \text{Span} - \text{Span}/2 \text{ (Differential)}$$

Binary readings are always unsigned integers.

Where span is the maximum voltage minus the minimum voltage from the tables above. The actual nominal conversions are provided in the tables below, and should be used if the actual calibration constants are not read for some reason. Most applications will use the actual calibrations constants (Slope and Offset) stored in the internal flash.

$$\text{Volts} = (\text{Slope} * \text{Bits}) + \text{Offset}$$

Since the U3 uses multiplexed channels connected to a single analog-to-digital converter (ADC), all low-voltage channels have the same calibration for a given configuration. High-voltage channels have individual scaling circuitry out front, and thus the calibration is unique for each channel.

See [Section 5.4](#) for detail about the location of the U3 calibration constants.

2.6.2.1 - Analog Inputs With DAC1 Enabled (Hardware Revisions 1.20 & 1.21 only) [U3 Datasheet]

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This Section only applies to the older hardware revisions 1.20 and 1.21. Starting with hardware revision 1.30, DAC1 is always enabled and does not affect the analog inputs.

The previous information assumed that DAC1 is disabled. If DAC1 is enabled, then the internal reference (Vref = 2.44 volts) is not available for the ADC, and instead the internal regulator voltage (Vreg = 3.3 volts) is used as the reference for the ADC. Vreg is not as stable as Vref, but more stable than Vs (5 volt power supply). Following are the nominal input voltage ranges for the analog inputs, assuming that DAC1 is enabled.

Table 2.6.2.1-1. Nominal Analog Input Voltage Ranges (DAC1 Enabled)

	Max V	Min V
Single-Ended	3.3	0
Differential	3.3	-3.3
Special - 10/+20	N/A	N/A

Note that the minimum differential input voltage of -3.3 volts means that the positive channel can be as much as 3.3 volts less than the negative channel, not that a channel can measure 3.3 volts less than ground. The voltage of any analog input pin, compared to ground, must be in the range -0.3 to +3.6 volts, for specified performance. See Appendix A for voltage limits to avoid damage.

Negative channel numbers 30 and 32 are not valid with DAC1 enabled.

When DAC1 is enabled, the slope/offset calibration constants are not used to convert raw readings to voltages. Rather, the Vreg value is retrieved from the Mem area, and used with the approximate single-ended or differential conversion equations above, where Span is Vreg (single-ended) or 2Vreg (differential).

2.6.3 - Typical Analog Input Connections [U3 Datasheet]

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Typical Analog Input Connections Overview

A common question is "can this sensor/signal be measured with the U3". Unless the signal has a voltage (referred to U3 ground) beyond the limits in [Appendix A](#), it can be connected without damaging the U3, but more thought is required to determine what is necessary to make useful measurements with the U3 or any measurement device.

Voltage (versus ground): The single-ended analog inputs on the U3 measure a voltage with respect to U3 ground. The differential inputs measure the voltage difference between two channels, but the voltage on each channel with respect to ground must still be within the common mode limits specified in [Appendix A](#). When measuring parameters other than voltage, or voltages too big or too small for the U3, some sort of sensor or transducer is required to produce the proper voltage signal. Examples are a temperature sensor, amplifier, resistive voltage divider, or perhaps a combination of such things.

Impedance: When connecting the U3, or any measuring device, to a signal source, it must be considered what impact the measuring device will have on the signal. The main consideration is whether the currents going into or out of the U3 analog input will cause noticeable voltage errors due to the impedance of the source. To maintain consistent 12-bit results, it is recommended to keep the source impedance within the limits specified in [Appendix A](#).

Resolution (and Accuracy): Based on the measurement type and resolution of the U3, the resolution can be determined in terms of voltage or engineering units. For example, assume some temperature sensor provides a 0-10 mV signal, corresponding to 0-100 degrees C. Samples are then acquired with the U3 using the 0-2.44 volt single-ended input range, resulting in a voltage resolution of about $2.44/4096 = 596 \mu\text{V}$. That means there will be about 17 discrete steps across the 10 mV span of the signal, and the temperature resolution is about 6 degrees C. If this experiment required a resolution of 1 degrees C, this configuration would not be sufficient. Accuracy will also need to be considered. [Appendix A](#) places some boundaries on expected accuracy, but an in-system calibration can generally be done to provide absolute accuracy down to the linearity (INL) limits of the U3.

Speed: How fast does the signal need to be sampled? For instance, if the signal is a waveform, what information is needed: peak, averageRMS, shape, frequency, ... ? Answers to these questions will help decide how many points are needed per waveform cycle, and thus what sampling rate is required. In the case of multiple channels, the scan rate is also considered. See [Sections 3.1](#) and [3.2](#).

2.6.3.1 - Signal from the LabJack [U3 Datasheet]

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One example of measuring a signal from the U3 itself, is with an analog output. All I/O on the U3 share a common ground, so the voltage on an analog output (DAC) can be measured by simply connecting a single wire from that terminal to an AIN terminal (FIO/EIO). The analog output must be set to a voltage within the range of the analog input.

2.6.3.2 - Unpowered Isolated Signal [U3 Datasheet]

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An example of an unpowered isolated signal would be a photocell where the sensor leads are not shorted to any external voltages. Such a sensor typically has two leads, where the positive lead connects to an AIN terminal and the negative lead connects to a GND terminal.

2.6.3.3 - Signal Powered By the LabJack [U3 Datasheet]

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A typical example of this type of signal is a 3-wire temperature sensor. The sensor has a power and ground wire that connect to Vs and GND on the LabJack, and then has a signal wire that simply connects to an AIN terminal.

Another variation is a 4-wire sensor where there are two signal wires (positive and negative) rather than one. If the negative signal is the same as power ground, or can be shorted ground, then the positive signal can be connected to AIN and a single-ended measurement can be made. A typical example where this does not work is a bridge type sensor, such as pressure sensor, providing the raw bridge output (and no amplifier). In this case the signal voltage is the difference between the positive and negative signal, and the negative signal cannot be shorted to ground. Such a signal could be measured using a differential input on the U3.

2.6.3.4 - Signal Powered Externally [U3 Datasheet]

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An example is a box with a wire coming out that is defined as a 0-2 volt analog signal and a second wire labeled as ground. The signal is known to have 0-2 volts compared to the ground wire, but the complication is what is the voltage of the box ground compared to the LabJack ground.

If the box is known to be electrically isolated from the LabJack, the box ground can simply be connected to LabJackGND. An example would be if the box was plastic, powered by an internal battery, and does not have any wires besides the signal and ground which are connected to AINx and

GND on the LabJack.

If the box ground is known to be the same as the LabJackGND, then perhaps only the one signal wire needs to be connected to the LabJack, but it generally does not hurt to go ahead and connect the ground wire to LabJack GND with a 100 Ω resistor. You definitely do not want to connect the grounds without a resistor.

If little is known about the box ground, aDMM can be used to measure the voltage of box ground compared to LabJackGND. As long as an extreme voltage is not measured, it is generally OK to connect the box ground to LabJack GND, but it is a good idea to put in a 100 Ω series resistor to prevent large currents from flowing on the ground. Use a small wattage resistor (typically 1/8 or 1/4 watt) so that it blows if too much current does flow. The only current that should flow on the ground is the return of the analog input bias current, which is only microamps.

The SGND terminals (on the same terminal block asSPC) can be used instead of GND for externally powered signals. A series resistor is not needed as SGND is fused to prevent overcurrent, but a resistor will eliminate confusion that can be caused if the fuse is tripping and resetting.

In general, if there is uncertainty, a good approach is to use aDMM to measure the voltage on each signal/ground wire without any connections to the U3. If no large voltages are noted, connect the ground to U3 SGND with a 100 Ω series resistor. Then again use the DMM to measure the voltage of each signal wire before connecting to the U3.

Another good general rule is to use the minimum number of ground connections. For instance, if connecting 8 sensors powered by the same external supply, or otherwise referred to the same external ground, only a single ground connection is needed to the U3. Perhaps the ground leads from the 8 sensors would be twisted together, and then a single wire would be connected to a 100 Ω resistor which is connected to U3 ground.

2.6.3.5 - Amplifying Small Signal Voltages [U3 Datasheet]

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The best results are generally obtained when a signal voltage spans the full analog input range of the LabJack. If the signal is too small it can be amplified before connecting to the LabJack. One good way to handle low-level signals such as thermocouples is the [LJTick-InAmp](#), which is a 2-channel instrumentation amplifier module that plugs into the U3 screw-terminals.

For a do-it-yourself solution, the following figure shows an operational amplifier (op-amp) configured as non-inverting:

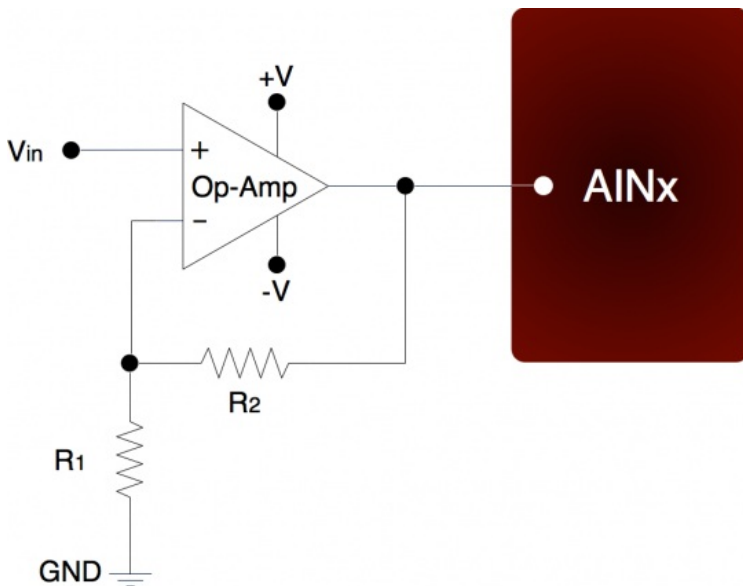


Figure 2.6.3.5-1. Non-Inverting Op-Amp Configuration

The gain of this configuration is:

$$V_{out} = V_{in} * (1 + (R_2/R_1))$$

100 k Ω is a typical value for R_2 . Note that if $R_2=0$ (short-circuit) and $R_1=\text{inf}$ (not installed), a simple buffer with a gain equal to 1 is the result.

There are numerous criteria used to choose an op-amp from the thousands that are available. One of the main criteria is that the op-amp can handle the input and output signal range. Often, a single-supply rail-to-rail input and output (RIRO) is used as it can be powered from V_s and GND and pass signals within the range 0- V_s . The OPA344 from Texas Instruments (ti.com) is good for many 5 volt applications.

The op-amp is used to amplify (and buffer) a signal that is referred to the same ground as the LabJack (single-ended). If instead the signal is differential (i.e. there is a positive and negative signal both of which are different than ground), an instrumentation amplifier (in-amp) should be used. An in-amp converts a differential signal to single-ended, and generally has a simple method to set gain.

2.6.3.6 - Signal Voltages Beyond 0-2.44 Volts (and Resistance Measurement) [U3 Datasheet]

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The normal input range for a low voltage analog input channel (FIO/EIO) on the U3 is about 0-2.44 volts. There is also a Special 0-3.6V range available on those inputs. The easiest way to handle larger voltages is often by using the [LJTick-Divider](#), which is a two channel buffered divider module that plugs into the U3 screw-terminals.

The basic way to handle higher unipolar voltages is with a resistive voltage divider. The following figure shows the resistive voltage divider assuming that the source voltage (V_{in}) is referred to the same ground as the U3 (GND).

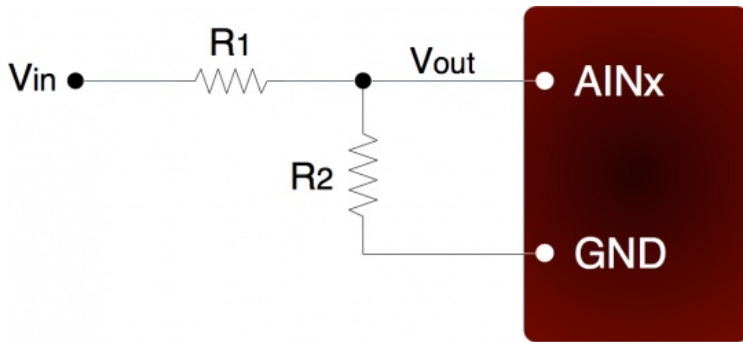


Figure 2.6.3.6-1. Voltage Divider Circuit

The attenuation of this circuit is determined by the equation:

$$V_{out} = V_{in} * (R2 / (R1 + R2))$$

This divider is easily implemented by putting a resistor ($R1$) in series with the signal wire, and placing a second resistor ($R2$) from the AIN terminal to a GND terminal. To maintain specified analog input performance, $R1$ should not exceed the values specified in Appendix A, so $R1$ can generally be fixed at the max recommended value and $R2$ can be adjusted for the desired attenuation.

The divide by 2 configuration where $R1 = R2 = 10\text{ k}\Omega$ (max source impedance limit for low-voltage channels), presents a $20\text{ k}\Omega$ load to the source, meaning that a 5 volt signal will have to be able to source/sink up to $+250\text{ }\mu\text{A}$. Some signal sources might require a load with higher resistance, in which case a buffer should be used. The following figure shows a resistive voltage divider followed by an op-amp configured as non-inverting unity-gain (i.e. a buffer).

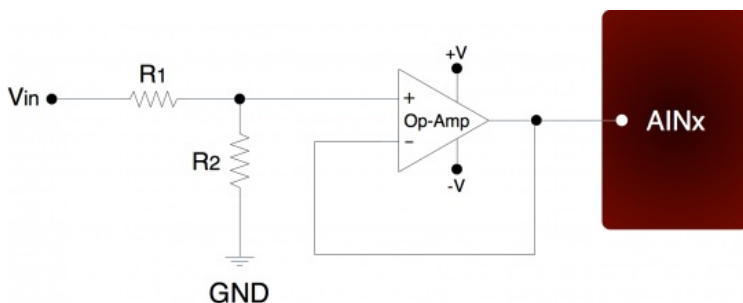


Figure 2.6.3.6-2. Buffered Voltage Divider Circuit

The op-amp is chosen to have low input bias currents so that large resistors can be used in the voltage divider. For 0-5 volt applications, where the amp will be powered from V_s and GND, a good choice would be the OPA344 from Texas Instruments (ti.com). The OPA344 has a very small bias current that changes little across the entire voltage range. Note that when powering the amp from V_s and GND, the input and output to the op-amp is limited to that range, so if V_s is 4.8 volts your signal range will be 0-4.8 volts.

To handle bipolar voltages, you also need offset or level-shifting. Refer to application note [SLOA097](#) from ti.com for more information.

The information above also applies to resistance measurement. A common way to measure resistance is to build a voltage divider as shown in Figure 2.6.3.6-1, where one of the resistors is known and the other is the unknown. If V_{in} is known and V_{out} is measured, the voltage divider equation can be rearranged to solve for the unknown resistance.

2.6.3.7 - Measuring Current (Including 4-20 mA) with a Resistive Shunt [U3 Datasheet]

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The best way to handle 4-20 mA signals is with the [LJTick-CurrentShunt](#), which is a two channel active current to voltage converter module that plugs into the UE9 screw-terminals.

The following figure shows a typical method to measure the current through a load, or to measure the 4-20 mA signal produced by a 2-wire (loop-powered) current loop sensor. The current shunt shown in the figure is simply a resistor.

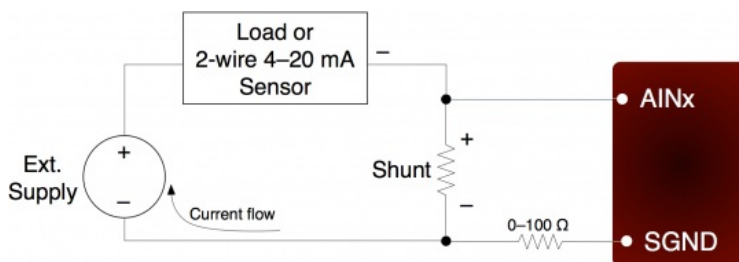


Figure 2.6.3.7-1. Current Measurement With Arbitrary Load or 2-Wire 4-20 mA Sensor

When measuring a 4-20 mA signal, a typical value for the shunt would be 120 Ω . This results in a 0.48 to 2.40 volt signal corresponding to 4-20 mA. The external supply must provide enough voltage for the sensor and the shunt, so if the sensor requires 5 volts the supply must provide at least 7.4 volts.

For applications besides 4-20 mA, the shunt is chosen based on the maximum current and how much voltage drop can be tolerated across the shunt. For instance, if the maximum current is 1.0 amp, and 1.0 volts of drop is the most that can be tolerated without affecting the load, a 1.0 Ω resistor could be used. That equates to 1.0 watts, though, which would require a special high wattage resistor. A better solution would be to use a 0.1 Ω shunt, and then use an amplifier to increase the small voltage produced by that shunt. If the maximum current to measure is too high (e.g. 100 amps), it will be difficult to find a small enough resistor and a Hall effect sensor should be considered instead of a shunt.

The following figure shows typical connections for a 3-wire 4-20 mA sensor. A typical value for the shunt would be 120 Ω which results in 0.48 to 2.40 volts.

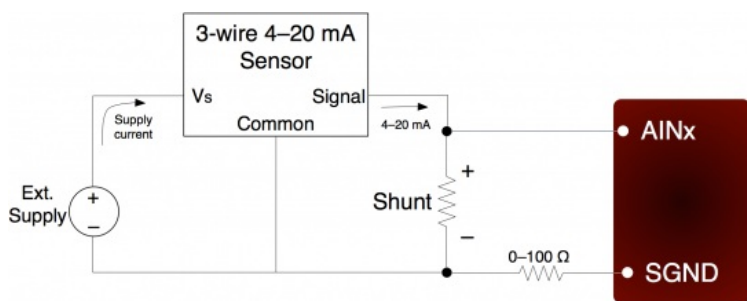


Figure 2.6.3.7-2. Current Measurement With 3-Wire 4-20 mA (Sourcing) Sensor

The sensor shown in the above figure is a sourcing type, where the signal sources the 4-20 mA current which is then sent through the shunt resistor and sunk into ground. Another type of 3-wire sensor is the sinking type, where the 4-20 mA current is sourced from the positive supply, sent through the shunt resistor, and then sunk into the signal wire. If sensor ground is connected to U3 ground, the sinking type of sensor presents a problem, as at least one side of the resistor has a high common mode voltage (equal to the positive sensor supply). If the sensor is isolated, a possible solution is to connect the sensor signal or positive sensor supply to U3 ground (instead of sensor ground). This requires a good understanding of grounding and isolation in the system. The LJTick-CurrentShunt is often a simple solution.

Both figures show a 0-100 Ω resistor in series with SGND, which is discussed in general in [Section 2.6.3.4](#). In this case, if SGND is used (rather than GND), a direct connection (0 Ω) should be good.

The best way to handle 4-20 mA signals is with the [LJTick-CurrentShunt](#), which is a two channel active current to voltage converter module that plugs into the U3 screw-terminals.

2.6.3.8 - Floating/Unconnected Inputs [U3 Datasheet]

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The reading from a floating (no external connection) analog input channel can be tough to predict and is likely to vary with sample timing and adjacent sampled channels. Keep in mind that a floating channel is not at 0 volts, but rather is at an undefined voltage. In order to see 0 volts, a 0 volt signal (such as GND) should be connected to the input.

Some data acquisition devices use a resistor, from the input to ground, to bias an unconnected input to read 0. This is often just for “cosmetic” reasons so that the input reads close to 0 with floating inputs, and a reason not to do that is that this resistor can degrade the input impedance of the analog input.

In a situation where it is desired that a floating channel read a particular voltage, say to detect a broken wire, a resistor (pull-down or pull-up) can be placed from the AINx screw terminal to the desired voltage (GND, VS, DACx, ...). A 100 k Ω resistor should pull the analog input readings to within 50 mV of any desired voltage, but obviously degrades the input impedance to 100 k Ω . For the specific case of pulling a floating channel to 0 volts, a 1 M Ω resistor to GND can typically be used to provide analog input readings of less than 50 mV. This information is for a low-voltage analog input channel on a U3.

Note that the four high-voltage channels on the U3-HV do sit at a predictable 1.4 volts. You can use a pull-down or pull-up resistor with the high-voltage inputs, but because their input impedance is lower the resistor must be lower (~1k might be typical) and thus the signal is going to have to drive substantial current.

2.6.3.9 - Signal Voltages Near Ground [U3 Datasheet]

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The nominal input range of a low-voltage single-ended analog input is 0-2.44 volts. So the nominal minimum voltage is 0.0 volts, but the variation in that minimum can be about +/-40 mV, and thus the actual minimum voltage could be 0.04 volts.

This is not an offset error, but just a minimum limit. Assume the minimum limit of your U3 happens to be 10 mV. If you apply a voltage of 0.02 volts it will read 0.02 volts. If you apply a voltage of 0.01 volts it will read 0.01 volts. If you apply a voltage less than 0.01 volts, however, it will still read the minimum limit of 0.01 volts in this case.

One impact of this, is that a short to GND is usually not a good test for noise and accuracy. We often use a 1.5 volt battery for simple tests.

If performance all the way to 0.0 is needed, use a differential reading (which is pseudobipolar). Connect some other channel to GND with a small jumper, and then take a differential reading of your channel compared to that grounded channel.

The nominal input range of a high-voltage single-ended analog input is +/-10 volts, so readings around 0.0 are right in the middle of the range and not an issue.

2.6.4 - Internal Temperature Sensor [U3 Datasheet]

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The U3 has an internal temperature sensor. Although this sensor measures the temperature inside the U3, which is warmer than ambient, it has been calibrated to read actual ambient temperature, although should only be expected to be accurate to within a few degrees C. For best results the temperature of the entire U3 must stabilize relative to the ambient temperature, which can take on the order of 1 hour. Best results will be obtained in still air in an environment with slowly changing ambient temperatures.

With the UD driver, the internal temperature sensor is read by acquiring single-ended analog input channel 30, and returns degrees K. Use channel 30 anywhere you would use an analog input channel (e.g. with eAIN).

2.7 - DAC [U3 Datasheet]

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DAC Overview

The LabJack U3 has 2 analog outputs (DAC0 and DAC1) that are available on the screw terminals. Each analog output can be set to a voltage between about 0.04 and 4.95 volts with 10 bits of resolution (8 bits on older hardware revision 1.20/1.21). The maximum output voltage is limited by the supply voltage to the U3.

Starting with hardware revision 1.30 (2008), DAC1 is always enabled and does not affect the analog inputs, but with older hardware the second analog output is only available in certain configurations. With hardware revisions <1.30, if the analog inputs are using the internal 2.4 volt reference (the most accurate option), then DAC1 outputs a fixed voltage of $1.5 \times V_{ref}$. Also with hardware revisions <1.30, if DAC1 is enabled the analog inputs use Vreg (3.3 volts) as the ADC reference, which is not as stable as the internal 2.4 volt reference.

The DAC outputs are derived as a percentage of Vreg, and then amplified by 1.5, so any changes in Vreg will have a proportionate affect on the DAC outputs. Vreg is more stable than Vs (5 volt supply voltage), as it is the output from a 3.3 volt regulator.

The DACs are derived from PWM signals converted to DC with a 2nd order low pass filter. With the default timer clock of 48 MHz, the PWM frequency is 732 Hz and the output filter with a 16 Hz cutoff frequency works great. If the timer clock is lowered, which might be done for desired behavior on Timer0 & Timer1 (Section 2.9), the DACs will have more noise where the frequency of the noise is the timer clock frequency divided by 65536. This effect is more exaggerated with the 10-bit DACs on hardware revision 1.30+, compared to the 8-bit DACs on previous hardware revisions. The noise with a timer clock of 48/12/4/1 MHz is roughly 5/20/100/600 mV. If lower noise performance is needed at lower timer clock frequencies, use the power-up default setting in LJControlPanel to force the device to use 8-bit DAC mode (uses the low-level CompatibilityOptions byte documented in Section 5.2.2), since the PWM frequency is clock/256 rather than clock/65536. A large capacitor (at least 220 μ F) from DACn to GND can also be used to reduce noise.

Each channel of PWM hardware used to create the DACs has 2 bytes available to hold the duty cycle value. With 8-bit DACs the hardware will store the current value and new value, and switch over to the new value right at the start of a new cycle, thus assuring glitch-free operation. With 10-bit DACs this is not possible, and the new value is applied immediately. That means that on a DAC update to a new value a rising edge could be skipped, which would result in 1 cycle of all low, which after the filter would result in a glitch that looks like a negative going spike of perhaps 100-200 mV. If this is not acceptable use 8-bit DAC mode as mentioned in the previous paragraph.

The analog outputs have 2nd order low pass output filters with a 3 dB cutoff around 16 Hz, limiting the frequency of output waveforms to less than that, and corresponding to a step response time constant of about 10 ms. The reason for 16 Hz is that the cutoff frequency needs to be at least this low to filter the 732 Hz (default) PWM source to an acceptable DC output.

The analog output commands are sent as raw binary values (low level functions). For a desired output voltage, the binary value can be approximated as:

Bits(uncalibrated) = (Volts/4.95)*256

For a proper calculation, though, use the calibration values (Slope and Offset) stored in the internal flash on the processor [Section 5.4](#):

$$\text{Bits} = (\text{Slope} * \text{Volts}) + \text{Offset}$$

The previous apply when using the original 8-bit DAC commands supported on all hardware versions. To take advantage of the 10-bit resolution on hardware revision 1.30, new commands have been added ([Section 5.2.5](#)) where the binary values are aligned to 16-bits. The calibration constants are still aligned to 8-bits, however, so the slope and offset should each be multiplied by 256 before using in the above formula.

The analog outputs can withstand a continuous short-circuit to ground, even when set at maximum output.

Voltage should never be applied to the analog outputs, as they are voltage sources themselves. In the event that a voltage is accidentally applied to either analog output, they do have protection against transient events such as ESD (electrostatic discharge) and continuous overvoltage (or undervoltage) of a few volts.

There is an accessory available from LabJack called the [LJTick-DAC](#) that provides a pair of 14-bit analog outputs with a range of ± 10 volts. The LJTick-DAC plugs into any digital I/O block, and thus up to 10 of these can be used per U3 to add 20 analog outputs. The LJTick-DAC has various improvements compared to the built-in DACs on the U3:

- Range of +10.0 to -10.0 volts.
- Resolution of 14-bits.
- Slew rate of 0.1 V/ μ s.
- Based on a reference, rather than regulator, so more accurate and stable.
- Does not affect analog inputs in any configuration.

2.7.1 - Typical Analog Output Connections [U3 Datasheet]

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2.7.1.1 - High Current Output [U3 Datasheet]

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The DACs on the U3 can output quite a bit of current, but they have 50 Ω of source impedance that will cause voltage drop. To avoid this voltage drop, an op-amp can be used to buffer the output, such as the non-inverting configuration shown in [Figure 2-3](#). A simple RC filter can be added between the DAC output and the amp input for further noise reduction. Note that the ability of the amp to source/sink current near the power rails must still be considered. A possible op-amp choice would be the TLV246x family (ti.com).

2.7.1.2 - Different Output Ranges [U3 Datasheet]

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See the end of this section for information about the [LJTick-DAC](#) which has a ± 10 V range.

The typical output range of the DACs is about 0.04 to 4.95 volts. For other unipolar ranges, an op-amp in the non-inverting configuration ([Figure 2.6-1](#)) can be used to provide the desired gain. For example, to increase the maximum output from 4.95 volts to 10.0 volts, a gain of 2.02 is required. If R2 (in [Figure 2-3](#)) is chosen as 100 k Ω , then an R1 of 97.6 k Ω is the closest 1% resistor that provides a gain greater than 2.02. The +V supply for the op-amp would have to be greater than 10 volts.

For bipolar output ranges, such as ± 10 volts, a similar op-amp circuit can be used to provide gain and offset, but of course the op-amp must be powered with supplies greater than the desired output range (depending on the ability of the op-amp to drive its outputs close to the power rails). If ± 10 , ± 12 , or ± 15 volt supplies are available, consider using the LT1490A op-amp (linear.com), which can handle a supply span up to 44 volts.

A reference voltage is also required to provide the offset. In the following circuit, DAC1 is used to provide a reference voltage. The actual value of DAC1 can be adjusted such that the circuit output is 0 volts at the DAC0 mid-scale voltage, and the value of R1 can be adjusted to get the desired gain. A fixed reference (such as 2.5 volts) could also be used instead of DAC1.

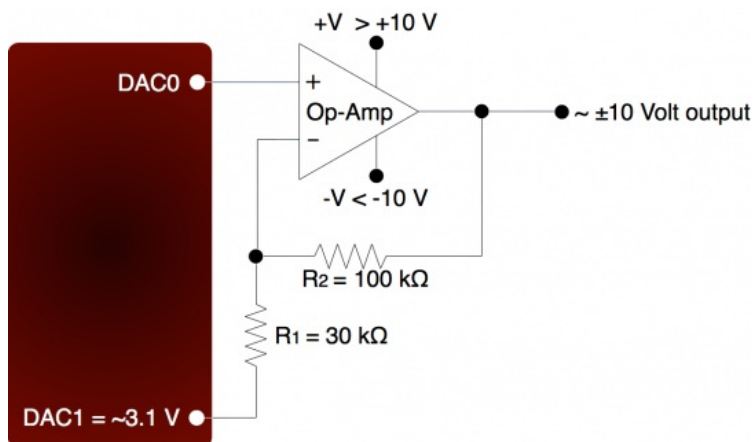


Figure 2.7.1.2-1. ±10 Volt DAC Output Circuit

A two-point calibration should be done to determine the exact input/output relationship of this circuit. Refer to application note SLOA097 from ti.com for further information about gain and offset design with op-amps.

LJTick-DAC:

There is an accessory available from LabJack called the **LJTick-DAC** that provides a pair of 14-bit analog outputs with a range of ± 10 volts. The LJTick-DAC plugs into any digital I/O block, and thus up to 10 of these can be used per U3 to add 20 analog outputs. The LJTick-DAC has various improvements compared to the built-in DACs on the U3:

- Range of +10.0 to -10.0 volts.
- Resolution of 14-bits.
- Slew rate of 0.1 V/ μ s.
- Based on a reference, rather than regulator, so more accurate and stable.
- Does not affect analog inputs in any configuration.

2.8 - Digital I/O [U3 Datasheet]

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Digital I/O Overview

The LabJack U3 has up to 20 digital I/O channels. 16 are available from the flexible I/O lines, and 4 dedicated digital I/O (CIO0-CIO3) are available on the DB15 connector. The first 4 lines, FIO0-FIO3, are unavailable on the U3-HV. Each digital line can be individually configured as input, output-high, or output-low. The digital I/O use 3.3 volt logic and are 5 volt tolerant.

The LabJackUD driver uses the following bit numbers to specify all the digital lines:

0-7 FIO0-FIO7 (0-3 unavailable on U3-HV)
 8-15 EIO0-EIO7
 16-19 CIO0-CIO3

The "F", "E", and "C" designators have little special meaning. They are just arbitrary letters used to designate different groupings of digital I/O. The generic designator DIOx is sometimes used to describe any digital I/O from 0 to 19, so for example an alternative name for EIO0 is DIO8.

The 8 FIO lines appear on the built-in screw-terminals, while the 8 EIO and 4 CIO lines appear only on the DB15 connector. See the DB15 Section of this User's Guide for more information.

Max Current & Overvoltage Protection

All the digital I/O include an internal series resistor that provides overvoltage/short-circuit protection. These series resistors also limit the ability of these lines to sink or source current. Refer to the specifications in [Appendix A](#).

Tri-State I/O

All digital I/O on the U3 have 3 possible states: input, output-high, or output-low. Each bit of I/O can be configured individually. When configured as an input, a bit has a ~ 100 k Ω pull-up resistor to 3.3 volts (all digital I/O are 5 volt tolerant). When configured as output-high, a bit is connected to the internal 3.3 volt supply (through a series resistor). When configured as output-low, a bit is connected to GND (through a series resistor).

When only lightly loaded, an input terminal will measure about 3.3 volts if measured with a DMM, and thus it can be tough to use a DMM to tell whether a line is set to input or output-high. A couple tips to tell the difference:

1. Look for a slight change where output-high measures a little higher. For example, a DMM might show 3.300 for input and 3.315 for output-high.
2. Add a load resistor. If you add a 100k from DIOx to GND, it should read about 1.6V for input and 3.3V for output-high.

5 Volt Output

The fact that the digital I/O are specified as 5-volt tolerant means that 5 volts can be connected to a digital input without problems (see the actual limits in the specifications in Appendix A). If 5 volts is needed from a digital output, consider the following solutions:

- Use the [LJTick-DigitalOut5V](#) to convert a pair of digital outputs to 5V logic.
- In some cases, an open-collector style output can be used to get a 5V signal. To get a low set the line to **output-low**, and to get a high set the line to **input** (... note that this does not work with timer outputs, e.g. PWM, as they toggle the line between output-low and output-high). When the line is set to input, the voltage on the line is determined by a pull-up resistor. The U3 has an internal ~100k resistor to 3.3V, but an external resistor can be added to a different voltage. Whether this will work depends on how much current the load is going to draw and what the required logic thresholds are. Say for example a 10k resistor is added from EIO0 to VS. EIO0 has an internal 100k pull-up to 3.3 volts and a series output resistance of about 180 ohms. Assume the load draws just a few microamps or less and thus is negligible. When EIO0 is set to input, there will be 100k to 3.3 volts in parallel with 10k to 5 volts, and thus the line will sit at about 4.85 volts. When the line is set to output-low, there will be 180 ohms in series with the 10k, so the line will be pulled down to about 0.1 volts.
- A surefire way to get 5 volts from a digital output is to add a simple logic buffer IC that is powered by 5 volts and recognizes 3.3 volts as a high input. Consider the CD74ACT541E from TI (or the inverting CD74ACT540E). All that is needed is a few wires to bring VS, GND, and the signal from the LabJack to the chip. This chip can level shift up to eight 0/3.3 volt signals to 0/5 volt signals and provides high output drive current (+/-24 mA).
- Note that the 2 DAC channels on the U3 can be set to 5 volts, providing 2 output lines with such capability.

Basic Tests

For basic testing use the Test Panel in LJControlPanel to change the state of a particular DIO while using a DMM to measure the voltage of that DIO versus GND. (Make sure [DMM leads are properly connected](#).)

Below are typical voltages with:

- No load
- 1k resistor from DIO to GND
- 1k resistor from DIO to VS

The values are a little different for FIO versus EIO/CIO as the latter has [less series impedance](#).

FIO		
	FIO vs GND with no load	
	Input	3.28 volts
	Output-low	0.02 volts
	Output-high	3.30 volts
	FIO vs GND with a 1k resistor installed FIO to GND	
	Input	0.025 volts
	Output-low	0.015 volts
	Output-high	2.2 volts
	FIO vs GND with a 1k resistor installed FIO to VS	
	Input	5.0 volts
	Output-low	1.7 volts
	Output-high	3.9 volts
EIO/CIO		
	EIO/CIO vs GND with no load	
	Input	3.29 volts
	Output-low	0.03 volts
	Output-high	3.30 volts

	EIO/CIO vs GND with a 1k resistor installed EIO/CIO to GND	
	Input	0.025 volts
	Output-low	0.027 volts
	Output-high	2.9 volts
	EIO/CIO vs GND with a 1k resistor installed EIO/CIO to VS	
	Input	5.0 volts
	Output-low	0.6 volts
	Output-high	3.6 volts

Boot-Up Defaults

The power-up condition of the digital I/O can be configured by the user with the "Config Defaults" option in LJControlPanel. From the factory, all digital I/O are configured to power-up as inputs. Note that even if the power-up default for a line is changed to output-high or output-low, there is a delay of about 5 ms at power-up where all digital I/O are in the factory default condition. For more information [see this forum topic](#).

Why Are My Digital I/O "High" at Boot-Up?

The implied question here is "why do my DIO boot up as output-high from the factory". The answer is that per the "Boot-Up Defaults" section above the factory default state for all DIO is input, but since inputs have a 100k internal pull-up they will read 3.3 volts if only lightly loaded. So likely you are seeing that the state of your DIO is input, not output-high. Also see the "Tri-State I/O" section above.

Making An Input Read Low By Default

If you want a floating digital input to read low, an external pull-down resistor can be added to overpower the internal 100k pull-up. 4.7k to 22k would be a typical range for this pull-down, with 10k being a solid choice for most applications.

Software Interface

The low-level Feedback function ([Section 5.2.5](#)) writes and reads all digital I/O. For information about using digital I/O under the Windows LabJackUD driver, see [Section 4.3.5](#). See [Section 3](#) for timing information.

Bit-Packed Integers

Many function parameters contain specific bits within a single integer parameter to write/read specific information. In particular, most digital I/O parameters contain the information for each bit of I/O in one integer, where each bit of I/O corresponds to the same bit in the parameter (e.g. the direction of FIO0 is set in bit 0 of parameter FIODir). For instance, in the low-level function ConfigU3, the parameter FIODirection is a single byte (8 bits) that writes/reads the power-up direction of each of the 8 FIO lines:

- if FIODirection is 0, all FIO lines are input,
- if FIODirection is 1 (2^0), FIO0 is output, FIO1-FIO7 are input,
- if FIODirection is 5 ($2^0 + 2^2$), FIO0 and FIO2 are output, all other FIO lines are input,
- if FIODirection is 255 ($2^0 + \dots + 2^7$), FIO0-FIO7 are output.

2.8.1 - Typical Digital I/O Connections [U3 Datasheet]

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2.8.1.1 - Input: Driven Signals [U3 Datasheet]

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The most basic connection to a U3 digital input is a driven signal, often called push-pull. With a push-pull signal the source is typically providing a high voltage for logic high and zero volts for logic low. This signal is generally connected directly to the U3 digital input, considering the voltage specifications in [Appendix A](#). If the signal is over 5 volts, it can still be connected with a series resistor. The digital inputs have protective devices that clamp the voltage at GND and VS, so the series resistor is used to limit the current through these protective devices. For instance, if a 24 volt signal is connected through a 22 k Ω series resistor, about 19 volts will be dropped across the resistor, resulting in a current of 0.9 mA, which is no problem for the U3. The series resistor should be 22 k Ω or less, to be strong enough to overpower the internal 100k pull-up and make sure the voltage on the I/O line when low is pulled below 0.8 volts.

The other possible consideration with the basic push-pull signal is the ground connection. If the signal is known to already have a common ground with the U3, then no additional ground connection is used. If the signal is known to not have a common ground with the U3, then the signal ground can simply be connected to U3 GND. If there is uncertainty about the relationship between signal ground and U3 ground (e.g. possible common ground through AC mains), then a ground connection with a $\sim 10\ \Omega$ series resistor is generally recommended (see [Section 2.6.3.4](#)).

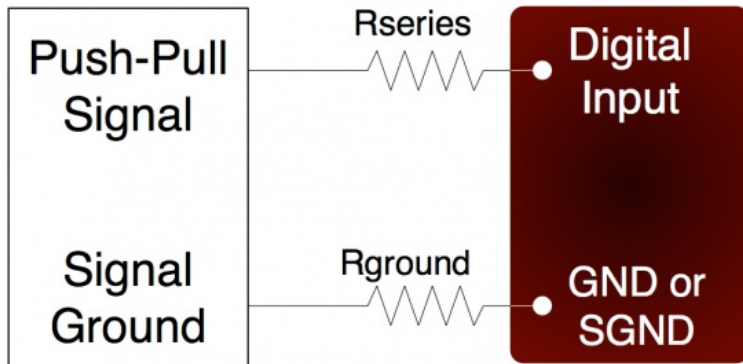


Figure 2.8.1.1-1. Driven Signal Connection To Digital Input

Figure 2.8.1.1-1 shows typical connections. R_{ground} is typically 0-100 Ω . R_{series} is typically 0 Ω (short-circuit) for 3.3/5 volt logic, or 22 k Ω (max) for high-voltage logic. Note that an individual ground connection is often not needed for every signal. Any signals powered by the same external supply, or otherwise referred to the same external ground, should share a single ground connection to the U3 if possible.

When dealing with a new sensor, a push-pull signal is often incorrectly assumed when in fact the sensor provides an open-collector signal as described next.

2.8.1.2 - Input: Open-Collector Signals [U3 Datasheet]

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For details about open-collector, open-drain, NPN, or PNP connections, see the [Open-Collector Signals App Note](#)

2.8.1.3 - Input: Mechanical Switch Closure [U3 Datasheet]

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To detect whether a mechanical switch (dry contact) is open or closed, connect one side of the switch to U3 ground and the other side to a digital input. The behavior is very similar to the open-collector described above.

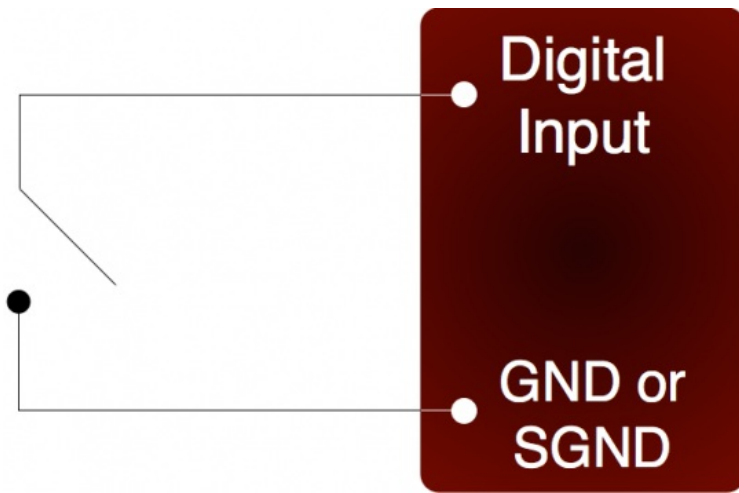


Figure 2.8.1.3-1. Basic Mechanical Switch Connection To Digital Input

When the switch is open, the internal 100 k Ω pull-up resistor will pull the digital input to about 3.3 volts (logic high). When the switch is closed, the ground connection will overpower the pull-up resistor and pull the digital input to 0 volts (logic low). Since the mechanical switch does not have any electrical connections, besides to the LabJack, it can safely be connected directly to GND, without using a series resistor or SGND.

When the mechanical switch is closed (and even perhaps when opened), it will bounce briefly and produce multiple electrical edges rather than a single high/low transition. For many basic digital input applications, this is not a problem as the software can simply poll the input a few times in succession to make sure the measured state is the steady state and not a bounce. For applications using timers or counters, however, this usually is a problem. The hardware counters, for instance, are very fast and will increment on all the bounces. Some solutions to this issue are:

- Software Debounce: If it is known that a real closure cannot occur more than once per some interval, then software can be used to limit the number of counts to that rate.
- Firmware Debounce: See [Section 2.9.1](#) for information about timer mode 6.
- Active Hardware Debounce: Integrated circuits are available to debounce switch signals. This is the most reliable hardware solution. See the MAX6816 (maxim-ic.com) or EDE2008 (elabinc.com).
- Passive Hardware Debounce: A combination of resistors and capacitors can be used to debounce a signal. This is not foolproof, but works fine in most applications.

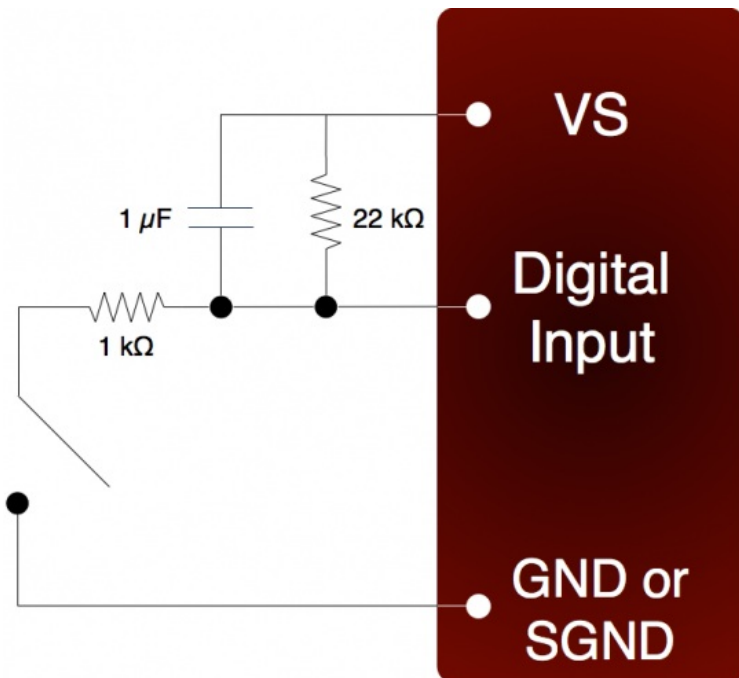


Figure 2.8-5. Passive Hardware Debounce

Figure 2.8-5 shows one possible configuration for passive hardware debounce. First, consider the case where the 1 k Ω resistor is replaced by a short circuit. When the switch closes it immediately charges the capacitor and the digital input sees logic low, but when the switch opens the capacitor slowly discharges through the 22 k Ω resistor with a time constant of 22 ms. By the time the capacitor has discharged enough for the digital input to see logic high, the mechanical bouncing is done. The main purpose of the 1 k Ω resistor is to limit the current surge when the switch is closed. 1 k Ω limits the maximum current to about 5 mA, but better results might be obtained with smaller resistor values.

2.8.1.4 - Output: Controlling Relays [U3 Datasheet]

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All the digital I/O lines have series resistance that restricts the amount of current they can sink or source, but solid-state relays (SSRs) can usually be controlled directly by the digital I/O. The SSR is connected as shown in the following diagram, where VS (~5 volts) connects to the positive control input and the digital I/O line connects to the negative control input (sinking configuration).

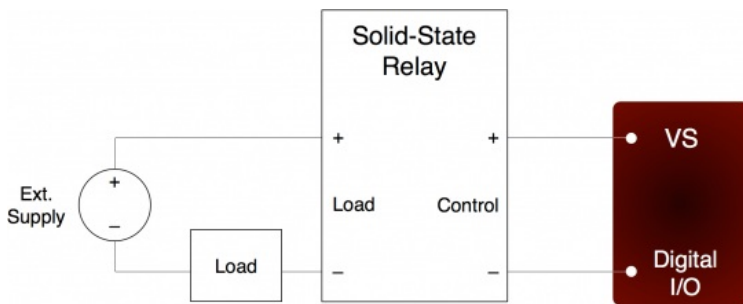


Figure 2.8.1.4-1. Relay Connections (Sinking Control, High-Side Load Switching)

When the digital line is set to output-low, control current flows and the relay turns on. When the digital line is set to input, control current does not flow and the relay turns off. When the digital line is set to output-high, some current flows, but whether the relay is on or off depends on the specifications of a particular relay. It is recommended to only use output-low and input.

For example, the Series 1 (D12/D24) or Series T (TD12/TD24) relays from Crydom specify a max turn-on of 3.0 volts, a min turn-off of 1.0 volts, and a nominal input impedance of 1500 Ω .

- When the digital line is set to output-low, it is the equivalent of a ground connection with 180 Ω (EIO/CIO) or 550 Ω (FIO) in series. When using an EIO/CIO line, the resulting voltage across the control inputs of the relay will be about $5 \times 1500 / (1500 + 180) = 4.5$ volts (the other 0.5 volts is dropped across the internal resistance of the EIO/CIO line). With an FIO line the voltage across the inputs of the relay will be about $5 \times 1500 / (1500 + 550) = 3.7$ volts (the other 1.3 volts are dropped across the internal resistance of the FIO line). Both of these are well above the 3.0 volt threshold for the relay, so it will turn on.
- When the digital line is set to input, it is the equivalent of a 3.3 volt connection with 100 k Ω in series. The resulting voltage across the control inputs of the relay will be close to zero, as virtually all of the 1.7 volt difference (between VS and 3.3) is dropped across the internal 100 k Ω resistance. This is well below the 1.0 volt threshold for the relay, so it will turn off.
- When the digital line is set to output-high, it is the equivalent of a 3.3 volt connection with 180 Ω (EIO/CIO) or 550 Ω (FIO) in series. When using an EIO/CIO line, the resulting voltage across the control inputs of the relay will be about $1.7 \times 1500 / (1500 + 180) = 1.5$ volts. With an FIO line the voltage across the inputs of the relay will be about $1.7 \times 1500 / (1500 + 550) = 1.2$ volts. Both of these are in the 1.0-3.0 volt region that is not defined for these example relays, so the resulting state is unknown.

Note that sinking excessive current into digital outputs can cause noticeable shifts in analog input readings. For example, the FIO sinking configuration above sinks about 2.4 mA into the digital output to turn the SSR on, which could cause a shift of roughly 1 mV to analog input readings.

Mechanical relays require more control current than SSRs, and cannot be controlled directly by the digital I/O on the U3. To control higher currents with the digital I/O, some sort of buffer is used. Some options are a discrete transistor (e.g. 2N2222), a specific chip (e.g. ULN2003), or an op-amp.

Note that the U3 DACs can source enough current to control almost any SSR and even some mechanical relays, and thus can be a convenient way to control 1 or 2 relays. With the DACs you would typically use a sourcing configuration (DAC/GND) rather than sinking (VS/DAC).

The RB12 relay board is a useful accessory available from LabJack. This board connects to the DB15 connector on the U3 and accepts up to 12 industry standard I/O modules (designed for Opto22 G4 modules and similar).

Another accessory available from LabJack is the LJTick-RelayDriver. This is a two channel module that plugs into the U3 screw-terminals, and allows two digital lines to each hold off up to 50 volts and sink up to 200 mA. This allows control of virtually any solid-state or mechanical relay.

2.9 - Timers/Counters [U3 Datasheet]

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Timers / Counters Overview

The U3 has 2 timers (Timer0-Timer1) and 2 counters (Counter0-Counter1). When any of these timers or counters are enabled, they take over an FIO/EIO line in sequence (Timer0, Timer1, Counter0, then Counter1), starting with FIO0+TimerCounterPinOffset. Some examples:

1 Timer enabled, Counter0 disabled, Counter1 disabled, and TimerCounterPinOffset=4:
FIO4=Timer0

1 Timer enabled, Counter0 disabled, Counter1 enabled, and TimerCounterPinOffset=6:
FIO6=Timer0
FIO7=Counter1

2 Timers enabled, Counter0 enabled, Counter1 enabled, and TimerCounterPinOffset=8:
EIO0=Timer0

EIO1=Timer1
 EIO2=Counter0
 EIO3=Counter1

Starting with hardware revision 1.30, timers/counters cannot appear on FIO0-3, and thus **TimerCounterPinOffset must be 4-8**. A value of 0-3 will result in an error. This error can be suppressed by a power-up default setting in LJControlPanel. If suppressed, a 0-3 will result in an offset of 4.

Timers and counters can appear on various pins, but other I/O lines never move. For example, Timer1 can appear anywhere from FIO4 to EIO1, depending on TimerCounterPinOffset and whether Timer0 is enabled. On the other hand, FIO5 (for example), is always on the screw terminal labeled FIO5, and AIN5 (if enabled) is always on that same screw terminal.

Applicable digital I/O are automatically configured as input or output as needed when timers and counters are enabled, and stay that way when the timers/counters are disabled.

Timers and counters use digital I/O hardware so the digital I/O specs from [Appendix A](#) apply. An input recognizes 0.0-0.8V as low and 2.0-5.8V as high, so rising and falling edges must transition between those levels. For more information about signal connections see [Section 2.8.1](#).

There are special channels numbers that can be used to read timer and counter values. These channel numbers can be used most places where you would use analog input channel numbers, such as LJLogUD and LJStreamUD. See [Section 3.2.1](#) for more information.

Timers

The timers (Timer0-Timer1) have various modes available. These are listed in the following table and more details can be found in [Section 2.9.1](#).

Table 2.9-1. U3 Timer Modes

Index (Low-level & UD)	
0	16-bit PWM output
1	8-bit PWM output
2	Period input (32-bit, rising edges)
3	Period input (32-bit, falling edges)
4	Duty cycle input
5	Firmware counter input
6	Firmware counter input (with debounce)
7	Frequency output
8	Quadrature input
9	Timer stop input (odd timers only)
10	System timer low read (default mode)
11	System timer high read
12	Period input (16-bit, rising edges)
13	Period input (16-bit, falling edges)
14	Line-to-Line input

Both timers use the same timer clock. There are 7 choices for the timer clock base:

Table 2.9-2. U3 Timer Clock Base Options

Index (Low-level/UD)	
0 / 20	4 MHz
1 / 21	12 MHz
2 / 22	48 MHz (default)
3 / 23	1 MHz /Divisor (Counter0 not available)
4 / 24	4 MHz /Divisor (Counter0 not available)
5 / 25	12 MHz /Divisor (Counter0 not available)
6 / 26	48 MHz /Divisor (Counter0 not available)

Note that these clocks apply to the U3 hardware revision 1.21+. With hardware revision 1.20 all clocks are half of the values above.

The first 3 clocks have a fixed frequency, and are not affected by TimerClockDivisor. The frequency of the last 4 clocks can be further adjusted by

TimerClockDivisor, but when using these clocks Counter0 is not available. When Counter0 is not available, it does not use an external FIO/EIO pin. The divisor has a range of 0-255, where 0 corresponds to a division of 256.

Note that the DACs ([Section 2.7](#)) are derived from PWM signals that are affected by the timer clock frequency. The default timer clock frequency of the U3 is set to 48 MHz, as this results in the minimum DAC output noise. If the frequency is lowered, the DACs will have more noise, where the frequency of the noise is the timer clock frequency divided by 2^{16} .

Counters

Each counter (Counter0 or Counter1) consists of a 32-bit register that accumulates the number of falling edges detected on the external pin. If a counter is reset and read in the same function call, the read returns the value just before the reset.

Although the timer clock frequency does not affect counters, note that Counter0 is not available with certain timer clock base frequencies (those that support a divisor). In such a case, it does not use an external FIO/EIO pin. An error will result if an attempt is made to enable Counter0 when one of these frequencies is configured. Similarly, an error will result if an attempt is made to configure one of these frequencies when Counter0 is enabled.

Usage

Usage for all timers & counters is pretty much the same. First you configure, then you read and/or write. The effect of the parameters can vary depending on timer mode, so see the [timer mode descriptions](#) for more details.

Test using the Test panel in LJControlPanel

Before attempting to use LJLogUD or your own software, first test using the Test panel in LJControlPanel. Configure timers & counters as desired and confirm the results are as expected.

Configure

To configure counters, write to the following:

CounterEnable
TimerCounterPinOffset

To configure timers, write to the following:

NumberTimersEnabled
TimerClockBase
TimerClockDivisor
TimerCounterPinOffset
TimerValue
TimerMode

There are 2 basic ways to configure:

1. Configure the power-up defaults of device as desired, then power cycle the device. On Windows this can be done using the Config Defaults screen in LJControlPanel.
2. Configure at the start of your program. For the Windows UD library see [Sections 4.3.6](#) and [4.3.9](#). Find any timer example for your programming language and it should demonstrate writes to the above parameters.

Read & Write

Depending on the timer/counter, you might want to do reads of TimerValue or do updates of TimerValue.

2.9.1 - Timer Mode Descriptions [U3 Datasheet]

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2.9.1.1 - PWM Output (16-Bit, Mode 0) [U3 Datasheet]

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Outputs a pulse width modulated rectangular wave output. Value passed should be 0-65535, and determines what portion of the total time is spent low (out of 65536 total increments). That means the duty cycle can be varied from 100% (0 out of 65536 are low) to 0.0015% (65535 out of 65536 are low).

The overall frequency of the PWM output is the clock frequency specified by TimerClockBase/TimerClockDivisor divided by 2^6 . The following table

shows the range of available PWM frequencies based on timer clock settings.

Table 2.9.1.1-1. 16-bit PWM Frequencies

PWM16 Frequency Ranges		
TimerClockBase	Divisor=1	Divisor=256
0 4 MHz	61.04	N/A
1 12 MHz	183.11	N/A
2 48 MHz (default)	732.42	N/A
3 1 MHz /Divisor	15.26	0.06
4 4 MHz /Divisor	61.04	0.238
5 12 MHz /Divisor	183.11	0.715
6 48 MHz /Divisor	732.42	2.861

Note that the clocks above apply to the U3 hardware revision 1.21. With hardware revision 1.20 all clocks are half of those values.

The same clock applies to all timers, so all 16-bit PWM channels will have the same frequency and will have their falling edges at the same time.

PWM output starts by setting the digital line to output-low for the specified amount of time. The output does not necessarily start instantly, but rather has to wait for the internal clock to roll. For 16-bit PWM output, the start delay varies from 0.0 to $\text{TimerClockDivisor} * 65536 / \text{TimerClockBase}$. For example, if $\text{TimerClockBase} = 48 \text{ MHz}$ and $\text{TimerClockDivisor} = 1$, PWM frequency is 732 Hz, PWM period is 1.4 ms, and the start delay will vary from 0 to 1.4 ms.

If a duty cycle of 0.0% (totally off) is required, consider using a simple inverter IC such as the CD74ACT540E from TI. Or you can switch the mode of the timer to some input mode, and add an external pull-down to hold the line low when set to input.

2.9.1.2 - PWM Output (8-Bit, Mode 1) [U3 Datasheet]

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Outputs a pulse width modulated rectangular wave output. Value passed should be 0-65535, and determines what portion of the total time is spent low (out of 65536 total increments). The lower byte is actually ignored since this is 8-bit PWM. That means the duty cycle can be varied from 100% (0 out of 65536 are low) to 0.4% (65280 out of 65536 are low).

The overall frequency of the PWM output is the clock frequency specified by $\text{TimerClockBase} / \text{TimerClockDivisor}$ divided by 2. The following table shows the range of available PWM frequencies based on timer clock settings.

Table 2.9.1.2-1. 8-bit PWM Frequencies

PWM8 Frequency Ranges		
TimerClockBase	Divisor=1	Divisor=256
0 4 MHz	15625	N/A
1 12 MHz	46875	N/A
2 48 MHz (default)	187500	N/A
3 1 MHz /Divisor	3906.25	15.259
4 4 MHz /Divisor	15625	61.035
5 12 MHz /Divisor	46875	183.105
6 48 MHz /Divisor	187500	732.422

Note that the clocks above apply to the U3 hardware revision 1.21. With hardware revision 1.20 all clocks are half of those values.

The same clock applies to all timers, so all 8-bit PWM channels will have the same frequency and will have their falling edges at the same time.

PWM output starts by setting the digital line to output-low for the specified amount of time. The output does not necessarily start instantly, but rather has to wait for the internal clock to roll. For 8-bit PWM output, the start delay varies from 0.0 to $\text{TimerClockDivisor} * 256 / \text{TimerClockBase}$. For example, if $\text{TimerClockBase} = 48 \text{ MHz}$ and $\text{TimerClockDivisor} = 256$, PWM frequency is 732 Hz, PWM period is 1.4 ms, and the start delay will vary from 0 to 1.4 ms.

If a duty cycle of 0.0% (totally off) is required, consider using a simple inverter IC such as the CD74ACT540E from TI. Or you can switch the mode of the timer to some input mode, and add an external pull-down to hold the line low when set to input.

2.9.1.3 - Period Measurement (32-Bit, Modes 2 & 3) [U3 Datasheet]

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Mode 2: On every rising edge seen by the external pin, this mode records the number of clock cycles (clock frequency determined by $\text{TimerClockBase} / \text{TimerClockDivisor}$) between this rising edge and the previous rising edge. The value is updated on every rising edge, so a read

returns the time between the most recent pair of rising edges.

In this 32-bit mode, the processor must jump to an interrupt service routine to record the time, so small errors can occur if another interrupt is already in progress. The possible error sources are:

- Other edge interrupt timer modes (2/3/4/5/8/9/12/13). If an interrupt is already being handled due to an edge on the other timer, delays of a few microseconds are possible.
- If a stream is in progress, every sample is acquired in a high-priority interrupt. These interrupts could cause delays on the order of 10 microseconds.
- The always active U3 system timer causes an interrupt 61 times per second. If this interrupt happens to be in progress when the edge occurs, a delay of about 1 microsecond is possible. If the software watchdog is enabled, the system timer interrupt takes longer to execute and a delay of a few microseconds is possible.

Note that the minimum measurable period is limited by the edge rate limit discussed in Section 2.9.2.

See [Section 3.2.1](#) for a special condition if stream mode is used to acquire timer data in this mode.

Writing a value of zero to the timer performs a reset. After reset, a read of the timer value will return zero until a new edge is detected. If a timer is reset and read in the same function call, the read returns the value just before the reset.

Mode 3 is the same except that falling edges are used instead of rising edges.

Edge Rate Limits

This edge-detecting timer mode requires processing resources as an interrupt is required to handle each edge. See more about edge rate limits in [Section 2.9.2](#).

2.9.1.4 - Duty Cycle Measurement (Mode 4) [U3 Datasheet]

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Records the high and low time of a signal on the external pin, which provides the duty cycle, pulse width, and period of the signal. Returns 4 bytes, where the first two bytes (least significant word or LSW) are a 16-bit value representing the number of clock ticks during the high signal, and the second two bytes (most significant word or MSW) are a 16-bit value representing the number of clock ticks during the low signal. The clock frequency is determined by $\text{TimerClockBase}/\text{TimerClockDivisor}$.

The appropriate value is updated on every edge, so a read returns the most recent high/low times. Note that a duty cycle of 0% or 100% does not have any edges.

To select a clock frequency, consider the longest expected high or low time, and set the clock frequency such that the 16-bit registers will not overflow. In other words, to measure 0% to 100% duty cycle for a given signal frequency, you need to set your clock frequency low enough such that the overall period of the signal is less than $65535 \cdot 1/\text{TimerClockFrequency}$. That equates to:

$$f_{\text{clock}} \leq 65535 \cdot f_{\text{signal}}$$

Note that the minimum measurable high/low time is limited by the edge rate limit discussed in [Section 2.9.2](#).

When using the LabJackUD driver the value returned is the entire 32-bit value. To determine the high and low time this value should be split into a high and low word. One way to do this is to do a modulus divide by 2^{16} to determine the LSW, and a normal divide by 2^{16} (keep the quotient and discard the remainder) to determine the MSW.

Writing a value of zero to the timer performs a reset. After reset, a read of the timer value will return zero until a new edge is detected. If a timer is reset and read in the same function call, the read returns the value just before the reset. The duty cycle reset is special, in that if the signal is low at the time of reset, the high-time/low-time registers are set to 0/65535, but if the signal is high at the time of reset, the high-time/low-time registers are set to 65535/0. Thus if no edges occur before the next read, it is possible to tell if the duty cycle is 0% or 100%.

Edge Rate Limits

This edge-detecting timer mode requires processing resources as an interrupt is required to handle each edge. See more about edge rate limits in [Section 2.9.2](#).

2.9.1.5 - Firmware Counter Input (Mode 5) [U3 Datasheet]

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On every rising edge seen by the external pin, this mode increments a 32-bit register. Unlike the pure hardware counters, these timer counters require that the firmware jump to an interrupt service routine on each edge.

Writing a value of zero to the timer performs a reset. After reset, a read of the timer value will return zero until a new edge is detected. If a timer is reset and read in the same function call, the read returns the value just before the reset.

Edge Rate Limits

This edge-detecting timer mode requires processing resources as an interrupt is required to handle each edge. See more about edge rate limits in [Section 2.9.2](#).

2.9.1.6 - Firmware Counter Input With Debounce (Mode 6) [U3 Datasheet]

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Intended for frequencies less than 10 Hz, this mode adds a debounce feature to the firmware counter, which is particularly useful for signals from mechanical switches. On every applicable edge seen by the external pin, this mode increments a 32-bit register. Unlike the pure hardware counters, these timer counters require that the firmware jump to an interrupt service routine on each edge.

The debounce period is set by writing the timer value. The low byte of the timer value is a number from 0-255 that specifies a debounce period in 16 ms increments (plus an extra 0-16 ms of variability):

$$\text{Debounce Period} = (0-16 \text{ ms}) + (\text{TimerValue} * 16 \text{ ms})$$

In the high byte (bits 8-16) of the timer value, bit 0 determines whether negative edges (bit 0 clear) or positive edges (bit 0 set) are counted.

Assume this mode is enabled with a value of 1, meaning that the debounce period is 16-32 ms and negative edges will be counted. When the input detects a negative edge, it increments the count by 1, and then waits 16-32 ms before re-arming the edge detector. Any negative edges within the debounce period are ignored. This is good behavior for a normally-high signal where the switch closure causes a brief low signal (see [Section 2.8.1.3](#)). The debounce period can be set long enough so that bouncing on both the switch closure and switch open is ignored.

Writing a value of zero to the timer performs a reset. After reset, a read of the timer value will return zero until a new edge is detected. If a timer is reset and read in the same function call, the read returns the value just before the reset.

Edge Rate Limits

This edge-detecting timer mode requires processing resources as an interrupt is required to handle each edge. See more about edge rate limits in [Section 2.9.2](#).

2.9.1.7 - Frequency Output (Mode 7) [U3 Datasheet]

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Frequency Output Overview

Outputs a square wave at a frequency determined by $\text{TimerClockBase} / \text{TimerClockDivisor}$ divided by $2 * \text{Timer\#Value}$. The Value passed should be between 0-255, where 0 is a divisor of 256. By changing the clock configuration and timer value, a wide range of frequencies can be output, as shown in the following table:

Table 2.9.1.7-1. Mode 7 Frequency Ranges

Mode 7 Frequency Ranges		
	Divisor=1	Divisor=1
TimerClockBase	Value=1	Value=256
0 4 MHz	2000000	7812.5
1 12 MHz	6000000	23437.5
2 48 MHz (default)	24000000	93750
	Divisor=1	Divisor=256
	Value=1	Value=256
3 1 MHz /Divisor	500000	7.629
4 4 MHz /Divisor	2000000	30.518
5 12 MHz /Divisor	6000000	91.553
6 48 MHz /Divisor	24000000	366.211

Note that the clocks above apply to the U3 hardware revision 1.21. With hardware revision 1.20 all clocks are half of those values.

The frequency output has a -3 dB frequency of about 10 MHz on the FIO lines. Accordingly, at high frequencies the output waveform will get less square and the amplitude will decrease.


The output does not necessarily start instantly, but rather has to wait for the internal clock to roll. For the Frequency Output mode, the start delay varies from 0.0 to $\text{TimerClockDivisor} \times 256 / \text{TimerClockBase}$. For example, if $\text{TimerClockBase} = 48 \text{ MHz}$ and $\text{TimerClockDivisor} = 256$, the start delay will vary from 0 to 1.4 ms.

Frequency List for U3 Timer Mode 7

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CSV list of the 262,144 possible frequency output options on the U3 hardware rev 1.21+. Columns are Hz, base clock, clock divisor, and timer value. Oct 27, 2008.

File Attachment:

 [U3_FreqOutList_Hz_Base_Divisor_Value.csv](#)

2.9.1.8 - Quadrature Input (Mode 8) [U3 Datasheet]

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Requires both timers, where Timer0 will be quadrature channel A, and Timer1 will be quadrature channel B. The U3 does 4x quadrature counting, and returns the current count as a signed 32-bit integer (2's complement). The same current count is returned on both timer value parameters.

Writing a value of zero to either or both timers performs a reset of both. After reset, a read of either timer value will return zero until a new quadrature count is detected. If a timer is reset and read in the same function call, the read returns the value just before the reset.

4X Counting

Quadrature mode uses the very common 4X counting method, which provides the highest resolution possible. That means you get a count for every edge (rising & falling) on both phases (A & B). Thus if you have an encoder that provides 32 PPR, and you rotate that encoder forward 1 turn, the timer Value register will be incremented by +128 counts.

Z-phase Support

Quadrature mode supports Z-Phase. When enabled this feature will set the count to zero when the specified IO line sees a logic high.

Z-phase is controlled by the value written to the timer during initialization. To enable z-phase support set bit 15 to 1 and set bits 0 through 4 to the DIO number that Z is connected to. EG: for a Z-line on EIO3 set the timer value to 0x800B or 32779. This value should be sent to both the A and B timers.

Note that the LabJack will only check Z when it sees an edge on A or B.

Z-phase support requires Firmware 1.30 or later.

2's Complement

Other timer modes return unsigned values, but this timer mode is unique in that it returns a signed value from -2147483648 to +2147483647. That is, a 32-bit 2's complement value. When you do a timer value read and get back a single float from the UD driver, the math is already done and you get back a value from -2147483648.0 to +2147483647.0, but when using the special channels 20x/23x/224 you get the LSW and MSW separately and have to do the math yourself. Search for 2's complement math for your particular programming language.

In a language such as C++, you start by doing using unsigned 32-bit variables & constants to compute $\text{Value} = (\text{MSW} * 65536) + \text{LSW}$. Then simply cast Value to a signed 32-bit integer.

In a language such as Java that does not support unsigned integers, do everything with signed 64-bit variables & constants. First calculate $\text{Value} = (\text{MSW} * 65536) + \text{LSW}$. If $\text{Value} < 2147483648$, you are done. If $\text{Value} \geq 2147483648$, do $\text{ActualValue} = -1 * (4294967296 - \text{Value})$.

Edge Rate Limits

This edge-detecting timer mode requires processing resources as an interrupt is required to handle each edge. See more about edge rate limits in [Section 2.9.2](#).

Can I Use a Simple Counter Instead?

If you need to track absolute position with changing direction (forward & reverse), you need to use the timer feature described above to decode quadrature signals, as the relationship between edges on the A & B phases indicates direction. However, if movement is only in one direction, or you always know the direction of movement, you can just connect one phase (A or B) to a [hardware counter](#) (2 available on the U3) or [firmware](#)

[counter](#) (2 available on the U3). Hardware counters can handle much faster edge rates (up to 8 MHz per [Appendix A](#)). Firmware counters have the same edge rate limits as quadrature, but the firmware counter is counting 1/4th the edges of a quadrature timer and thus can handle a signal with 4x the frequency, and also is only using 1 timer rather than 2.

2.9.1.9 - Timer Stop Input (Mode 9) [U3 Datasheet]

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This mode should only be assigned to Timer1. On every rising edge seen by the **external pin**, this mode increments a 16-bit register. When that register matches the specified timer value (stop count value), Timer0 is stopped. The range for the stop count value is 1-65535. Generally, the signal applied to Timer1 is from Timer0, which is configured in some output timer mode. One place where this might be useful is for stepper motors, allowing control over a certain number of steps.

Note that the timer is counting from the external pin like other input timer modes, so you must connect something to the stop timer input pin. For example, if you are using Timer1 to stop Timer0 which is outputting pulses, you must connect a jumper from Timer0 to Timer1.

Once this timer reaches the specified stop count value, and stops the adjacent timer, the timers must be reconfigured to restart the output.

When Timer0 is stopped, it is still enabled but just not outputting anything. Thus rather than returning to whatever previous digital I/O state was on that terminal, it goes to the state "digital-input" (which has a 100 kΩ pull-up to 3.3 volts). That means the best results are generally obtained if the terminal used by Timer0 was initially configured as digital input (factory default), rather than output-high or output-low. This will result in negative going pulses, so if you need positive going pulses consider using a simple inverter IC such as the CD74ACT540E from TI.

The MSW of the read from this timer mode returns the number of edges counted, but does not increment past the stop count value. The LSW of the read returns edges waiting for.

Edge Rate Limits

This edge-detecting timer mode requires processing resources as an interrupt is required to handle each edge. See more about edge rate limits in [Section 2.9.2](#).

2.9.1.10 - System Timer Low/High Read (Modes 10 & 11) [U3 Datasheet]

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The LabJack U3 has a free-running internal 64-bit system timer with a frequency of 4 MHz. Timer modes 10 & 11 return the lower or upper 32-bits of this timer. An FIO line is allocated for these modes like normal, even though they are internal readings and do not require any external connections. This system timer cannot be reset, and is not affected by the timer clock.

If using both modes 10 & 11, read both in the same low-level command and read 10 before 11.

Mode 11, the upper 32 bits of the system timer, is not available for stream reads. Note that when streaming on the U3, the timing is known anyway (elapsed time = scan rate * scan number) and it does not make sense to stream the system timer modes 10 or 11.

Note that system timer runs at 2MHz on U3 hardware 1.20.

2.9.1.11 - Period Measurement (16-Bit, Modes 12 & 13) [U3 Datasheet]

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Similar to the 32-bit edge-to-edge timing modes described earlier (modes 2 & 3), except that hardware capture registers are used to record the edge times. This limits the times to 16-bit values, but is accurate to the resolution of the clock, and not subject to any errors due to firmware processing delays.

To select a clock frequency, consider the longest expected period, and set the clock frequency such that the 16-bit registers will not overflow. In other words, to measure a given signal frequency, you need to set your clock frequency low enough such that the overall period of the signal is less than $65535 * 1/\text{TimerClockFrequency}$. That equates to:

$$f_{\text{clock}} \leq 65535 * f_{\text{signal}}$$

Edge Rate Limits

This edge-detecting timer mode requires processing resources as an interrupt is required to handle each edge. See more about edge rate limits in [Section 2.9.2](#).

2.9.1.12 - Line-to-Line Measurement (Mode 14) [U3 Datasheet]

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This timer mode requires firmware 1.30 or later.

Introduction:

The Line-to-Line timer mode uses two timers to measure the time between specified edges on two different lines. For instance, you can measure the time between a rising edge on Timer0 and a falling edge on Timer1. When the LabJack sees the specified edge on Timer0 it starts counting until it sees the specified edge on Timer1. High resolution up to 20.8ns can be achieved with this mode.

Configuring:

To configure a LabJack for Line-to-Line mode set an even timer and the next (odd) timer to mode 14. The timer values determine the edge that the timer will respond to, 1 being rising, 0 being falling. So, if Timer0's value is 0 and Timer1's is 1 then the LabJack will measure the time between a falling edge on Timer0 to a rising edge on Timer1.

Readings:

Once configured the timer will return zero until both specified edges have been detected. The time difference in TimerClock periods is then returned by both timers until they are reset. Both timers will return the same reading, so it is only necessary to read one or the other. To convert to time, divide the value returned by the timer clock. This mode returns 16-bit values, so care should be taken to be sure that the specified condition does not exceed the maximum time. The maximum time can be calculated by $(2^{16}-1)/\text{TimerClock}$.

Resetting:

Once a measurement has been acquired the even timer needs to be reset before the LabJack will measure again. Values specified when resetting have no effect. Once reset the even timer will return zero until a new measurement has been completed. Resetting the odd timer is optional, if not reset it will continue to return the last measurement until a new one has been completed.

2.9.2 - Timer Operation/Performance Notes [U3 Datasheet]

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Note that the specified timer clock frequency is the same for all timers. That is, TimerClockBase and TimerClockDivisor are singular values that apply to all timers. Modes 0, 1, 2, 3, 4, 7, 12, and 13, all are affected by the clock frequency, and thus the simultaneous use of these modes has limited flexibility. This is often not an issue for modes 2 and 3 since they use 32-bit registers.

The output timer modes (0, 1, and 7) are handled totally by hardware. Once started, no processing resources are used and other U3 operations do not affect the output.

The edge-detecting timer input modes do require U3 processing resources, as an interrupt is required to handle each edge. Timer modes 2, 3, 5, 6, 9, 12, and 13 must process every applicable edge (rising **or** falling). Timer modes 4 and 8 must process every edge (rising **and** falling). To avoid missing counts, keep the total number of processed edges (all timers) less than 30,000 per second (hardware V1.21). That means that in the case of a single timer, there should be no more than 1 edge per 33 μ s. For multiple timers, all can process an edge simultaneously, but if for instance both timers get an edge at the same time, 66 μ s should be allowed before any further edges are applied. If streaming is occurring at the same time, the maximum edge rate will be less (7,000 per second), and since each edge requires processing time the sustainable stream rates can also be reduced.

2.10 - SPC (... and SCL/SDA/SCA) [U3 Datasheet]

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The SPC terminal is used for manually resetting default values or jumping in/out of flash programming mode.

Power up the U3 with a jumper securely installed from FIO6 to SPC (FIO2 to SCL on U3 1.20/1.21), then remove the jumper and power cycle the device again. This resets all power-up settings to factory default values.

See the [LJSelfUpgrade](#) page for information about jumpers to switch between flash mode and normal mode.

Hardware revision 1.20 and 1.21, had terminals labeled SCL, SDA, and/or SCA. On revision 1.20, these terminals did nothing except that SCL is used for the SPC functionality described above. On revision 1.21, these terminals were used for asynchronous functionality, and SCL is used for the SPC functionality described above. Note that these terminals never have anything to do with I²C.

2.11 - DB15 [U3 Datasheet]

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DB15 Overview

The DB15 connector brings out 12 additional I/O. It has the potential to be used as an expansion bus, where the 8 EIO are data lines and the 4 CIO are control lines. The EIO are flexible I/O as described in [Section 2.5](#), so can be used as digital input, digital, output, analog input, timer, or counter.

In the Windows LabJackUD driver, the EIO are addressed as digital I/O bits 8 through 15, and the CIO are addressed as bits 16-19.

0-7 FIO0-FIO7
8-15 EIO0-EIO7
16-19 CIO0-CIO3

These 12 channels include an internal series resistor that provides overvoltage/short-circuit protection. These series resistors also limit the ability of these lines to sink or source current. Refer to the specifications in [Appendix A](#).

All digital I/O on the U3 have 3 possible states: input, output-high, or output-low. Each bit of I/O can be configured individually. When configured as an input, a bit has a ~100 kΩ pull-up resistor to 3.3 volts. When configured as output-high, a bit is connected to the internal 3.3 volt supply (through a series resistor). When configured as output-low, a bit is connected to GND (through a series resistor).

Table 2.11-1. DB15 Connector Pinouts

DB15 Pinouts			
1	Vs	9	CIO0
2	CIO1	10	CIO2
3	CIO3	11	GND
4	EIO0	12	EIO1
5	EIO2	13	EIO3
6	EIO4	14	EIO5
7	EIO6	15	EIO7
8	GND		

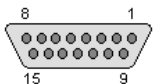


Figure 2.11-2. Standard DB15 pin numbers looking into the female connector on the U3

2.11.1 - CB15 Terminal Board [U3 Datasheet]

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The [CB15 terminal board](#) connects to the LabJack U3's DB15 connector. It provides convenient screw terminal access to the 12 digital I/O available on the DB15 connector. The CB15 is designed to connect directly to the LabJack, or can connect via a standard 15-line 1:1 male-female DB15 cable.



2.11.2 - RB12 Relay Board [U3 Datasheet]

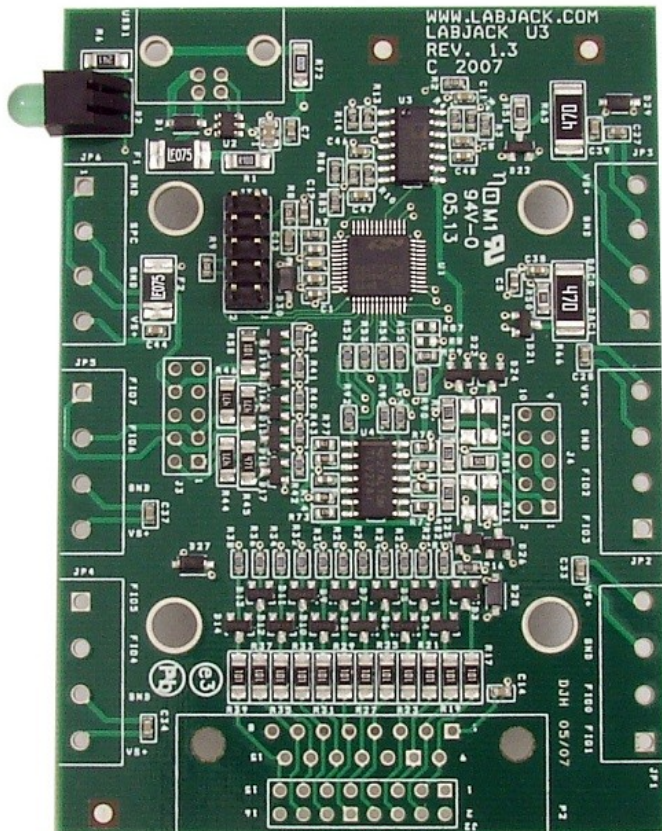
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The **RB12 relay board** provides a convenient interface for the U3 to industry standard digital I/O modules, allowing electricians, engineers, and other qualified individuals, to interface a LabJack with high voltages/currents. The RB12 relay board connects to the DB15 connector on the LabJack, using the 12 EIO/CIO lines to control up to 12 I/O modules. Output or input types of digital I/O modules can be used. The RB12 is designed to accept G4 series digital I/O modules from Opto22, and compatible modules from other manufacturers such as the G5 series from Grayhill. Output modules are available with voltage ratings up to 200 VDC or 280 VAC, and current ratings up to 3.5 amps.

2.12 - U3-OEM [U3 Datasheet]

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There is an OEM version of the U3 available. It is a board only (no enclosure, no screwdriver, no cable), and does not have most of the through-hole components installed. The picture below shows how the U3-OEM ships by default. Leaving the through-hole parts off makes the OEM board very flexible. Many applications do not need the through-hole parts, but if needed they are much easier to install than uninstall. See [Appendix B](#) for PCB dimensions.



Screw-terminals are not installed. The PCB is designed for 5mm screw-terminals, but 5.08mm should fit also.

The DB15 connector is not installed.

The USB connector is also not installed.

The 2x5 header JTAG is normally installed on the U3 and U3-OEM. This header is for factory use. You can use a mating header for physical support purposes, but it should not connect to anything.

As an alternative to the screw-terminals and DB15, the U3 PCB has holes available for standard 0.1" pin-header installation. Connectors J3 & J4 are 2x5 holes that provide pin-header access to the connections that would normally appear on the left and right screw-terminals. The 2x8 connector J2 provides a pin-header alternative to the DB15 connector. All these connector holes are always present, but J2 is obstructed when the DB15 is installed. The idea is that an OEM can connect ribbon cables to the pin-headers, or even plug the U3 directly to the customers main board designed with mating pin-header receptacles. See Appendix B for connector coordinates on the PCB.

Table 2.12-1. J2 Connector Pin-Headers

J2			
1	GND	2	VS
3	CIO0	4	CIO1
5	CIO2	6	CIO3
7	GND	8	EIO0
9	EIO1	10	EIO2
11	EIO3	12	EIO4
13	EIO5	14	EIO6
15	EIO7	16	GND

Table 2.12-2. J3 Connector Pin-Headers

J3			
1	FIO4	2	FIO5
3	FIO6	4	FIO7
5	VS	6	GND
7	GND *	8	SPC **
9	VS	10	GND

* SDA on <1.30

** SCL on <1.30

Table 2.12-3. J4 Connector Pin-Headers

J4			
1	FIO0	2	FIO1
3	FIO2	4	FIO3
5	VS	6	GND
7	DAC0	8	DAC1
9	VS	10	GND

USB (USB1)

There are 4 holes for a standard Type-B USB connection (plus a couple large holes for mounting tabs). Looking at the bottom (solder-side) of the PCB with the USB/LED end of the PCB up, GND (pin 4, black wire) is in the upper-right corner. Then clockwise it goes Vbus (5 volts, lower-right, pin 1, red wire), D- (lower-left, pin 2, white wire), and D+ (upper-left, pin 3, green wire). If using a normal Type-B USB connector, it must be installed on the component side of the PCB.

A normal USB cable has a shield, and the normal Type-B connector connects the cable shield to the mounting tabs on the connector which are then soldered into the large USB mounting holes on the PCB. If you are not using a normal USB connector, and have a shield in your USB cable, we recommend that the shield be connected to either of the large USB mounting holes on the PCB.

Alternative Power Supply

Generally 5 volt power is provided via the USB connector holes, and usually it is provided from the USB host. There are few reasons, if any, to power the U3 from anything besides the USB host. The only valid reason we hear has to do with keeping the U3 powered even when the USB host loses power, which is an unusual requirement in itself since the U3 does not really do anything without a USB host connected. If you are considering an external supply for reasons related to noise or stability, you are probably "barking up the wrong tree" and should contact support@labjack.com.

The power supply provided by USB is typically 5 volts +/-5% @500 mA. The basic way to use an alternate supply is connecting it to hole 1 of the USB connector holes, instead of the supply from the USB host. Or if using a USB cable, cut the red wire inside the cable and connect your positive supply lead to that (also might need a connection of the negative supply lead to the black wire but don't cut it). You can also connect an external supply to VS/GND screw-terminals (after cutting the red wire in the USB cable), but it is preferable to bring the supply in through the USB connector.

Note that USB ground and the external supply common/negative/ground must both connect to GND on the U3 (which could mean just the ground in the USB cable is needed if the power supply is already connected to that same ground). Also note that you never want 2 supplies connected directly to each other without any mechanism to prevent one supply from backfeeding the other.

Parts

There are many options for the parts that can be installed on these OEM boards, but here are some typical parts available aDigikey.com:

Screw-Terminals (4-pole, 5mm): [Weidmuller 9993300000](#), [Phoenix MKDS 1.5/4](#).

DB15 (0.318", female): [NorComp 182-015-213R531](#).

Pin Headers (0.1"): 2x8 => [Molex 0010897162](#), 2x5 => [Molex 0010897102](#).

USB (Type B): [FCI 61729-0010BLF](#), [TE Connectivity 292304-2](#), [Samtec USBR-B-S-S-O-TH](#) (high retention).

OEM-Friendly Installers

OEM-friendly Software Installers (Windows)

To make deploying OEM LabJacks alongside LabJack software easier, there are two options:

- Call the LabJack [Basic Installer](#) silently from your own installer
- Use the [Example LabJack Installer](#) to create your own installer

2.13 - Hardware Revision Notes [U3 Datasheet]

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The U3 has undergone three hardware revisions.

U3A = Hardware Version 1.20

U3B = Hardware Version 1.21

U3C = Hardware Version 1.30

How to Determine Hardware Version:

- Displayed by LJControlPanel.
- Returned through the UD library using IOType LJ_ioGET_CONFIG with special channel [LJ_chHARDWARE_VERSION](#).
- Returned by the Low-level [ConfigU3](#) function.
- Printed on the serial number label on the bottom of the red enclosure.

Changes from 1.20 to 1.21:

Starting September of 2006, all U3 shipments changed from hardware revision 1.20 to 1.21. Following are the some of the main changes in revision 1.21:

- The default timer clock frequency is 48 MHz.
- All TimerBaseClock frequencies are twice the previous frequencies.
- The input timer edge limit is now 30,000 edges/second, compared to the old limit of 10,000 edges/second.
- Stream mode is now supported. See Section 3.2.
- Other new functions are supported, including Watchdog, SPI, Asynch, I2C, and SHT1X.
- Typical supply current is 50 mA.

Changes from 1.21 to 1.30:

Hardware revision 1.30 was released in mid-March 2008 with 2 variations: U3-LV and U3-HV. The U3-LV is the most compatible with the previous U3, and the only changes possibly affecting backwards compatibility are:

- Timers/Counters cannot appear on FIO0-3. TimerCounterPinOffset must be 4-8. A value of 0-3 will result in an error. This error can be suppressed by a power-up default setting in LJControlPanel. If suppressed, a 0-3 will result in an offset of 4.
- The 3.66 reference voltage is no longer available on the REF/DAC1 terminal.
- There is no longer a buzzer.
- SDA terminal is gone. SCL terminal changed to SPC.
- UART (Asynch functionality) no longer uses SDA and SPC terminals, but rather uses terminals dynamically assigned after timers and counters. Also, the BaudFactor is different.

Other changes:

- Analog outputs are now specified for 10-bit resolution and DAC1 is always enabled. The higher resolution is available with a new IOType in the low-level Feedback function, which the high-level UD driver uses automatically. This causes the DACs to have more noise when the timer clock is decreased from the default of 48 MHz, so there is a compatibility option available in LJControlPanel to use 8-bit DACs.
- On the U3-HV only, the first four flexible I/O are fixed as analog inputs (AIN0-AIN3), and have scaling such that the input range is ± 10 volts normally, and $+20$ to -10 volts when using the "Special" range. The input impedance of these four lines is roughly $1\text{ M}\Omega$, which is good, but less than the normal low voltage analog inputs. Analog/digital configuration and all other digital operations on these pins are ignored. FIO4-EIO7 are still available as flexible I/O, same as the U3-LV.
- Timer and counter settings can be saved to startup defaults.

Revision 1.20/21 U3s cannot be upgraded to 1.30.

3 - Operation [U3 Datasheet]

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Operation Overview

The following sections discuss command/response mode and stream mode.

Command/response mode is where communication is initiated by a command from the host which is followed by a response from the LabJack. Command/response is generally used at 1000 scans/second or slower and is generally simpler than stream mode. [See Section 3.1 for details about command/response data rates.](#)

Stream mode is a continuous hardware-timed input mode where a list of channels is scanned at a specified scan rate. The scan rate specifies the interval between the beginning of each scan. The samples within each scan are acquired as fast as possible. As samples are collected automatically by the LabJack, they are placed in a buffer on the LabJack, until retrieved by the host. Stream mode is generally used at 10 scans/second or faster. [See Section 3.2 for details about stream data rates](#)

Command/response mode is generally best for minimum-latency applications such as feedback control. By latency here we mean the time from when a reading is acquired to when it is available in the host software. A reading or group of readings can be acquired in times on the order of a millisecond.

Stream mode is generally best for maximum-throughput applications where latency is not so important. Data is acquired very fast, but to sustain the fast rates it must be buffered and moved from the LabJack to the host in large chunks. For example, a typical stream application might set up the LabJack to acquire a single analog input at 50,000 samples/second. The LabJack moves this data to the host in chunks of 25 samples each. The Windows UD driver moves data from the USB host memory to the UD driver memory in chunks of 2000 samples. The user application might read data from the UD driver memory once a second in a chunk of 50,000 samples. The computer has no problem retrieving, processing, and storing, 50k samples once per second, but it could not do that with a single sample 50k times per second.

The U3 supports 1 active stream at 1 scan rate. Command-response calls can be used while a stream is active to read/write almost everything except analog inputs (which includes the internal temperature sensor), as stream mode takes over the analog input system.

3.1 - Command/Response [U3 Datasheet]

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Everything besides streaming is done in command/response mode, meaning that all communication is initiated by a command from the host which is followed by a response from the U3.

For everything besides pin configuration, the low-level Feedback function is the primary function used, as it writes and reads virtually all I/O on the U3. The Windows UD driver uses the Feedback function under-the-hood to handle most requests besides configuration and streaming.

The following tables show typical measured execution times for command/response mode. The time varies primarily with the number of analog inputs requested, and is not noticeably affected by the number of digital I/O, DAC, timer, and counter operations.

These times were measured using the example program "allio.c" (VC6_LJUD). The program executes a loop 1000 times and divides the total time

by 1000, and thus include everything (Windows latency, UD driver overhead, communication time, U3 processing time, etc.).

Table 3.1-1. Typical Feedback Function Execution Times (QuickSample=0, LongSettling=0)

	USB high-high	USB other	
# AIN	[milliseconds]	[milliseconds]	
0	0.6	4	<- Write/Read all DIO, DACs, Timers and Counters
1	1	4	
4	2.4	4	
8	4.7	9.2	
16	8.3	12.2	

Table 3.1-2. Typical Feedback Function Execution Times (QuickSample=1, LongSettling=0)

	USB high-high	USB other	
# AIN	[milliseconds]	[milliseconds]	
0	0.6	4	<- Write/Read all DIO, DACs, Timers and Counters
1	0.7	4	
4	1	4	
8	2.1	8	
16	3	8	

Table 3.1-3. Typical Feedback Function Execution Times (QuickSample=0, LongSettling=1)

	USB high-high	USB other	
# AIN	[milliseconds]	[milliseconds]	
0	0.6	4	<- Write/Read all DIO, DACs, Timers and Counters
1	4.2	5.2	
4	16	17	
8	31	36	
16	60	62	

A “USB high-high” configuration means the U3 is connected to a high-speed USB2 hub which is then connected to a high-speed USB2 host. Even though the U3 is not a high-speed USB device, such a configuration does provide improved performance. Typical examples of “USB other” would be a U3 connected to an old full-speed hub (hard to find) or more likely a U3 connected directly to the USB host (even if the host supports high-speed).

The analog inputs have a QuickSample option where each conversion is done faster at the expense of increased noise. This is enabled by passing a nonzero value for put_config special channel *LJ_chAIN_RESOLUTION*. There is also a LongSettling option where additional settling time is added between the internal multiplexer configuration and the analog to digital conversion. This allows signals with more source impedance, and is enabled by passing a nonzero value for put_config special channel *LJ_chAIN_SETTLING_TIME*. Both of these options are disabled by default, so the first table above shows the default conditions.

The first row in each of the above tables (#AIN = 0) includes a write and read to all I/O on the U3 besides analog inputs (digital I/O DACs, timers, and counters). The times in other rows basically consist of that fixed overhead plus the time per analog input channel, so times can be interpolated for other numbers of channels.

How about for 2 analog input channels with QuickSample=0 and LongSettling=0? You know that 1 channel takes about 1.0 ms and 4 channels takes about 2.4 ms. That means it takes about $(2.4 - 1.0)/(4 - 1) = 0.46$ ms/channel plus overhead of about 0.6 ms, so 2 channels would take about $(2 * 0.46) + 0.6 = 1.5$ ms.

How about for 20 channels? This is a little different because the commands and/or responses for 20 channels can't fit in one low-level packet. From Section 5.2.5, the Feedback command has room for 57 bytes of command data and 55 bytes of response data. From Section 5.2.5.1, the AIN low-level IOType has 3 command bytes and 2 response bytes. That means the low-level Feedback command can hold 19 commands and 27 responses. Thus the commands are limiting and 19 channels is the most we can get into 1 low-level Feedback packet. The timing for 20-channels can then be calculated as a 19-channel read plus a subsequent 1-channel read. If you do an Add/Go/Get block with 20 channels the UD driver will split it like that.

The tables above were measured with U3 hardware version 1.21 which started shipping in late August of 2006. The times could be up to twice as

long with hardware version 1.20 or less.

3.2 - Stream Mode [U3 Datasheet]

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Stream Mode Overview

The highest input data rates are obtained in stream mode, which is supported with U3 hardware version 1.21 or higher. Hardware version 1.21 started shipping in late August of 2006. Contact LabJack for information about upgrading older U3s. Stream is a continuous hardware timed input mode where a list of channels is scanned at a specified scan rate. The scan rate specifies the interval between the beginning of each scan. The samples within each scan are acquired as fast as possible.

As samples are collected, they are placed in a small FIFO buffer on the U3, until retrieved by the host. The buffer typically holds 984 samples, but the size ranges from 512 to 984 depending on the number of samples per packet. Each data packet has various measures to ensure the integrity and completeness of the data received by the host.

Since the data buffer on the U3 is very small it uses a feature called auto-recovery. If the buffer overflows, the U3 will continue streaming but discard data until the buffer is emptied, and then data will be stored in the buffer again. The U3 keeps track of how many packets are discarded and reports that value. Based on the number of packets discarded, the UD driver adds the proper number of dummy samples (-9999.0) such that the correct timing is maintained.

The table below shows various stream performance parameters. Some systems might require a USB high-high configuration to obtain the maximum speed in the last row of the table. A "USB high-high" configuration means the U3 is connected to a high-speed USB2 hub which is then connected to a high-speed USB2 host. Even though the U3 is not a high-speed USB device, such a configuration does often provide improved performance.

Stream data rates over USB can also be limited by other factors such as speed of the PC and program design. One general technique for robust continuous streaming would be increasing the priority of the stream process.

The max sample rate of the U3 is 50 ksamples/second. The max scan rate depends on how many channels you are sampling per scan:

Sample => A reading from one channel.

Scan => One reading from all channels in the scan list.

SampleRate = NumChannels * ScanRate

ScanRate = SampleRate / NumChannels

For example, if streaming 5 channels at ResolutionIndex=0 and all at Range=+-10V, the max scan rate is 10 kscans/second (calculated from 50 ksamples/second divided by 5).

Table 3.2-1. Streaming at Various Resolutions

Low-Level	UD	Max Stream	ENOB	ENOB	Noise	Interchannel
Res Index	Res Index	(Samples/s)	(RMS)	(Noise-Free)	(Counts)	Delay (μs)
0	100	2500	12.8	10	±2	320
1	101	10000	11.9	9	±4	82
2	102	20000	11.3	8.4	±6	42
3	103	50000	10.5	7.5	±11	12.5

Full resolution streaming is limited to 2500 samples/s, but higher speeds are possible at the expense of reduced effective resolution (increased noise). The first column above is the index passed in the Resolution parameter to the low-level StreamConfig function, while the second column is the corresponding index for the Resolution parameter in the UD driver. In the UD driver, the default Resolution index is 0, which corresponds to automatic selection. In this case, the driver will use the highest resolution for the specified sample rate.

ENOB stands for effective number of bits. The first ENOB column is the commonly used "effective" resolution, and can be thought of as the resolution obtained by most readings. This data is calculated by collecting 128 samples and evaluating the standard deviation (RMS noise). The second ENOB column is the noise-free resolution, and is the resolution obtained by all readings. This data is calculated by collecting 128 samples and evaluating the maximum value minus the minimum value (peak-to-peak noise). Similarly, the Noise Counts column is the peak-to-peak noise based on counts from a 12-bit reading.

Interchannel delay is the time between successive channels within a stream scan.

3.2.1 - Streaming Digital Inputs, Timers, and Counters [U3 Datasheet]

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There are special channel numbers that allow digital inputs, timers, and counters, to be streamed in with analog input data.

Table 3.2.1-1. Special Stream Channels

Channel #	
193	EIO_FIO
194	CIO
200	Timer0
201	Timer1
210	Counter0
211	Counter1
224	TC_Capture
230	Timer0 with reset
231	Timer1 with reset
240	Counter0 with reset
241	Counter1 with reset

Special Channels:

193: Returns the input states of 16 bits of digital I/O. FIO is the lower 8 bits and EIO is the upper 8 bits.

194: Returns the input states of 16 bits of digital I/O. CIO is the lower 8 bits.

200-201 and 210-211: Retrieves the least significant word (LSW, lower 2 bytes) of the specified timer/counter. At the same time that any one of these is sampled, the most significant word (MSW, upper 2 bytes) of that particular timer/counter is stored in an internal capture register (TC_Capture), so that the proper value can be sampled later in the scan. For any timer/counter where the MSW is wanted, channel number 224 must be sampled after that channel and before any other timer/counter channel. For example, a scan list of {200,224,201,224} would get the LSW of Timer0, the MSW of Timer0, the LSW of Timer1, and the MSW of Timer1. A scan list of {200,201,224} would get the LSW of Timer0, the LSW of Timer1, and the MSW of Timer1 (MSW of Timer0 is lost).

230-231 and 240-241: These channels perform the same operation as their 200-211 counterpart above, then reset the timer or counter.

Adding these special channels to the stream scan list does not configure those inputs. If any of the FIO or EIO lines have been configured as outputs, timers, counter, or analog inputs, a channel 193 read will still be performed without error but the values from those bits should be ignored. The timers/counters (200-224) must be configured before streaming using normal timer/counter configuration commands.

The timing for these special channels is the same as for normal analog channels. For instance, a stream of the scan list {0,1,200,224,201,224} counts as 6 channels, and the maximum scan rate is determined by taking the maximum sample rate at the specified resolution and dividing by 6.

It is not recommended to stream timers configured in mode 2 or 3 (32-bit period measurement). It is possible for the LSW to roll, but the MSW be captured before it is incremented. That means that only the LSW is reliable, and thus you might as well just use the 16-bit modes.

Mode 11, the upper 32 bits of the system timer, is not available for stream reads. Note that when streaming on the U3, the timing is known anyway (elapsed time = ScanNumber / ScanRate) and it does not make sense to stream the system timer modes 10 or 11.

Command-Response

These special channel numbers can be used in command-response also. In command response they will return 32-bit values and thus the capture channel (224) is not used. This is useful in a program set up to do command-response reads of analog inputs only, such as LJLogUD.

4 - LabJackUD High-Level Driver [U3 Datasheet]

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Overview

LabJackUD is the high-level Windows driver for the U3, U6 and UE9. LabJackUD is also referred to as UD and LJUD.

For Linux or Mac OS X, use the [Exodriver](#).

The low-level U3 functions are described in [Section 5](#), but most Windows applications will use the LabJackUD driver instead.

UD Overview Page - Installation Details (Referencable)

Installation Details

The UD library is supported on Windows XP and newer. The archived 3.06 version supports Windows 98, ME, and 2000.

Please install the software before making a USB connection to a LabJack.

The download version of the installer consists of a single executable. This installer places the driver (LabJackUD.dll) in the Windows System directory, along with a support DLL (LabJackWUSB.dll). Generally this is:

- Windows 98/ME:
 - C:\Windows\System\
- Windows 2000/XP and 32-bit Windows Vista/7/8/10:
 - C:\Windows\System32\
- 64-bit Windows Vista/7/8/10:
 - C:\Windows\System32\ (64-bit drivers)
 - C:\Windows\SysWOW64\ (32-bit drivers)

Other files, including the header and Visual C library file, are installed to the LabJack drivers directory. Defaults are:

- 32-bit Windows:
 - C:\Program Files\LabJack\Drivers\
- 64-bit Windows:
 - C:\Program Files (x86)\LabJack\Drivers\

4.1 - Overview [U3 Datasheet]

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Quick Overview

The UD driver has just a handful of functions, and those functions all have the same 4-6 parameters.

Functions

The main functions are used to build a list of requests (Add), execute the list (Go), and read the result of each request (Get). Some functions (e.g. eGet) combine Add-Go-Get into a single function call.

Parameters

The parameters are described at the bottom of the Overview page. Usually the meaning of each parameter should be apparent (e.g. Channel is the channel number you are operating on), but when it is not the applicable pseudocode section (4.3) should have any extra information that is needed.

- Handle: Always the handle.
- IOType: Always the IOType.
- Channel: If something besides a channel number the pseudocode section will tell you.
- Value: Always the value.
- X1: Usually not used, but if used the pseudocode section will tell you.
- UserData: Usually not used, but if used the pseudocode section will tell you.

Extended Overview

The LabJackUD driver is the Windows driver/library for the U3, and also the U6 and UE9. LabJackUD is also referred to as UD and LJUD.

For other LabJack devices, go to the Software page.

To download, see: UD Installer.

The general operation of the LabJackUD functions is as follows:

- Open a LabJack U3, U6 or UE9.
- Build a list of requests to perform (Add).
- Execute the list (Go).
- Read the result of each request (Get).

At the core, the UD driver only has 4 basic functions: Open, AddRequest, Go, and GetResult. Then with these few functions, there are many constants used to specify the desired actions. When programming in any language, it is recommended to have the header file handy, so that constants can be copied and pasted into the code.

The first type of constant is an IOType, which is always passed in the IOType parameter of a function call. One example of an IOType is the constant `LJ_ioPUT_DAC`, which is used to update the value of an analog output (DAC).

The second type of constant is a Channel Constant, also called a Special Channel. These constants are always passed in the Channel parameter of a function call. For the most part, these are used when a request is not specific to a particular channel, and go with the configuration IOTypes (*LJ_ioPUT_CONFIG* or *LJ_ioGET_CONFIG*). One example of a Special Channel is the constant *LJ_chLOCALID*, which is used to write or read the local ID of the device.

The third major type of constant used by the UD driver is a Value Constant. These constants are always passed in the Value parameter of a function call. One example of a Value Constant is the constant *LJ_tmPWM8*, which specifies a timer mode. This constant has a numeric value of 1, which could be passed instead, but using the constant *LJ_tmPWM8* makes for programming code that is easier to read.

Following is pseudocode that performs various actions. First, a call is done to open the device. The primary work done with this call is finding the desired device and creating a handle that points to the device for further function calls. In addition, opening the device performs various configuration and initialization actions, such as reading the calibration constants from the device:

```
//Use the following line to open the first found LabJack U3
//over USB and get a handle to the device.
//The general form of the open function is:
//OpenLabJack (DeviceType, ConnectionType, Address, FirstFound, *Handle)

//Open the first found LabJack U3 over USB.
IngErrorcode = OpenLabJack (LJ_dtU3, LJ_ctUSB, "1", TRUE, &IngHandle);
```

Second, a list of requests is built in the UD driver using *AddRequest* calls. This does not involve any low-level communication with the device, and thus the execution time is relatively instantaneous:

```
//Request that DAC0 be set to 2.5 volts.
//The general form of the AddRequest function is:
//AddRequest (Handle, IOType, Channel, Value, x1, UserData)
IngErrorcode = AddRequest (IngHandle, LJ_ioPUT_DAC, 0, 2.50, 0, 0);

//Request a read from AIN3 (FIO3), assuming it has been enabled as
//an analog line.
IngErrorcode = AddRequest (IngHandle, LJ_ioGET_AIN, 3, 0, 0, 0);
```

Third, the list of requests is processed and executed using a *Go* call. In this step, the driver determines which low-level commands must be executed to process all the requests, calls those low-level functions, and stores the results. This example consists of two requests, one analog input read and one analog output write, which can both be handled in a single low-level *Feedback* call ([Section 5.2.5](#)):

```
//Execute the requests.
IngErrorcode = GoOne (IngHandle);
```

Finally, *GetResult* calls are used to retrieve the results (errorcodes and values) that were stored by the driver during the *Go* call. This does not involve any low-level communication with the device, and thus the execution time is relatively instantaneous:

```
//Get the result of the DAC0 request just to check for an errorcode.
//The general form of the GetResult function is:
//GetResult (Handle, IOType, Channel, *Value)
IngErrorcode = GetResult (IngHandle, LJ_ioPUT_DAC, 0, 0);

//Get the AIN3 voltage. We pass the address to dblValue and the
//voltage will be returned in that variable.
IngErrorcode = GetResult (IngHandle, LJ_ioGET_AIN, 3, &dblValue);
```

The *AddRequest/Go/GetResult* method is often the most efficient. As shown above, multiple requests can be executed with a single *Go()* or *GoOne()* call, and the driver might be able to optimize the requests into fewer low-level calls. The other option is to use the *eGet* or *ePut* functions which combine the *AddRequest/Go/GetResult* into one call. The above code would then look like (assuming the U3 is already open):

```
//Set DAC0 to 2.5 volts.
//The general form of the ePut function is:
//ePut (Handle, IOType, Channel, Value, x1)
IngErrorcode = ePut (IngHandle, LJ_ioPUT_DAC, 0, 2.50, 0);

//Read AIN3.
//The general form of the eGet function is:
//eGet (Handle, IOType, Channel, *Value, x1)
IngErrorcode = eGet (IngHandle, LJ_ioGET_AIN, 3, &dblValue, 0);
```

In the case of the U3, the first example using *add/go/get* handles both the *DAC* command and *AIN* read in a single low-level call, while in the second example using *ePut/eGet* two low-level commands are used. Examples in the following documentation will use both the *add/go/get* method and the *ePut/eGet* method, and they are generally interchangeable. See [Section 4.3](#) for more pseudocode examples.

All the request and result functions always have 4 common parameters, and some of the functions have 2 extra parameters:

- **Handle** – This is an input to all request/result functions that tells the function what LabJack it is talking to. The handle is obtained from the *OpenLabJack* function.
- **IOType** – This is an input to all request/result functions that specifies what type of action is being done.
- **Channel** – This is an input to all request/result functions that generally specifies which channel of I/O is being written/read, although with the config IOTypes special constants are passed for channel to specify what is being configured.
- **Value** – This is an input or output to all request/result functions that is used to write or read the value for the item being operated on.
- **x1** – This parameter is only used in some of the request/result functions, and is used when extra information is needed for certain IOTypes. In this parameter has meaning for an IOType that will be pointed out in the pseudocode sections of this datasheet.
- **UserData** – This parameter is only used in some of the request/result functions, and is data that is simply passed along with the request, and returned unmodified by the result. Can be used to store any sort of information with the request, to allow a generic parser to determine what

should be done when the results are received. If this parameter has meaning for an IOType that will be pointed out in the pseudocode sections of this datasheet.

4.1.1 - Function Flexibility [U3 Datasheet]

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The driver is designed to be flexible so that it can work with various different LabJacks with different capabilities. It is also designed to work with different development platforms with different capabilities. For this reason, many of the functions are repeated with different forms of parameters, although their internal functionality remains mostly the same. In this documentation, a group of functions will often be referred to by their shortest name. For example, a reference to Add or AddRequest most likely refers to any of the three variations: AddRequest(), AddRequestS() or AddRequestSS(). In the sample code, alternate functions (S or SS versions) can generally be substituted as desired, changing the parameter types accordingly. All samples here are written in pseudo-C. Functions with an "S" or "SS" appended are provided for programming languages that can't include the LabJackUD.h file and therefore can't use the constants included. It is generally poor programming form to hardcode numbers into function calls, if for no other reason than it is hard to read. Functions with a single "S" replace the IOType parameter with a const char * which is a string. A string can then be passed with the name of the desired constant. Functions with a double "SS" replace both the IOType and Channel with strings. OpenLabJackS replaces both DeviceType and ConnectionType with strings since both take constants. For example: In C, where the LabJackUD.h file can be included and the constants used directly:

```
AddRequest(Handle, LJ_ioGET_CONFIG, LJ_chHARDWARE_VERSION,0,0,0);
```

The bad way (hard to read) when LabJackUD.h cannot be included:

```
AddRequest(Handle, 1001, 10, 0, 0, 0);
```

The better way when LabJackUD.h cannot be included, is to pass strings:

```
AddRequestSS(Handle, "LJ_ioGET_CONFIG", "LJ_chHARDWARE_VERSION",0,0,0);
```

Continuing on this vein, the function StringToConstant() is useful for error handling routines, or with the GetFirst/Next functions which do not take strings. The StringToConstant() function takes a string and returns the numeric constant. So, for example:

```
LJ_ERROR err;
err = AddRequestSS(Handle, "LJ_ioGET_CONFIG", "LJ_chHARDWARE_VERSION",0,0,0);
if (err == StringToConstant("LJE_INVALID_DEVICE_TYPE"))
    do some error handling..
```

Once again, this is much clearer than:

```
if (err == 2)
```

4.1.2 - Multi-Threaded Operation [U3 Datasheet]

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The UD driver is completely thread safe. With some very minor exceptions, all these functions can be called from multiple threads at the same time and the driver will keep everything straight. Because of this Add, Go, and Get must be called from the same thread for a particular set of requests/results. Internally the list of requests and results are split by thread. This allows multiple threads to be used to make requests without accidentally getting data from one thread into another. If requests are added, and then results return *LJE_NO_DATA_AVAILABLE* or a similar error, chances are the requests and results are in different threads.

The driver tracks which thread a request is made in by the thread ID. If a thread is killed and then a new one is created, it is possible for the new thread to have the same ID. Its not really a problem if Add is called first, but if Get is called on a new thread results could be returned from the thread that already ended.

As mentioned, the list of requests and results is kept on a thread-by-thread basis. Since the driver cannot tell when a thread has ended, the results are kept in memory for that thread regardless. This is not a problem in general as the driver will clean it all up when unloaded. When it can be a problem is in situations where threads are created and destroyed continuously. This will result in the slow consumption of memory as requests on old threads are left behind. Since each request only uses 44 bytes, and as mentioned the ID's will eventually get recycled, it will not be a huge memory loss. In general, even without this issue, it is strongly recommended to not create and destroy a lot of threads. It is terribly slow and inefficient. Use thread pools and other techniques to keep new thread creation to a minimum. That is what is done internally.

The one big exception to the thread safety of this driver is in the use of the Windows TerminateThread() function. As is warned in the MSDN documentation, using TerminateThread() will kill the thread without releasing any resources, and more importantly, releasing any synchronization objects. If TerminateThread() is used on a thread that is currently in the middle of a call to this driver, more than likely a synchronization object will be left open on the particular device and access to the device will be impossible until the application is restarted. On some devices, it can be worse. On devices that have interprocess synchronization, such as the U12, calling TerminateThread() may kill all access to the device through this driver no matter which process is using it and even if the application is restarted. Avoid using TerminateThread()! All device calls have a timeout, which defaults to 1 second, but can be changed. Make sure to wait at least as long as the timeout for the driver to finish.

4.2 - Function Reference [U3 Datasheet]

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Function Reference Overview

The LabJack driver file is named LabJackUD.dll, and contains the functions described in this section.

Some parameters are common to many functions:

* **LJ_ERROR** – A LabJack specific numeric errorcode. 0 means no error. (long, signed 32-bit integer).

* **LJ_HANDLE** – This value is returned by OpenLabJack, and then passed on to other functions to identify the opened LabJack. (long, signed 32-bit integer).

To maintain compatibility with as many languages as possible, every attempt has been made to keep the parameter types very basic. Also, many functions have multiple prototypes. The declarations that follow, are written in C.

To help those unfamiliar with strings in C, these functions expect null terminated 8 bitASCII strings. How this translates to a particular development environment is beyond the scope of this documentation. A const char * is a pointer to a string that won't be changed by the driver. Usually this means it can simply be a constant such as "this is a string". A char * is a pointer to a string that will be changed. Enough bytes must be preallocated to hold the possible strings that will be returned. Functions with char * in their declaration will have the required length of the buffer documented below.

Pointers must be initialized in general, although null (0) can be passed for unused or unneeded values. The pointers for GetStreamData and RawIn/RawOut requests are not optional. Arrays and char * type strings must be initialized to the proper size before passing to the DLL.

4.2.1 - ListAll() [U3 Datasheet]

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Returns all the devices found of a given DeviceType and ConnectionType. Searching over Ethernet relies on the DiscoveryUDP function ([Section 5.2.3](#)), which might not work on certain network configurations.

ListAllS() is a special version where DeviceType and ConnectionType are strings rather than longs. This is useful for passing string constants in languages that cannot include the header file. The strings should contain the constant name as indicated in the header file (such as "LJ_dtUE9" and "LJ_ctUSB"). The declaration for the S version of open is the same as below except for (const char *pDeviceType, const char *pConnectionType, ...).

Declaration:

```
LJ_ERROR _stdcall ListAll ( long DeviceType,
                          long ConnectionType,
                          long *pNumFound,
                          long *pSerialNumbers,
                          long *pIDs,
                          double *pAddresses)
```

Parameter Description:

Returns: LabJack errorcodes or 0 for no error.

Inputs:

- **DeviceType** – The type of LabJack to search for. LJ_dtUE9 (9), LJ_dtU3 (3), or LJ_dtU6 (6). For other LabJack devices, see [What driver/library should I use with my LabJack?](#)
- **ConnectionType** – Enter the constant for the type of connection to use in the search. LJ_ctUSB (1) or LJ_ctETHERNET (2) (UE9 only).
- **pSerialNumbers** – Must pass a pointer to a buffer with at least 128 elements.
- **pIDs** – Must pass a pointer to a buffer with at least 128 elements.
- **pAddresses** – Must pass a pointer to a buffer with at least 128 elements.

Outputs:

- **pNumFound** – Returns the number of devices found, and thus the number of valid elements in the return arrays.
- **pSerialNumbers** – Array contains serial numbers of any found devices.
- **pIDs** – Array contains local IDs of any found devices.
- **pAddresses** – Array contains IP addresses of any found devices. The function DoubleToStringAddress() is useful to convert these to string notation.

4.2.2 - OpenLabJack() [U3 Datasheet]

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Call OpenLabJack() before communicating with a device. This function can be called multiple times, however, once a LabJack is open, it remains

open until your application ends (or the DLL is unloaded). If OpenLabJack is called repeatedly with the same parameters, thus requesting the same type of connection to the same LabJack, the driver will simply return the same LJ_HANDLE every time. Internally, nothing else happens. This includes when the device is reset, or disconnected. Once the device is reconnected, the driver will maintain the same handle. If an open call is made for USB, and then Ethernet, a different handle will be returned for each connection type and both connections will be open.

OpenLabJackS() is a special version of open where DeviceType and ConnectionType are strings rather than longs. This is useful for passing string constants in languages that cannot include the header file. The strings should contain the constant name as indicated in the header file (such as "LJ_dtUE9" and "LJ_ctUSB"). The declaration for the S version of open is the same as below except for (const char *pDeviceType, const char *pConnectionType, ...).

Declaration:

```
LJ_ERROR _stdcall OpenLabJack ( long DeviceType,
                              long ConnectionType,
                              const char *pAddress,
                              long FirstFound,
                              LJ_HANDLE *pHandle)
```

Parameter Description:

Returns: LabJack errorcodes or 0 for no error.

Inputs:

- **DeviceType** – The type of LabJack to open. LJ_dtUE9 (9), LJ_dtU3 (3), or LJ_dtU6 (6). For other LabJack devices, see [What driver/library should I use with my LabJack?](#)
- **ConnectionType** – Enter the constant for the type of connection. LJ_ctUSB (1) or LJ_ctETHERNET (2) (UE9 only).
- **pAddress** – For USB, pass the local ID or serial number of the desired LabJack. For Ethernet pass the IP address of the desired LabJack. If FirstFound is true, Address is ignored.
- **FirstFound** – If true, then the Address and ConnectionType parameters are ignored and the driver opens the first LabJack found with the specified DeviceType. Generally only recommended when a single LabJack is connected. Currently only supported with USB. If a USB device is not found, it will try Ethernet but with the given Address.

Outputs:

- **pHandle** – A pointer to a handle for a LabJack.

4.2.3 - eGet() and ePut() [U3 Datasheet]

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The eGet and ePut functions do AddRequest, Go, and GetResult, in one step.

The eGet versions are designed for inputs or retrieving parameters as they take a pointer to a double where the result is placed, but can be used for outputs if pValue is preset to the desired value. This is also useful for things like StreamRead where a value is input and output (number of scans requested and number of scans returned).

The ePut versions are designed for outputs or setting configuration parameters and will not return anything except the errorcode.

eGetPtr() is a 32-bit and 64-bit pointer-address-safe version of the eGet function where the x1 parameter is a void *. This is required when passing a pointer safely to x1. When passing a non-pointer long value to x1, use the normal version of the function. The declaration for the Ptr version is the same as the normal version except for (... , void *x1).

eGetS() and ePutS() are special versions of these functions where IOType is a string rather than a long. This is useful for passing string constants in languages that cannot include the header file, and is generally used with all IOTypes except put/get config. The string should contain the constant name as indicated in the header file (such as "LJ_ioANALOG_INPUT"). The declarations for the S versions are the same as the normal versions except for (... , const char *pIOType, ...).

eGetSS() and ePutSS() are special versions of these functions where IOType and Channel are strings rather than longs. This is useful for passing string constants in languages that cannot include the header file, and is generally only used with the put/get config IOTypes. The strings should contain the constant name as indicated in the header file (such as "LJ_ioPUT_CONFIG" and "LJ_chLOCALID"). The declaration for the SS versions are the same as the normal versions except for (... , const char *pIOType, const char *pChannel, ...).

The declaration for ePut is the same as eGet except that Value is not a pointer (... , double Value, ...), and thus is an input only.

Declaration:

```
LJ_ERROR _stdcall eGet ( LJ_HANDLE Handle,
                        long IOType,
                        long Channel,
                        double *pValue,
                        long x1)
```

Parameter Description:

Returns: LabJack errorcodes or 0 for no error.

Inputs:

- **Handle** – Handle returned by OpenLabJack().
- **IOType** – The type of request. See Section 4.3 ([UE9](#), [U6](#), [U3](#)) of your device's user guide.
- **Channel** – The channel number of the particular IOType.

- **pValue** – Pointer to Value sends and receives data.
- **x1** – Optional parameter used by some IOTypes.

Outputs:

- **pValue** – Pointer to Value sends and receives data.

Declaration:

```
LJ_ERROR _stdcall ePut ( LJ_HANDLE Handle,
                        long IOType,
                        long Channel,
                        double Value,
                        long x1)
```

Parameter Description:

Returns: LabJack errorcodes or 0 for no error.

Inputs:

- **Handle** – Handle returned by OpenLabJack().
- **IOType** – The type of request. See Section 4.3 ([UE9](#), [U6](#), [U3](#)) of your device's user guide.
- **Channel** – The channel number of the particular IOType.
- **Value** – The value to set.
- **x1** – Optional parameter used by some IOTypes.

Outputs:

- **None**

4.2.4 - eAddGoGet() [U3 Datasheet]

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This function passes multiple requests via arrays, then executes a GoOne() and returns all the results via the same arrays.

The parameters that start with "a" are arrays, and all must be initialized with at least a number of elements equal to NumRequests.

Declaration:

```
LJ_ERROR _stdcall eAddGoGet ( LJ_HANDLE Handle,
                             long NumRequests,
                             long *aIOTypes,
                             long *aChannels,
                             double *aValues,
                             long *ax1s,
                             long *aRequestErrors,
                             long *GoError,
                             long *aResultErrors)
```

Parameter Description:

Returns: LabJack errorcodes or 0 for no error.

Inputs:

- **Handle** – Handle returned by OpenLabJack().
- **NumRequests** – This is the number of requests that will be made, and thus the number of results that will be returned. All the arrays must be initialized with at least this many elements.
- **aIOTypes** – An array which is the list of IOTypes.
- **aChannels** – An array which is the list of Channels.
- **aValues** – An array which is the list of Values to write.
- **ax1s** – An array which is the list of x1s.

Outputs:

- **aValues** – An array which is the list of Values read.
- **aRequestErrors** – An array which is the list of errorcodes from each AddRequest().
- **GoError** – The errorcode returned by the GoOne() call.
- **aResultErrors** – An array which is the list of errorcodes from each GetResult().

4.2.5 - AddRequest() [U3 Datasheet]

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Adds an item to the list of requests to be performed on the next call to Go() or GoOne().

When AddRequest() is called on a particular Handle after a Go() or GoOne() call, all data from previous requests is lost and cannot be retrieved by any of the Get functions until a Go function is called again. This is on a device by device basis, so you can call AddRequest() with a different handle while a device is busy performing its I/O.

AddRequest() only clears the request and result lists on the device handle passed and only for the current thread. For example, if a request is added to each of two different devices, and then a new request is added to the first device but not the second, a call to Go() will cause the first device to execute the new request and the second device to execute the original request.

In general, the execution order of a list of requests in a single Go call is unpredictable, except that all configuration type requests are executed before acquisition and output type requests.

AddRequestPtr() is a 32-bit and 64-bit pointer-address-safe version of the Add function where the x1 parameter is a void *. This is required when passing a pointer safely to x1. When passing a non-pointer long value to x1, use the normal AddRequest() function. The declaration for the Ptr version of Add is the same as below except for (... , void *x1, ...).

AddRequestS() is a special version of the Add function where IOType is a string rather than a long. This is useful for passing string constants in languages that cannot include the header file, and is generally used with all IOTypes except put/get config. The string should contain the constant name as indicated in the header file (such as "LJ_ioANALOG_INPUT"). The declaration for the S version of Add is the same as below except for (... , const char *pIOType, ...).

AddRequestSS() is a special version of the Add function where IOType and Channel are strings rather than longs. This is useful for passing string constants in languages that cannot include the header file, and is generally only used with the put/get config IOTypes. The strings should contain the constant name as indicated in the header file (such as "LJ_ioPUT_CONFIG" and "LJ_chLOCALID"). The declaration for the SS version of Add is the same as below except for (... , const char *pIOType, const char *pChannel, ...).

Declaration:

```
LJ_ERROR_stdcall AddRequest ( LJ_HANDLE Handle,
                             long IOType,
                             long Channel,
                             double Value,
                             long x1,
                             double UserData)
```

Parameter Description:

Returns: LabJack errorcodes or 0 for no error.

Inputs:

- **Handle** – Handle returned by OpenLabJack().
- **IOType** – The type of request. See Section 4.3 ([UE9](#), [U6](#), [U3](#)) of your device's user guide.
- **Channel** – The channel number of the particular IOType.
- **Value** – Value passed for output channels.
- **x1** – Optional parameter used by some IOTypes.
- **UserData** – Data that is simply passed along with the request, and returned unmodified by GetFirstResult() or GetNextResult(). Can be used to store any sort of information with the request, to allow a generic parser to determine what should be done when the results are received.

Outputs:

- **None**

4.2.6 - Go() [U3 Datasheet]

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After using AddRequest() to make an internal list of requests to perform, call Go() to actually perform the requests. This function causes all requests on all open LabJacks to be performed. After calling Go(), call GetResult() or similar to retrieve any returned data or errors.

Go() can be called repeatedly to repeat the current list of requests. Go() does not clear the list of requests. Rather, after a call to Go(), the first subsequent AddRequest() call to a particular device will clear the previous list of requests on that particular device only.

Note that for a single Go() or GoOne() call, the order of execution of the request list cannot be predicted. Since the driver does internal optimization, it is quite likely not the same as the order of AddRequest() function calls. One thing that is known, is that configuration settings like ranges, stream settings, and such, will be done before the actual acquisition or setting of outputs.

Declaration:

```
LJ_ERROR_stdcall Go()
```

Parameter Description:

Returns: LabJack errorcodes or 0 for no error.

Inputs:

- **None**

Outputs:

- **None**

4.2.7 - GoOne() [U3 Datasheet]

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After using AddRequest() to make an internal list of requests to perform, call GoOne() to actually perform the requests. This function causes all requests on one particular LabJack to be performed. After calling GoOne(), call GetResult() or similar to retrieve any returned data or errors.

GoOne() can be called repeatedly to repeat the current list of requests. GoOne() does not clear the list of requests. Rather, after a particular device has performed a GoOne(), the first subsequent AddRequest() call to that device will clear the previous list of requests on that particular device only.

Note that for a single Go() or GoOne() call, the order of execution of the request list cannot be predicted. Since the driver does internal optimization, it is quite likely not the same as the order of AddRequest() function calls. One thing that is known, is that configuration settings like ranges, stream settings, and such, will be done before the actual acquisition or setting of outputs.

Declaration:

```
LJ_ERROR _stdcall GoOne( LJ_HANDLE Handle )
```

Parameter Description:

Returns: LabJack errorcodes or 0 for no error.

Inputs:

- **Handle** – Handle returned by OpenLabJack().

Outputs:

- **None**

4.2.8 - GetResult() [U3 Datasheet]

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Calling either Go function creates a list of results that matches the list of requests. Use GetResult() to read the result and errorcode for a particular IOType and Channel. Normally this function is called for each associated AddRequest() item. Even if the request was an output, the errorcode should be evaluated.

None of the Get functions will clear results from the list. The first AddRequest() call subsequent to a Go call will clear the internal lists of requests and results for a particular device.

When processing raw in/out or stream data requests, the call to a Get function does not actually cause the data arrays to be filled. The arrays are filled during the Go call (if data is available), and the Get call is used to find out many elements were placed in the array.

GetResultS() is a special version of the Get function where IOType is a string rather than a long. This is useful for passing string constants in languages that cannot include the header file, and is generally used with all IOTypes except put/get config. The string should contain the constant name as indicated in the header file (such as "LJ_ioANALOG_INPUT"). The declaration for the S version of Get is the same as below except for (... , const char *pIOType, ...).

GetResultSS() is a special version of the Get function where IOType and Channel are strings rather than longs. This is useful for passing string constants in languages that cannot include the header file, and is generally only used with the put/get config IOTypes. The strings should contain the constant name as indicated in the header file (such as "LJ_ioPUT_CONFIG" and "LJ_chLOCALID"). The declaration for the SS version of Get is the same as below except for (... , const char *pIOType, const char *pChannel, ...).

It is acceptable to pass NULL (or 0) for any pointer that is not required.

Declaration:

```
LJ_ERROR _stdcall GetResult( LJ_HANDLE Handle,
                           long IOType,
                           long Channel,
                           double *pValue)
```

Parameter Description:

Returns: LabJack errorcodes or 0 for no error.

Inputs:

- **Handle** – Handle returned by OpenLabJack().
- **IOType** – The type of request. See Section 4.3 (UE9, U6, U3) of your device's user guide.
- **Channel** – The channel number of the particular IOType.

Outputs:

- **pValue** – A pointer to the result value.

4.2.9 - GetFirstResult() and GetNextResult() [U3 Datasheet]

[Log in](#) or [register](#) to post comments

Calling either Go function creates a list of results that matches the list of requests. Use GetFirstResult() and GetNextResult() to step through the list

of results in order. When either function returns LJE_NO_MORE_DATA_AVAILABLE, there are no more items in the list of results. Items can be read more than once by calling GetFirstResult() to move back to the beginning of the list.

UserData is provided for tracking information, or whatever else the user might need.

None of the Get functions clear results from the list. The first AddRequest() call subsequent to a Go call will clear the internal lists of requests and results for a particular device.

When processing raw in/out or stream data requests, the call to a Get function does not actually cause the data arrays to be filled. The arrays are filled during the Go call (if data is available), and the Get call is used to find out many elements were placed in the array.

It is acceptable to pass NULL (or 0) for any pointer that is not required.

The parameter lists are the same for the GetFirstResult() and GetNextResult() declarations.

Declaration:

```
LJ_ERROR _stdcall GetFirstResult ( LJ_HANDLE Handle,
                                long *pIOType,
                                long *pChannel,
                                double *pValue,
                                long *px1,
                                double *pUserData)
```

Parameter Description:

Returns: LabJack errorcodes or 0 for no error.

Inputs:

- **Handle** – Handle returned by OpenLabJack().

Outputs:

- **pIOType** – A pointer to the IOType of this item in the list.
- **pChannel** – A pointer to the channel number of this item in the list.
- **pValue** – A pointer to the result value.
- **px1** – A pointer to the x1 parameter of this item in the list.
- **pUserData** – A pointer to data that is simply passed along with the request, and returned unmodified. Can be used to store any sort of information with the request, to allow a generic parser to determine what should be done when the results are received.

4.2.10 - DoubleToStringAddress() [U3 Datasheet]

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Some special-channels of the config IOType pass IP address (and others) in a double. This function is used to convert the double into a string in normal decimal-dot or hex-dot notation.

Declaration:

```
LJ_ERROR _stdcall DoubleToStringAddress ( double Number,
                                         char *pString,
                                         long HexDot)
```

Parameter Description:

Returns: LabJack errorcodes or 0 for no error.

Inputs:

- **Number** – Double precision number to be converted.
- **pString** – Must pass a buffer for the string of at least 24 bytes.
- **HexDot** – If not equal to zero, the string will be in hex-dot notation rather than decimal-dot.

Outputs:

- **pString** – A pointer to the string representation.

4.2.11 - StringToDoubleAddress() [U3 Datasheet]

[Log in](#) or [register](#) to post comments

Some special-channels of the config IOType pass IP address (and others) in a double. This function is used to convert a string in normal decimal-dot or hex-dot notation into a double.

Declaration:

```
LJ_ERROR _stdcall StringToDoubleAddress ( const char *pString,
                                         double *pNumber,
                                         long HexDot)
```

Parameter Description:

Returns: LabJack errorcodes or 0 for no error.

Inputs:

- **pString** – A pointer to the string representation.
- **HexDot** – If not equal to zero, the passed string should be in hex-dot notation rather than decimal-dot.

Outputs:

- **pNumber** – A pointer to the double precision representation.

4.2.12 - StringToConstant() [U3 Datasheet]

[Log in](#) or [register](#) to post comments

Converts the given string to the appropriate constant number. Used internally by the S functions, but could be useful to the end user when using the GetFirst/Next functions without the ability to include the header file. In this case a comparison could be done on the return values such as:

```
if (IOType == StringToConstant("LJ_ioANALOG_INPUT"))
```

This function returns *LJ_INVALID_CONSTANT* if the string is not recognized.

Declaration:

```
long _stdcall StringToConstant ( const char *pString )
```

Parameter Description:

Returns: Constant number of the passed string.

Inputs:

- **pString** – A pointer to the string representation of the constant.

Outputs:

- **None**

4.2.13 - ErrorToString() [U3 Datasheet]

[Log in](#) or [register](#) to post comments

Outputs a string describing the given error code or an empty string if not found.

Declaration:

```
void _stdcall ErrorToString ( LJ_ERROR ErrorCode,  
                             char *pString)
```

Parameter Description:

Returns: LabJack errorcodes or 0 for no error.

Inputs:

- **ErrorCode** – LabJack errorcode.
- **pString** – Must pass a buffer for the string of at least 256 bytes.

Outputs:

- ***pString** – A pointer to the string representation of the errorcode.

4.2.14 - GetDriverVersion() [U3 Datasheet]

[Log in](#) or [register](#) to post comments

Returns the version number of this Windows LabJack driver.

Declaration:

```
double _stdcall GetDriverVersion();
```

Parameter Description:

Returns: Driver version.

Inputs:

- **None**

Outputs:

- **None**

4.2.15 - TCVoltsToTemp() [U3 Datasheet]

[Log in](#) or [register](#) to post comments

A utility function to convert thermocouple voltage readings to temperature.

Declaration:

```
LJ_ERROR _stdcall TCVoltsToTemp ( long TCType,
                                double TCVolts,
                                double CJTempK,
                                double *pTCTempK)
```

Parameter Description:

Returns: LabJack errorcodes or 0 for no error.

Inputs:

- **TCType** – A constant that specifies the thermocouple type: LJ_ttB, LJ_ttE, LJ_ttJ, LJ_ttK, LJ_ttN, LJ_ttR, LJ_ttS, or LJ_ttT.
- **TCVolts** – The thermocouple voltage.
- **CJTempK** – The temperature of the cold junction in degrees K.

Outputs:

- **pTCTempK** – Returns the calculated thermocouple temperature.

4.2.16 - ResetLabJack() [U3 Datasheet]

[Log in](#) or [register](#) to post comments

Sends a reset command to the LabJack hardware. Reset causes the device to go to the reset/power-up configuration.

Resetting the LabJack does not invalidate the handle, thus the device does not have to be opened again after a reset, but a Go call is likely to fail for a couple seconds after until the LabJack is ready.

Declaration:

```
LJ_ERROR _stdcall ResetLabJack ( LJ_HANDLE Handle );
```

Parameter Description:

Returns: LabJack errorcodes or 0 for no error.

Inputs:

- **Handle** – Handle returned by OpenLabJack().

Outputs:

- **None**

4.2.17 - eAIN() [U3 Datasheet]

[Log in](#) or [register](#) to post comments

An easy function that returns a reading from one analog input. This is a simple alternative to the very flexible IOType based method normally used by this driver.

When needed, this function automatically configures the specified channel(s) for analog input.

Declaration:

```
LJ_ERROR _stdcall eAIN ( LJ_HANDLE Handle,
                        long ChannelP,
                        long ChannelN,
                        double *Voltage,
                        long Range,
                        long Resolution,
                        long Settling,
                        long Binary,
                        long Reserved1,
                        long Reserved2)
```

Parameter Description:

Returns: LabJack errorcodes or 0 for no error.

Inputs:

- **Handle** – Handle returned by OpenLabJack().
- **ChannelP** – The positive AIN channel to acquire.

- **ChannelN** – The negative AIN channel to acquire. For single-ended channels on the U3, this parameter should be 31 or 199 (see Section 2.6.1).
- **Range** – Ignored on the U3.
- **Resolution** – Pass a nonzero value to enable QuickSample.
- **Settling** – Pass a nonzero value to enable LongSettling.
- **Binary** – If this is nonzero (True), the Voltage parameter will return the raw binary value.
- **Reserved (1&2)** – Pass 0.

Outputs:

- **Voltage** – Returns the analog input reading, which is generally a voltage.

4.2.18 - eDAC() [U3 Datasheet]

[Log in](#) or [register](#) to post comments

An easy function that writes a value to one analog output. This is a simple alternative to the very flexible IOType based method normally used by this driver.

When needed, this function automatically enables the specified analog output.

Declaration:

```
LJ_ERROR _stdcall eDAC ( LJ_HANDLE Handle,
                        long Channel,
                        double Voltage,
                        long Binary,
                        long Reserved1,
                        long Reserved2)
```

Parameter Description:

Returns: LabJack errorcodes or 0 for no error.

Inputs:

- **Handle** – Handle returned by OpenLabJack().
- **Channel** – The analog output channel to write to.
- **Voltage** – The voltage to write to the analog output.
- **Binary** – If this is nonzero (True), the value passed for Voltage should be binary. For example, pass 32768.0 in the double parameter for mid-scale output.
- **Reserved (1&2)** – Pass 0.

4.2.19 - eDI() [U3 Datasheet]

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An easy function that reads the state of one digital input. This is a simple alternative to the very flexible IOType based method normally used by this driver.

When needed, this function automatically configures the specified channel as a digital input.

Declaration:

```
LJ_ERROR _stdcall eDI ( LJ_HANDLE Handle,
                        long Channel,
                        long *State)
```

Parameter Description:

Returns: LabJack errorcodes or 0 for no error.

Inputs:

- **Handle** – Handle returned by OpenLabJack().
- **Channel** – The channel to read. 0-19 corresponds to FIO0-CIO3.

Outputs:

- **State** – Returns the state of the digital input. 0=False=Low and 1=True=High.

4.2.20 - eDO() [U3 Datasheet]

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An easy function that writes the state of one digital output. This is a simple alternative to the very flexible IOType based method normally used by this driver.

When needed, this function automatically configures the specified channel as a digital output.

Declaration:

```
LJ_ERROR _stdcall eDO ( LJ_HANDLE Handle,
                      long Channel,
                      long State)
```

Parameter Description:

Returns: LabJack errorcodes or 0 for no error.

Inputs:

- **Handle** – Handle returned by OpenLabJack().
- **Channel** – The channel to write to. 0-19 corresponds to FIO0-CIO3.
- **State** – The state to write to the digital output. 0=False=Low and 1=True=High.

4.2.21 - eTCConfig() [U3 Datasheet]

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An easy function that configures and initializes all the timers and counters. This is a simple alternative to the very flexible IOType based method normally used by this driver.

When needed, this function automatically configures the needed lines as digital.

Declaration:

```
LJ_ERROR _stdcall eTCConfig ( LJ_HANDLE Handle,
                          long *aEnableTimers,
                          long *aEnableCounters,
                          long TCPinOffset,
                          long TimerClockBaseIndex,
                          long TimerClockDivisor,
                          long *aTimerModes,
                          double *aTimerValues,
                          long Reserved1,
                          long Reserved2)
```

Parameter Description:

Returns: LabJack errorcodes or 0 for no error.

Inputs:

- **Handle** – Handle returned by OpenLabJack().
- **aEnableTimers** – An array where each element specifies whether that timer is enabled. Timers must be enabled in order starting from 0, so for instance, Timer0 and Timer2 cannot be enabled without enabling Timer1 also. A nonzero value for an array element specifies to enable that timer. Array size must be equal to the number of timers available on the device.¹
- **aEnableCounters** – An array where each element specifies whether that counter is enabled. Counters do not have to be enabled in order starting from 0, so Counter1 can be enabled when Counter0 is disabled. A nonzero value for an array element specifies to enable that counter. Array size must be equal to the number of counters available on the device.²
- **TCPinOffset** – Value specifies where to start assigning timers and counters.³
- **TimerClockBaseIndex** – Pass a constant to set the timer base clock. The default is device specific.⁴
- **TimerClockDivisor** – Pass a divisor from 0-255 where 0 is a divisor of 256.
- **aTimerModes** – An array where each element is a constant specifying the mode for that timer. Array size must be equal to the number of timers available on the device.¹
- **aTimerValues** – An array where each element is specifies the initial value for that timer. Array size must be equal to the number of timers available on the device.¹
- **Reserved (1&2)** – Pass 0.

¹ Number of timers UE9:6, U6:4, U3:2

² Number of counters UE9:2, U6:2, U3:2

³ Pin offset UE9:Ignored, U6:0-8, U3:4-8

⁴ Default constant UE9:LJ_tc750KHZ, U6:LJ_tc48MHZ, U3:LJ_tc48MHZ

4.2.22 - eTCValues() [U3 Datasheet]

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An easy function that updates and reads all the timers and counters. This is a simple alternative to the very flexible IOType based method normally used by this driver.

Declaration:

```
LJ_ERROR _stdcall eTCValues ( LJ_HANDLE Handle,
                          long *aReadTimers,
                          long *aUpdateResetTimers,
                          long *aReadCounters,
                          long *aResetCounters,
```

```
double *aTimerValues,
double *aCounterValues,
long Reserved1,
long Reserved2)
```

Parameter Description:

Returns: LabJack errorcodes or 0 for no error.

Inputs:

- **Handle** – Handle returned by OpenLabJack().
- **aReadTimers** – An array where each element specifies whether to read that timer. A nonzero value for an array element specifies to read that timer.¹
- **aUpdateResetTimers** – An array where each element specifies whether to update/reset that timer. A nonzero value for an array element specifies to update/reset that timer.¹
- **aReadCounters** – An array where each element specifies whether to read that counter. A nonzero value for an array element specifies to read that counter.²
- **aResetCounters** – An array where each element specifies whether to reset that counter. A nonzero value for an array element specifies to reset that counter.²
- **aTimerValues** – An array where each element is the new value for that timer. Each value is only updated if the appropriate element is set in the aUpdateResetTimers array.¹
- **Reserved (1&2)** – Pass 0.

Outputs:

- **aTimerValues** – An array where each element is the value read from that timer if the appropriate element is set in the aReadTimers array.
- **aCounterValues** – An array where each element is the value read from that counter if the appropriate element is set in the aReadCounters array.

¹ Array size must be equal to the number of timers available on the device. UE9:6, U6:4, U3:2

² Array size must be equal to the number of counters available on the device. UE9:2, U6:2, U3:2

4.3 - Example Pseudocode [U3 Datasheet]

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Example Pseudocode Overview

The following pseudocode examples are simplified for clarity, and in particular no error checking is shown. The language used for the pseudocode is C.

4.3.1 - Open [U3 Datasheet]

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The initial step is to open the LabJack and get a handle that the driver uses for further interaction. The DeviceType for the U3 is:

```
LJ_dtU3
```

There is only one valid ConnectionType for the U3:

```
LJ_ctUSB
```

Following is example pseudocode to open a U3 over USB:

```
//Open the first found LabJack U3 over USB.
OpenLabJack (LJ_dtU3, LJ_ctUSB, "1", TRUE, &IngHandle);
```

The reason for the quotes around the address ("1"), is because the address parameter is a string in the OpenLabJack function.

The ampersand (&) in front of IngHandle is a C notation that means we are passing the address of that variable, rather than the value of that variable. In the definition of the OpenLabJack function, the handle parameter is defined with an asterisk (*) in front, meaning that the function expects a pointer, i.e. an address.

In general, a function parameter is passed as a pointer (address) rather than a value, when the parameter might need to output something. The parameter value passed to a function in C cannot be modified in the function, but the parameter can be an address that points to a value that can be changed. Pointers are also used when passing arrays, as rather than actually passing the array, an address to the first element in the array is passed.

Talking to multiple devices from a single application is no problem. Make multiple open calls to get a handle to each device and be sure to set FirstFound=FALSE:

```
//Open U3s with Local ID #2 and #3.
OpenLabJack (LJ_dtU3, LJ_ctUSB, "2", FALSE, &IngHandleA);
OpenLabJack (LJ_dtU3, LJ_ctUSB, "3", FALSE, &IngHandleB);
```

... then when making further calls use the handle for the desired device.

4.3.2 - Configuration [U3 Datasheet]

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One of the most important operations on the U3 is configuring the flexible I/O as digital or analog. The following 4 IOTypes are used for that:

```
LJ_ioPUT_ANALOG_ENABLE_BIT
LJ_ioGET_ANALOG_ENABLE_BIT
LJ_ioPUT_ANALOG_ENABLE_PORT //x1 is number of bits.
LJ_ioGET_ANALOG_ENABLE_PORT //x1 is number of bits.
```

When a request is done with one of the portIOTypes, the Channel parameter is used to specify the starting bit number, and the x1 parameter is used to specify the number of applicable bits. Following are some pseudocode examples:

```
//Configure FIO3 as an analog input.
ePut (IngHandle, LJ_ioPUT_ANALOG_ENABLE_BIT, 3, 1, 0);

//Configure FIO3 as digital I/O.
ePut (IngHandle, LJ_ioPUT_ANALOG_ENABLE_BIT, 3, 0, 0);

//Configure FIO0-FIO2 and EIO0-EIO7 as analog, all others as digital. That
//means a starting channel of 0, a value of b1111111100000111 (=d65287), and
//all 16 bits will be updated.
ePut (IngHandle, LJ_ioPUT_ANALOG_ENABLE_PORT, 0, 65287, 16);

//Configure FIO2-FIO4 as analog, and FIO5-FIO6 as digital, without
//configuring any other bits. That means a starting channel of 2,
//a value of b00111 (=d7), and 5 bits will be updated.
ePut (IngHandle, LJ_ioPUT_ANALOG_ENABLE_PORT, 2, 7, 5);
```

Because of the pin configuration interaction between digital I/O, analog inputs, and timers/counters, many software applications will need to initialize the flexible I/O to a known pin configuration. One way to do this is with the following pseudocode:

```
ePut (IngHandle, LJ_ioPUT_CONFIG, LJ_chNUMBER_TIMERS_ENABLED, 0, 0);
ePut (IngHandle, LJ_ioPUT_CONFIG, LJ_chTIMER_COUNTER_PIN_OFFSET, 4, 0);
ePut (IngHandle, LJ_ioPUT_CONFIG, LJ_chTIMER_CLOCK_BASE, LJ_tc48MHZ, 0);
ePut (IngHandle, LJ_ioPUT_CONFIG, LJ_chTIMER_CLOCK_DIVISOR, 0, 0);
ePut (IngHandle, LJ_ioPUT_COUNTER_ENABLE, 0, 0, 0);
ePut (IngHandle, LJ_ioPUT_COUNTER_ENABLE, 1, 0, 0);
ePut (IngHandle, LJ_ioPUT_DAC_ENABLE, 1, 0, 0); //Ignored on hardware rev 1.30+.
ePut (IngHandle, LJ_ioPUT_ANALOG_ENABLE_PORT, 0, 0, 16);
```

This disables all timers and counters, sets the timer/counter pin offset to 4, sets the timer clock base to 48 MHz (no divisor), sets the timer clock divisor to 0, and sets all flexible I/O to digital. A simpler option is using the following IOType created exactly for this purpose, which does the same thing as the 8 function calls above:

```
ePut (IngHandle, LJ_ioPIN_CONFIGURATION_RESET, 0, 0, 0);
```

There are two IOTypes used to write or read general U3 configuration parameters:

```
LJ_ioPUT_CONFIG
LJ_ioGET_CONFIG
```

The following constants are then used in the channel parameter of the config function call to specify what is being written or read:

```
LJ_chLOCALID //0-255, Default=1
LJ_chHARDWARE_VERSION
LJ_chSERIAL_NUMBER
LJ_chFIRMWARE_VERSION
LJ_chBOOTLOADER_VERSION
LJ_chPRODUCTID
LJ_chLED_STATE
LJ_chU3HV //Reads TRUE if U3-HV. False if U3-LV or hardware version <1.30.
```

Following is example pseudocode to write and read the local ID:

```
//Set the local ID to 4.
ePut (IngHandle, LJ_ioPUT_CONFIG, LJ_chLOCALID, 4, 0);
```

```
//Read the local ID.
```

```
eGet (IngHandle, LJ_ioGET_CONFIG, LJ_chLOCALID, &dblValue, 0);
```

Currently there is no IOType for configuring all the power-up default settings. However, using low-level functionality you can.

To configure the U3 power-up default settings, first configure your I/O to the power-up default settings you want. This includes the configuration settings on this page, [Analog Outputs](#), [Digital I/O](#) and [Timers & Counters](#).

Then use the [Raw Output/Input](#) functionality to send/receive (LJ_ioRAW_OUT/LJ_ioRAW_IN) the low-level [SetDefaults](#) command/response. SetDefaults causes the current or last used U3 configuration to be stored in flash as the power-up defaults.

4.3.3 - Analog Inputs [U3 Datasheet]

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The IOTypes to retrieve a command/response analog input reading are:

```
LJ_ioGET_AIN      //Single-ended. Negative channel is fixed as 31/199.
LJ_ioGET_AIN_DIFF //Specify negative channel in x1.
```

The following are special channels, used with the get/put config IOTypes, to configure parameters that apply to all analog inputs:

```
LJ_chAIN_RESOLUTION //QuickSample enabled if TRUE.
LJ_chAIN_SETTLING_TIME //LongSettling enabled if TRUE.
LJ_chAIN_BINARY
```

Following is example pseudocode to read analog inputs:

```
//Execute the pin_configuration_reset IOType so that all
//pin assignments are in the factory default condition.
//The ePut function is used, which combines the add/go/get.
ePut (IngHandle, LJ_ioPIN_CONFIGURATION_RESET, 0, 0, 0);

//Configure FIO1, FIO2, and FIO6 as analog, all others as
//digital (see Section 4.3.2). b01000110 = 0x46 = d70.
//The ePut function is used, which combines the add/go/get.
ePut (IngHandle, LJ_ioPUT_ANALOG_ENABLE_PORT, 0, 70, 16);

//Now, an add/go/get block to execute multiple requests.

//Request a single-ended read from AIN2.
AddRequest (IngHandle, LJ_ioGET_AIN, 2, 0, 0, 0);

//Request a differential read of AIN1-AIN6.
AddRequest (IngHandle, LJ_ioGET_AIN_DIFF, 1, 0, 6, 0);

//Request a differential read of AIN1-Vref.
AddRequest (IngHandle, LJ_ioGET_AIN_DIFF, 1, 0, 30, 0);

//Request a single-ended read of AIN1.
AddRequest (IngHandle, LJ_ioGET_AIN_DIFF, 1, 0, 199, 0);

//Request a read of AIN1 using the special 0-3.6 volt range.
AddRequest (IngHandle, LJ_ioGET_AIN_DIFF, 1, 0, 32, 0);

//Execute the requests.
GoOne (IngHandle);

//Since multiple requests were made with the same IOType
//and Channel, and only x1 was different, GetFirst/GetNext
//must be used to retrieve the results. The simple
//GetResult function does not use the x1 parameter and
//thus there is no way to specify which result is desired.
//Rather than specifying the IOType and Channel of the
//result to be read, the GetFirst/GetNext functions retrieve
//the results in order. Normally, GetFirst/GetNext are best
//used in a loop, but here they are simply called in succession.

//Retrieve AIN2 voltage. GetFirstResult returns the IOType,
//Channel, Value, x1, and UserData from the first request.
//In this example we are just retrieving the results in order
//and Value is the only parameter we need.
GetFirstResult (IngHandle, 0, 0, &dblValue, 0, 0);

//Get the AIN1-AIN6 voltage.
GetNextResult (IngHandle, 0, 0, &dblValue, 0, 0);

//Get the AIN1-Vref voltage.
GetNextResult (IngHandle, 0, 0, &dblValue, 0, 0);

//Get the AIN1 voltage.
GetNextResult (IngHandle, 0, 0, &dblValue, 0, 0);
```



```
//Get the AIN1 voltage (special 0-3.6 volt range).
GetNextResult (IngHandle, 0, 0, &dblValue, 0, 0);
```

4.3.4 - Analog Outputs [U3 Datasheet]

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The IOType to set the voltage on an analog output is:

```
LJ_ioPUT_DAC
```

The following are IOTypes used to write/read the enable bit for DAC1:

```
LJ_ioPUT_DAC_ENABLE //Ignored on hardware rev 1.30+, as DAC1 always enabled.
LJ_ioGET_DAC_ENABLE
```

The following is a special channel, used with the get/put config IOTypes, to configure a parameter that applies to all DACs:

```
LJ_chDAC_BINARY //If set nonzero put_dac values should be 0-65535.
```

Following is example pseudocode to set DAC0 to 2.5 volts:

```
//Set DAC0 to 2.5 volts.
ePut (IngHandle, LJ_ioPUT_DAC, 0, 2.50, 0);
```

4.3.5 - Digital I/O [U3 Datasheet]

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There are eight IOTypes used to write or read digital I/O information:

```
LJ_ioGET_DIGITAL_BIT //Also sets direction to input.
LJ_ioGET_DIGITAL_BIT_DIR
LJ_ioGET_DIGITAL_BIT_STATE
LJ_ioGET_DIGITAL_PORT //Also sets directions to input. x1 is number of bits.
LJ_ioGET_DIGITAL_PORT_DIR //x1 is number of bits.
LJ_ioGET_DIGITAL_PORT_STATE //x1 is number of bits.
```

```
LJ_ioPUT_DIGITAL_BIT //Also sets direction to output.
LJ_ioPUT_DIGITAL_PORT //Also sets directions to output. x1 is number of bits.
```

DIR is short for direction. 0=input and 1=output.

The general bit and port IOTypes automatically control direction, but the _DIR and _STATE ones do not. These can be used to read the current condition of digital I/O without changing the current condition. Note that the _STATE reads are actually doing a read using the input circuitry, not reading the state value last written. When you use LJ_ioGET_DIGITAL_BIT_STATE or LJ_ioGET_DIGITAL_PORT_STATE on a line set to output, it leaves it set as output, but it is doing an actual state read based on the voltage(s) on the pin(s). So if you set a line to output-high, but then something external is driving it low, it might read low.

When a request is done with one of the port IOTypes, the Channel parameter is used to specify the starting bit number, and the x1 parameter is used to specify the number of applicable bits. The bit numbers corresponding to different I/O are:

```
0-7 FIO0-FIO7
8-15 EIO0-EIO7
16-19 CIO0-CIO3
```

Note that the GetResult function does not have an x1 parameter. That means that if two (or more) port requests are added with the same IOType and Channel, but different x1, the result retrieved by GetResult would be undefined. The GetFirstResult/GetNextResult commands do have the x1 parameter, and thus can handle retrieving responses from multiple port requests with the same IOType and Channel.

Following is example pseudocode for various digital I/O operations:

```
//Execute the pin_configuration_reset IOType so that all
//pin assignments are in the factory default condition.
//The ePut function is used, which combines the add/go/get.
ePut (IngHandle, LJ_ioPIN_CONFIGURATION_RESET, 0, 0, 0);
```

```
//Now, an add/go/get block to execute multiple requests.
```

```
//Request a read from FIO2.
AddRequest (IngHandle, LJ_ioGET_DIGITAL_BIT, 2, 0, 0, 0);
```

```
//Request a read from FIO4-EIO5 (10-bits starting
//from digital channel #4).
```

```

AddRequest (IngHandle, LJ_ioGET_DIGITAL_PORT, 4, 0, 10, 0);

//Set FIO3 to output-high.
AddRequest (IngHandle, LJ_ioPUT_DIGITAL_BIT, 3, 1, 0, 0);

//Set EIO6-CIO2 (5-bits starting from digital channel #14)
//to b10100 (=d20). That is EIO6=0, EIO7=0, CIO0=1,
//CIO1=0, and CIO2=1.
AddRequest (IngHandle, LJ_ioPUT_DIGITAL_PORT, 14, 20, 5, 0);

//Execute the requests.
GoOne (IngHandle);

//Get the FIO2 read.
GetResult (IngHandle, LJ_ioGET_DIGITAL_BIT, 2, &dblValue);

//Get the FIO4-EIO5 read.
GetResult (IngHandle, LJ_ioGET_DIGITAL_PORT, 4, &dblValue);

```

4.3.6 - Timers & Counters [U3 Datasheet]

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There are eight IOTypes used to write or read timer and counter information:

```

LJ_ioGET_COUNTER
LJ_ioPUT_COUNTER_ENABLE
LJ_ioGET_COUNTER_ENABLE
LJ_ioPUT_COUNTER_RESET //Sets flag to reset on next read.

LJ_ioGET_TIMER
LJ_ioPUT_TIMER_VALUE
LJ_ioPUT_TIMER_MODE
LJ_ioGET_TIMER_MODE

```

In addition to specifying the channel number, the following mode constants are passed in the value parameter when doing a request with the timer mode IOType:

```

LJ_tmPWM16 //16-bit PWM output
LJ_tmPWM8 //8-bit PWM output
LJ_tmRISINGEDGES32 //Period input (32-bit, rising edges)
LJ_tmFALLINGEDGES32 //Period input (32-bit, falling edges)
LJ_tmDUTYCYCLE //Duty cycle input
LJ_tmFIRMCOUNTER //Firmware counter input
LJ_tmFIRMCOUNTERDEBOUNCE //Firmware counter input (with debounce)
LJ_tmFREQUOT //Frequency output
LJ_tmQUAD //Quadrature input
LJ_tmTIMERSTOP //Timer stop input (odd timers only)
LJ_tmSYSTIMERLOW //System timer low read
LJ_tmSYSTIMERHIGH //System timer high read
LJ_tmRISINGEDGES16 //Period input (16-bit, rising edges)
LJ_tmFALLINGEDGES16 //Period input (16-bit, falling edges)

```

The following are special channels, used with the get/put config IOTypes, to configure a parameter that applies to all timers/counters:

```

LJ_chNUMBER_TIMERS_ENABLED //0-2
LJ_chTIMER_CLOCK_BASE //Set with the constant values below. Gets the low-level value.
LJ_chTIMER_CLOCK_DIVISOR //0-255, where 0=256
LJ_chTIMER_COUNTER_PIN_OFFSET //4-8 only starting with hardware rev 1.30.

```

With the clock base special channel above, the following constants are passed in the value parameter to select the frequency:

```

LJ_tc4MHZ //4 MHz clock base
LJ_tc12MHZ //12 MHz clock base
LJ_tc48MHZ //48 MHz clock base
LJ_tc1MHZ_DIV //1 MHz clock base w/ divisor (no Counter0)
LJ_tc4MHZ_DIV //4 MHz clock base w/ divisor (no Counter0)
LJ_tc12MHZ_DIV //12 MHz clock base w/ divisor (no Counter0)
LJ_tc48MHZ_DIV //48 MHz clock base w/ divisor (no Counter0)
LJ_tcSYS //Equivalent to LJ_tc48MHZ

```

Constant values used to set (put) the timer clock base differ from the low-level timer clock base retrieved (get) from the device. Constant value - 20 = low-level value. [Table 2.9-2](#) documents these values.

Following is example pseudocode for configuring various timers and a hardware counter:

```

//Execute the pin_configuration_reset IOType so that all
//pin assignments are in the factory default condition.
//The ePut function is used, which combines the add/go/get.
ePut (IngHandle, LJ_ioPIN_CONFIGURATION_RESET, 0, 0, 0);

```

```
//First, an add/go/get block to configure the timers and counters.

//Set the pin offset to 4, which causes the timers to start on FIO4.
AddRequest (IngHandle, LJ_ioPUT_CONFIG, LJ_chTIMER_COUNTER_PIN_OFFSET, 4, 0, 0);

//Enable both timers. They will use FIO4-FIO5
AddRequest (IngHandle, LJ_ioPUT_CONFIG, LJ_chNUMBER_TIMERS_ENABLED, 2, 0, 0);

//Make sure Counter0 is disabled.
AddRequest (IngHandle, LJ_ioPUT_COUNTER_ENABLE, 0, 0, 0, 0);

//Enable Counter1. It will use the next available line, FIO6.
AddRequest (IngHandle, LJ_ioPUT_COUNTER_ENABLE, 1, 1, 0, 0);

//All output timers use the same timer clock, configured here. The
//base clock is set to 48MHZ_DIV, meaning that the clock divisor
//is supported and Counter0 is not available. Note that this timer
//clock base is not valid with U3 hardware version 1.20.
AddRequest (IngHandle, LJ_ioPUT_CONFIG, LJ_chTIMER_CLOCK_BASE, LJ_tc48MHZ_DIV, 0, 0);

//Set the timer clock divisor to 48, creating a 1 MHz timer clock.
AddRequest (IngHandle, LJ_ioPUT_CONFIG, LJ_chTIMER_CLOCK_DIVISOR, 48, 0, 0);

//Configure Timer0 as 8-bit PWM. It will have a frequency
//of 1M/256 = 3906.25 Hz.
AddRequest (IngHandle, LJ_ioPUT_TIMER_MODE, 0, LJ_tmPWM8, 0, 0);

//Initialize the 8-bit PWM with a 50% duty cycle.
AddRequest (IngHandle, LJ_ioPUT_TIMER_VALUE, 0, 32768, 0, 0);

//Configure Timer1 as duty cycle input.
AddRequest (IngHandle, LJ_ioPUT_TIMER_MODE, 1, LJ_tmDUTYCYCLE, 0, 0);

//Execute the requests.
GoOne (IngHandle);
```

The following pseudocode demonstrates reading input timers/counters and updating the values of output timers. The e-functions are used in the following pseudocode, but some applications might combine the following calls into a single add/go/get block so that a single low-level call is used.

```
//Change Timer0 PWM duty cycle to 25%.
ePut (IngHandle, LJ_ioPUT_TIMER_VALUE, 0, 49152, 0);

//Read duty-cycle from Timer1.
eGet (IngHandle, LJ_ioGET_TIMER, 1, &dblValue, 0);

//The duty cycle read returns a 32-bit value where the
//least significant word (LSW) represents the high time
//and the most significant word (MSW) represents the low
//time. The times returned are the number of cycles of
//the timer clock. In this case the timer clock was set
//to 1 MHz, so each cycle is 1 microsecond.
dblHighCycles = (double)((((unsigned long)dblValue) % (65536)));
dblLowCycles = (double)((((unsigned long)dblValue) / (65536)));
dblDutyCycle = 100 * dblHighCycles / (dblHighCycles + dblLowCycles);
dblHighTime = 0.000001 * dblHighCycles;
dblLowTime = 0.000001 * dblLowCycles;

//Read the count from Counter1. This is an unsigned 32-bit value.
eGet (IngHandle, LJ_ioGET_COUNTER, 1, &dblValue, 0);
```

Following is pseudocode to reset the input timer and the counter:

```
//Reset the duty-cycle measurement (Timer1) to zero, by writing
//a value of zero. The duty-cycle measurement is continuously
//updated, so a reset is normally not needed, but one reason
//to reset to zero is to detect whether there has been a new
//measurement or not.
ePut (IngHandle, LJ_ioPUT_TIMER_VALUE, 1, 0, 0);

//Read & reset Counter1. Note that with the U3 reset is just
//setting a driver flag to reset on the next read, so reset
//is generally combined with a read in an add/go/get block.
//The order of the read & reset within the block does not
//matter ... the read will always happen right before the reset.
AddRequest (IngHandle, LJ_ioGET_COUNTER, 1, 0, 0, 0);
AddRequest (IngHandle, LJ_ioPUT_COUNTER_RESET, 1, 1, 0, 0);
GoOne (IngHandle);
GetResult (IngHandle, LJ_ioGET_COUNTER, 1, &dblValue);
GetResult (IngHandle, LJ_ioPUT_COUNTER_RESET, 1, 0);
```

Note that if a timer/counter is read and reset at the same time (in the same Add/Go/Get block), the read will return the value just before reset.

4.3.7 - Stream Mode [U3 Datasheet]

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The highest input data rates are obtained in stream mode, which is supported with U3 hardware version 1.21 or higher. See Section 3.2 for more information about stream mode.

There are five IOTypes used to control streaming:

```
LJ_ioCLEAR_STREAM_CHANNELS
LJ_ioADD_STREAM_CHANNEL
LJ_ioADD_STREAM_CHANNEL_DIFF //Put negative channel in x1.
LJ_ioSTART_STREAM           //Value returns actual scan rate.
LJ_ioSTOP_STREAM
LJ_ioGET_STREAM_DATA
```

The following constant is passed in the Channel parameter with the get stream data IOType to specify a read returning all scanned channels, rather than retrieving each scanned channel separately:

```
LJ_chALL_CHANNELS
```

The following are special channels, used with the get/put config IOTypes, to write or read various stream values:

```
LJ_chSTREAM_SCAN_FREQUENCY
LJ_chSTREAM_BUFFER_SIZE //UD driver stream buffer size in samples.
LJ_chSTREAM_WAIT_MODE
LJ_chSTREAM_DISABLE_AUTORECOVERY
LJ_chSTREAM_BACKLOG_COMM //Read-only. 0=0% and 256=100%.
LJ_chSTREAM_BACKLOG_UD //Read-only. Number of samples.
LJ_chSTREAM_SAMPLES_PER_PACKET //Default 25. Range 1-25.
LJ_chSTREAM_READS_PER_SECOND //Default 25.
```

With the wait mode special channel above, the following constants are passed in the value parameter to select the behavior when reading data:

```
LJ_swNONE //No wait. Immediately return available data.
LJ_swALL_OR_NONE //No wait. Immediately return requested amount, or none.
LJ_swPUMP //Advanced message pump wait mode.
LJ_swSLEEP //Wait until requested amount available.
```

The backlog special channels return information about how much data is left in the stream buffer on the U3 or in the UD driver. These parameters are updated whenever a stream packet is read by the driver, and thus might not exactly reflect the current state of the buffers, but can be useful to detect problems.

When streaming, the processor acquires data at precise intervals, and transfers it to a buffer on the U3 itself. The U3 has a small buffer (512-984 samples) for data waiting to be transferred to the host. The `LJ_chSTREAM_BACKLOG_COMM` special channel specifies how much data is left in the U3 buffer (*COMM* or *CONTROL* are the same thing on the U3), where 0 means 0% full and 256 would mean 100% full. The UD driver retrieves stream data from the U3 in the background, but if the computer or communication link is too slow for some reason, the driver might not be able to read the data as fast as the U3 is acquiring it, and thus there will be data left over in the U3 buffer.

To obtain the maximum stream rates documented in Section 3.2, the data must be transferred between host and U3 in large chunks. The amount of data transferred per low-level packet is controlled by `LJ_chSTREAM_SAMPLES_PER_PACKET`. The driver will use the parameter `LJ_chSTREAM_READS_PER_SECOND` to determine how many low-level packets to retrieve per read.

The size of the UD stream buffer on the host is controlled by `LJ_chSTREAM_BUFFER_SIZE`. The application software on the host must read data out of the UD stream buffer fast enough to prevent overflow. After each read, use `LJ_chSTREAM_BACKLOG_UD` to determine how many samples are left in the buffer.

Since the data buffer on the U3 is very small a feature called auto-recovery is used. If the buffer overflows, the U3 will continue streaming but discard data until the buffer is emptied, and then data will be stored in the buffer again. The U3 keeps track of how many packets are discarded and reports that value. Based on the number of packets discarded, the UD driver adds the proper number of dummy samples (-9999.0) such that the correct timing is maintained. Auto-recovery will generally occur when the U3 buffer is 90-95% full.

In stream mode the LabJack acquires inputs at a fixed interval, controlled by the hardware clock on the device itself, and stores the data in a buffer. The LabJackUD driver automatically reads data from the hardware buffer and stores it in a PC RAM buffer until requested. The general procedure for streaming is:

- Update configuration parameters.
- Build the scan list.
- Start the stream.
- Periodically retrieve stream data in a loop.
- Stop the stream.

Following is example pseudocode to configure a 2-channel stream.

```
//Set the scan rate.
```

```

AddRequest (IngHandle, LJ_ioPUT_CONFIG, LJ_chSTREAM_SCAN_FREQUENCY, scanRate, 0, 0);

//Give the UD driver a 5 second buffer (scanRate * 2 channels * 5 seconds).
AddRequest (IngHandle, LJ_ioPUT_CONFIG, LJ_chSTREAM_BUFFER_SIZE, scanRate*2*5, 0, 0);

//Configure reads to wait and retrieve the desired amount of data.
AddRequest (IngHandle, LJ_ioPUT_CONFIG, LJ_chSTREAM_WAIT_MODE, LJ_swSLEEP, 0, 0);

//Define the scan list as singled ended AIN2 then differential AIN3-AIN9.
AddRequest (IngHandle, LJ_ioCLEAR_STREAM_CHANNELS, 0, 0, 0, 0);
AddRequest (IngHandle, LJ_ioADD_STREAM_CHANNEL, 2, 0, 0, 0);
AddRequest (IngHandle, LJ_ioADD_STREAM_CHANNEL_DIFF, 3, 0, 9, 0);

//Execute the requests.
GoOne (IngHandle);

```

Next, start the stream:

```

//Start the stream.
eGet(IngHandle, LJ_ioSTART_STREAM, 0, &dblValue, 0);

//The actual scan rate is dependent on how the desired scan rate divides into
//the LabJack clock. The actual scan rate is returned in the value parameter
//from the start stream command.
actualScanRate = dblValue;
actualSampleRate = 2*dblValue;

```

Once a stream is started, the data must be retrieved periodically to prevent the buffer from overflowing. To retrieve data, add a request with IOType *LJ_ioGET_STREAM_DATA*. The Channel parameter should be *LJ_chALL_CHANNELS* or a specific channel number (ignored for a single channel stream). The Value parameter should be the number of scans (all channels) or samples (single channel) to retrieve. The x1 parameter should be a pointer to an array that has been initialized to a sufficient size. Keep in mind that the required number of elements if retrieving all channels is number of scans * number of channels.

Data is stored interleaved across all streaming channels. In other words, if two channels are streaming, 0 and 1, and *LJ_chALL_CHANNELS* is the channel number for the read request, the data will be returned as Channel0, Channel1, Channel0, Channel1, etc. Once the data is read it is removed from the internal buffer, and the next read will give new data.

If multiple channels are being streamed, data can be retrieved one channel at a time by passing a specific channel number in the request. In this case the data is not removed from the internal buffer until the last channel in the scan is requested. Reading the data from the last channel (not necessarily all channels) is the trigger that causes the block of data to be removed from the buffer. This means that if three channels are streaming, 0, 1 and 2 (in that order in the scan list), and data is requested from channel 0, then channel 1, then channel 0 again, the request for channel 0 the second time will return the same data as the first request. New data will not be retrieved until after channel 2 is read, since channel 2 is last in the scan list. If the first get stream data request is for 10 samples from channel 1, the reads from channels 0 and 2 also must be for 10 samples. Note that when reading stream data one channel at a time (not using *LJ_chALL_CHANNELS*), the scan list cannot have duplicate channel numbers.

There are three basic wait modes for retrieving the data:

- **LJ_swNONE:** The Go call will retrieve whatever data is available at the time of the call up to the requested amount of data. A Get command should be called to determine how many scans were retrieved. This is generally used with a software timed read interval. The number of samples read per loop iteration will vary, but the time per loop iteration will be pretty consistent. Since the LabJack clock could be faster than the PC clock, it is recommended to request more scans than are expected each time so that the application does not get behind.
- **LJ_swSLEEP:** This makes the Go command a blocking call. The Go command will loop until the requested amount of is retrieved or no new data arrives from the device before timeout. In this mode, the hardware dictates the timing of the application ... you generally do not want to add a software delay in the read loop. The time per loop iteration will vary, but the number of samples read per loop will be the same every time. A Get command should be called to determine whether all the data was retrieved, or a timeout condition occurred and none of the data was retrieved.
- **LJ_swALL_OR_NONE:** If available, the Go call will retrieve the amount of data requested, otherwise it will retrieve no data. A Get command should be called to determine whether all the data was returned or none. This could be a good mode if hardware timed execution is desirable, but without the application continuously waiting in SLEEP mode.

The following pseudocode reads data continuously in SLEEP mode as configured above:

```

//Read data until done.
while(!done)
{
    //Must set the number of scans to read each iteration, as the read
    //returns the actual number read.
    numScans = 1000;

    //Read the data. Note that the array passed must be sized to hold
    //enough SAMPLES, and the Value passed specifies the number of SCANS
    //to read.
    eGetPtr(IngHandle, LJ_ioGET_STREAM_DATA, LJ_chALL_CHANNELS, &numScans, array);
    actualNumberRead = numScans;

    //When all channels are retrieved in a single read, the data
    //is interleaved in a 1-dimensional array. The following lines
    //get the first sample from each channel.
    channelA = array[0];
    channelB = array[1];
}

```

```
//Retrieve the current U3 backlog. The UD driver retrieves
//stream data from the U3 in the background, but if the computer
//is too slow for some reason the driver might not be able to read
//the data as fast as the U3 is acquiring it, and thus there will
//be data left over in the U3 buffer.
eGet(IngHandle, LJ_ioGET_CONFIG, LJ_chSTREAM_BACKLOG_COMM, &dblCommBacklog, 0);

//Retrieve the current UD driver backlog. If this is growing, then
//the application software is not pulling data from the UD driver
//fast enough.
eGet(IngHandle, LJ_ioGET_CONFIG, LJ_chSTREAM_BACKLOG_UD, &dblUDBacklog, 0);
}
```

Finally, stop the stream:

```
//Stop the stream.
errorcode = ePut (Handle, LJ_ioSTOP_STREAM, 0, 0, 0);
```

4.3.8 - Raw Output/Input [U3 Datasheet]

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There are two IOTypes used to write or read raw data. These can be used to make low-level function calls (Section 5) through the UD driver. The only time these generally might be used is to access some low-level device functionality not available in the UD driver.

```
LJ_ioRAW_OUT
LJ_ioRAW_IN
```

When using these IOTypes, channel # specifies the desired communication pipe. For the U3, 0 is the normal pipe while 1 is the streaming pipe. The number of bytes to write/read is specified in value (1-16384), and x1 is a pointer to a byte array for the data. When retrieving the result, the value returned is the number of bytes actually read/written.

Following is example pseudocode to write and read the low-level command ConfigTimerClock ([Section 5.2.4](#)).

```
writeArray[2] = {0x05,0xF8,0x02,0x0A,0x00,0x00,0x00,0x00,0x00,0x00,0x00};
numBytesToWrite = 10;
numBytesToRead = 10;

//Raw Out. This command writes the bytes to the device.
eGetPtr(IngHandle, LJ_ioRAW_OUT, 0, &numBytesToWrite, pwriteArray);

//Raw In. This command reads the bytes from the device.
eGetPtr(IngHandle, LJ_ioRAW_IN, 0, &numBytesToRead, preadArray);
```

4.3.9 - Easy Functions [U3 Datasheet]

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The easy functions are simple alternatives to the very flexible IOType based method normally used by this driver. There are 6 functions available:

```
eAIN() //Read 1 analog input.
eDAC() //Write to 1 analog output.
eDI() //Read 1 digital input.
eDO() //Write to 1 digital output.
eTCCfg() //Configure all timers and counters.
eTCValues() //Update/reset and read all timers and counters.
```

In addition to the basic operations, these functions also automatically handle configuration as needed. For example, eDO() sets the specified line to digital output if previously configured as analog and/or input, and eAIN() sets the line to analog if previously configured as digital. Passing a -1 to any of the configuration parameters (resolution, range, etc) will use the driver's current value instead of having to specify it. This is useful so that you can use the driver's default values for those properties, which will work in most cases.

The first 4 functions should not be used when speed is critical with multi-channel reads. These functions use one low-level function per operation, whereas using the normal Add/Go/Get method with IOTypes, many operations can be combined into a single low-level call. With single channel operations, however, there will be little difference between using an easy function or Add/Go/Get.

The last two functions handle almost all functionality related to timers and counters, and will usually be as efficient as any other method. These easy functions are recommended for most timer/counter applications.

Following is example pseudocode:

```
//Take a single-ended measurement from AIN3.
//eAIN (Handle, ChannelP, ChannelN, *Voltage, Range, Resolution,
// Settling, Binary, Reserved1, Reserved2)
//
eAIN(IngHandle, 3, 31, &dblVoltage, 0, 0, 0, 0, 0, 0);
```

```

printf("AIN3 value = %.3f\n",dblVoltage);

//Set DAC0 to 3.1 volts.
//eDAC (Handle, Channel, Voltage, Binary, Reserved1, Reserved2)
//
eDAC(IngHandle, 0, 3.1, 0, 0, 0);

//Read state of FIO2.
//eDI (Handle, Channel, *State)
//
eDI(IngHandle, 2, &IngState);
printf("FIO2 state = %.0f\n",IngState);

//Set FIO3 to output-high.
//eDO (Handle, Channel, State)
//
eDO(IngHandle, 3, 1);

//Enable and configure 1 output timer and 1 input timer, and enable Counter0.
//Fill the arrays with the desired values, then make the call.
alngEnableTimers = {1,1}; //Enable Timer0-Timer1
alngTimerModes = {LJ_tmPWM8,LJ_tmRISINGEDGES32}; //Set timer modes
adbITimerValues = {16384,0}; //Set PWM8 duty-cycle to 75%.
alngEnableCounters = {1,0}; //Enable Counter0
//
//eTCCConfig (Handle, *aEnableTimers, *aEnableCounters, TCPinOffset,
// TimerClockBaseIndex, TimerClockDivisor, *aTimerModes,
// *aTimerValues, Reserved1, Reserved2);
//
eTCCConfig(IngHandle, alngEnableTimers, alngEnableCounters, 4, LJ_tc48MHZ, 0, alngTimerModes, adbITimerValues, 0, 0);

//Read and reset the input timer (Timer1), read and reset Counter0, and update
//the value (duty-cycle) of the output timer (Timer0).
//Fill the arrays with the desired values, then make the call.
alngReadTimers = {0,1}; //Read Timer1
alngUpdateResetTimers = {1,1}; //Update Timer0 and reset Timer1
alngReadCounters = {1,0}; //Read Counter0
alngResetCounters = {1,0}; //Reset Counter0
adbITimerValues = {32768,0}; //Change Timer0 duty-cycle to 50%
//
//eTCValues (Handle, *aReadTimers, *aUpdateResetTimers, *aReadCounters,
// *aResetCounters, *aTimerValues, *aCounterValues, Reserved1,
// Reserved2);
//
eTCValues(IngHandle, alngReadTimers, alngUpdateResetTimers, alngReadCounters, alngResetCounters, adbITimerValues, adbICounterValues, 0, 0);
printf("Timer1 value = %.0f\n",adbITimerValues[1]);
printf("Counter0 value = %.0f\n",adbICounterValues[0]);

```

4.3.10 - SPI Serial Communication [U3 Datasheet]

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The U3 (hardware version 1.21+ only) supports [Serial Peripheral Interface](#) communication as the master only. SPI is a synchronous serial protocol typically used to communicate with chips that support SPI as slave devices.

This serial link is not an alternative to the USB connection. Rather, the host application will write/read data to/from the U3 over USB, and the U3 communicates with some other device using the serial protocol. Using this serial protocol is considered an advanced topic. A good knowledge of the protocol is recommended, and a logic analyzer or oscilloscope might be needed for troubleshooting.

There is one IOType used to write/read data over the SPI bus:

LJ_ioSPI_COMMUNICATION // Value= number of bytes (1-50). x1= array.

The following are special channels, used with the get/put configIOTypes, to configure various parameters related to the SPI bus. See the low-level function description in [Section 5.2.15](#) for more information about these parameters:

```

LJ_chSPI_AUTO_CS
LJ_chSPI_DISABLE_DIR_CONFIG
LJ_chSPI_MODE
LJ_chSPI_CLOCK_FACTOR
LJ_chSPI_MOSI_PIN_NUM
LJ_chSPI_MISO_PIN_NUM
LJ_chSPI_CLK_PIN_NUM
LJ_chSPI_CS_PIN_NUM

```

Following is example pseudocode to configure SPI communication:

```

//First, configure the SPI communication.

```

```
//Enable automatic chip-select control.
AddRequest(IngHandle, LJ_ioPUT_CONFIG, LJ_chSPI_AUTO_CS,1,0,0);

//Do not disable automatic digital i/o direction configuration.
AddRequest(IngHandle, LJ_ioPUT_CONFIG, LJ_chSPI_DISABLE_DIR_CONFIG,0,0,0);

//Mode A: CPOL=0, CPHA=0.
AddRequest(IngHandle, LJ_ioPUT_CONFIG, LJ_chSPI_MODE,0,0,0);

//Maximum clock rate (~100kHz).
AddRequest(IngHandle, LJ_ioPUT_CONFIG, LJ_chSPI_CLOCK_FACTOR,0,0,0);

//Set MOSI to FIO2.
AddRequest(IngHandle, LJ_ioPUT_CONFIG, LJ_chSPI_MOSI_PIN_NUM,2,0,0);

//Set MISO to FIO3.
AddRequest(IngHandle, LJ_ioPUT_CONFIG, LJ_chSPI_MISO_PIN_NUM,3,0,0);

//Set CLK to FIO0.
AddRequest(IngHandle, LJ_ioPUT_CONFIG, LJ_chSPI_CLK_PIN_NUM,0,0,0);

//Set CS to FIO1.
AddRequest(IngHandle, LJ_ioPUT_CONFIG, LJ_chSPI_CS_PIN_NUM,1,0,0);

//Execute the configuration requests.
GoOne(IngHandle);
```

Following is pseudocode to do the actual SPI communication:

```
//Transfer the data.
eGetPtr(IngHandle, LJ_ioSPI_COMMUNICATION, 0, &numBytesToTransfer, array);
```

4.3.11 - I²C Serial Communication [U3 Datasheet]

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The U3 (hardware version 1.21+ only) supports Inter-Integrated Circuit (I²C or I2C) communication as the master only. I²C is a synchronous serial protocol typically used to communicate with chips that support I²C as slave devices. Any 2 digital I/O lines are used for SDA and SCL. Note that the I²C bus generally requires pull-up resistors of perhaps 4.7 kΩ from SDA to Vs and SCL to Vs, and also note that the screw terminals labeled SDA and SCL (if present) are not used for I²C.

This serial link is not an alternative to the USB connection. Rather, the host application will write/read data to/from the U3 over USB, and the U3 communicates with some other device using the serial protocol. Using this serial protocol is considered an advanced topic. A good knowledge of the protocol is recommended, and a logic analyzer or oscilloscope might be needed for troubleshooting.

There is one IOType used to write/read I²C data:

LJ_ioI2C_COMMUNICATION

The following are special channels used with the I²C IOType above:

```
LJ_chI2C_READ    // Value= number of bytes (0-52). x1= array.
LJ_chI2C_WRITE   // Value= number of bytes (0-50). x1= array.
LJ_chI2C_GET_ACKS
```

The following are special channels, used with the get/put config IOTypes, to configure various parameters related to the I²C bus. See the low-level function description in [Section 5.2.19](#) for more information about these parameters:

```
LJ_chI2C_ADDRESS_BYTE
LJ_chI2C_SCL_PIN_NUM // 0-19. Pull-up resistor usually required.
LJ_chI2C_SDA_PIN_NUM // 0-19. Pull-up resistor usually required.
LJ_chI2C_OPTIONS
LJ_chI2C_SPEED_ADJUST
```

The [LJTick-DAC](#) is an accessory from LabJack with an I²C 24C01 EEPROM chip. Following is example pseudocode to configure I²C to talk to that chip:

```
//The AddressByte of the EEPROM on the LJTick-DAC is 0xA0 or decimal 160.
AddRequest(IngHandle, LJ_ioPUT_CONFIG, LJ_chI2C_ADDRESS_BYTE,160,0,0);

//SCL is FIO0
AddRequest(IngHandle, LJ_ioPUT_CONFIG, LJ_chI2C_SCL_PIN_NUM,0,0,0);

//SDA is FIO1
AddRequest(IngHandle, LJ_ioPUT_CONFIG, LJ_chI2C_SDA_PIN_NUM,1,0,0);

//See description of low-level I2C function.
AddRequest(IngHandle, LJ_ioPUT_CONFIG, LJ_chI2C_OPTIONS,0,0,0);
```



```
//See description of low-level I2C function. 0 is max speed of about 150 kHz.
AddRequest(IngHandle, LJ_ioPUT_CONFIG, LJ_chI2C_SPEED_ADJUST,0,0,0);
```

```
//Execute the configuration requests.
GoOne(IngHandle);
```

Following is pseudocode to read 4 bytes from the EEPROM:

```
//Initial read of EEPROM bytes 0-3 in the user memory area.
//We need a single I2C transmission that writes the address and then reads
//the data. That is, there needs to be an ack after writing the address,
//not a stop condition. To accomplish this, we use Add/Go/Get to combine
//the write and read into a single low-level call.
numWrite = 1;
array[0] = 0; //Memory address. User area is 0-63.
AddRequestPtr(IngHandle, LJ_ioI2C_COMMUNICATION, LJ_chI2C_WRITE, numWrite, array, 0);

numRead = 4;
AddRequestPtr(IngHandle, LJ_ioI2C_COMMUNICATION, LJ_chI2C_READ, numRead, array, 0);

//Execute the requests.
GoOne(IngHandle);
```

For more example code, see the I2C.cpp example in the VC6_LJUD archive.

4.3.12 - Asynchronous Serial Communication [U3 Datasheet]

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The U3 (hardware version 1.21+ only) has a UART available that supports asynchronous serial communication. On hardware version 1.30 the TX (transmit) and RX (receive) lines appear on FIO/EIO after any timers and counters, so with no timers/counters enabled, and pin offset set to 4, TX=FIO4 and RX=FIO5. On hardware version 1.21, the UART uses SDA for TX and SCL for RX.

Communication is in the common 8/n/1 format. Similar to RS-232, except that the logic is normal CMOS/TTL. Connection to an RS-232 device will require a converter chip such as the MAX233, which inverts the logic and shifts the voltage levels.

This serial link is not an alternative to the USB connection. Rather, the host application will write/read data to/from the U3 over USB, and the U3 communicates with some other device using the serial protocol. Using this serial protocol is considered an advanced topic. A good knowledge of the protocol is recommended, and a logic analyzer or oscilloscope might be needed for troubleshooting. Also consider that a better way to do RS-232 (or RS-485 or RS-422) communication is with a standard USB<=>RS-232 adapter/converter/dongle, so the user should have a particular reason to not use that and use a U3 instead.

There is one IOType used to write/read asynchronous data:

LJ_ioASYNCH_COMMUNICATION

The following are special channels used with the asynchIOType above:

```
LJ_chASYNCH_ENABLE // Enables UART to begin buffering RX data.
LJ_chASYNCH_RX      // Value= returns pre-read buffer size. x1= array.
LJ_chASYNCH_TX      // Value= number to send (0-56), number in RX buffer. x1= array.
LJ_chASYNCH_FLUSH   // Flushes the RX buffer. All data discarded. Value ignored.
```

When using *LJ_chASYNCH_RX*, the Value parameter returns the size of the Asynch buffer before the read. If the size is 32 bytes or less, that is how many bytes were read. If the size is more than 32 bytes, then the call read 32 this time and there are still bytes left in the buffer.

When using *LJ_chASYNCH_TX*, specify the number of bytes to send in the Value parameter. The Value parameter returns the size of the Asynch read buffer.

The following is a special channel, used with the get/put configIOTypes, to specify the baud rate for the asynchronous communication:

LJ_chASYNCH_BAUDFACTOR // 16-bit value for hardware V1.30. 8-bit for V1.21.

With hardware revision 1.30 this is a 16-bit value that sets the baud rate according the following formula: $\text{BaudFactor16} = 2^{16} - 48000000 / (2 * \text{Desired Baud})$. For example, a BaudFactor16 = 63036 provides a baud rate of 9600 bps. With hardware revision 1.21, the value is only 8-bit and the formula is $\text{BaudFactor8} = 2^8 - \text{TimerClockBase} / (\text{Desired Baud})$.

Following is example pseudocode for asynchronous communication:

```
//Set data rate for 9600 bps communication.
ePut(IngHandle, LJ_ioPUT_CONFIG, LJ_chASYNCH_BAUDFACTOR, 63036, 0);

//Enable UART.
ePut(IngHandle, LJ_ioASYNCH_COMMUNICATION, LJ_chASYNCH_ENABLE, 1, 0);

//Write data.
eGetPtr(IngHandle, LJ_ioASYNCH_COMMUNICATION, LJ_chASYNCH_TX, &numBytes, array);
```

```
//Read data. Always initialize array to 32 bytes.
eGetPtr(IngHandle, LJ_ioASYNCH_COMMUNICATION, LJ_chASYNCH_RX, &numBytes, array);
```

4.3.13 - Watchdog Timer [U3 Datasheet]

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The U3 (hardware version 1.21+ only) has firmware based watchdog capability. Unattended systems requiring maximum up-time might use this capability to reset the U3 or the entire system. When any of the options are enabled, an internal timer is enabled which resets on any incoming USB communication. If this timer reaches the defined TimeoutPeriod before being reset, the specified actions will occur. Note that while streaming, data is only going out, so some other command will have to be called periodically to reset the watchdog timer.

Timeout of the watchdog on the U3 can be specified to cause a device reset, update the state of 1 digital I/O (must be configured as output by user), or both.

Typical usage of the watchdog is to configure the reset defaults (condition of digital I/O and analog outputs) as desired (use the “config defaults” option in LJControlPanel V2.26+), and then use the watchdog simply to reset the device on timeout. For initial testing, “config defaults” in LJCP can be used to enable the watchdog all the time, but often it is desirable to enable/disable the watchdog in user software so it is only active while that software is running.

Note that some USB hubs do not like to have any USB device repeatedly reset. With such hubs, the operating system will quit reenumerating the device on reset and the computer will have to be rebooted, so avoid excessive resets with hubs that seem to have this problem.

If the watchdog is accidentally configured to reset the processor with a very low timeout period (such as 1 second), it could be difficult to establish any communication with the device. In such a case, the reset-to-default jumper can be used to turn off the watchdog. Power up the U3 with a jumper securely installed from FIO6 to SPC (FIO2 to SCL on U3 1.20/1.21), then remove the jumper and power cycle the device again. This resets all power-up settings to factory default values.

There is one IOType used to configure and control the watchdog:

```
LJ_ioSWDT_CONFIG // Channel is enable or disable constant.
```

The watchdog settings are stored in non-volatile flash memory (and reloaded at reset), so every request with this IOType causes a flash erase/write. The flash has a rated endurance of at least 20000 writes, which is plenty for reasonable operation, but if this IOType is called in a high-speed loop the flash could be damaged.

The following are special channels used with the watchdog config IOType above:

```
LJ_chSWDT_ENABLE // Value is timeout in seconds (3-65535).
LJ_chSWDT_DISABLE
```

The following are special channels, used with the put config IOType, to configure watchdog options. These parameters cause settings to be updated in the driver only. The settings are not actually sent to the hardware until the LJ_ioSWDT_CONFIG IOType (above) is used:

```
LJ_chSWDT_RESET_DEVICE
LJ_chSWDT_UDPATE_DIOA
LJ_chSWDT_DIOA_CHANNEL
LJ_chSWDT_DIOA_STATE
```

Following is example pseudocode to configure and enable the watchdog:

```
//Initialize EIO2 to output-low, which also forces the direction to output.
//It would probably be better to do this by configuring the power-up defaults.
AddRequest(IngHandle, LJ_ioPUT_DIGITAL_BIT, 10,0,0,0);

//Specify that the device should be reset on timeout.
AddRequest(IngHandle, LJ_ioPUT_CONFIG, LJ_chSWDT_RESET_DEVICE,1,0,0);

//Specify that the state of the digital line should be updated on timeout.
AddRequest(IngHandle, LJ_ioPUT_CONFIG, LJ_chSWDT_UDPATE_DIOA,1,0,0);

//Specify that EIO2 is the desired digital line.
AddRequest(IngHandle, LJ_ioPUT_CONFIG, LJ_chSWDT_DIOA_CHANNEL,10,0,0);

//Specify that the digital line should be set high.
AddRequest(IngHandle, LJ_ioPUT_CONFIG, LJ_chSWDT_DIOA_STATE,1,0,0);

//Enable the watchdog with a 60 second timeout.
AddRequest(IngHandle, LJ_ioSWDT_CONFIG, LJ_chSWDT_ENABLE,60,0,0);

//Execute the requests.
GoOne(IngHandle);
```

Following is pseudocode to disable the watchdog:

```
//Disable the watchdog.
```

4.3.14 - Miscellaneous [U3 Datasheet]

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The following are special channels, used with the [get/put config IOTypes](#), to read/write the [calibration memory](#) and user memory:

LJ_chCAL_CONSTANTS // x1 points to an array with 20 doubles.
LJ_chUSER_MEM // x1 points to an array with 256 bytes.

LJ_chCAL_CONSTANTS makes 5 calls to the low-level ReadCal or EraseCal/WriteCal functions to do a consecutive read or erase/write of 5x 32-byte blocks of calibration memory, or 160 bytes total. Those bytes represent 20x 64-bit fixed point values in hardware, but are passed to/from the UD as 20 doubles.

LJ_chUSER_MEM makes 8 calls to the low-level ReadMem or EraseMem/WriteMem functions to do a consecutive read or erase/write of 8x 32-byte. This function accesses the first 256 bytes of user-mem. The second 256 bytes can only be accessed using the low-level functions. blocks of user memory.

For more information, see the low-level descriptions in [Sections 5.2.6 - 5.2.8](#), and see the Memory example in the VC6_LJUD archive. Note that this non-volatile flash memory has an erase cycle rating of 20k/100k (minimum/typical) so you do not want to erase & write in a fast loop for very long.

The following wait IOType is used to create a delay between other actions:

LJ_ioPUT_WAIT // Channel ignored. Value = 0-4194176 microseconds.

Any value (in microseconds) from 0-4194176 can be passed, but the actual resolution is 128 microseconds (U3C = 128 μ s, U3B = 64 μ s, U3A = 128 μ s).

This is typically used to put a small delay between two actions that will execute in the same low-level Feedback command. It is useful when the desired delay is less than what can be accomplished through software.

For example, a 1.024 millisecond pulse can be created by executing a single Add/Go/Get block that sequentially requests to set FIO4 to output-high, wait 1024 microseconds, then set FIO4 to output-low.

4.4 - Errorcodes [U3 Datasheet]

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UD Errorcodes

All functions return an LJ_ERROR error code as listed in the following tables.

Table 4.4-1. Request Level Error Codes

Errorcode	Name	Description
-2	LJE_UNABLE_TO_READ_CALDATA	Warning: Defaults used instead.
-1	LJE_DEVICE_NOT_CALIBRATED	Warning: Defaults used instead.
0	LJE_NOERROR	
1	LJE_COMMAND_LIST_ERROR	Error detected in eAddGoGet. Check the updated parameters aRequestErrors, GoError, and aResultErrors for specific errors.
2	LJE_INVALID_CHANNEL_NUMBER	Channel that does not exist (e.g. DAC2), or data from stream is requested on a channel that is not in the scan list.
3	LJE_INVALID_RAW_INOUT_PARAMETER	
4	LJE_UNABLE_TO_START_STREAM	
5	LJE_UNABLE_TO_STOP_STREAM	
6	LJE_NOTHING_TO_STREAM	
7	LJE_UNABLE_TO_CONFIG_STREAM	
8	LJE_BUFFER_OVERRUN	Overrun of the UD stream buffer.
9	LJE_STREAM_NOT_RUNNING	
10	LJE_INVALID_PARAMETER	

11	LJE_INVALID_STREAM_FREQUENCY	
12	LJE_INVALID_AIN_RANGE	
13	LJE_STREAM_CHECKSUM_ERROR	
14	LJE_STREAM_COMMAND_ERROR	
15	LJE_STREAM_ORDER_ERROR	Stream packet received out of sequence.
16	LJE_AD_PIN_CONFIGURATION_ERROR	Analog request on a digital pin, or vice versa.
17	LJE_REQUEST_NOT_PROCESSED	Previous request had an error.
19	LJE_SCRATCH_ERROR	
20	LJE_DATA_BUFFER_OVERFLOW	
21	LJE_ADC0_BUFFER_OVERFLOW	
22	LJE_FUNCTION_INVALID	
23	LJE_SWDT_TIME_INVALID	
24	LJE_FLASH_ERROR	
25	LJE_STREAM_IS_ACTIVE	
26	LJE_STREAM_TABLE_INVALID	
27	LJE_STREAM_CONFIG_INVALID	
28	LJE_STREAM_BAD_TRIGGER_SOURCE	
30	LJE_STREAM_INVALID_TRIGGER	
31	LJE_STREAM_ADC0_BUFFER_OVERFLOW	
33	LJE_STREAM_SAMPLE_NUM_INVALID	
34	LJE_STREAM_BIPOLAR_GAIN_INVALID	
35	LJE_STREAM_SCAN_RATE_INVALID	
36	LJE_TIMER_INVALID_MODE	
37	LJE_TIMER_QUADRATURE_AB_ERROR	
38	LJE_TIMER_QUAD_PULSE_SEQUENCE	
39	LJE_TIMER_BAD_CLOCK_SOURCE	
40	LJE_TIMER_STREAM_ACTIVE	
41	LJE_TIMER_PWMSTOP_MODULE_ERROR	
42	LJE_TIMER_SEQUENCE_ERROR	
43	LJE_TIMER_SHARING_ERROR	
44	LJE_TIMER_LINE_SEQUENCE_ERROR	
45	LJE_EXT_OSC_NOT_STABLE	
46	LJE_INVALID_POWER_SETTING	
47	LJE_PLL_NOT_LOCKED	
48	LJE_INVALID_PIN	
49	LJE_IOTYPE_SYNC_ERROR	
50	LJE_INVALID_OFFSET	
51	LJE_FEEDBACK_IOTYPE_NOT_VALID	
52	LJE_SHT_CRC	
53	LJE_SHT_MEASREADY	
54	LJE_SHT_ACK	
55	LJE_SHT_SERIAL_RESET	
56	LJE_SHT_COMMUNICATION	
57	LJE_AIN_WHILE_STREAMING	AIN not available to command-response functions while the UE9 is stream.
58	LJE_STREAM_TIMEOUT	
59	LJE_STREAM_CONTROL_BUFFER_OVERFLOW	
60	LJE_STREAM_SCAN_OVERLAP	New scan started before the previous scan completed. Scan rate is too high.
61	LJE_FIRMWARE_VERSION_IOTYPE	IOType not supported with this firmware.
62	LJE_FIRMWARE_VERSION_CHANNEL	Channel not supported with this firmware.
63	LJE_FIRMWARE_VERSION_VALUE	Value not supported with this firmware.
64	LJE_HARDWARE_VERSION_IOTYPE	IOType not supported with this hardware.
65	LJE_HARDWARE_VERSION_CHANNEL	Channel not supported with this hardware.
66	LJE_HARDWARE_VERSION_VALUE	Value not supported with this hardware.
67	LJE_CANT_CONFIGURE_PIN_FOR_ANALOG	
68	LJE_CANT_CONFIGURE_PIN_FOR_DIGITAL	
69	LJE_LJTDAC_ACK_ERROR	

70	LJE_TC_PIN_OFFSET_MUST_BE_4_TO_8	
71	LJE_INVALID_DIFFERENTIAL_CHANNEL	
72	LJE_DSP_SIGNAL_OUT_OF_RANGE	
73	LJE_STREAM_INVALID_CONNECTION	
112	UART_TIMEOUT	
113	UART_NOTCONNECTED	Use ConfigIO to assign pins to UART
114	UART_NOTENABLED	Use AsynchConfig to enable UART
115	I2C_BUS_BUSY	

Table 4.4-2. Group Level Error Codes

Errorcode	Name	Description
1000	LJE_MIN_GROUP_ERROR	Errors above this number stop all requests.
1001	LJE_UNKNOWN_ERROR	Unrecognized error that is caught.
1002	LJE_INVALID_DEVICE_TYPE	
1003	LJE_INVALID_HANDLE	
1004	LJE_DEVICE_NOT_OPEN	AddRequest() called even though Open() failed.
1005	LJE_NO_DATA_AVAILABLE	GetResult() call without calling a Go function, or a channel is passed that was not in the request list.
1006	LJE_NO_MORE_DATA_AVAILABLE	
1007	LJE_LABJACK_NOT_FOUND	LabJack not found at the given ID or address.
1008	LJE_COMM_FAILURE	Unable to send or receive the correct number of bytes.
1009	LJE_CHECKSUM_ERROR	
1010	LJE_DEVICE_ALREADY_OPEN	USB connection in use by another process.
1011	LJE_COMM_TIMEOUT	
1012	LJE_USB_DRIVER_NOT_FOUND	
1013	LJE_INVALID_CONNECTION_TYPE	
1014	LJE_INVALID_MODE	
1015	LJE_DEVICE_NOT_CONNECTED	Previously open device is no longer connected.

Table 4.4-1 lists the errors which are specific to a request. For example, LJE_INVALID_CHANNEL_NUMBER. If this error occurs, other requests are not affected.

Table 4.4-2 lists errors which cause all pending requests for a particular Go() to fail with the same error. If this type of error is received the state of any of the request is not known. For example, if requests are executed with a single Go() to set the AIN range and read an AIN, and the read fails with an LJE_COMM_FAILURE, it is not known whether the AIN range was set to the new value or whether it is still set at the old value.

5 - Low-level Function Reference [U3 Datasheet]

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Low-Level Function Reference Overview

This section describes the low level functions of the U3. These are commands sent over USB directly to the processor on the U3.

The majority of Windows users will use the high-level UD driver rather than these low-level functions.

5.1 - General Protocol [U3 Datasheet]

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Following is a description of the general U3 low-level communication protocol. There are two types of commands:

Normal: 1 command word plus 0-7 data words.

Extended: 3 command words plus 0-125 data words.

Normal commands have a smaller packet size and can be faster in some situations. Extended commands provide more commands, better error detection, and a larger maximum data payload.

Table 5.1-1. Normal command format

Byte			
0	Checksum8: Includes bytes 1-15		
1	Command Byte: DCCCCWWW		
		Bit 7: Destination Bit:	
			0 = Local,
			1 = Remote.
		Bits 6-3: Normal command number (0-14).	
		Bits 2-0: Number of data words.	
2-15	Data words.		

Table 5.1-2. Extended command format:

Byte			
0	Checksum8: Includes bytes 1-5		
1	Command Byte: D1111WWW		
		Bit 7: Destination Bit:	
			0 = Local,
			1 = Remote.
		Bits 6-3: 1111 specifies that this is an extended command	
		Bits 2-0: Used with some commands.	
2	Number of data words.		
3	Extended command number.		
4	Checksum16 (LSB)		
5	Checksum16 (MSB)		
6-255	Data words.		

Checksum calculations:

All checksums are a "1's complement checksum". Both the 8-bit and 16-bit checksum are unsigned. Sum all applicable bytes in an accumulator, 1 at a time. Each time another byte is added, check for overflow (carry bit), and if true add one to the accumulator.

In a high-level language, do the following for the 8-bit normal command checksum:

1. Get the subarray consisting of bytes 1 and up.
2. Convert bytes to U16 and sum into a U16 accumulator.
3. Divide by 2^8 and sum the quotient and remainder.
4. Divide by 2^8 and sum the quotient and remainder.

In a high-level language, do the following for an extended command 16-bit checksum:

1. Get the subarray consisting of bytes 6 and up.
2. Convert bytes to U16 and sum into a U16 accumulator (can't overflow).

Then do the following for the 8-bit extended checksum:

1. Get the subarray consisting of bytes 1 through 5.
2. Convert bytes to U16 and sum into a U16 accumulator.
3. Divide by 2^8 and sum the quotient and remainder.
4. Divide by 2^8 and sum the quotient and remainder.

Destination bit:

This bit specifies whether the command is destined for the local or remote target. This bit is ignored on the U3.

Multi-byte parameters:

In the following function definitions there are various multi-byte parameters. The least significant byte of the parameter will always be found at the lowest byte number. For instance, bytes 10 through 13 of CommConfig are the IP address which is 4 bytes long. Byte 10 is the least significant byte (LSB), and byte 13 is the most significant byte (MSB).

Masks:

Some functions have mask parameters. The WriteMask found in some functions specifies which parameters are to be written. If a bit is 1, that parameter will be updated with the new passed value. If a bit is 0, the parameter is not changed and only a read is performed.

The AINMask found in some functions specifies which analog inputs are acquired. This is a 16-bit parameter where each bit corresponds to AIN0-AIN15. If a bit is 1, that channel will be acquired.

The digital I/O masks, such as FIOMask, specify that the passed value for direction and state are updated if a bit 1. If a bit of the mask is 0 only a read is performed on that bit of I/O.

Binary Encoded Parameters:

Many parameters in the following functions use specific bits within a single integer parameter to write/read specific information. In particular, most digital I/O parameters contain the information for each bit of I/O in one integer, where each bit of I/O corresponds to the same bit in the parameter (e.g. the direction of FIO0 is set in bit 0 of parameter FIODir). For instance, in the function ControlConfig, the parameter FIODir is a single byte (8 bits) that writes/reads the direction of each of the 8 FIO lines:

- if FIODir is 0, all FIO lines are input,
- if FIODir is 1 (2^0), FIO0 is output, FIO1-FIO7 are input,
- if FIODir is 5 ($2^0 + 2^2$), FIO0 and FIO2 are output, all other FIO lines are input,
- if FIODir is 255 ($2^0 + \dots + 2^7$), FIO0-FIO7 are output.

5.2 - Low-Level Functions [U3 Datasheet]

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5.2.1 - Bad Checksum [U3 Datasheet]

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If the processor detects a bad checksum in any command, the following 2-byte normal response will be sent and nothing further will be done.

Response:	
Byte	
0	0xB8
1	0xB8

5.2.2 - ConfigU3 [U3 Datasheet]

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Writes and reads various configuration settings. Although this function has many of the same parameters as other functions, most parameters in this case are affecting the power-up values, not the current values. There is a hardware method to restore bytes 9-20 to the factory default value of 0x00: Power up the U3 with a short from FIO6 to SPC (FIO2 to SCL on U3 1.20/1.21), then remove the jumper and power cycle the device again.

If WriteMask is nonzero, some or all default values are written to flash. The U3 flash has a rated endurance of at least 20000 writes, which is plenty for reasonable operation, but if this function is called in a high-speed loop with a nonzero WriteMask, the flash could eventually be damaged.

Note: If the stream is running, you cannot update any of the values (WriteMask must equal 0).

Table 5.2.2-1. ConfigU3 Command Response

Command:		
Byte		
0	Checksum8	
1	0xF8	
2	0x0A	
3	0x08	
4	Checksum16 (LSB)	
5	Checksum16 (MSB)	
6	WriteMask0	
		Bit 5: CompatibilityOptions Defaults
		Bit 4: TimerClockConfig and Divisor Defaults
		Bit 3: LocalID
		Bit 2: DAC Defaults
		Bit 1: Digital I/O Defaults
		Bit 0: Reserved
7	WriteMask1 (Reserved)	
8	LocalID	
9	TimerCounterConfig	
		Bits 4-7: TimerCounterPinOffset
		Bit 3: Enable Counter1

		Bit 2: Enable Counter0
		Bit 6: F: Number of timers enabled
10	FIOAnalog	
11	FIODirection	
12	FIOState	
13	EIOAnalog	
14	EIODirection	
15	EIOState	
16	CIODirection	
17	CIOState	
18	DAC1Enable	
19	SWDT DAC0 RESPONSE	
20	SWDT DAC1 RESPONSE	
21	TimerClockConfig	
22	TimerClockDivisor (0 = ÷256)	
23	CompatibilityOptions	
24	0x00	
25	0x00	
Response:		
Byte		
0	Checksum8	
1	0xF8	
2	0x10	
3	0x08	
4	Checksum16 (LSB)	
5	Checksum16 (MSB)	
6	Errorcode	
7	Reserved	
8	Reserved	
9-10	FirmwareVersion	
11-12	BootloaderVersion	
13-14	HardwareVersion	
15-18	SerialNumber	
19-20	ProductID	
21	LocalID	
22	TimerCounterMask	
23	FIOAnalog	
24	FIODirection	
25	FIOState	
26	EIOAnalog	
27	EIODirection	
28	EIOState	
29	CIODirection	
30	CIOState	
31	DAC1Enable	
32	DAC0	
33	DAC1	
34	TimerClockConfig	
35	TimerClockDivisor (0 = ÷256)	
36	CompatibilityOptions	
37	VersionInfo	

- **WriteMask:** Has bits that determine which, if any, of the parameters will be written to flash as the reset defaults. If a bit is 1, that parameter will be updated with the new passed value. If a bit is 0, the parameter is not changed and only a read is performed. Note that reads return reset defaults, not necessarily current values (except for LocalID). For instance, the value returned by FIODirection is the directions at reset, not necessarily the current directions.
- **LocalID:** If the WriteMask bit 3 is set, the value passed become the default value, meaning it is written to flash and used at reset. This is a user-configurable ID that can be used to identify a specific LabJack. The return value of this parameter is the current value and the power-up default value.
- **TimerCounterConfig:** If the WriteMask bit 1 is set, the value passed becomes the default value, meaning it is written to flash and used at reset. The return value of this parameter is a read of the power-up default. See [Section 5.2.3](#).
- **FIO/EIO/CIO:** If the WriteMask bit 3 is set, the values passed become the default values, meaning they are written to flash and used at reset. Regardless of the mask bit, this function has no effect on the current settings. The return value of these parameters are a read of the power-up defaults.
- **DAC:** If the WriteMask bit 2 is set, the values passed become the default values, meaning they are written to flash and used at reset. Regardless of the mask bit, this function has no effect on the current settings. The return values of these parameters are a read of the power-up defaults.

- **TimerClockConfig & TimerClockDivisor:** If the WriteMask bit 4 is set, the values passed become the default values, meaning they are written to flash and used at reset. The return values of these parameters are a read of the power-up defaults. See [Section 5.2.4](#).
- **CompatibilityOptions:** If the WriteMask bit 5 is set, the value passed becomes the default value, meaning it is written to flash and used at reset. The return value of this parameter is a read of the power-up default. If bit 0 is set, Timer Counter Pin Offset errors are ignored. If bit 1 is set, all DAC operations will use 8-bit mode rather than 10-bit mode. Once this value has been changed the U3 will need to be restarted before the new setting will take affect.
- **FirmwareVersion:** Fixed parameter specifies the version number of the main firmware. A firmware upgrade will generally cause this parameter to change. The lower byte is the integer portion of the version and the higher byte is the fractional portion of the version.
- **BootloaderVersion:** Fixed parameter specifies the version number of the bootloader. The lower byte is the integer portion of the version and the higher byte is the fractional portion of the version.
- **HardwareVersion:** Fixed parameter specifies the version number of the hardware. The lower byte is the integer portion of the version and the higher byte is the fractional portion of the version.
- **SerialNumber:** Fixed parameter that is unique for every LabJack.
- **ProductID:** (3) Fixed parameter identifies this LabJack as a U3.
- **VersionInfo:** Bit 0 specifies U3B. Bit 1 specifies U3C and if set then bit 4 specifies -HV version.

5.2.3 - ConfigIO [U3 Datasheet]

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Writes and reads the current IO configuration.

Table 5.2.3-1. ConfigIO Command Response

Command:		
Byte		
0	Checksum8	
1	0xF8	
2	0x03	
3	0x0B	
4	Checksum16 (LSB)	
5	Checksum16 (MSB)	
6	WriteMask	
		Bit 5: Write UART Related settings
		Bit 4: Reserved, Pass 0
		Bit 3: EIOAnalog
		Bit 2: FIOAnalog
		Bit 1: DAC1Enable
		Bit 0 : TimerCounterConfig
7	Reserved	
8	TimerCounterConfig	
		Bits 4-7: TimerCounterPinOffset
		Bit 3: Enable Counter1
		Bit 2: Enable Counter0
		Bits 0-1: Number of timers enabled
9	DAC1Enable (ignored on hardware rev 1.30+)	
		Bit 2: Assign UART Pins (HW 1.30 only)
		Bit 1: Reserved, Pass 0
		Bit 0: Enable DAC1
10	FIOAnalog	
11	EIOAnalog	
Response:		
Byte		
0	Checksum8	
1	0xF8	
2	0x03	
3	0x0B	
4	Checksum16 (LSB)	
5	Checksum16 (MSB)	
6	Errorcode	
7	Reserved	
8	TimerCounterConfig	
9	DAC1Enable	
10	FIOAnalog	
11	EIOAnalog	

- **WriteMask:** Has bits that determine which, if any, of the parameters will be written.
- **TimerCounterConfig:** Used to enable/disable timers and counters. Timers/counters will be assigned to IO pins starting with FIO0 plus TimerCounterPinOffset (4-8 only starting with hardware revision 1.30). Timer0 takes the first IO pin, then Timer1, Counter0, and Counter1. Whenever this function is called and timers are enabled, the timers are initialized to mode 10, so the desired timer mode must always be specified after every call to this function. Note that Counter0 is not available when using a timer clock base that supports a timer clock divisor (TimerClockBase = 3-6).
- **Assign UART Pins:** On hardware 1.30 setting this bit will assign IO lines to theUART module. This setting will be ignored unless theUART write bit is set in the WriteMask byte.
- **DAC1Enable:** On hardware revisions 1.20/1.21 only, bit 0 enables DAC1. When DAC1 is disabled, it outputs a constant voltage of 1.5 times the internal Vref (~2.44 volts). When DAC1 is enabled, the internal Vref is not available for the analog inputs and Vreg (~3.3 volts) is used as the AIN reference. Starting with hardware revision 1.30, DAC1 is always enabled.
- **FIOAnalog:** Each bit determines whether that bit ofFIO is analog input (=1) or digital I/O (=0).
- **EIOAnalog:** Each bit determines whether that bit ofEIO is analog input (=1) or digital I/O (=0).

LabJackPython Example

```
>>> import u3
>>> d = u3.U3()
>>> d.debug = True
""Assign timer0 to FIO6, set FIO4, FIO5 as analog input, and set EIO1, EIO2 as analog input""
>>> d.configIO(TimerCounterPinOffset = 6, NumberOfTimersEnabled = 1, FIOAnalog = 0x30, EIOAnalog = 0x03)
Sent: [0xa8, 0xf8, 0x3, 0xb, 0xa1, 0x0, 0xd, 0x0, 0x61, 0x0, 0x30, 0x3]
Result: [0x9b, 0xf8, 0x3, 0xb, 0x94, 0x0, 0x0, 0x0, 0x61, 0x0, 0x30, 0x3]
{'NumberOfTimersEnabled': 1, 'TimerCounterPinOffset': 6, 'DAC1Enable': 0, 'FIOAnalog': 48, 'EIOAnalog': 3, 'TimerCounterConfig': 97, 'EnableCounter1': False, 'EnableCounter0': False}
```

5.2.4 - ConfigTimerClock [U3 Datasheet]

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Writes and read the timer clock configuration.

Table 5.2.4-1. ConfigTimerClock Command Response

Command:		
Byte		
0	Checksum8	
1	0xF8	
2	0x02	
3	0x0A	
4	Checksum16 (LSB)	
5	Checksum16 (MSB)	
6	Reserved	
7	Reserved	
8	TimerClockConfig	
		Bit 7: Configure the clock
		Bits 2-0: TimerClockBase
		b000: 4 MHz
		b001: 12 MHz
		b010: 48 MHz (Default)
		b011: 1 MHz/Divisor
		b100: 4 MHz/Divisor
		b101: 12 MHz/Divisor
		b110: 48 MHz/Divisor
9	TimerClockDivisor (0 = ±256)	
Response:		
Byte		
0	Checksum8	
1	0xF8	
2	0x02	
3	0x0A	
4	Checksum16 (LSB)	

5	Checksum16 (MSB)	
6	Errorcode	
7	Reserved	
8	TimerClockConfig	
9	TimerClockDivisor (0 = $\div 256$)	

- **TimerClockConfig:** Bit 7 determines whether the new TimerClockBase and TimerClockDivisor are written, or if just a read is performed. Bits 0-2 specify the TimerClockBase. If TimerClockBase is 3-6, then Counter0 is not available.
- **TimerClockDivisor:** The base timer clock is divided by this value, or divided by 256 if this value is 0. Only applies if TimerClockBase is 3-6.

5.2.5 - Feedback [U3 Datasheet]

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Feedback Overview

A flexible function that handles all command/response functionality. One or more IOTypes are used to perform a single write/read or multiple writes/reads.

Note that the general protocol described in [Section 5.1](#) defines byte 2 of an extended command as the number of data words, which is the number of words in a packet beyond the first 3 (a word is 2 bytes). Also note that the overall size of a packet must be an even number of bytes, so in this case an extra 0x00 is added to the end of the command and/or response if needed to accomplish this.

Since this command has a flexible size, byte 2 will vary. For instance, if a single IOType of PortStateRead (d26) is passed, byte 2 would be equal to 1 for the command and 3 for the response. If a single IOType of LED (d9) is passed, an extra 0 must be added to the command to make the packet have an even number of bytes, and byte 2 would be equal to 2. The response would also need an extra 0 to be even, and byte 2 would be equal to 2.

Table 5.2.5-1. Feedback Command Response

Command:	
Byte	
0	Checksum8
1	0xF8
2	0.5 + Number of Data Words (IOTypes and Data)
3	0x00
4	Checksum16 (LSB)
5	Checksum16 (MSB)
6	Echo
7-63	IOTypes and Data
Response:	
Byte	
0	Checksum8
1	0xF8
2	1.5 + Number of Data Words (If Errorcode = 0)
3	0x00
4	Checksum16 (LSB)
5	Checksum16 (MSB)
6	Errorcode
7	ErrorFrame
8	Echo
9-63	Data

- **IOTypes & Data:** One or more IOTypes can be passed in a single command, up to the maximum packet size. More info about the available IOTypes is below. In the outgoing command each IOType is passed and accompanied by 0 or more data bytes. In the incoming response, only data bytes are returned without the IOTypes.
- **Echo:** This byte is simply echoed back in the response. A host application might pass sequential numbers to ensure the responses are in order and associated with the proper command.
- **ErrorFrame:** If Errorcode is not zero, this parameter indicates which IOType caused the error. For instance, if the 3rd passed IOType caused the error, the ErrorFrame would be equal to 3. Also note that data is only returned for IOTypes before the one that caused the error, so if any IOType causes an error the overall function response will have less bytes than expected.

Table 5.2.5-2.

Name	IOType (dec)	WriteBytes	ReadBytes
AIN	1	3	2

WaitShort	5	2	0
WaitLong	6	2	0
LED	9	2	0
BitStateRead	10	2	1
BitStateWrite	11	2	0
BitDirRead	12	2	1
BitDirWrite	13	2	0
PortStateRead	26	1	3
PortStateWrite	27	7	0
PortDirRead	28	1	3
PortDirWrite	29	7	0
DAC0 (8-bit)	34	2	0
DAC1 (8-bit)	35	2	0
DAC0 (16-bit)	38	3	0
DAC1 (16-bit)	39	3	0
Timer0	42	4	4
Timer0Config	43	4	0
Timer1	44	4	4
Timer1Config	45	4	0
Counter0	54	2	4
Counter1	55	2	4
Buzzer	63	6	0

5.2.5.1 - AIN: IOType = 1 [U3 Datasheet]

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Table 5.2.5.1-1.

<u>AIN, 3 Command Bytes:</u>	
0	IOType = 1
1	Bits 4-0: Positive Channel
	Bit 6: LongSettling
	Bit 7: QuickSample
2	Negative Channel
<u>2 Response Bytes:</u>	
0	AIN LSB
1	AIN MSB

This IOType returns a single analog input reading.

Note: Do **not** use this IO type while streaming.

- **Positive Channel:** 0-15 for AIN0-AIN15, 30 for temp sensor, or 31 for Vreg. Note that AIN0-AIN7 appear on FIO0-FIO7, and AIN8-AIN15 appear on EIO0-EIO7.
- **LongSettling:** If this bit is set, additional settling time is added between the multiplexer configuration and the analog to digital conversion.
- **QuickSample:** If this bit is set, a faster analog input conversion is done, at the expense of increased noise. If bits 6 & 7 are both set, then the entire byte is a channel number (for digital I/O, timers, and counters).
- **Negative Channel:** 0-15 for AIN0-AIN15, 30 for Vref, or 31 for single-ended. Note that AIN0-AIN7 appear on FIO0-FIO7, and AIN8-AIN15 appear on EIO0-EIO7.
- **AIN LSB & MSB:** Analog input reading is returned justified as a 16-bit value (always unsigned).

Support - Python U3 Get Feedback Example

LabJackPython example session

Automatically extracted from [u3.py](#). Debugging turned on to show the bytes sent and received.

```

1 Analog Input Feedback command
2
3 specify the positive and negative channels to use
4 (0-16, 30 and 31 are possible)
5 also specify whether to turn on longSettle or quick Sample
6
7 returns 16-bit signed int sample
8
9 >>> import u3
10 >>> d = u3.U3()
11 >>> d.debug = True
12 >>> d.getFeedback(u3.AIN(PositiveChannel = 0, NegativeChannel=31, LongSettling=False, QuickSample=False))
13 Sent: [0x1b, 0xf8, 0x2, 0x0, 0x20, 0x0, 0x0, 0x1, 0x0, 0x1f]
14 Response: [0xab, 0xf8, 0x3, 0x0, 0xaf, 0x0, 0x0, 0x0, 0x20, 0x8f, 0x0]
15 [36640]

```

u3-feedback-AIN.txt hosted with ❤ by GitHub

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5.2.5.2 - WaitShort: IOType=5 [U3 Datasheet]

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Table 5.2.5.2-1.

WaitShort, 2 Command	
Bytes:	
0	IOType = 5
1	Time (*128 us)
0 Response Bytes:	

This IOType provides a way to add a delay during execution of the Feedback function. The typical use would be putting this IOType in between IOTypes that set a digital output line high and low, thus providing a simple way to create a pulse. Note that this IOType uses the same internal timer as stream mode, so cannot be used while streaming.

- **Time:** This value (0-255) is multiplied by 128 microseconds to determine the delay (U3C = 128 us, U3B = 64 us, U3A = 128 us).

Support - U3 Python Wait Short Example

LabJackPython example session

Automatically extracted from [u3.py](#). Debugging turned on to show the bytes sent and received.

```

1 WaitShort Feedback command
2
3 specify the number of 128us time increments to wait
4
5 >>> import u3
6 >>> d = u3.U3()
7 >>> d.debug = True
8 >>> d.getFeedback(u3.WaitShort(Time = 9))
9 Sent: [0x9, 0xf8, 0x2, 0x0, 0xe, 0x0, 0x0, 0x5, 0x9, 0x0]
10 Response: [0xfa, 0xf8, 0x2, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0]
11 [None]

```

u3-feedback-WaitShort.txt hosted with ❤ by GitHub

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5.2.5.3 - WaitLong: IOType=6 [U3 Datasheet]

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Table 5.2.5.3-1.

WaitLong, 2 Command	
Bytes:	
0	IOType = 6
1	

1	Time (*16 ms)
0	Response Bytes:

This IOType provides a way to add a delay during execution of the Feedback function. The typical use would be putting this IOType in between IOTypes that set a digital output line high and low, thus providing a simple way to create a pulse. Note that this IOType uses the same internal timer as stream mode, so cannot be used while streaming.

- **Time:** This value (0-255) is multiplied by 16384 microseconds (U3C = 16384 us, U3B = 16384 us, U3A = 32768 us) to determine the delay.

LabJackPython example session

Automatically extracted from [u3.py](#). Debugging turned on to show the bytes sent and received.

WaitLong Feedback command

specify the number of 16ms time increments to wait

```
>>> import u3
>>> d = u3.U3()
>>> d.debug = True
>>> d.getFeedback(u3.WaitLong(Time = 70))
Sent: [0x47, 0xf8, 0x2, 0x0, 0x4c, 0x0, 0x0, 0x6, 0x46, 0x0]
Response: [0xfa, 0xf8, 0x2, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0]
[None]
```

5.2.5.4 - LED: IOType=9 [U3 Datasheet]

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Table 5.2.5.4-1.

LED, 2 Command Bytes:	
0	IOType = 9
1	State
0 Response Bytes:	

This IOType simply turns the status LED on or off.

- **State:** 1=On, 0=Off.

u3 - LED code example

LabJackPython example session

Automatically extracted from [u3.py](#). Debugging turned on to show the bytes sent and received.

```
1  LED Toggle
2
3  specify whether the LED should be on or off by truth value
4
5  1 or True = On, 0 or False = Off
6
7  >>> import u3
8  >>> d = u3.U3()
9  >>> d.debug = True
10 >>> d.getFeedback(u3.LED(State = False))
11 Sent: [0x4, 0xf8, 0x2, 0x0, 0x9, 0x0, 0x0, 0x9, 0x0, 0x0]
12 Response: [0xfa, 0xf8, 0x2, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0]
13 [None]
14 >>> d.getFeedback(u3.LED(State = True))
15 Sent: [0x5, 0xf8, 0x2, 0x0, 0xa, 0x0, 0x0, 0x9, 0x1, 0x0]
16 Response: [0xfa, 0xf8, 0x2, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0]
17 [None]
```

5.2.5.5 - BitStateRead: IOType=10 [U3 Datasheet]

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Table 5.2.5.5-1. BitStateRead Command Response

BitStateRead, 2 Command Bytes:	
0	IOType = 10
1	Bits 0-4: IONumber
1 Response Byte:	
0	Bit 0: State

This IOType reads the state of a single bit of digital I/O. The direction setting of the IO will not be changed. When the digital IO is set to output the actual state of the line is read. Only lines configured as digital (not analog) return valid readings.

- **IO Number:** 0-7=FIO, 8-15=EIO, or 16-19=CIO.
- **State:** 1=High, 0=Low.

LabJackPython example session

Automatically extracted from [u3.py](#). Debugging turned on to show the bytes sent and received.

```

1 BitStateRead Feedback command
2
3 read the state of a single bit of digital I/O. Only digital
4 lines return valid readings.
5
6 IONumber: 0-7=FIO, 8-15=EIO, 16-19=CIO
7 return 0 or 1
8
9 >>> import u3
10 >>> d = u3.U3()
11 >>> d.debug = True
12 >>> d.getFeedback(u3.BitStateRead(IONumber = 5))
13 Sent: [0xa, 0xf8, 0x2, 0x0, 0xf, 0x0, 0x0, 0xa, 0x5, 0x0]
14 Response: [0xfb, 0xf8, 0x2, 0x0, 0x1, 0x0, 0x0, 0x0, 0x0, 0x1]
15 [1]
```

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5.2.5.6 - BitStateWrite: IOType=11 [U3 Datasheet]

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Table 5.2.5.6-1.

BitStateWrite, 2 Command Bytes:	
0	IOType = 11
1	Bits 0-4: IO Number
	Bit 7: State
0 Response Bytes:	

This IOType writes the state of a single bit of digital I/O. The direction of the specified line is forced to output.

- **IO Number:** 0-7=FIO, 8-15=EIO, or 16-19=CIO.
- **State:** 1=High, 0=Low.

u3 - bitstatewrite code example

LabJackPython example session

Automatically extracted from [u3.py](#). Debugging turned on to show the bytes sent and received.

```

1 BitStateWrite Feedback command
2
3 write a single bit of digital I/O. The direction of the
4 specified line is forced to output.
5
6 IONumber: 0-7=FIO, 8-15=EIO, 16-19=CIO
7 State: 0 or 1
8
9 >>> import u3
10 >>> d = u3.U3()
11 >>> d.debug = True
12 >>> d.getFeedback(u3.BitStateWrite(IONumber = 5, State = 0))
13 Sent: [0xb, 0xf8, 0x2, 0x0, 0x10, 0x0, 0x0, 0xb, 0x5, 0x0]
14 Response: [0xfa, 0xf8, 0x2, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0]
15 [None]

```

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5.2.5.7 - BitDirRead: IOType=12 [U3 Datasheet]

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Table 5.2.5.7-1.

BitDirRead. 2 Command	
Bytes:	
0	IOType = 12
1	Bits 0-4: IO Number
1 Response Byte:	
0	Bit 0: Direction

This IOType reads the direction of a single bit of digital I/O. This is the digital direction only, and does not provide any information as to whether the line is configured as digital or analog.

- **IO Number:** 0-7=FIO, 8-15=EIO, or 16-19=CIO.
- **Direction:** 1=Output, 0=Input.

u3 - bitdirread code example

LabJackPython example session

Automatically extracted from [u3.py](#). Debugging turned on to show the bytes sent and received.

```

1 Read the digital direction of one I/O
2
3 IONumber: 0-7=FIO, 8-15=EIO, 16-19=CIO
4 returns 1 = Output, 0 = Input
5
6 >>> import u3
7 >>> d = u3.U3()
8 >>> d.debug = True
9 >>> d.getFeedback(u3.BitDirRead(IONumber = 5))
10 Sent: [0xc, 0xf8, 0x2, 0x0, 0x11, 0x0, 0x0, 0xc, 0x5, 0x0]
11 Response: [0xfb, 0xf8, 0x2, 0x0, 0x1, 0x0, 0x0, 0x0, 0x0, 0x1]
12 [1]

```

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5.2.5.8 - BitDirWrite: IOType=13 [U3 Datasheet]

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Table 5.2.5.8-1. BitDirWrite Command Response

BitDirRead. 2 Command	
-----------------------	--

Bytes:	
0	IOType = 13
1	Bits 0-4: IO Number
	Bit 7: Direction
0	Response Bytes:

This IOType writes the direction of a single bit of digital I/O.

- **IO Number:** 0-7=FIO, 8-15=EIO, or 16-19=CIO.
- **Direction:** 1=Output, 0=Input.

u3 - bitdirwrite code example

LabJackPython example session

Automatically extracted from [u3.py](#). Debugging turned on to show the bytes sent and received.

```

1 BitDirWrite Feedback command
2
3 Set the digital direction of one I/O
4
5 IONumber: 0-7=FIO, 8-15=EIO, 16-19=CIO
6 Direction: 1 = Output, 0 = Input
7
8 >>> import u3
9 >>> d = u3.U3()
10 >>> d.debug = True
11 >>> d.getFeedback(u3.BitDirWrite(IONumber = 5, Direction = 0))
12 Sent: [0xd, 0xf8, 0x2, 0x0, 0x12, 0x0, 0x0, 0xd, 0x5, 0x0]
13 Response: [0xfa, 0xf8, 0x2, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0]
14 [None]
```

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5.2.5.9 - PortStateRead: IOType=26 [U3 Datasheet]

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Table 5.2.5.9-1.

PortStateRead. 1 Command	
Byte:	
0	IOType = 26
3 Response Bytes:	
0-2	State

This IOType reads the state of all digital I/O, where 0-7=FIO, 8-15=EIO, and 16-19=CIO. Only lines configured as digital (not analog) return valid readings.

- **State:** Each bit of this value corresponds to the specified bit of I/O such that 1=High and 0=Low. If all are low, State=d0. If all 20 standard digital I/O are high, State=d1048575. If FIO0-FIO2 are high, EIO0-EIO2 are high, CIO0 are high, and all other I/O are low (b0000000010000011100000111), State=d67335.

u3 - portstateread code example

LabJackPython example session

Automatically extracted from [u3.py](#). Debugging turned on to show the bytes sent and received.

```

1 PortStateRead Feedback command
2 Reads the state of all digital I/O.
3
4 >>> import u3
5 >>> d = u3.U3()
6 >>> d.debug = True
7 >>> d.getFeedback(u3.PortStateRead())
8 Sent: [0x14, 0xf8, 0x1, 0x0, 0x1a, 0x0, 0x0, 0x1a]
9 Response: [0xeb, 0xf8, 0x3, 0x0, 0xee, 0x1, 0x0, 0x0, 0x0, 0xe0, 0xff, 0xf]
10 [['CIO': 15, 'FIO': 224, 'EIO': 255]]

```

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5.2.5.10 - PortStateWrite: IOType=27 [U3 Datasheet]

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Table 5.2.5.10-1.

PortStateWrite. 7 Command	
Bytes:	
0	IOType = 27
1-3	WriteMask
4-6	State
0 Response Bytes:	

This IOType writes the state of all digital I/O, where 0-7=FIO, 8-15=EIO, and 16-19=CIO. The direction of the selected lines is forced to output.

The firmware does the actual updates in the following order: FIO4-5, FIO6-7, FIO0-3, EIO0-5, EIO6-7, CIO0, CIO1, CIO2, CIO3. These all happen within 1 microsecond or so, but EIO0-EIO5, for example, are all updated at the exact same time.

- **WriteMask:** Each bit specifies whether to update the corresponding bit of I/O.
- **State:** Each bit of this value corresponds to the specified bit of I/O such that 1=High and 0=Low. To set all low, State=d0. To set all 20 standard digital I/O high, State=d1048575. To set FIO0-FIO2 high, EIO0-EIO2 high, CIO0 high, and all other I/O low (b000000010000011100000111), State=d67335.

U3 - portstatewrite code example

LabJackPython example session

Automatically extracted from [u3.py](#). Debugging turned on to show the bytes sent and received.

```

1 PortStateWrite Feedback command
2
3 State: A list of 3 bytes representing FIO, EIO, CIO
4 WriteMask: A list of 3 bytes, representing which to update.
5     The Default is all ones.
6
7 >>> import u3
8 >>> d = u3.U3()
9 >>> d.debug = True
10 >>> d.getFeedback(u3.PortStateWrite(State = [0xab, 0xcd, 0xef], WriteMask = [0xff, 0xff, 0xff]))
11 Sent: [0x81, 0xf8, 0x4, 0x0, 0x7f, 0x5, 0x0, 0x1b, 0xff, 0xff, 0xff, 0xab, 0xcd, 0xef]
12 Response: [0xfa, 0xf8, 0x2, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0]
13 [None]

```

u3-feedback-PortStateWrite.txt hosted with ❤ by GitHub

[view raw](#)

5.2.5.11 - PortDirRead: IOType=28 [U3 Datasheet]

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Table 5.2.5.11-1. PortDirRead Command Response

PortDirRead. 1 Command

Byte:	
0	IOType = 28
3 Response Bytes:	
0-2	Direction

This IOType reads the directions of all digital I/O, where 0-7=FIO, 8-15=EIO, and 16-19=CIO. These are the digital directions only, and do not provide any information as to whether the lines are configured as digital or analog.

- **Direction:** Each bit of this value corresponds to the specified bit of I/O such that 1=Output and 0=Input. If all are input, Direction=d0. If all 20 standard digital I/O are output, Direction=d1048575. If FIO0-FIO2 are output, EIO0-EIO2 are output, CIO0 are output, and all other I/O are input (b000000010000011100000111), Direction=d67335.

LabJackPython example session

Automatically extracted from [u3.py](#). Debugging turned on to show the bytes sent and received.

```

1  PortDirRead Feedback command
2  Reads the direction of all digital I/O.
3
4  >>> import u3
5  >>> d = u3.U3()
6  >>> d.debug = True
7  >>> d.getFeedback(u3.PortDirRead())
8  Sent: [0x16, 0xf8, 0x1, 0x0, 0x1c, 0x0, 0x0, 0x1c]
9  Response: [0xfb, 0xf8, 0x3, 0x0, 0xfe, 0x1, 0x0, 0x0, 0x0, 0xf0, 0xff, 0xf]
10 [['CIO': 15, 'FIO': 240, 'EIO': 255]]

```

u3-feedback-PortDirRead.txt hosted with ♥ by GitHub

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5.2.5.12 - PortDirWrite: IOType=29 [U3 Datasheet]

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Table 5.2.5.12-1. PortDirWrite Command Response

PortStateWrite. 7 Command Bytes:	
0	IOType = 29
1-3	WriteMask
4-6	Direction
0 Response Bytes:	

This IOType writes the direction of all digital I/O, where 0-7=FIO, 8-15=EIO, and 16-19=CIO. Note that the desired lines must be configured as digital (not analog).

- **WriteMask:** Each bit specifies whether to update the corresponding bit of I/O.
- **Direction:** Each bit of this value corresponds to the specified bit of I/O such that 1=Output and 0=Input. To configure all as input, Direction=d0. For all 20 standard digital I/O as output, Direction=d1048575. To configure FIO0-FIO2 as output, EIO0-EIO2 as output, CIO0 as output, and all other I/O as input (b000000010000011100000111), Direction=d67335.

u3 - portdirwrite code example

LabJackPython example session

Automatically extracted from [u3.py](#). Debugging turned on to show the bytes sent and received.

```

1  PortDirWrite Feedback command
2
3  Direction: A list of 3 bytes representing FIO, EIO, CIO
4  WriteMask: A list of 3 bytes, representing which to update. Default is all ones.
5
6
7  >>> import u3
8  >>> d = u3.U3()
9  >>> d.debug = True
10 >>> d.getFeedback(u3.PortDirWrite(Direction = [0xaa, 0xcc, 0xff], WriteMask = [0xff, 0xff, 0xff]))
11 Sent: [0x91, 0xf8, 0x4, 0x0, 0x8f, 0x5, 0x0, 0x1d, 0xff, 0xff, 0xff, 0xaa, 0xcc, 0xff]
12 Response: [0xfa, 0xf8, 0x2, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0]
13 [None]

```

u3-feedback-PortDirWrite.txt hosted with ♥ by GitHub

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5.2.5.13 - DAC# (8-bit): IOType=34,35 [U3 Datasheet]

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Table 5.2.5.13-1. DAC 8-bit Command Response

DAC# (8-bit), 2 Command Bytes:	
0	IOType = 34, 35
1	Value
0 Response Bytes:	

This IOType controls a single analog output.

- **Value:** 0=Minimum, 255=Maximum.

U3 - DAC stuff code examples

LabJackPython example session

Automatically extracted from [u3.py](#). Debugging turned on to show the bytes sent and received.

```

1  8-bit DAC Feedback command
2
3  Controls a single analog output
4
5  Dac: 0 or 1
6  Value: 0-255
7
8  >>> import u3
9  >>> d = u3.U3()
10 >>> d.debug = True
11 >>> d.getFeedback(u3.DAC8(Dac = 0, Value = 0x55))
12 Sent: [0x72, 0xf8, 0x2, 0x0, 0x77, 0x0, 0x0, 0x22, 0x55, 0x0]
13 Response: [0xfa, 0xf8, 0x2, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0]
14 [None]

```

u3-feedback-DAC8.txt hosted with ♥ by GitHub

[view raw](#)

```

1 8-bit DAC Feedback command for DAC0
2
3 Controls DAC0 in 8-bit mode.
4
5 Value: 0-255
6
7 >>> import u3
8 >>> d = u3.U3()
9 >>> d.debug = True
10 >>> d.getFeedback(u3.DAC0_8(Value = 0x33))
11 Sent: [0x50, 0xf8, 0x2, 0x0, 0x55, 0x0, 0x0, 0x22, 0x33, 0x0]
12 Response: [0xfa, 0xf8, 0x2, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0]
13 [None]

```

u3-feedback-DAC0_8.txt hosted with ♥ by GitHub

[view raw](#)

```

1 8-bit DAC Feedback command for DAC1
2
3 Controls DAC1 in 8-bit mode.
4
5 Value: 0-255
6
7 >>> import u3
8 >>> d = u3.U3()
9 >>> d.debug = True
10 >>> d.getFeedback(u3.DAC1_8(Value = 0x22))
11 Sent: [0x40, 0xf8, 0x2, 0x0, 0x45, 0x0, 0x0, 0x23, 0x22, 0x0]
12 Response: [0xfa, 0xf8, 0x2, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0]
13 [None]

```

u3-feedback-DAC1_8.txt hosted with ♥ by GitHub

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5.2.5.14 - DAC# (16-bit): IOType=38,39 [U3 Datasheet]

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Table 5.2.5.14-1. DAC 16 bit Command Response

DAC# (16-bit), 3 Command Bytes:	
0	IOType = 38, 39
1	Value LSB
2	Value MSB
0 Response Bytes:	

This IOType controls a single analog output.

- **Value:** 0=Minimum, 65535=Maximum.

u3 - dac16 code snippets

LabJackPython example session

Automatically extracted from [u3.py](#). Debugging turned on to show the bytes sent and received.

```

1 16-bit DAC Feedback command
2
3 Controls a single analog output
4
5 Dac: 0 or 1
6 Value: 0-65535
7
8 >>> import u3
9 >>> d = u3.U3()
10 >>> d.debug = True
11 >>> d.getFeedback(u3.DAC16(Dac = 0, Value = 0x5566))
12 Sent: [0xdc, 0xf8, 0x2, 0x0, 0xe1, 0x0, 0x0, 0x26, 0x66, 0x55]
13 Response: [0xfa, 0xf8, 0x2, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0]
14 [None]

```

u3-feedback-DAC16.txt hosted with ♥ by GitHub

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```

1 16-bit DAC Feedback command for DAC0
2
3 Controls DAC0 in 16-bit mode.
4
5 Value: 0-65535
6
7 >>> import u3
8 >>> d = u3.U3()
9 >>> d.debug = True
10 >>> d.getFeedback(u3.DAC0_16(Value = 0x1122))
11 Sent: [0x54, 0xf8, 0x2, 0x0, 0x59, 0x0, 0x0, 0x26, 0x22, 0x11]
12 Response: [0xfa, 0xf8, 0x2, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0]
13 [None]

```

u3-feedback-DAC0_16.txt hosted with ♥ by GitHub

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```

1 16-bit DAC Feedback command for DAC1
2
3 Controls DAC1 in 16-bit mode.
4
5 Value: 0-65535
6
7 >>> import u3
8 >>> d = u3.U3()
9 >>> d.debug = True
10 >>> d.getFeedback(u3.DAC1_16(Value = 0x2233))
11 Sent: [0x77, 0xf8, 0x2, 0x0, 0x7c, 0x0, 0x0, 0x27, 0x33, 0x22]
12 Response: [0xfa, 0xf8, 0x2, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0]
13 [None]

```

u3-feedback-DAC1_16.txt hosted with ♥ by GitHub

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5.2.5.15 - Timer#: IOType=42,44 [U3 Datasheet]

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Table 5.2.5.15-1. Timer Command Response

Timer#. 4 Command	
Bytes:	
0	IOType = 42, 44
1	Bit 0: UpdateReset
2	Value LSB
3	Value MSB
4 Response Bytes:	
0	Timer LSB

1	Timer
2	Timer
3	Timer MSB

This IOType provides the ability to update/reset a given timer, and read the timer value.

- **Value:** These values are only updated if the UpdateReset bit is 1. The meaning of this parameter varies with the timer mode.
- **Timer:** Returns the value from the timer module. This is the value before reset (if reset was done).

U3 - timer code examples

LabJackPython example session

Automatically extracted from [u3.py](#). Debugging turned on to show the bytes sent and received.

```

1  For reading the value of the Timer. It provides the ability to update/reset
2  a given timer, and read the timer value.
3  (Section 5.2.5.14 of the User's Guide)
4
5  timer: Either 0 or 1 for timer 0 or timer 1
6
7  UpdateReset: Set True if you want to update the value
8
9  Value: Only updated if the UpdateReset bit is 1. The meaning of this
10     parameter varies with the timer mode.
11
12  Mode: Set to the timer mode to handle any special processing. See classes
13     QuadratureInputTimer and TimerStopInput1.
14
15  Returns an unsigned integer of the timer value, unless Mode has been
16  specified and there are special return values. See Section 2.9.1 for
17  expected return values.
18
19  >>> import u3
20  >>> d = u3.U3()
21  >>> d.debug = True
22  >>> d.configIO(NumberOfTimersEnabled = 1)
23  Sent: [0x49, 0xf8, 0x3, 0xb, 0x42, 0x0, 0x1, 0x0, 0x41, 0x0, 0x0, 0x0]
24  Response: [0x57, 0xf8, 0x3, 0xb, 0x50, 0x0, 0x0, 0x0, 0x41, 0x0, 0xf, 0x0]
25  {'NumberOfTimersEnabled': 1, 'TimerCounterPinOffset': 4, 'DAC1Enable': 0, 'FIOAnalog': 15, 'EIOAnalog': 0, 'TimerCounterConfig': 65, 'EnableCounter1': False, 'EnableCounter0': False}
26  >>> d.getFeedback(u3.Timer(timer = 0, UpdateReset = False, Value = 0, Mode = None))
27  Sent: [0x26, 0xf8, 0x3, 0x0, 0x2a, 0x0, 0x0, 0x2a, 0x0, 0x0, 0x0, 0x0]
28  Response: [0xfc, 0xf8, 0x4, 0x0, 0xfe, 0x1, 0x0, 0x0, 0x63, 0xdd, 0x4c, 0x72, 0x0]
29  [1917640035]
```

```

1 For reading the value of the Timer0. It provides the ability to
2 update/reset Timer0, and read the timer value.
3 (Section 5.2.5.14 of the User's Guide)
4
5 UpdateReset: Set True if you want to update the value
6
7 Value: Only updated if the UpdateReset bit is 1. The meaning of this
8 parameter varies with the timer mode.
9
10 Mode: Set to the timer mode to handle any special processing. See classes
11 QuadratureInputTimer and TimerStopInput1.
12
13 >>> import u3
14 >>> d = u3.U3()
15 >>> d.debug = True
16 >>> d.configIO(NumberOfTimersEnabled = 1)
17 Sent: [0x49, 0xf8, 0x3, 0xb, 0x42, 0x0, 0x1, 0x0, 0x41, 0x0, 0x0, 0x0]
18 Response: [0x57, 0xf8, 0x3, 0xb, 0x50, 0x0, 0x0, 0x0, 0x41, 0x0, 0xf, 0x0]
19 {'NumberOfTimersEnabled': 1, 'TimerCounterPinOffset': 4, 'DAC1Enable': 0, 'FIOAnalog': 15, 'EIOAnalog': 0, 'TimerCounterConfig': 65, 'EnableCounter1': False, 'EnableCounter0': False}
20 >>> d.getFeedback(u3.Timer0(UpdateReset = False, Value = 0, Mode = None))
21 Sent: [0x26, 0xf8, 0x3, 0x0, 0x2a, 0x0, 0x0, 0x2a, 0x0, 0x0, 0x0, 0x0]
22 Response: [0x51, 0xf8, 0x4, 0x0, 0x52, 0x2, 0x0, 0x0, 0x0, 0xf6, 0x90, 0x46, 0x86, 0x0]
23 [2252771574]

```

u3-feedback-Timer0.txt hosted with ❤ by GitHub

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```

1 For reading the value of the Timer1. It provides the ability to
2 update/reset Timer1, and read the timer value.
3 (Section 5.2.5.14 of the User's Guide)
4
5 UpdateReset: Set True if you want to update the value
6
7 Value: Only updated if the UpdateReset bit is 1. The meaning of this
8 parameter varies with the timer mode.
9
10 Mode: Set to the timer mode to handle any special processing. See classes
11 QuadratureInputTimer and TimerStopInput1.
12
13 >>> import u3
14 >>> d = u3.U3()
15 >>> d.debug = True
16 >>> d.configIO(NumberOfTimersEnabled = 2)
17 Sent: [0x4a, 0xf8, 0x3, 0xb, 0x43, 0x0, 0x1, 0x0, 0x42, 0x0, 0x0, 0x0]
18 Response: [0x58, 0xf8, 0x3, 0xb, 0x51, 0x0, 0x0, 0x0, 0x42, 0x0, 0xf, 0x0]
19 {'NumberOfTimersEnabled': 2, 'TimerCounterPinOffset': 4, 'DAC1Enable': 0, 'FIOAnalog': 15, 'EIOAnalog': 0, 'TimerCounterConfig': 66, 'EnableCounter1': False, 'EnableCounter0': False}
20 >>> d.getFeedback(u3.Timer1(UpdateReset = False, Value = 0, Mode = None))
21 Sent: [0x28, 0xf8, 0x3, 0x0, 0x2c, 0x0, 0x0, 0x2c, 0x0, 0x0, 0x0, 0x0]
22 Response: [0x8d, 0xf8, 0x4, 0x0, 0x8e, 0x2, 0x0, 0x0, 0x0, 0xf3, 0x31, 0xd0, 0x9a, 0x0]
23 [2597335539]

```

u3-feedback-Timer1.txt hosted with ❤ by GitHub

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```

1 For reading Quadrature input timers. They are special because their values
2 are signed.
3
4 (Section 2.9.1.8 of the User's Guide)
5
6 Args:
7     UpdateReset: Set True if you want to reset the counter.
8     Value: Set to 0, and UpdateReset to True to reset the counter.
9
10 Returns a signed integer.
11
12 >>> import u3
13 >>> d = u3.U3()
14 >>> d.debug = True
15 >>> d.configIO(NumberOfTimersEnabled = 2)
16 Sent: [0x4a, 0xf8, 0x3, 0xb, 0x43, 0x0, 0x1, 0x0, 0x42, 0x0, 0x0, 0x0]
17 Response: [0x58, 0xf8, 0x3, 0xb, 0x51, 0x0, 0x0, 0x0, 0x42, 0x0, 0xf, 0x0]
18 {'NumberOfTimersEnabled': 2, 'TimerCounterPinOffset': 4, 'DAC1Enable': 0, 'FIOAnalog': 15, 'EIOAnalog': 0, 'TimerCounterConfig': 66, 'EnableCounter1': False, 'EnableCounter0': False}
19 >>> # Setup the two timers to be quadrature
20 >>> d.getFeedback(u3.Timer0Config(8), u3.Timer1Config(8))
21 Sent: [0x66, 0xf8, 0x5, 0x0, 0x68, 0x0, 0x0, 0x2b, 0x8, 0x0, 0x0, 0x2d, 0x8, 0x0, 0x0, 0x0]
22 Response: [0xfa, 0xf8, 0x2, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0]
23 [None, None]
24 >>> # Read the value
25 [0]
26 >>> d.getFeedback(u3.QuadratureInputTimer())
27 Sent: [0x26, 0xf8, 0x3, 0x0, 0x2a, 0x0, 0x0, 0x2a, 0x0, 0x0, 0x0, 0x0]
28 Response: [0xf5, 0xf8, 0x4, 0x0, 0xf5, 0x3, 0x0, 0x0, 0xf8, 0xff, 0xff, 0x0]
29 [-8]
30 >>> d.getFeedback(u3.QuadratureInputTimer())
31 Sent: [0x26, 0xf8, 0x3, 0x0, 0x2a, 0x0, 0x0, 0x2a, 0x0, 0x0, 0x0, 0x0]
32 Response: [0x9, 0xf8, 0x4, 0x0, 0xc, 0x0, 0x0, 0xc, 0x0, 0x0, 0x0, 0x0]
33 [12]

```

u3-feedback-QuadratureInputTimer.txt hosted with ❤ by GitHub

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```

1 For reading a stop input timer. They are special because the value returns
2 the current edge count and the stop value.
3
4 (Section 2.9.1.9 of the User's Guide)
5
6 Args:
7     UpdateReset: Set True if you want to update the value.
8     Value: The stop value. Only updated if the UpdateReset bit is 1.
9
10 Returns a tuple where the first value is current edge count, and the second
11 value is the stop value.
12
13 >>> import u3
14 >>> d = u3.U3()
15 >>> d.debug = True
16 >>> d.configIO(NumberOfTimersEnabled = 2)
17 Sent: [0x4a, 0xf8, 0x3, 0xb, 0x43, 0x0, 0x1, 0x0, 0x42, 0x0, 0x0, 0x0]
18 Response: [0x58, 0xf8, 0x3, 0xb, 0x51, 0x0, 0x0, 0x0, 0x42, 0x0, 0xf, 0x0]
19 {'NumberOfTimersEnabled': 2, 'TimerCounterPinOffset': 4, 'DAC1Enable': 0, 'FIOAnalog': 15, 'EIOAnalog': 0, 'TimerCounterConfig': 66, 'EnableCounter1': False, 'EnableCounter0': False}
20 >>> # Setup the timer to be Stop Input
21 >>> d.getFeedback(u3.Timer1Config(9, Value = 30))
22 Sent: [0x50, 0xf8, 0x3, 0x0, 0x54, 0x0, 0x0, 0x2d, 0x9, 0x1e, 0x0, 0x0]
23 Response: [0xfa, 0xf8, 0x2, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0]
24 [None]
25 >>> d.getFeedback(u3.TimerStopInput1())
26 Sent: [0x28, 0xf8, 0x3, 0x0, 0x2c, 0x0, 0x0, 0x2c, 0x0, 0x0, 0x0, 0x0]
27 Response: [0x1b, 0xf8, 0x4, 0x0, 0x1e, 0x0, 0x0, 0x0, 0x1e, 0x0, 0x0, 0x0]
28 [(0, 0)]

```

5.2.5.16 - Timer#Config: IOType=43,45 [U3 Datasheet]

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Table 5.2.5.16-1. TimerConfig Command Response

Timer#Config. 4 Command Bytes:	
0	IOType = 43, 45
1	TimerMode
2	Value LSB
3	Value MSB
0 Response Bytes:	

This IOType configures a particular timer.

- **TimerMode:** See [Section 2.9](#) for more information about the available modes.
- **Value:** The meaning of this parameter varies with the timer mode.

U3 - timerconfig code examples

LabJackPython example session

Automatically extracted from [u3.py](#). Debugging turned on to show the bytes sent and received.

```

1  This IOType configures a particular timer.
2
3  timer = # of the timer to configure
4
5  TimerMode = See Section 2.9 for more information about the available modes.
6
7  Value = The meaning of this parameter varies with the timer mode.
8
9  >>> import u3
10 >>> d = u3.U3()
11 >>> d.debug = True
12 >>> d.configIO(NumberOfTimersEnabled = 1)
13 Sent: [0x49, 0xf8, 0x3, 0xb, 0x42, 0x0, 0x1, 0x0, 0x41, 0x0, 0x0, 0x0]
14 Response: [0x57, 0xf8, 0x3, 0xb, 0x50, 0x0, 0x0, 0x0, 0x41, 0x0, 0xf, 0x0]
15 {'NumberOfTimersEnabled': 1, 'TimerCounterPinOffset': 4, 'DAC1Enable': 0, 'FIOAnalog': 15, 'EIOAnalog': 0, 'TimerCounterConfig': 65, 'EnableCounter1': False, 'EnableCounter0': False}
16 >>> d.getFeedback(u3.TimerConfig(timer = 0, TimerMode = 0, Value = 0))
17 Sent: [0x27, 0xf8, 0x3, 0x0, 0x2b, 0x0, 0x0, 0x2b, 0x0, 0x0, 0x0, 0x0]
18 Response: [0xfa, 0xf8, 0x2, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0]
19 [None]
20 >>> d.getFeedback(u3.TimerConfig(timer = 0, TimerMode = 0, Value = 65535))
21 Sent: [0x27, 0xf8, 0x3, 0x0, 0x29, 0x2, 0x0, 0x2b, 0x0, 0xff, 0xff, 0x0]
22 Response: [0xfa, 0xf8, 0x2, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0]
23 [None]
```

```

1 This IOType configures Timer0.
2
3 TimerMode = See Section 2.9 for more information about the available modes.
4
5 Value = The meaning of this parameter varies with the timer mode.
6
7 >>> import u3
8 >>> d = u3.U3()
9 >>> d.debug = True
10 >>> d.configIO(NumberOfTimersEnabled = 1)
11 Sent: [0x49, 0xf8, 0x3, 0xb, 0x42, 0x0, 0x1, 0x0, 0x41, 0x0, 0x0, 0x0]
12 Response: [0x57, 0xf8, 0x3, 0xb, 0x50, 0x0, 0x0, 0x0, 0x41, 0x0, 0xf, 0x0]
13 {'NumberOfTimersEnabled': 1, 'TimerCounterPinOffset': 4, 'DAC1Enable': 0, 'FIOAnalog': 15, 'EIOAnalog': 0, 'TimerCounterConfig': 65, 'EnableCounter1': False, 'EnableCounter0': False}
14 >>> d.getFeedback(u3.Timer0Config(TimerMode = 1, Value = 0))
15 Sent: [0x28, 0xf8, 0x3, 0x0, 0x2c, 0x0, 0x0, 0x2b, 0x1, 0x0, 0x0, 0x0]
16 Response: [0xfa, 0xf8, 0x2, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0]
17 [None]
18 >>> d.getFeedback(u3.Timer0Config(TimerMode = 1, Value = 65535))
19 Sent: [0x28, 0xf8, 0x3, 0x0, 0x2a, 0x2, 0x0, 0x2b, 0x1, 0xff, 0xff, 0x0]
20 Response: [0xfa, 0xf8, 0x2, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0]
21 [None]

```

u3-feedback-Timer0Config.txt hosted with ❤ by GitHub

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```

1 This IOType configures Timer1.
2
3 TimerMode = See Section 2.9 for more information about the available modes.
4
5 Value = The meaning of this parameter varies with the timer mode.
6
7 >>> import u3
8 >>> d = u3.U3()
9 >>> d.debug = True
10 >>> d.configIO(NumberOfTimersEnabled = 2)
11 Sent: [0x4a, 0xf8, 0x3, 0xb, 0x43, 0x0, 0x1, 0x0, 0x42, 0x0, 0x0, 0x0]
12 Response: [0x58, 0xf8, 0x3, 0xb, 0x51, 0x0, 0x0, 0x0, 0x42, 0x0, 0xf, 0x0]
13 {'NumberOfTimersEnabled': 2, 'TimerCounterPinOffset': 4, 'DAC1Enable': 0, 'FIOAnalog': 15, 'EIOAnalog': 0, 'TimerCounterConfig': 66, 'EnableCounter1': False, 'EnableCounter0': False}
14 >>> d.getFeedback(u3.Timer1Config(TimerMode = 6, Value = 1))
15 Sent: [0x30, 0xf8, 0x3, 0x0, 0x34, 0x0, 0x0, 0x2d, 0x6, 0x1, 0x0, 0x0]
16 Response: [0xfa, 0xf8, 0x2, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0]
17 [None]

```

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5.2.5.17 - Counter#: IOType=54,55 [U3 Datasheet]

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Table 5.2.5.17-1. Counter Command Response

Counter#, 2 Command Bytes:	
0	IOType = 54, 55
1	Bit 0: Reset
4 Response Bytes:	
0	Counter LSB
1	Counter
2	Counter
3	Counter MSB

This IOType reads a hardware counter, and optionally can do a reset.

- **Reset:** Setting this bit resets the counter to 0 after reading.
- **Counter:** Returns the current count from the counter if enabled. This is the value before reset (if reset was done).

U3 - counter code examples

LabJackPython example session

Automatically extracted from [u3.py](#). Debugging turned on to show the bytes sent and received.

```

1 Counter Feedback command
2
3 Reads a hardware counter, optionally resetting it
4
5 counter: 0 or 1
6 Reset: True ( or 1 ) = Reset, False ( or 0 ) = Don't Reset
7
8 Returns the current count from the counter if enabled. If reset,
9 this is the value before the reset.
10 >>> import u3
11 >>> d = u3.U3()
12 >>> d.debug = True
13 >>> d.configIO(EnableCounter0 = True, FIOAnalog = 15)
14 Sent: [0x5f, 0xf8, 0x3, 0xb, 0x58, 0x0, 0x5, 0x0, 0x44, 0x0, 0xf, 0x0]
15 Response: [0x5a, 0xf8, 0x3, 0xb, 0x53, 0x0, 0x0, 0x0, 0x44, 0x0, 0xf, 0x0]
16 {'NumberOfTimersEnabled': 0, 'TimerCounterPinOffset': 4, 'DAC1Enable': 0, 'FIOAnalog': 15, 'EIOAnalog': 0, 'TimerCounterConfig': 68, 'EnableCounter1': False, 'EnableCounter0': True}
17 >>> d.getFeedback(u3.Counter(counter = 0, Reset = False))
18 Sent: [0x31, 0xf8, 0x2, 0x0, 0x36, 0x0, 0x0, 0x36, 0x0, 0x0]
19 Response: [0xfc, 0xf8, 0x4, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0]
20 [0]
21 >>> # Tap a ground wire to counter 0
22 >>> d.getFeedback(u3.Counter(counter = 0, Reset = False))
23 Sent: [0x31, 0xf8, 0x2, 0x0, 0x36, 0x0, 0x0, 0x36, 0x0, 0x0]
24 Response: [0xe9, 0xf8, 0x4, 0x0, 0xec, 0x0, 0x0, 0x0, 0x0, 0xe8, 0x4, 0x0, 0x0, 0x0, 0x0]
25 [1256]

```

u3-feedback-Counter.txt hosted with ❤ by GitHub

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```

1 Counter0 Feedback command
2
3 Reads hardware counter0, optionally resetting it
4
5 Reset: True ( or 1 ) = Reset, False ( or 0 ) = Don't Reset
6
7 Returns the current count from the counter if enabled. If reset,
8 this is the value before the reset.
9
10 >>> import u3
11 >>> d = u3.U3()
12 >>> d.debug = True
13 >>> d.configIO(EnableCounter0 = True, FIOAnalog = 15)
14 Sent: [0x5f, 0xf8, 0x3, 0xb, 0x58, 0x0, 0x5, 0x0, 0x44, 0x0, 0xf, 0x0]
15 Response: [0x5a, 0xf8, 0x3, 0xb, 0x53, 0x0, 0x0, 0x0, 0x44, 0x0, 0xf, 0x0]
16 {'NumberOfTimersEnabled': 0, 'TimerCounterPinOffset': 4, 'DAC1Enable': 0, 'FIOAnalog': 15, 'EIOAnalog': 0, 'TimerCounterConfig': 68, 'EnableCounter1': False, 'EnableCounter0': True}
17 >>> d.getFeedback(u3.Counter0( Reset = False ) )
18 Sent: [0x31, 0xf8, 0x2, 0x0, 0x36, 0x0, 0x0, 0x36, 0x0, 0x0]
19 Response: [0xfc, 0xf8, 0x4, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0]
20 [0]
21 >>> # Tap a ground wire to counter 0
22 >>> d.getFeedback(u3.Counter0(Reset = False))
23 Sent: [0x31, 0xf8, 0x2, 0x0, 0x36, 0x0, 0x0, 0x36, 0x0, 0x0]
24 Response: [0xe, 0xf8, 0x4, 0x0, 0x11, 0x0, 0x0, 0x0, 0x11, 0x0, 0x0, 0x0, 0x0]
25 [17]
26 >>> # Tap a ground wire to counter 0
27 >>> d.getFeedback(u3.Counter0(Reset = False))
28 Sent: [0x31, 0xf8, 0x2, 0x0, 0x36, 0x0, 0x0, 0x36, 0x0, 0x0]
29 Response: [0x19, 0xf8, 0x4, 0x0, 0x1c, 0x0, 0x0, 0x0, 0x0, 0xb, 0x11, 0x0, 0x0, 0x0, 0x0]
30 [4363]

```

```

1 Counter1 Feedback command
2
3 Reads hardware counter1, optionally resetting it
4
5 Reset: True ( or 1 ) = Reset, False ( or 0 ) = Don't Reset
6
7 Returns the current count from the counter if enabled. If reset,
8 this is the value before the reset.
9
10 >>> import u3
11 >>> d = u3.U3()
12 >>> d.debug = True
13 >>> d.configIO(EnableCounter1 = True, FIOAnalog = 15)
14 Sent: [0x63, 0xf8, 0x3, 0xb, 0x5c, 0x0, 0x5, 0x0, 0x48, 0x0, 0xf, 0x0]
15 Response: [0x5e, 0xf8, 0x3, 0xb, 0x57, 0x0, 0x0, 0x0, 0x48, 0x0, 0xf, 0x0]
16 {'NumberOfTimersEnabled': 0, 'TimerCounterPinOffset': 4, 'DAC1Enable': 0, 'FIOAnalog': 15, 'EIOAnalog': 0, 'TimerCounterConfig': 72, 'EnableCounter1': True, 'EnableCounter0': False}
17 >>> d.getFeedback(u3.Counter1(Reset = False))
18 Sent: [0x32, 0xf8, 0x2, 0x0, 0x37, 0x0, 0x0, 0x37, 0x0, 0x0]
19 Response: [0xfc, 0xf8, 0x4, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0, 0x0]
20 [0]
21 >>> # Tap a ground wire to counter 1
22 >>> d.getFeedback(u3.Counter1(Reset = False))
23 Sent: [0x32, 0xf8, 0x2, 0x0, 0x37, 0x0, 0x0, 0x37, 0x0, 0x0]
24 Response: [0xfd, 0xf8, 0x4, 0x0, 0x1, 0x0, 0x0, 0x0, 0x0, 0x1, 0x0, 0x0]
25 [1]
26 >>> # Tap a ground wire to counter 1
27 >>> d.getFeedback(u3.Counter1(Reset = False))
28 Sent: [0x32, 0xf8, 0x2, 0x0, 0x37, 0x0, 0x0, 0x37, 0x0, 0x0]
29 Response: [0xb4, 0xf8, 0x4, 0x0, 0xb7, 0x0, 0x0, 0x0, 0x0, 0x6b, 0x2b, 0x21, 0x0, 0x0]
30 [2173803]

```

5.2.5.18 - Buzzer: IOType=63 [U3 Datasheet]

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Table 5.2.5.18-1.

Buzzer, 6 Command Bytes:	
0	IOType = 63
1	Bit 0: Continuous
2	Period LSB
3	Period MSB
4	Toggles LSB
5	Toggles MSB
0 Response Bytes:	

This IOType is used to make the buzzer buzz. The buzzer is only available on hardware revisions 1.20 and 1.21, not on 1.30.

- **Continuous:** If this bit is set, the buzzer will toggle continuously.
- **Period:** This value determines how many main firmware loops the processor will execute before toggling the buzzer voltage.
- **Toggles:** If Continuous is false, this value specifies how many times the buzzer will toggle.

5.2.6 - ReadMem (ReadCal) [U3 Datasheet]

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Reads 1 block (32 bytes) from the non-volatile user or calibration memory. The available memory is different for different versions of the U3.

Hardware version 1.30 & 1.21:

Command number 0x2A accesses the user memory area which consists of 512 bytes (block numbers 0-15). Command number 0x2D accesses the calibration memory area consisting of 512 bytes (block numbers 0-15). Do not call this function while streaming. Note that there are 512 Bytes of user memory available, but only 256 Bytes are accessible when using the LJ_chUSER_MEM function in the UD driver.

Hardware version 1.20:

Command number 0x2A accesses the user memory area which consists of 256 bytes (block numbers 0-7). Command number 0x2D accesses the calibration memory area consisting of 256 bytes (block numbers 0-7). Do not call this function while streaming.

Table 5.2.6-1. ReadMem command response

Command:	
Byte	
0	Checksum8
1	0xF8
2	0x01
3	0x2A (0x2D)
4	Checksum16 (LSB)
5	Checksum16 (MSB)
6	0x00
7	BlockNum
Response:	
Byte	
0	Checksum8
1	0xF8
2	0x11
3	0x2A (0x2D)
4	Checksum16 (LSB)
5	Checksum16 (MSB)
6	Errorcode
7	0x00
8-39	32 Bytes of Data

5.2.7 - WriteMem (WriteCal) [U3 Datasheet]

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Writes 1 block (32 bytes) to the non-volatile user or calibration memory.

Command number 0x28 accesses the user memory area. Hardware version 1.2 has 256 bytes (block numbers 0-7), hardware versions 1.21 and 1.3 have 512 bytes (block 0-15). The ensure backwards compatibility only 256 Bytes are accessible when using the LJ_chUSER_MEM function in the UD driver.

Command number 0x2B accesses the calibration memory area consisting of 512 bytes (block numbers 0-15), of which the last 8 blocks are not used. Memory must be erased before writing. Do not call this function while streaming.

The U3 flash has a rated endurance of at least 20000 writes, which is plenty for reasonable operation, but if this function is called in a high-speed loop the flash could eventually be damaged. In the case of these functions, that means 20000 writes per 32-byte block.

Table 5.2.7-1. WriteMem Command Response

Command:	
Byte	
0	Checksum8
1	0xF8
2	0x11
3	0x28 (0x2B)
4	Checksum16 (LSB)
5	Checksum16 (MSB)
6	0x00
7	BlockNum
8-39	32 Bytes of Data
Response:	

Byte	
0	Checksum8
1	0xF8
2	0x01
3	0x28 (0x2B)
4	Checksum16 (LSB)
5	Checksum16 (MSB)
6	Errorcode
7	0x00

5.2.8 - EraseMem (EraseCal) [U3 Datasheet]

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The U3 uses flash memory that must be erased before writing. Command number 0x29 erases the entire user memory area. Command number 0x2C erases the entire calibration memory area. The EraseCal command has two extra constant bytes, to make it more difficult to call the function accidentally. Do not call this function while streaming.

Table 5.2.8-1. EraseMem Command Response

Command:	
Byte	
0	Checksum8
1	0xF8
2	0x00 (0x01)
3	0x29 (0x2C)
4	Checksum16 (LSB)
5	Checksum16 (MSB)
(6)	(0x4C)
(7)	(0x6C)
Response:	
Byte	
0	Checksum8
1	0xF8
2	0x01
3	0x29 (0x2C)
4	Checksum16 (LSB)
5	Checksum16 (MSB)
6	Errorcode
7	0x00

5.2.9 - Reset [U3 Datasheet]

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Causes a soft or hard reset. A soft reset consists of re-initializing the USB buffers without re-enumeration. A hard reset is a reboot of the processor and does cause re-enumeration.

Table 5.2.9-1. Reset Command Response

Command:		
Byte		
0	Checksum8	
1	0x99	
2	ResetOptions	
		Bit 1: Hard Reset
		Bit 0: Soft Reset
3	0x00	
Response:		
Byte		
0	Checksum8	
1	0x99	
2	0x00	

5.2.10 - StreamConfig [U3 Datasheet]

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Stream mode operates on a table of channels that are scanned at the specified scan rate. Before starting a stream, you need to call this function to configure the table and scan clock. Requires U3 hardware version 1.21 or higher.

Table 5.2.10-1. StreamConfig Command Response

Command:			
Byte			
0	Checksum8		
1	0xF8		
2	NumChannels + 3		
3	0x11		
4	Checksum16 (LSB)		
5	Checksum16 (MSB)		
6	NumChannels		
7	SamplesPerPacket (1-25)		
8	Reserved		
9	ScanConfig		
		Bit 7: Reserved	
		Bit 6: Reserved	
		Bit 3: Internal stream clock frequency.	
			b0: 4 MHz
			b1: 48 MHz
		Bit 2: Divide Clock by 256	
		Bits 0-1: Resolution	
			b00: 12.8-bit effective
			b01: 11.9-bit effective
			b10: 11.3-bit effective
			b11: 10.5-bit effective
10-11	Scan Interval (1-65535)		
12	PChannel		
13	NChannel		
Repeat 12-13 for each channel			
Response:			
Byte			
0	Checksum8		
1	0xF8		
2	0x01		
3	0x11		
4	Checksum16 (LSB)		
5	Checksum16 (MSB)		
6	Errorcode		
7	0x00		

- **NumChannels:** This is the number of channels you will sample per scan (1-25).
- **SamplesPerPacket:** Specifies how many samples will be pulled out of the U3FIFO buffer and returned per data read packet. For faster stream speeds, 25 samples per packet are required for data transfer efficiency. A small number of samples per packet would be desirable for low-latency data retrieval. Note that this parameter is not necessarily the same as the number of channels per scan. Even if only 1 channel is being scanned, SamplesPerPacket will usually be set to 25, so there are usually multiple scans per packet.
- **ScanConfig:** Has bits to specify the stream base clock and effective resolution.
- **ScanInterval:** (1-65535) This value divided by the clock frequency defined in the ScanConfig parameter, gives the interval (in seconds) between scans.

- **PChannel/NChannel:** For each channel, these two parameters specify the positive and negative voltage measurement point. PChannel is 0-7 for FIO0-FIO7, 8-15 for EIO0-EIO15, 30 for temp sensor, 31 for Vreg, or 193-224 for digital/timer/counter channels. NChannel is 0-7 for FIO0-FIO7, 8-15 for EIO0-EIO15, 30 for Vref, or 31 for single-ended. Note that firmware does not support setting the negative channel 199. Instead, specify 31 for single ended measurements. When using the UD driver's stream functions 199 will be automatically converted to 31 for ease of use.

5.2.11 - StreamStart [U3 Datasheet]

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Once the stream settings are configured, this function is called to start the stream. Call the StreamConfig function before every stream start. Requires U3 hardware version 1.21 or higher.

Table 5.2.11-1. StreamStart Command Response

Command:	
Byte	
0	0xA8
1	0xA8
Response:	
Byte	
0	Checksum8
1	0xA9
2	Errorcode
3	0x00

5.2.12 - StreamData [U3 Datasheet]

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After starting the stream, the data will be sent as available in the following format. Reads oldest data from buffer. Requires U3 hardware version 1.21 or higher.

Table 5.2.12-1. StreamData Response

Response:	
Byte	
0	Checksum8
1	0xF9
2	4 + SamplesPerPacket
3	0xC0
4	Checksum16 (LSB)
5	Checksum16 (MSB)
6-9	TimeStamp
10	PacketCounter
11	Errorcode
12-13	Sample0
62 (max)	Backlog
63 (max)	0x00

- **SamplesPerPacket:** From StreamConfig function.
- **TimeStamp:** Not currently implemented during normal operation, but after auto-recovery bytes 6-7 reports the number of packets missed (1-65535).
- **PacketCounter:** An 8-bit (0-255) counter that is incremented by one for each packet of data. Useful to make sure packets are in order and no packets are missing.
- **Sample#:** Stream data is placed in a FIFO (first in first out) buffer, so Sample0 is the oldest data read from the buffer. The analog input reading is returned justified as a 16-bit value. Differential readings are signed, while single-ended readings are unsigned.
- **Backlog:** When streaming, the processor acquires data at precise intervals, and transfers it to a FIFO buffer until it can be sent to the host. This value represents how much data is left in the buffer after this read. The value ranges from 0-255, where 256 would equal 100% full.

Stream mode on the U3 uses a feature called auto-recovery. If the stream buffer gets too full, the U3 will go into auto-recovery mode. In this mode, the U3 no longer stores new scans in the buffer, but rather new scans are discarded. Data already in the buffer will be sent until the buffer contains less samples than SamplesPerPacket, and every StreamData packet will have errorcode 59. Once the stream buffer contains less samples than SamplesPerPacket, the U3 will start to buffer new scans again. The next packet returned will have errorcode 60. This packet will have 1 dummy scan where each sample is 0xFFFF, and this scan separates new data from any pre auto-recovery data. Note that the dummy scan could be at the beginning, middle, or end of this packet, and can even extend to following packets. Also, the TimeStamp parameter in this packet contains the number of scans that were discarded, allowing correct time to be calculated. The dummy scan counts as one of the missing scans included in the TimeStamp value.

5.2.13 - StreamStop [U3 Datasheet]

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Requires U3 hardware version 1.21 or higher.

Table 5.2.13-1. StreamStop Command Response

Command:	
Byte	
0	0xB0
1	0xB0
Response:	
Byte	
0	Checksum8
1	0xB1
2	Errorcode
3	0x00

5.2.14 - Watchdog [U3 Datasheet]

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Requires U3 hardware version 1.21. Controls a firmware based watchdog timer. Unattended systems requiring maximum up-time might use this capability to reset the U3 or the entire system. When any of the options are enabled, an internal timer is enabled which resets on any incoming USB communication. If this timer reaches the defined TimeoutPeriod before being reset, the specified actions will occur. Note that while streaming, data is only going out, so some other command will have to be called periodically to reset the watchdog timer.

If the watchdog is accidentally configured to reset the processor with a very low timeout period (such as 1 second), it could be difficult to establish any communication with the device. In such a case, the reset-to-default jumper can be used to turn off the watchdog (sets bytes 7-10 to 0). Power up the U3 with a short from FIO6 to SPC (FIO2 to SCL on U3 1.20/1.21), then remove the jumper and power cycle the device again. This also affects the parameters in the ConfigU3 function.

The watchdog settings (bytes 7-10) are stored in non-volatile flash memory, so every call to this function where settings are written causes a flash erase/write. The flash has a rated endurance of at least 20000 writes, which is plenty for reasonable operation, but if this function is called in a high-speed loop the flash could be damaged.

Note: Do **not** call this function while streaming.

Table 5.2.14-1. Watchdog Command Response

Command:		
Byte		
0	Checksum8	
1	0xF8	
2	0x05	
3	0x09	
4	Checksum16 (LSB)	
5	Checksum16 (MSB)	
6	WriteMask	
		Bit 0: Write
7	WatchdogOptions	
		Bit 5: Reset on Timeout
		Bit 4: Set DIO State on Timeout
8-9	TimeoutPeriod	
10	DIOConfig	
		Bit 7: State
		Bit 0-4: DIO#
11	Reserved	
12	Reserved	
13	Reserved	
14	Reserved	
15	Reserved	
Response:		
Byte		

0	Checksum8	
1	0xF8	
2	0x05	
3	0x09	
4	Checksum16 (LSB)	
5	Checksum16 (MSB)	
6	Errorcode	
7	WatchdogOptions	
8-9	TimeoutPeriod	
10	DIOConfig	
11	Reserved	
12	Reserved	
13	Reserved	
14	Reserved	
15	Reserved	

- **WatchdogOptions:** The watchdog is enabled when this byte is nonzero. Set the appropriate bits to reset the device and/or update the state of 1 digital output.
- **TimeoutPeriod:** The watchdog timer is reset to zero on any incoming USB communication. Note that most functions consist of a write and read, but StreamData is outgoing only and does not reset the watchdog. If the watchdog timer is not reset before it counts up to TimeoutPeriod, the actions specified by WatchdogOptions will occur. The watchdog timer has a clock rate of about 1 Hz, so a TimeoutPeriod range of 3-65535 corresponds to about 3 to 65535 seconds.
- **DIOConfig:** Determines which digital I/O is affected by the watchdog, and the state it is set to. The specified DIO must have previously been configured for output. DIO# is a value from 0-19 according to the following: 0-7 => FIO0-FIO7, 8-15 => EIO0-EIO7, 16-19 => CIO0-CIO3

5.2.15 - SPI [U3 Datasheet]

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Requires U3 hardware version 1.21. Sends and receives serial data using SPI synchronous communication.

Table 5.2.15-1. SPI Command Response

Command:		
Byte		
0	Checksum8	
1	0xF8	
2	4 + NumSPIWords	
3	0x3A	
4	Checksum16 (LSB)	
5	Checksum16 (MSB)	
6	SPIOptions	
		Bit 7: AutoCS
		Bit 6: DisableDirConfig
		Bits 1-0: SPIMode (0=A, 1=B, 2=C, 3=D)
7	SPIClockFactor	
8	Reserved	
9	CSPinNum	
10	CLKPinNum	
11	MISOPinNum	
12	MOSIPinNum	
13	NumSPIBytesToTransfer	
14	SPIByte0	
...	...	
Response:		
Byte		
0	Checksum8	
1	0xF8	
2	1 + NumSPIWords	
3	0x3A	
4	Checksum16 (LSB)	
5	Checksum16 (MSB)	
6	Errorcode	
7	NumSPIBytesTransferred	
8	SPIByte0	
...	...	

- **NumSPIWords:** This is the number of SPI bytes divided by 2. If the number of SPI bytes is odd, round up and add an extra zero to the

packet.

- **SPIOptions:** If AutoCS is true, the CS line is automatically driven low during the SPI communication and brought back high when done. If DisableDirConfig is true, this function does not set the direction of the lines, whereas if it is false the lines are configured as CS=output, CLK=output, MISO=input, and MOSI=output. SPIMode specifies the standard SPI mode as discussed below.
- **SPIClockFactor:** Sets the frequency of the SPI clock. A zero corresponds to the maximum speed of about 80kHz and 255 the minimum speed of about 5.5kHz.
- **CS/CLK/MISO/MOSI - PinNum:** Assigns which digital I/O line is used for each SPI line. Value passed is 0-19 corresponding to the normal digital I/O numbers as specified in [Section 2.8](#).
- **NumSPIBytesToTransfer:** Specifies how many SPI bytes will be transferred (1-50).

The initial state of SCK is set properly (CPOL), by this function, before CS (chip select) is brought low (final state is also set properly before CS is brought high again). If CS is being handled manually, outside of this function, care must be taken to make sure SCK is initially set to CPOL before asserting CS.

All standard SPI modes supported (A, B, C, and D).

Mode A: CPOL=0, CPHA=0

Mode B: CPOL=0, CPHA=1

Mode C: CPOL=1, CPHA=0

Mode D: CPOL=1, CPHA=1

If Clock Phase (CPHA) is 1, data is valid on the edge going to CPOL. If CPHA is 0, data is valid on the edge going away from CPOL. Clock Polarity (CPOL) determines the idle state of SCK.

Up to 50 bytes can be written/read. Communication is full duplex so 1 byte is read at the same time each byte is written.

5.2.16 - AsynchConfig [U3 Datasheet]

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Requires U3 hardware version 1.21+. Configures the U3UART for asynchronous communication. On hardware version 1.30 the TX (transmit) and RX (receive) lines appear on FIO/EIO after any timers and counters, so with no timers/counters enabled, and pin offset set to 4, TX=FIO4 and RX=FIO5. On hardware version 1.21, the UART uses SDA for TX and SCL for RX. Communication is in the common 8/n/1 format. Similar to RS232, except that the logic is normal CMOS/TTL. Connection to an RS232 device will require a converter chip such as the MAX233, which inverts the logic and shifts the voltage levels.

Table 5.2.16-1. AsynchConfig Command Response

Command:		
Byte		
0	Checksum8	
1	0xF8	
2	0x02	
3	0x14	
4	Checksum16 (LSB)	
5	Checksum16 (MSB)	
6	0x00	
7	AsynchOptions	
		Bit 7: Update
		Bit 6: UARTEnable
		Bit 5: Reserved
8	BaudFactor LSB (1.30 only)	
9	BaudFactor MSB	
Response:		
Byte		
0	Checksum8	
1	0xF8	
2	0x02	
3	0x14	
4	Checksum16 (LSB)	
5	Checksum16 (MSB)	
6	Errorcode	
7	AsynchOptions	
8	BaudFactor LSB (1.30 only)	

9	BaudFactor MSB	
---	-------------------	--

- **AsynchOptions:**

Bit 7: Update If true, the new parameters are written (otherwise just a read is done).

Bit 6: UARTEnable If true, the UART module is enabled. Note that no data can be transferred until pins have been assigned to the UART module using the ConfigIO function.

- **BaudFactor16 (BaudFactor8):** This 16-bit value sets the baud rate according to the following formula: $\text{BaudFactor16} = 2^6 - 48000000 / (2 \times \text{Desired Baud})$. For example, a BaudFactor16 = 63036 provides a baud rate of 9600 bps. (With hardware revision 1.21, the value is only 8-bit and the formula is $\text{BaudFactor8} = 2^8 - \text{TimerClockBase} / (\text{Desired Baud})$).

5.2.17 - AsynchTX [U3 Datasheet]

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Requires U3 hardware version 1.21. Sends bytes to the U3UART which will be sent asynchronously on the transmit line.

Table 5.2.17-1. AsynchTX Command Response

Command:	
Byte	
0	Checksum8
1	0xF8
2	1 + NumAsynchWords
3	0x15
4	Checksum16 (LSB)
5	Checksum16 (MSB)
6	0x00
7	NumAsynchBytesToSend
8	AsynchByte0
...	...
Response:	
Byte	
0	Checksum8
1	0xF8
2	0x02
3	0x15
4	Checksum16 (LSB)
5	Checksum16 (MSB)
6	Errorcode
7	NumAsynchBytesSent
8	NumAsynchBytesInRXBuffer
9	0x00

- **NumAsynchWords:** This is the number of asynch data bytes divided by 2. If the number of bytes is odd, round up and add an extra zero to the packet.
- **NumAsynchBytesToSend:** Specifies how many bytes will be sent (0-56).
- **NumAsynchBytesInRXBuffer:** Returns how many bytes are currently in the RX buffer.

5.2.18 - AsynchRX [U3 Datasheet]

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Requires U3 hardware version 1.21. Reads the oldest 32 bytes from the U3UART RX buffer (received on receive terminal). The buffer holds 256 bytes.

Table 5.2.18-1. AsynchRX Command Response

Command:	
Byte	
0	Checksum8
1	0xF8
2	0x01
3	0x16
4	Checksum16 (LSB)
5	Checksum16 (MSB)
6	0x00
7	Flush
Response:	

Byte	
0	Checksum8
1	0xF8
2	1 + NumAsynchWords
3	0x15
4	Checksum16 (LSB)
5	Checksum16 (MSB)
6	Errorcode
7	NumAsynchBytesInRXBuffer
8	AsynchByte0
...	...
39	AsynchByte31

- **Flush:** If nonzero, the entire 256-byte RX buffer is emptied. If there are more than 32 bytes in the buffer that data is lost.
- **NumAsynchBytesInRXBuffer:** Returns the number of bytes in the buffer before this read.
- **AsynchByte#:** Returns the 32 oldest bytes from the RX buffer.

5.2.19 - I²C [U3 Datasheet]

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Requires U3 hardware version 1.21+-. Sends and receives serial data using I²C (I2C) synchronous communication.

Table 5.2.19-1. I2C Command Response

Command:		
Byte		
0	Checksum8	
1	0xF8	
2	4 + NumI2CWordsSend	
3	0x3B	
4	Checksum16 (LSB)	
5	Checksum16 (MSB)	
6	I2COptions	
		Bits 7-4: Reserved
		Bit 3: Enable Clock Stretching
		Bit 2: No Stop when restarting
		Bit 1: ResetAtStart
		Bit 0: Reserved
7	SpeedAdjust	
8	SDAPinNum	
9	SCLPinNum	
10	AddressByte	
11	Reserved	
12	NumI2CBytesToSend	
13	NumI2CBytesToReceive	
14	I2CByte0	
...	...	
Response:		
Byte		
0	Checksum8	
1	0xF8	
2	3 + NumI2CWordsReceive	
3	0x3B	
4	Checksum16 (LSB)	
5	Checksum16 (MSB)	
6	Errorcode	
7	Reserved	
8	AckArray0	
9	AckArray1	
10	AckArray2	
11	AckArray3	
12	I2CByte0	
...	...	

- **NumI2CWordsSend:** This is the number of I²C bytes to send divided by 2. If the number of bytes is odd, round up and add an extra zero to the packet. This parameter is actually just to specify the size of this packet, as the NumI2CbytesToSend parameter below actually specifies how many bytes will be sent.
- **I2COptions:** If ResetAtStart is true, an I²C bus reset will be done before communicating.
- **SpeedAdjust:** Allows the communication frequency to be reduced. 0 is the maximum speed of about 150 kHz. 20 is a speed of about 70 kHz. 255 is the minimum speed of about 10 kHz.
- **SDAP/SCLP -PinNum:** Assigns which digital I/O line is used for each I²C line. Value passed is 0-19 corresponding to the normal digital I/O numbers as specified in [Section 2.8](#). Note that the screw terminals labeled “SDA” and “SCL” on hardware revision 1.20 or 1.21 are not used for I²C. Note that the I²C bus generally requires pull-up resistors of perhaps 4.7 k Ω from SDA to Vs and SCL to Vs.
- **AddressByte:** This is the first byte of data sent on the I²C bus. The upper 7 bits are the address of the slave chip and bit 0 is the read/write bit. Note that the read/write bit is controlled automatically by the LabJack, and thus bit 0 is ignored.
- **NumI2CBytesToSend:** Specifies how many I²C bytes will be sent (0-50).
- **NumI2CBytesToReceive:** Specifies how many I²C bytes will be read (0-52).
- **I2Cbyte#:** In the command, these are the bytes to send. In the response, these are the bytes read.
- **NumI2CWordsReceive:** This is the number of I²C bytes to receive divided by 2. If the number of bytes is odd, the value is rounded up and an extra zero is added to the packet. This parameter is actually just to specify the size of this packet, as the NumI2CbytesToReceive parameter above actually specifies how many bytes to read.
- **AckArray#:** Represents a 32-bit value where bits are set if the corresponding I²C write byte was ACKed. Useful for debugging up to the first 32 write bytes of communication. Bit 0 corresponds to the last data byte, bit 1 corresponds to the second to last data byte, and so on up to the address byte. So if n is the number of data bytes, the ACK value should be $(2^{n(n+1)})-1$.

5.2.20 - SHT1X [U3 Datasheet]

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Requires U3 hardware version 1.21. Reads temperature and humidity from a Sensirion SHT1X sensor (which is used by the EI-1050). For more information, see the [EI-1050 datasheet](#), and the SHT1X datasheet from [sensirion.com](#).

Table 5.2.10-1. SHT1X Command Response

Command:	
Byte	
0	Checksum8
1	0xF8
2	0x02
3	0x39
4	Checksum16 (LSB)
5	Checksum16 (MSB)
6	DataPinNum (0-19)
7	ClockPinNum (0-19)
8	Reserved
9	Options
Response:	
Byte	
0	Checksum8
1	0xF8
2	0x05
3	0x39
4	Checksum16 (LSB)
5	Checksum16 (MSB)
6	Errorcode
7	0x00
8	StatusReg
9	StatusRegCRC
10-11	Temperature
12	TemperatureCRC
13-14	Humidity
15	HumidityCRC

- **Data/Clock -PinNum:** Assigns which digital I/O line is used for each SPI line. Value passed is 0-7 corresponding to FIO0-FIO7. State and direction are controlled automatically for the specified lines.
- **StatusReg:** Returns a read of the SHT1X status register.
- **Temperature:** Returns the raw binary temperature reading.
- **Humidity:** Returns the raw binary humidity reading.
- **#CRC:** Returns the CRC values from the sensor.

- Options:

- Bit 7: Read Humidity
- Bit 6: Read Temperature
- Bit 5-3: Reserved, write 0
- Bit 2: Enable Heater
- Bit 1: Reserved, write 0
- Bit 0: Resolution. 1 = 8-bit RH and 12-bit Temp, 0 = 12-bit RH and 14-bit Temp

5.2.21 - SetDefaults (SetToFactoryDefaults) [U3 Datasheet]

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Executing this function causes the current or last used values (or the factory defaults) to be stored in flash as the power-up defaults.

The U3 flash has a rated endurance of at least 20000 writes, which is plenty for reasonable operation, but if this function is called in a high-speed loop the flash could eventually be damaged.

Note: Do **not** call this function while streaming.

(The values in parenthesis will set the defaults to factory settings.)

Table 5.2.21-1. SetDefaults Command Response

<u>Command:</u>	
Byte	
0	Checksum8
1	0xF8
2	0x01
3	0x0E
4	Checksum16 (LSB)
5	Checksum16 (MSB)
6	0xBA (0x82)
7	0x26 (0xC7)
<u>Response:</u>	
Byte	
0	Checksum8
1	0xF8
2	0x01
3	0x0E
4	Checksum16 (LSB)
5	Checksum16 (MSB)
6	Errorcode
7	0x00

LabJackPython Example

```
>>> import u3
>>> d = u3.U3()
>>> d.debug = True
""Make the U3's current configuration the power-up defaults.""
>>> d.setDefaults()
Sent: [0xe8, 0xf8, 0x1, 0xe, 0xe0, 0x0, 0xba, 0x26]
Response: [0x8, 0xf8, 0x1, 0xe, 0x0, 0x0, 0x0, 0x0]
```

5.2.22 - ReadDefaults (ReadCurrent) [U3 Datasheet]

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Reads the power-up defaults from flash (Read the current configuration).

Table 5.2.22-1. ReadDefaults Command Response and Defaults map

<u>Command:</u>			Defaults Map		
Byte		<u>Block Number</u>	<u>Byte Offset</u>	<u>Description</u>	<u>Nominal Values</u>

0	Checksum8	0	0-3	Not Used	0x00
1	0xF8	0	4	FIO Directions	0x00
2	0x01	0	5	FIO States	0xFF
3	0x0E	0	6	FIO Analog	0x00
4	Checksum16 (LSB)	0	7	Not Used	0x00
5	Checksum16 (MSB)	0	8	EIO Directions	0x00
6	0x00	0	9	EIO States	0xFF
7	bits[0:6] BlockNum 0-7	0	10	EIO Analog	0x00
	bit 7: 1 = ReadCurrent	0	11	Not Used	0x00
		0	12	CIO Directions	0x00
Response:		0	13	CIO States	0xFF
Byte		0	14-15	Not Used	0x00
0	Checksum8	0	16	Config Write Mask	0x00 (NM)
1	0xF8	0	17	NumOfTimersEnabled	0x00
2	0x01	0	18	Counter Mask	0x00
3	0x0E	0	19	Pin Offset	0x04
4	Checksum16 (LSB)	0	20	Options	0x00
5	Checksum16 (MSB)	0	21-31	Not Used	0x00
6	Errorcode	1	0 (32)	Clock Source	0x02
7	0x00	1	1 (33)	Divisor	0x00
8-39	Data	1	2-15 (33-47)	Not Used	0x00
		1	16 (48)	TMR0 Mode	0x0A
		1	17 (49)	TMR0 Value L	0x00
		1	18 (50)	TMR0 Value H	0x00
		1	19 (51)	Not Used	0x00
		1	20 (52)	TMR1 Mode	0x0A
		1	21 (53)	TMR1 Value L	0x00
		1	22 (54)	TMR1 Value H	0x00
		1	23-31 (55-63)	Not Used	0x00
		2	0-15 (64-79)	Not Used	0x00
		2	16-17 (80-81)	DAC0 (2 Bytes)	0x0000
		2	18-19 (82-83)	Not Used	0x00
		2	20-21 (84-85)	DAC1 (2 Bytes)	0x0000
		2	22-31 (86-95)	Not Used	0x00
		3	0-15 (96-111)	AIN Neg Channel	0x1F
		3	16-31 (112-127)	Not Used	0x00

5.2.23 - 1-Wire [U3 Datasheet]

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This function performs 1-Wire communication.

Because the EIO lines on the U3 can be configured as either digital or analog, ensure that the I/O being used for 1-Wire sensor comm is first configured as either digital input or digital output, then use the function outlined below. For additional information on how to use this function, please see the [1-Wire App Note](#).

Table 5.2.23-1. 1-Wire Command Response

Command:		
Byte		
0	Csum8	
1	0xF8	
2	0x1D	
3	0x3C	

4	Csum16 L	
5	Csum16 H	
6	Options	
		Bit 0: DPU Control Enable
		Bit 1: DPU Polarity
		Bit 2: DPU Idle
7	Reserved	
8	Sense Pin	
9	DPU Pin	
10	Reserved	
11	ROM Function	
12	ROM0 (LSB)	
13	ROM1	
14	ROM2	
15	ROM3	
16	ROM4	
17	ROM5	
18	ROM6	
19	ROM7 (MSB)	
20	Reserved	
21	Num TX	
22	Reserved	
23	Num RX	
24	TX Byte 0	
...	...	
63	TX Byte 39	
Response:		
Byte		
0	Csum8	
1	0xF8	
2	0x1D	
3	0x3C	
4	Csum16 L	
5	Csum16 H	
6	Error Code	
7	Reserved	
8	Reserved	
9	Warnings	
		Bit 0: No Devices Detected
		Bit 1: Type 1 interrupt (Not Tested)
		Bit 2: Type 2 interrupt (Not Supported)
10	Reserved	
11	Reserved	
16	Data 0	
...	...	
63	Data 47	

Options: This byte provides control of the dynamic pull-up.

Bit 0: enables control of the DPU line.

Bit 1: sets the polarity of the switch. 1 = high on the specified DIO turns the switch on.

Bit 2: sets the idle state. 1 = DPU on while IDLE.

Sense Pin: This is the DIO on the LabJack that is connected to the data line of the 1-Wire bus.

DPU Pin: This is the DIO line that will control the dynamic pull-up if enabled in the options byte.

ROM Function: This byte specifies the function to be performed on the 1-Wire bus.

ROM[0:7]: This is the ROM of the target device or search path.

Num TX: This is the number of data bytes to transmit.

Num RX: This is the number of data bytes to receive.

Depending on the ROM function used the data returned can have different meanings. Refer to the following table for data definitions.

Table 5.2.23-2. ROM Functions

		Parameter	Data Returned	
ROM Function:	Number	ROM	Bytes 0-7	Bytes 8-15

Search ROM	0xF0	List of branches to take.	Discovered ROM Code	1s indicate detected branches.
Read ROM	0x33	None	ROM read from device	
Match ROM	0x55	The specific ROM		
Skip ROM	0xCC			
Alarm Search	0xEC			

Additional information

U3 firmware v1.31 and hardware v1.30 are required for 1-Wire.

Maxim has a 1-Wire app note which covers [Dynamic Pull-Ups](#), and another on the [search algorithm](#). There are several kinds of 1-wire temperature sensors from Maxim (DS1820, DS1821, DS1822, DS18S20, and DS18B20). The most common part is probably the [DS18B20](#). Note that these temperature sensors require about 750ms of time to resolve a temperature reading.

5.3 - Errorcodes [U3 Datasheet]

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Table 5.3-1. Low-level function errorcodes.

Error	Code (HEX)	Code (DEC)
SCRATCH_WRT_FAIL	0x01	1
SCRATCH_ERASE_FAIL	0x02	2
DATA_BUFFER_OVERFLOW	0x03	3
ADC0_BUFFER_OVERFLOW	0x04	4
FUNCTION_INVALID	0x05	5
SWDT_TIME_INVALID	0x06	6
XBR_CONFIG_ERROR	0x07	7
FLASH_WRITE_FAIL	0x10	16
FLASH_ERASE_FAIL	0x11	17
FLASH_JMP_FAIL	0x12	18
FLASH_PSP_TIMEOUT	0x13	19
FLASH_ABORT_RECIEVED	0x14	20
FLASH_PAGE_MISMATCH	0x15	21
FLASH_BLOCK_MISMATCH	0x16	22
FLASH_PAGE_NOT_IN_CODE_AREA	0x17	23
MEM_ILLEGAL_ADDRESS	0x18	24
FLASH_LOCKED	0x19	25
INVALID_BLOCK	0x1A	26
FLASH_ILLEGAL_PAGE	0x1B	27
FLASH_TOO_MANY_BYTES	0x1C	28
FLASH_INVALID_STRING_NUM	0x1D	29
SMBUS_INQ_OVERFLOW	0x20	32
SMBUS_OUTQ_UNDERFLOW	0x21	33
SMBUS_CRC_FAILED	0x22	34
SHT1x_COMM_TIME_OUT	0x28	40
SHT1x_NO_ACK	0x29	41
SHT1x_CRC_FAILED	0x2A	42
SHT1X_TOO_MANY_W_BYTES	0x2B	43
SHT1X_TOO_MANY_R_BYTES	0x2C	44
SHT1X_INVALID_MODE	0x2D	45
SHT1X_INVALID_LINE	0x2E	46
STREAM_IS_ACTIVE	0x30	48
STREAM_TABLE_INVALID	0x31	49
STREAM_CONFIG_INVALID	0x32	50
STREAM_BAD_TRIGGER_SOURCE	0x33	51
STREAM_NOT_RUNNING	0x34	52
STREAM_INVALID_TRIGGER	0x35	53
STREAM_ADC0_BUFFER_OVERFLOW	0x36	54
STREAM_SCAN_OVERLAP	0x37	55
STREAM_SAMPLE_NUM_INVALID	0x38	56
STREAM_BIPOLAR_GAIN_INVALID	0x39	57
STREAM_SCAN_RATE_INVALID	0x3A	58
STREAM_AUTORECOVER_ACTIVE	0x3B	59

STREAM_AUTORECOVER_REPORT	0x3C	60
STREAM_SOFTPWM_ON	0x3D	61
STREAM_INVALID_RESOLUTION	0x3F	63
PCA_INVALID_MODE	0x40	64
PCA_QUADRATURE_AB_ERROR	0x41	65
PCA_QUAD_PULSE_SEQUENCE	0x42	66
PCA_BAD_CLOCK_SOURCE	0x43	67
PCA_STREAM_ACTIVE	0x44	68
PCA_PWMSTOP_MODULE_ERROR	0x45	69
PCA_SEQUENCE_ERROR	0x46	70
PCA_LINE_SEQUENCE_ERROR	0x47	71
TMR_SHARING_ERROR	0x48	72
EXT_OSC_NOT_STABLE	0x50	80
INVALID_POWER_SETTING	0x51	81
PLL_NOT_LOCKED	0x52	82
INVALID_PIN	0x60	96
PIN_CONFIGURED_FOR_ANALOG	0x61	97
PIN_CONFIGURED_FOR_DIGITAL	0x62	98
IOTYPE_SYNCH_ERROR	0x63	99
INVALID_OFFSET	0x64	100
IOTYPE_NOT_VALID	0x65	101
INVALID_CODE	0x66	102
UART_TIMEOUT	0x70	112
UART_NOTCONNECTED	0x71	113
UART_NOTENALBED	0x72	114
I2C_BUS_BUSY	0x74	116
TOO_MANY_BYTES	0x76	118
TOO_FEW_BYTES	0x77	119
DSP_PERIOD_DETECTION_ERROR	0x80	128
DSP_SIGNAL_OUT_OF_RANGE	0x81	129
MODBUS_RSP_OVERFLOW	0x90	144
MODBUS_CMD_OVERFLOW	0x91	145

5.4 - Calibration Constants [U3 Datasheet]

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This information is only needed when using low-level functions and other ways of getting binary readings. Readings in volts already have the calibration constants applied. The UD driver, for example, normally returns voltage readings unless binary readings are specifically requested.

Calibration Constant

The majority of the U3's analog interface functions return or require binary values. Converting between binary and voltages requires the use of calibration constants and formulas.

When using Modbus the U3 will apply calibration automatically, so voltages are sent to and read from the U3, formatted as a float.

Which Constants Should I Use?

The calibration constants stored on the U3 can be categorized as follows:

- Analog Input
- Analog Output
- Internal Temperature

Analog Input: Since the U3 uses multiplexed channels connected to a single analog-to-digital converter (ADC), all low-voltage channels have the same calibration for a given configuration. High-voltage channels have individual scaling circuitry out front, and thus the calibration is unique for each channel. The table below shows where the various calibration values are stored in the Mem area. Generally when communication is initiated with the U3, four calls will be made to the ReadMem function to retrieve the first 4 blocks of memory. This information can then be used to convert all analog input readings to voltages. Again, the high level Windows DLL (LabJackUD) does this automatically.

Analog Output: Only two calibrations are provided, one for DAC0 and one for DAC1.

Internal Temperature: This calibration is applied to the bits of a reading from channel 30 (internal temp).

U3 Input Ranges

The U3 input ranges can be found in section 2.6.2 of the User's Guide. For your convenience, that table has been provided again below.

Table 2.6.2-1. Nominal Analog Input Voltage Ranges for Low-Voltage Channels

	Max V	Min V
Single-Ended	2.44	0
Differential	2.44	-2.44
Special 0-3.6	3.6	0

Table 2.6.2-2. Nominal Analog Input Voltage Ranges for High-Voltage Channels

	Max V	Min V
Single-Ended	10.3	-10.3
Differential	N/A	N/A
Special -10/+20	20.1	-10.3

U3 Calibration Formulas (Analog In)

The readings returned by the analog inputs are raw binary values (low level functions). An approximate voltage conversion can be performed as:

$$\text{Volts(uncalibrated)} = (\text{Bits}/65536) * \text{Span (Single-Ended)}$$

$$\text{Volts(uncalibrated)} = (\text{Bits}/65536) * \text{Span} - \text{Span}/2 \text{ (Differential)}$$

Where span is the maximum voltage minus the minimum voltage from the table above. For a proper voltage conversion, though, use the calibration values (Slope and Offset) stored in the internal flash on the Control processor.

$$\text{Volts} = (\text{Slope} * \text{Bits}) + \text{Offset}$$

U3 Calibration Formulas (Analog Out)

Writing to the U3's DAC require that the desired voltage be converted into a binary value. To convert the desired voltage to binary select the Slope and Offset calibration constants for the DAC being used and plug into the following formula.

$$\text{Bits} = (\text{DesiredVolts} * \text{Slope}) + \text{Offset}$$

U3 Calibration Formulas (Internal Temp)

Internal Temperature can be obtained by reading channel 30 and using the following formula.

$$\text{Temp (K)} = \text{Bits} * \text{TemperatureSlope}$$

U3 Calibration Constants

Below are the various calibration values are stored in the Mem area. Generally when communication is initiated with the U3, eight calls will be made to the ReadMem function to retrieve the first 8 blocks of memory. This information can then be used to convert all analog input readings to voltages. Again, the high level Windows DLL (LabJackUD) does this automatically.

Table 5.4-1. Normal Calibration Constant Memory Locations

	Starting			
Block #	Byte		Nominal Value	
0	0	LV AIN SE Slope	3.7231E-05	volts/bit
0	8	LV AIN SE Offset	0.0000E+00	volts
0	16	LV AIN Diff Slope	7.4463E-05	volts/bit
0	24	LV AIN Diff Offset	-2.4400E+00	volts

1	0	DAC0 Slope	5.1717E-01	bits/volt
1	8	DAC0 Offset	0.0000E+00	bits
1	16	DAC1 Slope	5.1717E+1	bits/volt
1	24	DAC1 Offset	0.0000E+00	bits
2	0	Temp Slope	1.3021E-02	degK/bit
2	8	Vref @Cal	2.4400E+00	volts
2	16	Reserved	-	-
2	24	Reserved	-	-

Table 5.4-2. Additional High-Voltage Calibration Constant Memory Locations

Block #	Byte		Nominal Value	
3	0	HV AIN0 Slope	3.1400E-4	volts/bit
3	8	HV AIN1 Slope	3.1400E-4	volts/bit
3	16	HV AIN2 Slope	3.1400E-4	volts/bit
3	24	HV AIN3 Slope	3.1400E-4	volts/bit
4	0	HV AIN0 Offset	-10.3	volts
4	8	HV AIN1 Offset	-10.3	volts
4	16	HV AIN2 Offset	-10.3	volts
4	24	HV AIN3 Offset	-10.3	volts

Format of the Calibration Constants

Each value is stored in 64-bit fixed point format (signed 32.32 little endian, 2's complement). Following are some examples of fixed point arrays and the associated floating point double values.

Table 5.4-3. Fixed Point Conversion Examples

Fixed Point Byte Array	Floating Point Double
{LSB, ..., MSB}	
{0,0,0,0,0,0,0,0}	0.0000000000
{0,0,0,0,1,0,0,0}	1.0000000000
{0,0,0,0,255,255,255,255}	-1.0000000000
{51,51,51,51,0,0,0,0}	0.2000000000
{205,204,204,204,255,255,255,255}	-0.2000000000
{73,20,5,0,0,0,0,0}	0.0000775030
{255,122,20,110,2,0,0,0}	2.4300000000
{102,102,102,38,42,1,0,0}	298.1500000000

Appendix A - Specifications [U3 Datasheet]

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Table A-1. Specifications at 25 degrees C and Vusb/Vext = 5.0V, except where noted.

Parameter	Conditions	Min	Typical	Max	Units
General					
USB Cable Length				5	meters
Supply Voltage		4	5	5.25	volts
Supply Current (1)	Hardware V1.21+		50		mA
Operating Temperature		-40		85	°C
Clock Error	-40 to 85 °C			1.5	%
Typ. Command Execution Time (2)	USB high-high	0.6			ms
	USB other	4			ms

Vs Outputs					
Typical Voltage (3)	Self-Powered	4.75	5	5.25	volts
	Bus-Powered	4	5	5.25	
Maximum Current (3)	Self-Powered		450		mA
	Bus-Powered		50		mA
(1) Typical current drawn by the U3 itself, not including any user connections.					
(2) Total typical time to execute a single Feedback function with no analog inputs. Measured by timing a Windows application that performs 1000 calls to the Feedback function. See Section 3.1 for more timing information.					
(3) These specifications are related to the power provided by the host/hub. Self- and bus-powered describes the host/hub, not the U3. Self-powered would apply to USB hubs with a power supply, all known desktop computer USB hosts, and some notebook computer USB hosts. An example of bus-powered would be a hub with no power supply, or many PDA ports. The current rating is the maximum current that should be sources through the U3 and out of the Vs terminals.					
Parameter	Conditions	Min	Typical	Max	Units
Analog Inputs					
Typical input Range (4)	Single-Ended, LV	0		2.44	volts
	Differential, LV	-2.44		2.44	volts
	Special, LV	0		3.6	volts
	Single-Ended, HV	-10.3		10.3	volts
	Special, HV	-10.3		20.1	volts
Max AIN Voltage to GND (5)	Valid Readings, LV	-0.3		3.6	volts
	Valid Readings, HV	-12.8		20.1	volts
Max AIN Voltage to GND (6)	No Damage, FIO	-10		10	volts
	No Damage, EIO	-6		6	volts
	No Damage, HV	-40		40	volts
Input Impedance (7)	LV		40		MΩ
	HV		1.3		MΩ
Source Impedance (7)	Long Settling Off, LV			10	kΩ
	Long Settling On, LV			200	kΩ
	Long Settling Off, HV			1	kΩ
	Long Settling On, HV			1	kΩ
Resolution	All Ranges		12		bits
	Single-Ended, LV, 0-2.44		0.6		mV
	Differential, LV, ±2.44		1.2		mV
	Special, LV, 0-3.6		1.2		mV
	Single-Ended, HV, ±10		5.0		mV
	Special, HV, -10 to +20		10.0		mV
Integral Linearity Error			±0.05		% FS
Differential Linearity Error			±1		counts
Absolute Accuracy (8)	Single-Ended %		±0.13		% FS
	Single-Ended LV volts		±3.2		mV
	Single-Ended HV volts		±26.8		mV
	Differential %		±0.25		% FS
	Differential LV volts		±6.4		mV
	Differential HV volts		N/A		
	Special 0-3.6 %		±0.25		% FS
	Special LV volts		±6.4		mV
	Special HV volts		±53.6		mV
Temperature Drift			15		ppm/°C
Noise (Peak-To-Peak) (9)	Quick Sample Off		±1		counts
	Quick Sample On		±2		counts
Effective Resolution (RMS) (10)	Quick Sample Off		>12		bits
Noise-Free Resolution (9)	Quick Sample Off		11		bits
Command/Response Speed	See Section 3.1				
Stream Performance	See Section 3.2				
* LV specs refer to low voltage analog inputs which are available on the U3-LV and U3-HV. HV specs refer to high voltage analog inputs which are available on the U3-HV only.					
(4) Note that these are typical input ranges. The actual minimum on the low voltage inputs might not go all the way to 0.0 as discussed in Section 2.6.3.9 . These are with DAC1 disabled on hardware version < 1.30.					
(5) This is the maximum voltage on any AIN pin compared to ground for valid measurements. Note that a differential channel has a minimum voltage of -2.44 volts, meaning that the positive channel can be 2.44 volts less than the negative channel, but					

no low-voltage AIN pin can go more than 0.3 volts below ground.

(6) Maximum voltage, compared to ground, to avoid damage to the device. Protection level is the same whether the device is powered or not.

(7) The low-voltage analog inputs essentially connect directly to a SAR ADC on the U3, presenting a capacitive load to the signal source. The high-voltage inputs connect first to a resistive level-shifter/divider. The key specification in both cases is the maximum source impedance. As long as the source impedance is not over this value, there will be no substantial errors due to impedance problems.

(8) Absolute error includes INL, DNL, and all other sources of internal error at 25 C and VS=5.0V. To equate the percentage to voltage, multiply the full voltage span by the percentage. For a single-ended low voltage input using the normal range the span is about 2.4 volts, so $2.4 * 0.0013$ gives ± 0.003 volts. For a single-ended high voltage input using the normal range the span is about 20 volts, so $20 * 0.0013$ gives ± 0.026 volts. Differential readings are not calibrated on high voltage channels.

(9) Measurements taken with AIN connected to a 2.048 reference (REF191 from Analog Devices) or GND. All "counts" data are aligned as 12-bit values. Noise-free data is determined by taking 128 readings and subtracting the minimum value from the maximum value.

(10) Effective (RMS) data is determined from the standard deviation of 128 readings. In other words, this data represents most readings, whereas noise-free data represents all readings.

Parameter	Conditions	Min	Typical	Max	Units
Analog Outputs (DAC)					
Nominal Output Range (11)	No Load	0.04		4.95	volts
	@ ± 2.5 mA	0.225		4.775	volts
Resolution			10		bits
Absolute Accuracy	5% to 95% FS		± 5		% FS
Integral Linearity Error			± 1		counts
Differential Linearity Error			± 1		counts
Max Output Current (12)	@ 2.0V		30		mA
Error Due To Loading (12)	@ 100 μ A		0.1		%
	@ 1 mA		1		%
Source Impedance (12)			50		Ω
Short Circuit Current (12,13)	5V to GND		50		mA
Cutoff Frequency (14)	-3 dB		16		Hz
Time Constant (14)			10		ms
Digital I/O, Timers, Counters					
Low Level Input Voltage		-0.3		0.8	volts
High Level Input Voltage		2		5.8	volts
Maximum Input Voltage (15)	FIO	-10		10	volts
	EIO/CIO	-6		6	volts
Output Low Voltage (16)	No Load		0		volts
--- FIO	Sinking 1 mA		0.55		volts
--- EIO/CIO	Sinking 1 mA		0.18		volts
--- EIO/CIO	Sinking 5 mA		0.9		volts
Output High Voltage (16)	No Load		3.3		volts
--- FIO	Sourcing 1 mA		2.75		volts
--- EIO/CIO	Sourcing 1 mA		3.12		volts
--- EIO/CIO	Sourcing 5 mA		2.4		volts
Short Circuit Current (16)	FIO		6		mA
	EIO/CIO		18		mA
Input Impedance	Pull-up to 3.3V		100		k Ω
Series Impedance (16)	FIO		550		Ω
	EIO/CIO		180		Ω
Counter Input Frequency (17)	Hardware V1.21+			8	MHz
Input Timer Total Edge Rate (18)	No Stream, V1.21+			30000	edges/s
	While Streaming			7000	edges/s

(11) Maximum and minimum analog output voltage is limited by the supply voltages (Vs and GND). The specifications assume Vs is 5.0 volts. Also, the ability of the DAC output buffer to driver voltages close to the power rails, decreases with increasing output current, but in most applications the output is not sinking/sourcing much current as the output voltage approaches GND.

(12) If the output is set to 3.5 volts and sourcing 30 mA, there will be about 2.0 volts at the DAC pin due to the 50 ohms of series impedance. Each DAC output is driven by a channel on an AD8544 op-amp, powered by VS & GND, and then goes through protection circuitry that includes 50 ohms of series impedance. The max output current is

determined by 3 main factors: short circuit current, ability of AD8544 to sink/source near power rails (Figure 22 of AD8544 datasheet), and the 50 ohms of series impedance.

(13) Continuous short circuit will not cause damage.

(14) The DAC outputs are created by filtering PWM signals, and the 2nd order 16 Hz output filter works great for the default PWM frequency of 732 Hz, but with lower frequency timer clocks the DAC outputs will be noisier. See Section 2.7 for more details. Time constant is the time it takes for the output to settle 63% of the way towards a new value.

(15) Maximum voltage to avoid damage to the device. Protection works whether the device is powered or not, but continuous voltages over 5.8 volts or less than -0.3 volts are not recommended when the U3 is unpowered, as the voltage will attempt to supply operating power to the U3 possibly causing poor start-up behavior.

(16) These specifications provide the answer to the question: "How much current can the digital I/O sink or source?". For instance, if EIO0 is configured as output-high and shorted to ground, the current sourced by EIO0 into ground will be about 18 mA (3.3/180). If connected to a load that draws 5 mA, EIO0 can provide that current but the voltage will droop to about 2.4 volts instead of the nominal 3.3 volts. If connected to a 180 ohm load to ground, the resulting voltage and current will be about 1.65 volts @ 9 mA.

(17) Hardware counters. 0 to 3.3 volt square wave. Limit 2 MHz with older hardware versions.

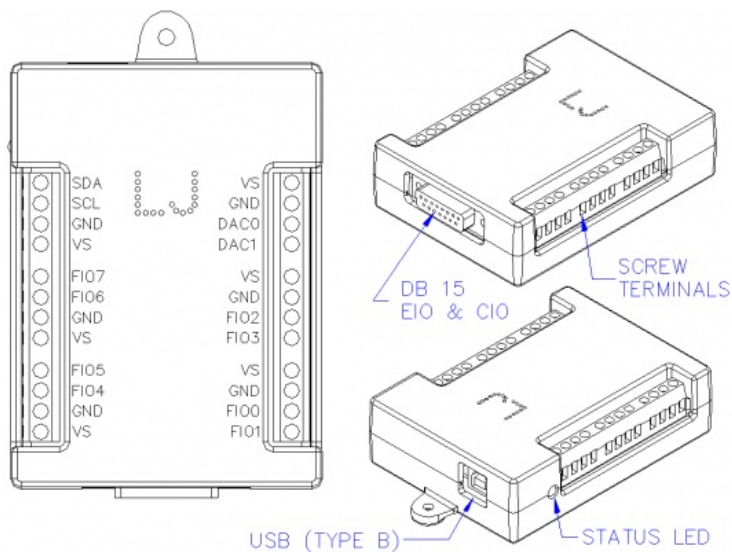
(18) To avoid missing edges, keep the total number of applicable edges on all applicable timers below this limit. See Section 2.9 for more information. Limit 10000 with older hardware versions.

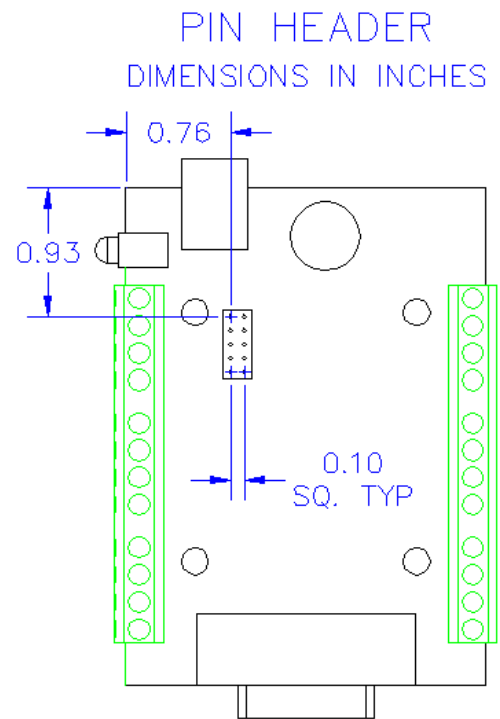
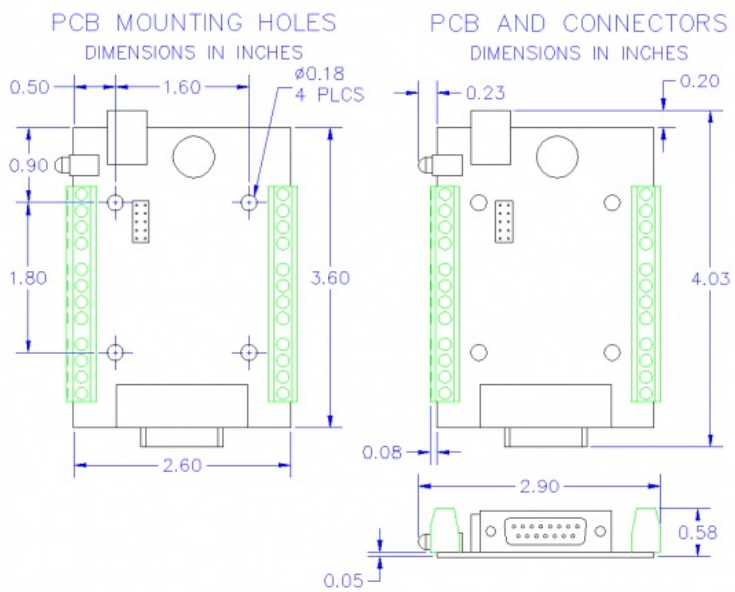
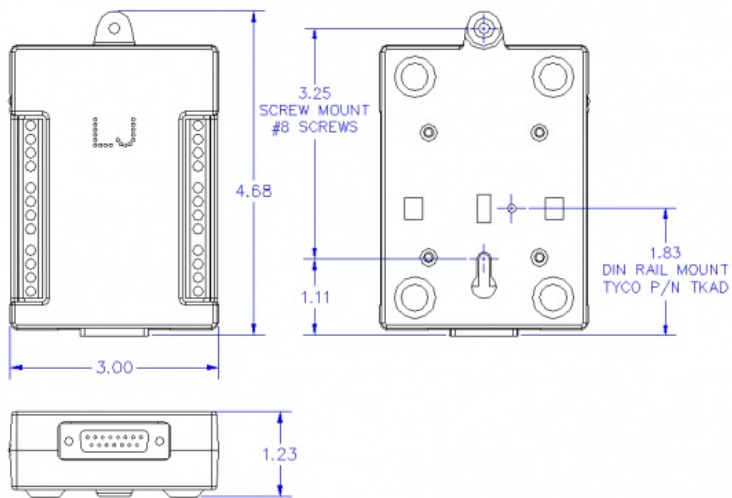
Appendix B - Enclosure and PCB Drawings [U3 Datasheet]

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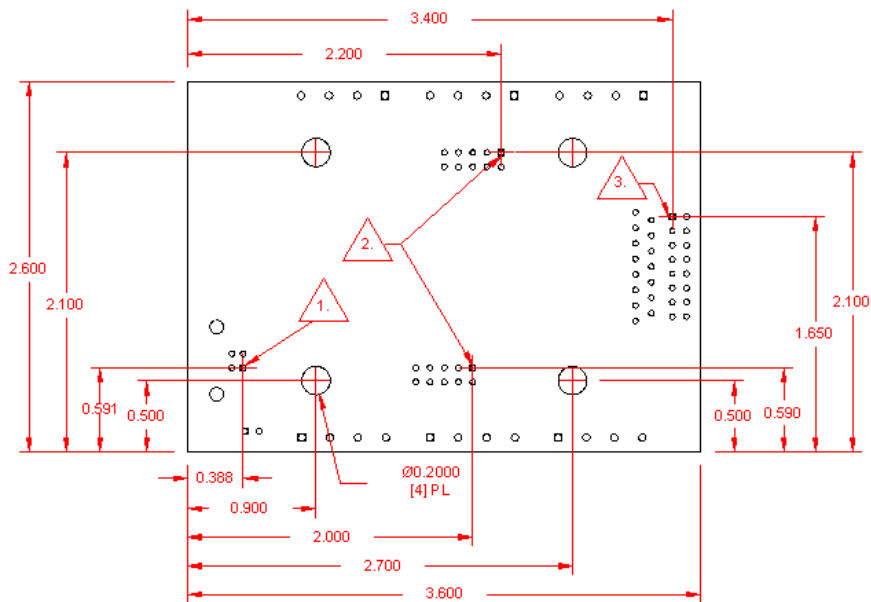
Various drawings follow. CAD drawings of the U3 attached to the bottom of this page (DWG, DXF, IGES, STEP).

The square holes on the back of the enclosure are for [DIN rail mounting adapters](#) (TE Connectivity part #TKAD).





U3 OEM PCB Dimensions



Notes on U3 OEM PCB Dimensions

1. USB, PIN1
2. OEM 2x5 HEADER, 0.100" PITCH, PIN 1
3. OEM 2X8 HEADER, 0.100" PITCH, PIN 1

More Details

See the [OEM page](#) for details on connector pin-headers, holes, power supply information, part options, and more.

Common neutral format CAD models are provided below. Right-click and select the "Save link as..." option to download STEP files.

File Attachment:

- [U3.IGS](#)
- [U3.STEP](#)
- [U3_OEM.STEP](#)
- [U3_PCB_Dimensions.dxf](#)
- [U3_OEM_Parasolid-20191017.zip](#)

U3 Firmware Revision History [U3 Datasheet]

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Firmware is available on the [U3 Firmware](#) page.

U3 Firmware Overview

Firmware files require [LJSelfUpgrade](#) V1.21 or higher in order to upgrade the U3, which is included in the LabJack installer.

U3A and U3B refer to old hardware versions of the U3. U3Afirmware is for U3 hardware 1.20, and U3Bfirmware is for U3 hardware 1.21. U3C is for the [U3-LV](#) and [U3-HV](#).

WARNING: This warning applies to anyone using a programming utility other than LJSelfUpgrade. Attempting to program a U3A with U3B firmware will result in improper programming. This is because the U3A does not have sufficient code space to handle the U3B firmware. If a U3A is programmed with U3B firmware the device can be recovered by jumpering to flash mode and loading U3A firmware. A U3B can be programmed with U3A firmware and will function as a U3A. LJSelfUpgrade will not allow firmware for one version to be programmed onto the other.

U3C Firmware (Hardware 1.30)

1.46: Watchdog wasn't getting cleared unless response packet was read very quickly. Fixed. Timer1 was returning incorrect data when set to modes 2 or 3.

Known Issues:

*Reading from analog inputs can change stream settings. To work around this issue, StreamConfig must be called before each StreamStart when repeatedly starting and stopping stream.

*U3Config does not return the correct DAC values. The values being returned are the watchdog timer response values.

1.44: Feedback will now throw an error when an AIN is read while stream is active. Added special range support to high voltage channels in modbus. Compatibility settings written by the user will now be loaded at startup. When calling the deprecated, low-level, SWDT config command the bit for enabling a DIO change will be recognized as the bit for DIOA not DIOB. Added a bus idle check to the I2C. Timer modes 5 and 6 (counter and counter with debounce) now respond to their proper timer mode numbers (they were swapped). Timer 1 will now be properly set during initialization. I2C will now return ACK information when only reading. Only one ack for the address will be reported. Improved timer performance. Changed the watchdog so that it will not clear until the host computer has read the response. Fixed a problem that was causing edge32 and edge16 timer modes to return improper data while streaming with reset. Increased I2C max clock stretch time to 1000 clock cycles. When a timer is set to mode 0 or 1 (PWM) reading the timer will return the controlling count.

[Click To Expand Change Log](#)

1.32: Added special channel support to CR functions. SPI modes changed to match documentation. Added Line-to-Line timer mode. Added 1-wire communication. ModBus related: Added DAC limit checking. Will now compensate for 8-bit or 16-bit DAC calibration. Added support for 3.3V input range. Added Negative Chn support for chn 199 (GND). Channels 193 and 124 for logging FIO, EIO, CIO in LJLogUD will now work correctly. Fixed an issue that was corrupting data returned by modbus. Added modbus data format check. Changed the default device name string to ASCII. Fixed an issue that could prevent counters from being enabled by the startup settings system. Fixed a problem that could force DACs to 8-bit mode.

1.24: Added extended default system. Added ModBus functionality. Added extended watchdog function. Fixed a problem that was clearing SWDT settings during initialization. Enabled descriptor string reading. USB has been updated to improve compatibility with Linux kernels prior to 2.6.28.

WARNING: Upgrading across this boundary will reset startup setting to the manufacturer's defaults. Downgrading across this boundary will result in randomized startup values.

1.12: Fixed an issue that was corrupting stream channels. Changed the time interval of the short_wait feedback function from 64 to 128us

1.11: Port digital state functions are now allowed to update EIO6 and EIO7.

1.10: Added a compatibility bit that forces the DACs to 8-bit mode to reduce noise.

1.09: Stream will no longer decode channel 193 to channel 1. Added stream channel 194 for CIO lines.

1.08: Improved UART support for hardware 1.30. Changed counters so that resets will occur after reading the value.

1.07: Fixed a problem that was causing EIO6 and 7 to not work with the I2C function.

1.04: Fixed a problem that was causing stream channel 193 to return inappropriate data.

1.03: Fixed a problem that was causing Digital port IO types to apply changes to incorrect channels. Fixed a problem that was causing analog read of EIO[0:3] to report voltages on other channels.

1.02: ConfigIO will no longer throw errors when no write bits are set.

U3B Firmware (Hardware 1.21)

1.54: Added a clock stretching feature to the I2C function. USB has been updated to improve compatibility with Linux kernels prior to 2.6.28.

1.52: Added an I2C option to disable the stop used when restarting. Sending 255 to the debounce function will now have the same effect as 254. Added an I2C option to enable clock stretching, up to a maximum of 50 clock periods.

1.50: State DIO functions will now force masked pins to output.

1.49: I2C acknowledge array now includes the address byte. Added an error to prevent AIN functions from being called while streaming. Added an error to prevent using channel 30 (Vref) as the negative channel while using Vdd as the reference voltage (30 is an invalid Negative channel while using Vdd as Vref). Stream channel 193 now returns FIO in the LSB to match the documentation.

1.44: Fixed CIO startup sequence. Default CIO settings will now be loaded properly. Removed level sensitivity from timer debounce mode. Debounce quantity is now 1+requested amount.

U3A Firmware (Hardware 1.20)

1.44: Improved code layout to prevent upgrade issues.

1.43: Fixed an upgrading issue that was causing flash corruption. The corruption caused the U3 to reset whenever a AIN command was executed.

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1.41: (May 3, 2007) Fixed an issue that was causing the U3 hardware version 1.20 to reset when acquiring analog data.

1.40: (April 6, 2007) Improved 16-bit PWM updates.

1.38: (March 6, 2007) Added support for reading the lower 32-bits of the system timer as a stream channel.

1.36: (February 27, 2007) More flash protection.

1.35: (February 7, 2007) A new error was added to indicate a missed scan count overflow while stream auto-recover is active. Added additional flash protection.

1.32: (January 22, 2007) Occasionally the U3 experiences unexpected flash modification. This new firmware adds several new flash protections to prevent unintentional flash changes.

1.28: (December 12, 2006) Improved counter reading in stream mode. (U3 hardware Rev 1.21 only).

1.26: (September 14, 2006) Added special stream channels for counters and timers. Firm counter data can now be read while streaming. (U3 hardware Rev 1.21 only).

1.20: (September 8, 2006) Major change to Upgrade procedure. (Requires LJSelfUpgrade 1.11)

1.19: (August 28, 2006) Initial stream implementation. (U3 hardware Rev 1.21 only)

1.18: (August 25, 2006) Updated settling times for the higher clock speed of the U3 rev 1.21.

1.17: (August 15, 2006) Added support for changing USB descriptor strings. Added logic to prevent roll issues in between low and high system timer reads.

1.16: (August 10, 2006) Fixed issue affecting FIO4 when streaming or using Wait IOType in Feedback function. Added I2C and UART functions.

1.14: (July 25, 2006) Moved power-up defaults to earlier in the startup sequence. Fixed a problem where DAC1 enable power-up default was not working correctly.

1.12: (July 19, 2006) Unknown functions now return checksum error.

1.10: (July 11, 2006) Fixed a problem that was preventing DAC defaults from working if DIO defaults were not being used.

1.09: (July 6, 2006) Minor changes made to the way that timers are enabled and disabled.

1.06: (May 30, 2006) Fixed issue with the FIOAnalog and EIOAnalog parameters in the ConfigIO function.

1.05: (May 18, 2006) Changed descriptors to fix a Mac enumeration issue that occurred with more than one U3 connected to a hub.

1.04: (May 2, 2006) Added initial functionality for hardware rev 1.21.

1.03: (April 18, 2006) Improved accuracy of the short wait function. Improved the SoftPWM functions. Resolution is now 100us.

1.02: (April 11, 2006) Changed stop timer mode to use positive edges. Write either PCA module will reset the PCA counter.