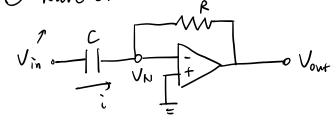


1) Rate (Derivative) Circuit



1) For the capacitor,

Take the derivative of both cides.

Since VH =0 > VH =0

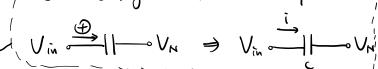
$$\Rightarrow \quad \forall \text{in } c = q = i \quad \longleftrightarrow$$

To determine the direction of i

Suppose Vin 70, which means that

/ the value of Vin is increasing.

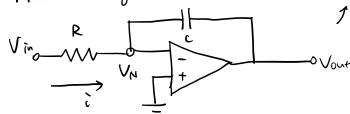
→ There will be more positive charges on the right-side of the capacitor.



2) For the operational amplifier,

$$\frac{V_{H}-V_{out}}{R}=i \implies V_{out}=-iR=-(RC)V_{in}$$

2 Area (Integral) Circuit



() For the resistor R on the input terminal,

$$\frac{V_{in} - V_{in}}{R} = i \qquad \frac{V_{in} = 0}{R}$$

2) For the operational amplifier,

$$(V_N - V_{out}) \cdot C = 9 \xrightarrow{V_N = 0} 9 = -V_{out} \cdot C$$

Take the derivatives of both sides,

$$\hat{q} = -c \cdot V_{out}$$
 \Longrightarrow $V_{out} = -\frac{1}{c} \cdot \frac{V_{in}}{R} = -\frac{1}{Rc} \cdot V_{in}$

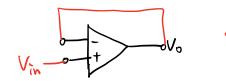
Take the integrals of both sides,

Vont =
$$-\frac{1}{RC} \int V in dt$$

A key points:

the topology position of resistor and capacitor in the rate/area circuits.

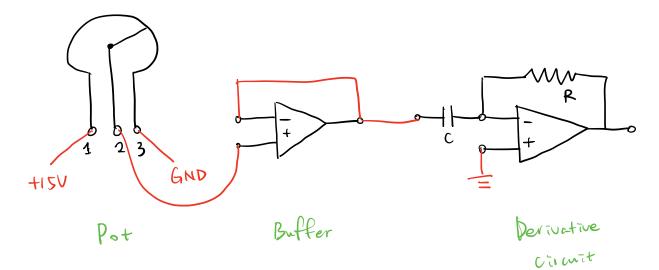
Review. Buffer circuit



Vo = Vin

Hint.

1) Connection diagram of the derivative circuit



Safety. A resistor can be connected before the pot.

Deliverables:

- 1) Pics of derivative and integral circuits
- 2) Carroup Number + Name