Chapter 3: Algebraic Operations

#Basic Knowledges.

1. Physical Illustration:

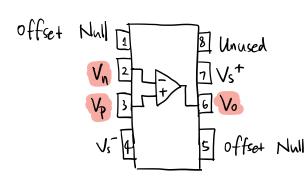
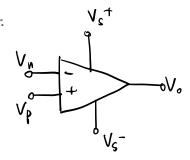


Diagram :



Important Terminals

Input: Vn, Vp

Output: Vo

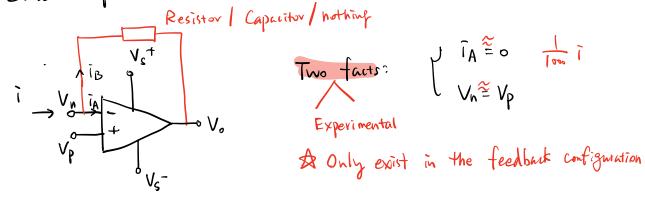
power: Vs-, Vs+

a Open loop Connection

$$\Rightarrow$$

Saturation

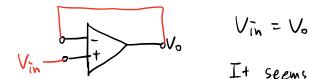
3. Closed loop Connection CFeed back Configuration)



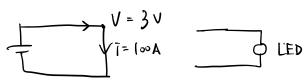
T= IA+ iB and IA=0

$$\frac{V_{n}-V_{o}}{R}=\tilde{I}_{B} \Rightarrow V_{o}=V_{n}-\tilde{I}_{B}R$$

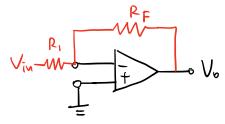
1) Buffering / Cascading



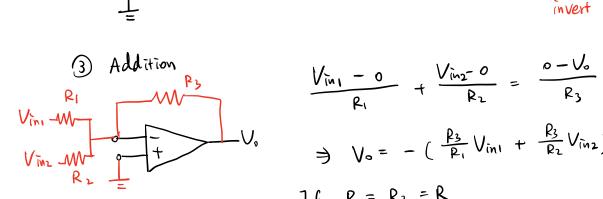
It seems like a direct wive connection



Gain and Inverting



$$\frac{\rho - V_0}{R_F} = \frac{V_{in}}{R_i} \Rightarrow V_0 = -\frac{R_F}{R_i} V_{in}$$
invert gain

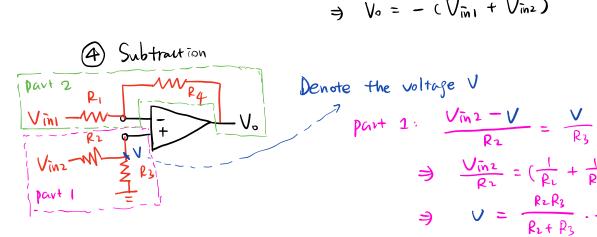


$$\frac{V_{\text{in}_1} - o}{R_1} + \frac{V_{\text{in}_2} - o}{R_2} = \frac{o - V_o}{R_3}$$

$$\Rightarrow V_0 = -\left(\frac{\beta_3}{R_1}V_{in1} + \frac{\beta_3}{R_2}V_{in2}\right)$$

$$1f R_1 = R_2 = R$$

$$\Rightarrow V_0 = -\frac{R_3}{R} \left(V_{\bar{1}n_1} + V_{\bar{1}n_2} \right)$$
Addition



$$\frac{V_{\bar{m}2} - V}{R_2} = \frac{V}{R_3}$$

$$\Rightarrow \frac{V_{\bar{m}2}}{R_2} = \left(\frac{1}{R_1} + \frac{1}{R_3}\right) V$$

$$\Rightarrow V = \frac{R_2 R_3}{R_1 + R_3} \cdot \frac{V_{\bar{m}2}}{R_2} = \frac{P_3}{R_1 + R_3} V_{\bar{m}2}$$

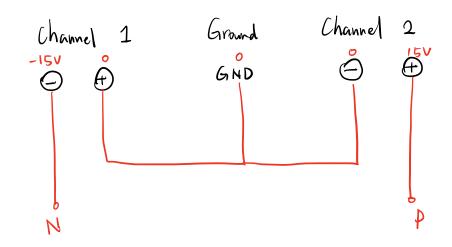
part 2:
$$\frac{V_{in1}-V}{R_1} = \frac{V-V_0}{R_4}$$

$$\frac{V_{in1} - V}{R_{1}} = \frac{V - V_{0}}{R_{4}} \quad \text{Sub.} \quad \frac{if R_{1} = R_{1} = R_{3} = R_{4} = R}{if R_{1} = R_{1} = R_{3} = R_{4} = R}$$

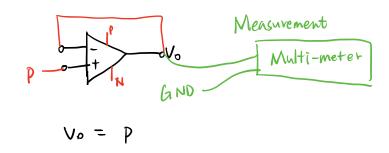
$$\Rightarrow V_{0} = (\frac{R_{4}}{R_{1}} + 1) V - \frac{R_{4}}{R_{1}} V_{in1} = (\frac{R_{4}}{R_{1}} + 1) \frac{R_{3}}{R_{1} + R_{3}} V_{in2} - \frac{R_{4}}{R_{1}} V_{in1} = -(V_{in1} - V_{in2})$$

Experiments Steps.

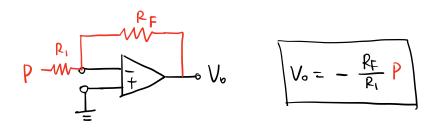
1) Set up the power source



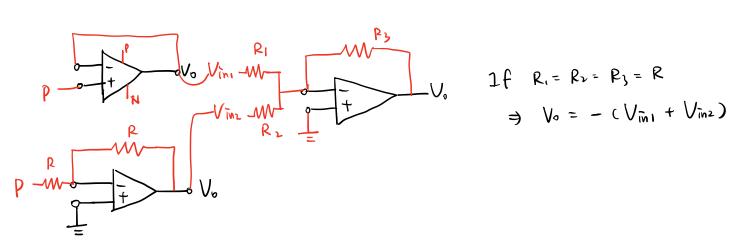
2 A. To buffer +15V



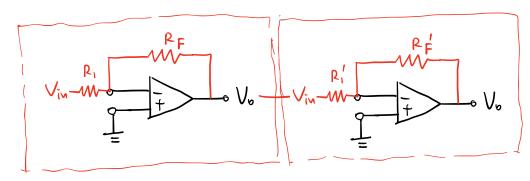
B. To step dawn from +15 V



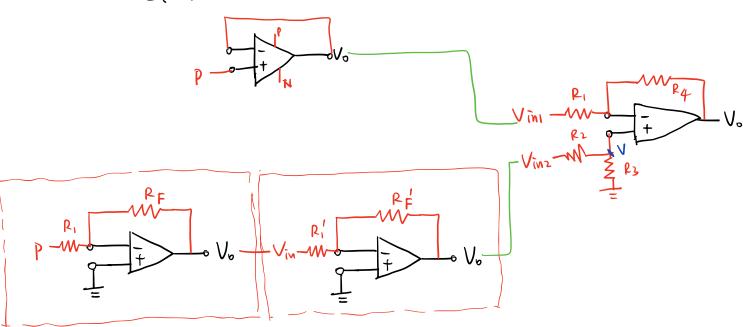
C. To sum the A and B outputs



D. To invert the B output



E. To substract the D output from the A output (A-D)



F. To get an output of 3V from the +15V input

Hints: use the parallel or series of resistors to create an "equivalent resistor" with new resistance value.