addi $t0, $zero, -50

addi $t1, $t0, 45

slt $t2, $t1, $zero

beq $zero, $t2, test

sub $t3, $t2, $t0

or $t4, $t1, $t2

and $t5, $t0, $t4

jal test

sw $t2, 8($zero)

lw $t3, 8($zero)

sw $t1, 4($zero)

bye:

j bye

test:

add $t3, $t1, $t2

jr $ra

RAM[0]  = 32'h2008ffce; RAM[1]  = 32'h2109002d;

        RAM[2]  = 32'h0120502a; RAM[3]  = 32'h100a0008;

        RAM[4]  = 32'h01485822; RAM[5]  = 32'h012a6025;

        RAM[6]  = 32'h010c6824; RAM[7]  = 32'h0c00000c;

        RAM[8]  = 32'hac0a0008; RAM[9]  = 32'h8c0b0008;

        RAM[10] = 32'hac090004; RAM[11] = 32'h0800000b;

        RAM[12] = 32'h012a5820; RAM[13] = 32'h03e00008;

        RAM[14] = 32'h00000000; RAM[15] = 32'h00000000;

golden\_reg[0] = 0;

golden\_reg[1] = 0;

golden\_reg[2] = 0;

golden\_reg[3] = 0;

golden\_reg[4] = 0;

golden\_reg[5] = 0;

golden\_reg[6] = 0;

golden\_reg[7] = 0;

golden\_reg[8] = -50;

golden\_reg[9] = -5;

golden\_reg[10] = 1;

golden\_reg[11] = 1;

golden\_reg[12] = -5;

golden\_reg[13] = -54;

golden\_reg[14] = 0;

golden\_reg[15] = 0;

golden\_reg[16] = 0;

golden\_reg[17] = 0;

golden\_reg[18] = 0;

golden\_reg[19] = 0;

golden\_reg[20] = 0;

golden\_reg[21] = 0;

golden\_reg[22] = 0;

golden\_reg[23] = 0;

golden\_reg[24] = 0;

golden\_reg[25] = 0;

golden\_reg[26] = 0;

golden\_reg[27] = 0;

golden\_reg[28] = 0;

golden\_reg[29] = 0;

golden\_reg[30] = 0;

golden\_reg[31] = 32;

golden\_dmem[0] = 0;

golden\_dmem[1] = -5;

golden\_dmem[2] = 1;

golden\_dmem[3] = 0;

golden\_dmem[4] = 0;

golden\_dmem[5] = 0;

golden\_dmem[6] = 0;

golden\_dmem[7] = 0;

golden\_dmem[8] = 0;

golden\_dmem[9] = 0;

golden\_dmem[10] = 0;

golden\_dmem[11] = 0;

golden\_dmem[12] = 0;

golden\_dmem[13] = 0;

golden\_dmem[14] = 0;

golden\_dmem[15] = 0;