EE113 Semiconductor Tutorial 10-1 Topic 5

1. Draw a fully labelled diagram of JFET and briefly explain how it functions.
2. For a JFET explain why the depletion layer width varies across the source drain channel.
3. Show what happens to the drain current of a JFET, ID, as both gate voltage, VG and drain voltage, VD, are varied. (That is draw the output curve of the JFET)
4. What does the term, JFET, stand for? What does the term, MOSFET, stand for?