

# sysstick



# Use of systick


- Used for precise timing
- Whenever the timer runs out it will trigger an interrupt
- We'll set it for 5ms
- Will be relied upon throughout the semester

# Create a new project

- Create a new project named “systick\_test” following instructions from Lab1
- Download and unpack systick\_minimum.zip from itslearning
- Add three files from zip to newly created project

# Set up interrupt handler

- Open the following file which was created automatically when you created the project

>  tm4c123gh6pm\_startup\_ccs\_gcc.c

# Set up handler

- Add the following lines to enable the interrupt handler
- Compile and run
- A blue light should be blinking on your board



```
31 //
32 void ResetISR(void);
33 static void NmiISR(void);
34 static void FaultISR(void);
35 static void IntDefaultHandler(void);
36
37 void systick_handler(void);
38
39 #ifndef HWREG
40 #define HWREG(x) (*((volatile uint32_t *)(x)))
41 #endif
42
43 //*****
44 //
45 // The entry point for the application.
46 //
47 //*****
48 extern int main(void);
49
50 //*****
51 //
52 // Reserve space for the system stack.
53 //
54 //*****
55 static uint32_t pui32Stack[128];
56
57 //*****
58 //
59 // External declarations for the interrupt handlers used by the application.
60 //
61 //*****
62 // To be added by user
63
64 //*****
65 //
66 // The vector table. Note that the proper constructs must be placed on this to
67 // ensure that it ends up at physical address 0x0000.0000 or at the start of
68 // the program if located at a start address other than 0.
69 //
70 //*****
71 __attribute__((section(".intvecs")))
72 void (* const g_pfnVectors[])(void) =
73 {
74     (void (*)(void))((uint32_t)pui32Stack + sizeof(pui32Stack)),
75     // The initial stack pointer
76     ResetISR,
77     // The reset handler
78     NmiISR,
79     // The NMI handler
80     FaultISR,
81     // The hard fault handler
82     IntDefaultHandler,
83     // The MPU fault handler
84     IntDefaultHandler,
85     // The bus fault handler
86     IntDefaultHandler,
87     // The usage fault handler
88     0,
89     // Reserved
90     0,
91     // Reserved
92     0,
93     // Reserved
94     0,
95     // Reserved
96     IntDefaultHandler,
97     // SVCall handler
98     IntDefaultHandler,
99     // Debug monitor handler
100     0,
101     // Reserved
102     IntDefaultHandler,
103     // The PendSV handler
104     systick_handler,
105     // The SysTick handler
106     IntDefaultHandler,
107     // GPIO Port A
```



# Systick.c

```
1 #include <stdint.h>
2 #include "tm4c123gh6pm.h"
3
4
5
6 #define SYSTICK_RELOAD_VALUE 80000          // 5 mS
7
8 // Missing definitions in tm4c123gh6pm.h file
9 #define NVIC_INT_CTRL_PEND_SYST  0x04000000 // Pend a systick int
10 #define NVIC_INT_CTRL_UNPEND_SYST 0x02000000 // Unpend a systick int
11
12 #define SYSTICK_PRIORITY 0x7E
13
14 volatile int ticks = 0;
15
16 void systick_handler(void)
17 /*****
18 *   Function : See module specification (.h-file).
19 *****/
20 {
21     // Hardware clears systick int request
22     ticks++;
23 }
24
```

# Main.c

```
3 /**
4  * main.c
5  */
6 |
7 #include <stdint.h>
8 #include "tm4c123gh6pm.h"
9 #include "systick.h"
10
11 #define TIM_1_SEC      200
12
13 extern int ticks;
14
15 int main(void)
16 {
17     int alive_timer = TIM_1_SEC;
18
19     disable_global_int();
20     init_systick();
21     enable_global_int();
22
23     int dummy;
24     SYSCTL_RCGC2_R = SYSCTL_RCGC2_GPIOF;    // enable the GPIO port that is used for the on-board LEDs and switches
25     dummy = SYSCTL_RCGC2_R;                  // dummy read to insert a few cycles after enabling the peripheral
26     GPIO_PORTF_DIR_R = 0x0E;                 // set the direction as output for LED pins on PortF (PF1 - PF3)
27     GPIO_PORTF_DEN_R = 0x1E;                 // enable the GPIO pins for digital function (PF1 - PF4)
28     GPIO_PORTF_PUR_R = 0x10;                 // enable internal pull-up resistor for switch (PF4)
29
30     while(1)                                // loop forever
31     {
32         while( !ticks );
33
34         // The following will be executed every 5mS
35         ticks--;
36
37         if( ! --alive_timer )
38         {
39             alive_timer      = TIM_1_SEC;
40             GPIO_PORTF_DATA_R ^= 0x04;
41         }
42     }
43     return 0;
44 }
45
46 ~
```