manual viewer

08/02/2021

## **LAN7500 Customer Evaluation Board**

**Schematic Revision 1.1 PCB Revision A** 

Assy 6588

Circuit Diagrams utilizing SMSC Products Are included As Means of Illustrating Typicial Semiconductor Applications: Consequently Compiler Information Sufficient For Construction Purposes is Not Necessarily Given. The Information Purposes is Not Necessarily Given. The Information Purposes is Not Necessarily Given. The Information Personal Processed And is Believed To Be Entirely Reliable. However, No Responsibility is Assumed For Inaccuracies. Furthemore, Such Information Does Not Convey To The Purchaser Of The Semiconductor Devices Described Any License Under The Patent Rights of SiNSC Orothers. SINSC Reserves The Right To Make Changes At Any Time In Order To improve Design And Supply The Best Product Possible.

Assembly: 56 Lead QFN w/ Exposed GND Pad

Board Form Factor: USB / Ethernet Stand-Alone PCB

Chip: SMSC LAN7500

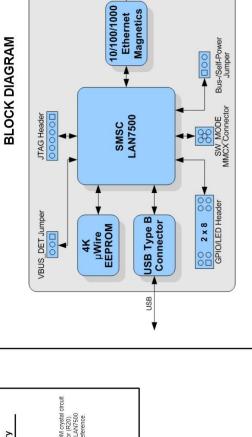
Assy 6588

Board:

**Design Details** 



## Page 2: Removed 1.0M crystal circuit parallel resistor (R20). Page 2: Correction to LAN7500 part number reference. Revision History Rev 1.0: Initial release, Rev A Rev 1.0 to 1.1:



Ethernet

RJ45

SUCCESS BY DESIGN

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