

AUTOSAR

Networked Embedded Systems

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Overview



- **Introduction**
- **Middleware: Run-Time Environment**
- **HW-Abstraction: Basic Software**
- **Scheduling**
- **Multicore** extentions
- **Conclusion**

Introduction

RTE

BSW

Scheduling

Multicore

Conclusion

Introduction

- **AUT**omotive **O**pen **S**ystem **A**rchitecture
- Initial Release: 2005
- Backward compatible to OSEK (Time)

“Cooperate on standards,
compete on implementation.”

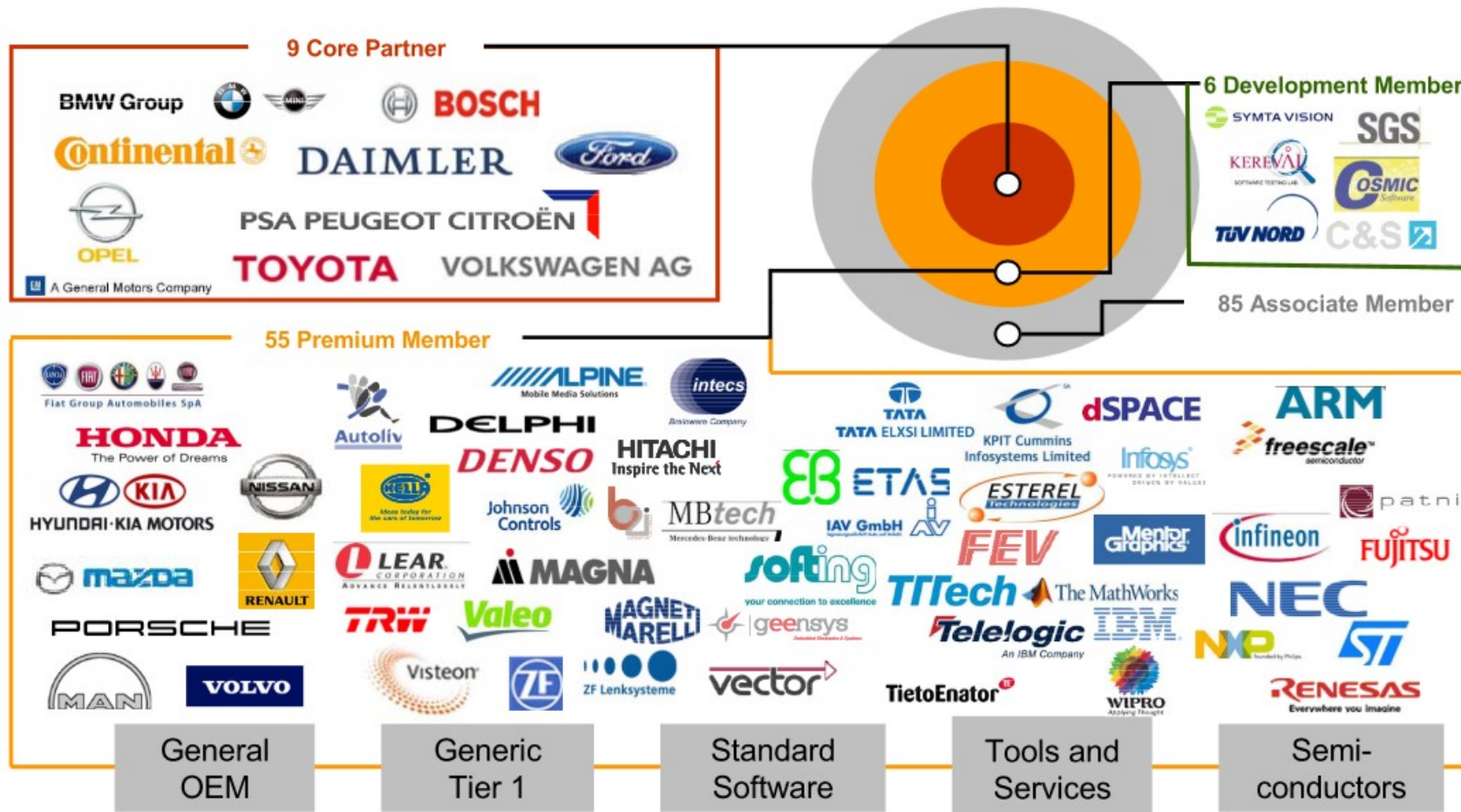


Efficiency and Quality

Partner & Member



Status: 10th October 2008 [Tut3]



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Goals

- Efficiency
- Quality
- Scalability
- Managing Complexity
- Maintainability
- Transferability
- Standardization

Architecture

Application Layer

Run-Time Environment (RTE)

Basic Software (BSW)

Microcontroller



Introduction

RTE

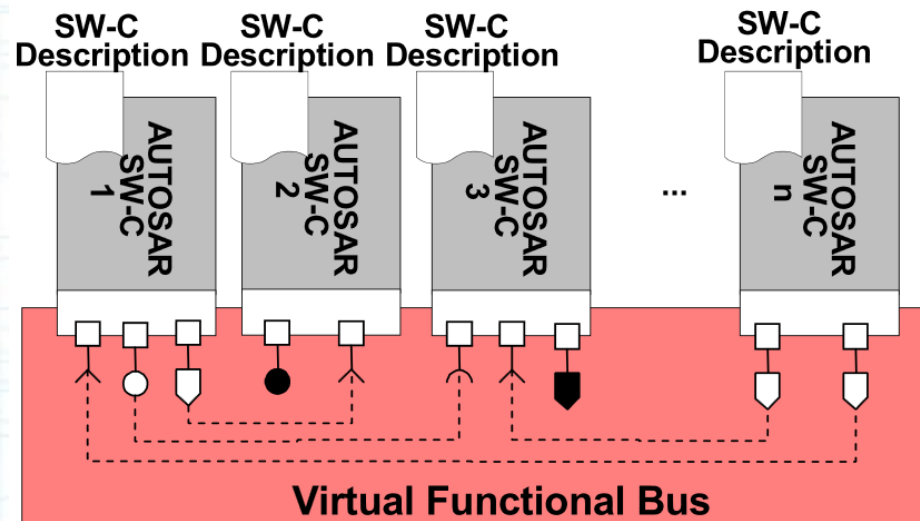
BSW

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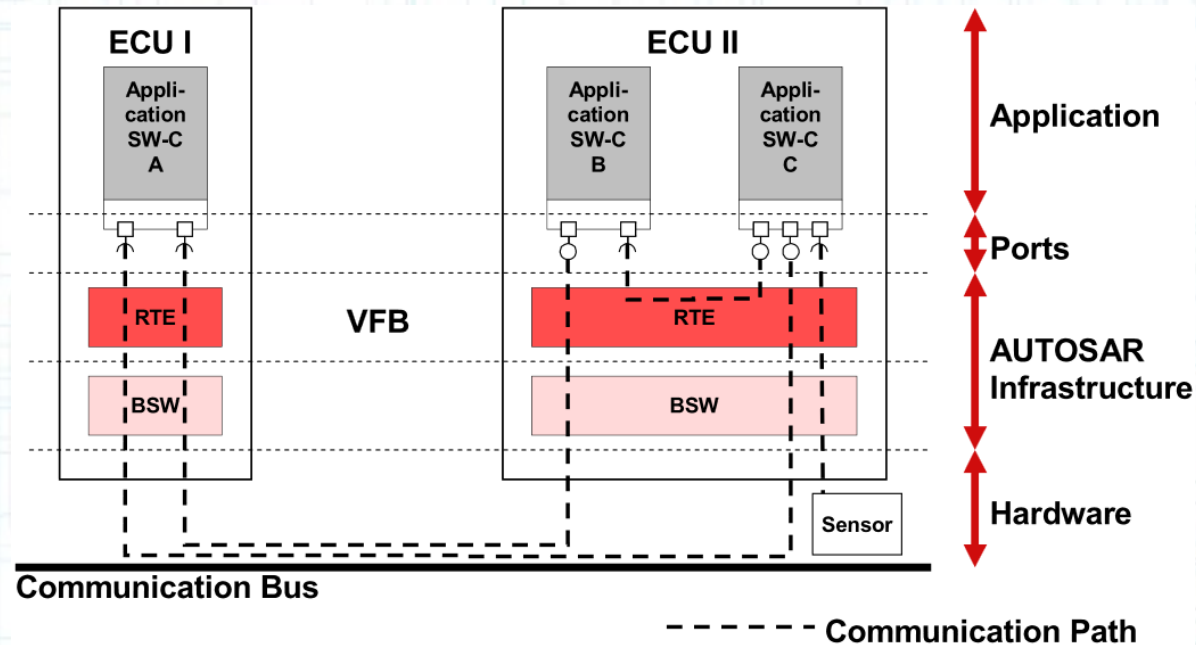
Conclusion

VFB



- Decoupling of software and infrastructure
- Application = interconnected **SW-Cs**
SoftWare Component

Communication

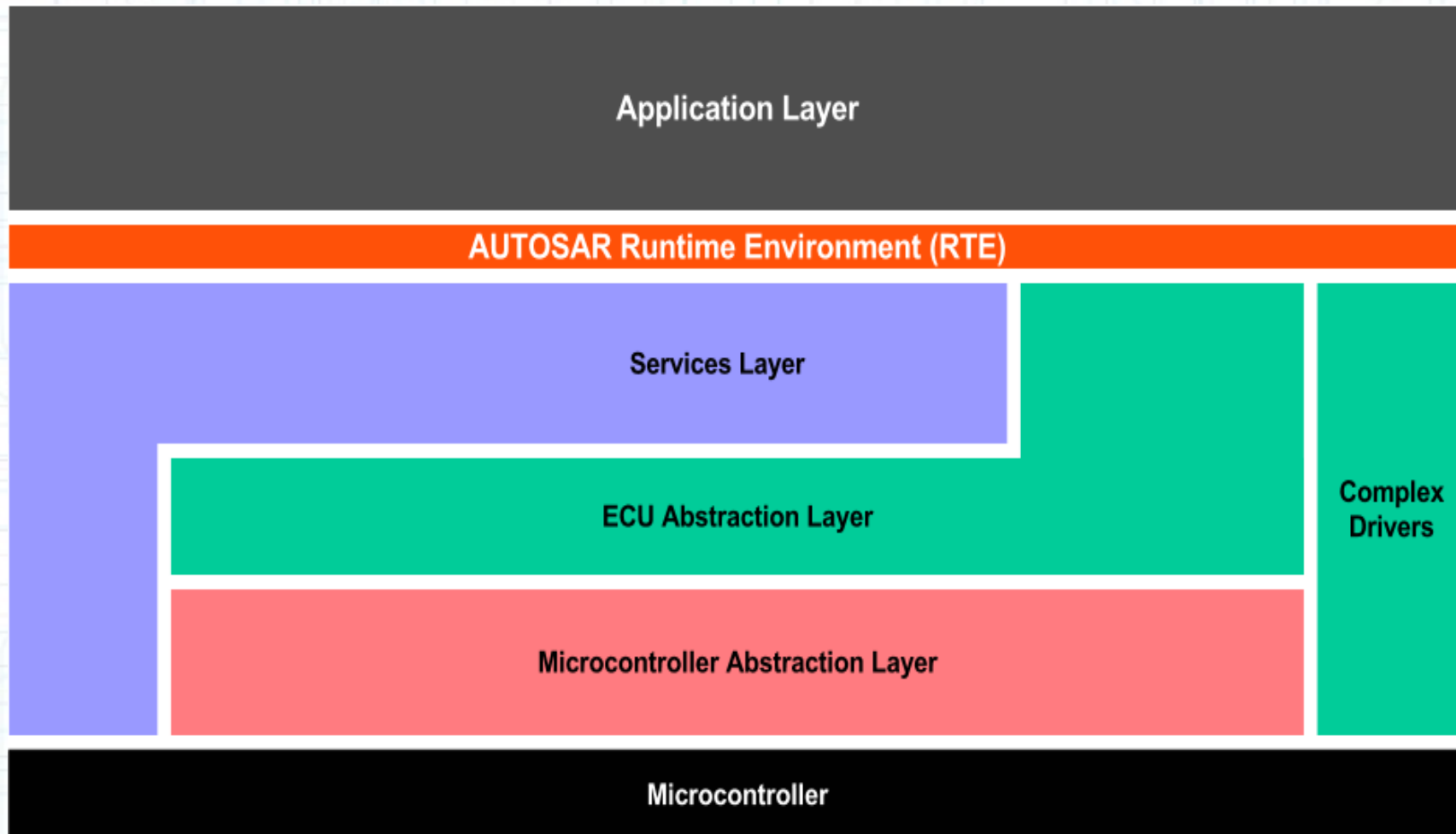


- Channeling communication via RTE
- Hiding communication in BSW

Communication

- Patterns:
 - Sender-receiver
 - Mode: explicit/ implicit
 - Client-server
- Signal
 - triggered/ pending
 - data (*queued*) or event (*unqueued*)

Basic Software



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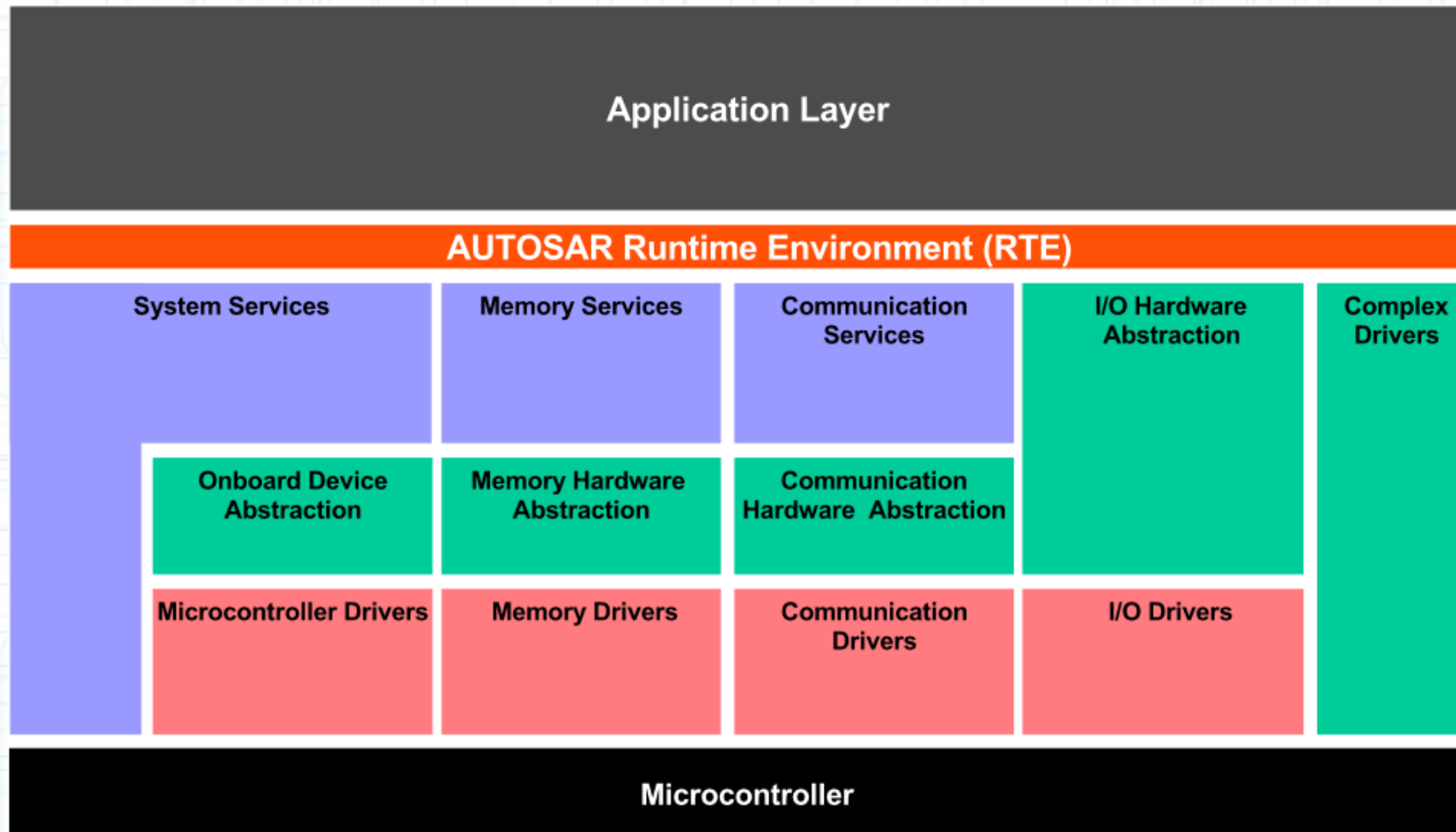
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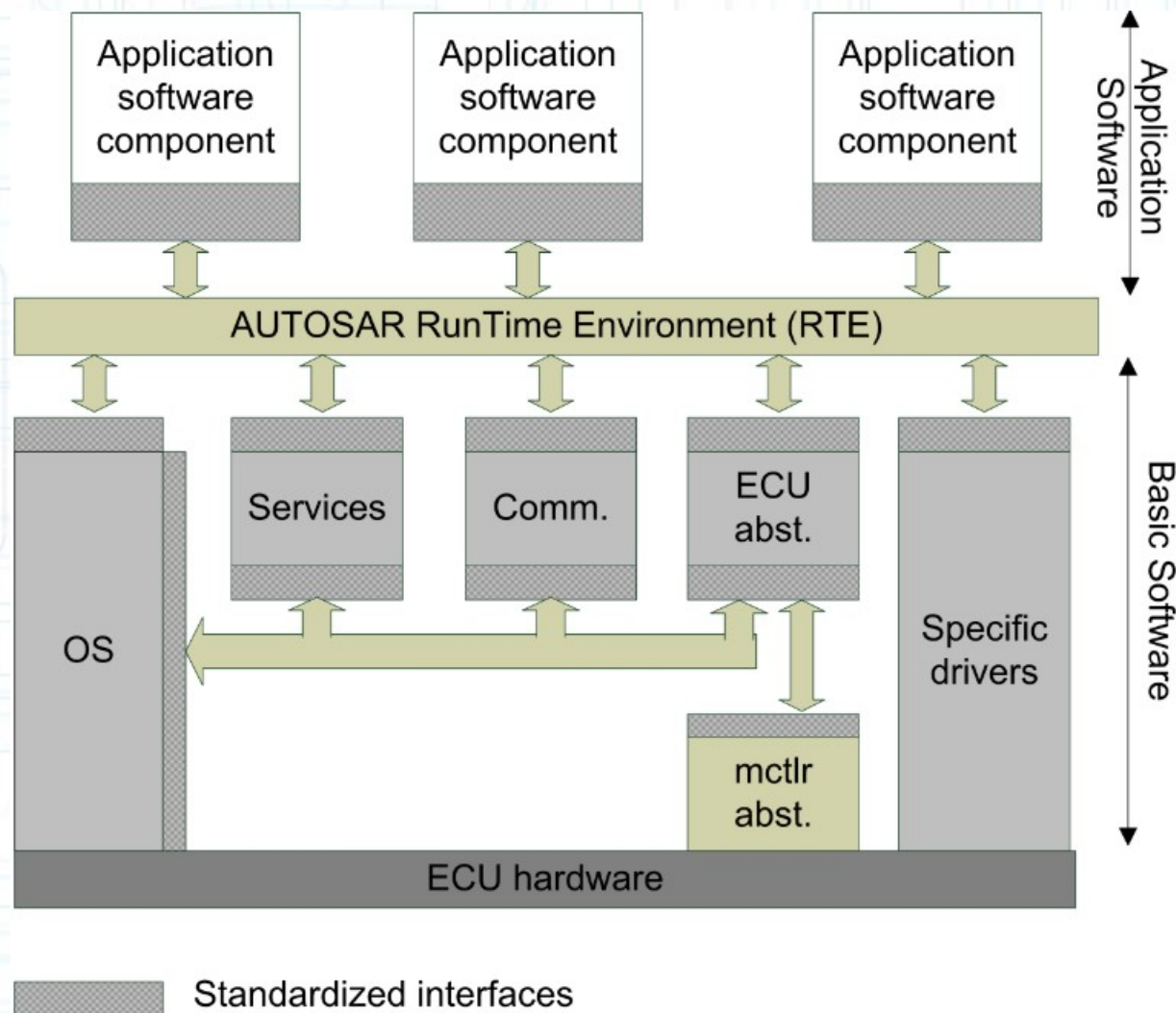
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Simplified View



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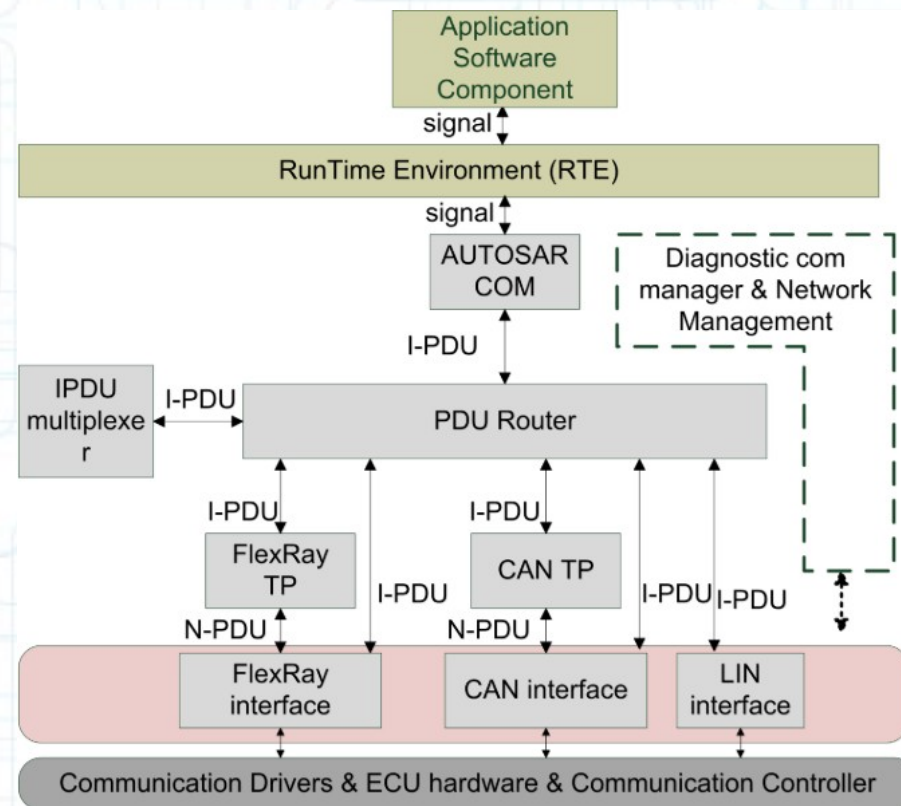
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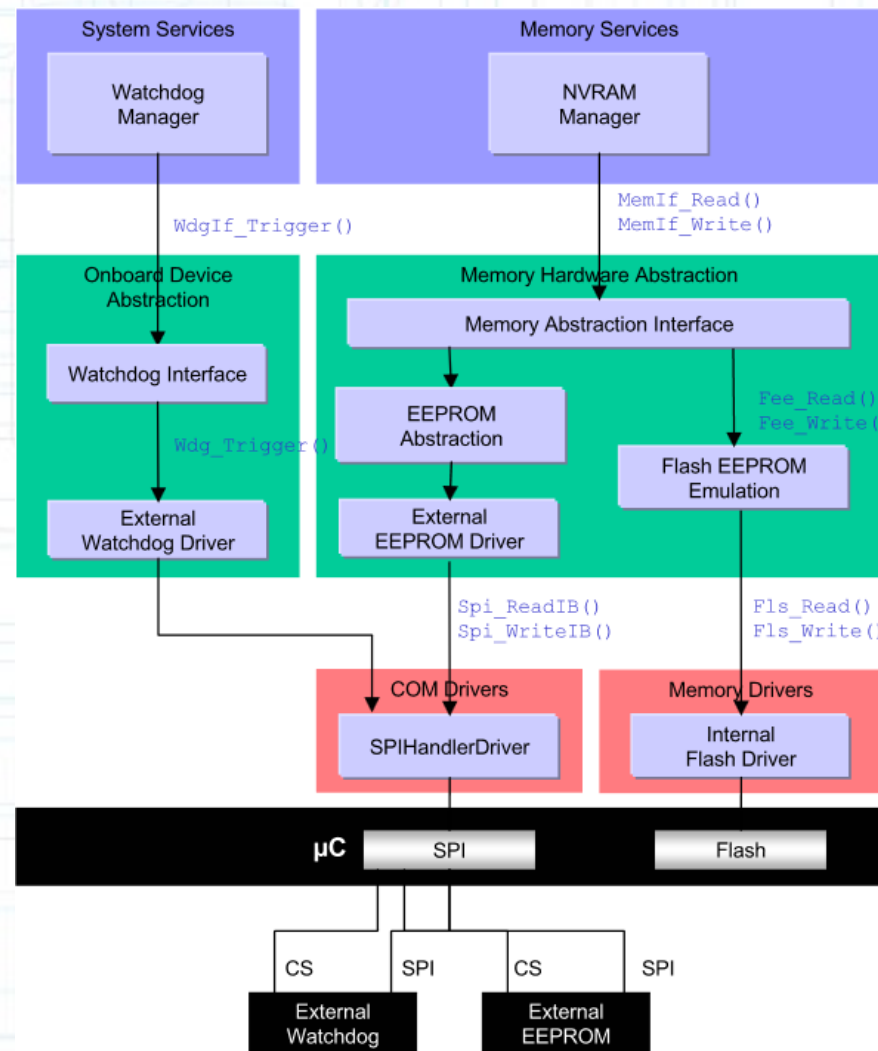
Communication

- Behavioural and timing properties
- Signal
- I-PDU: Interactional
- N-PDU: Network
- L-PDU: Data Link
- Local Transmission on same ECU

Overview on some communication modules



Interaction of Layers



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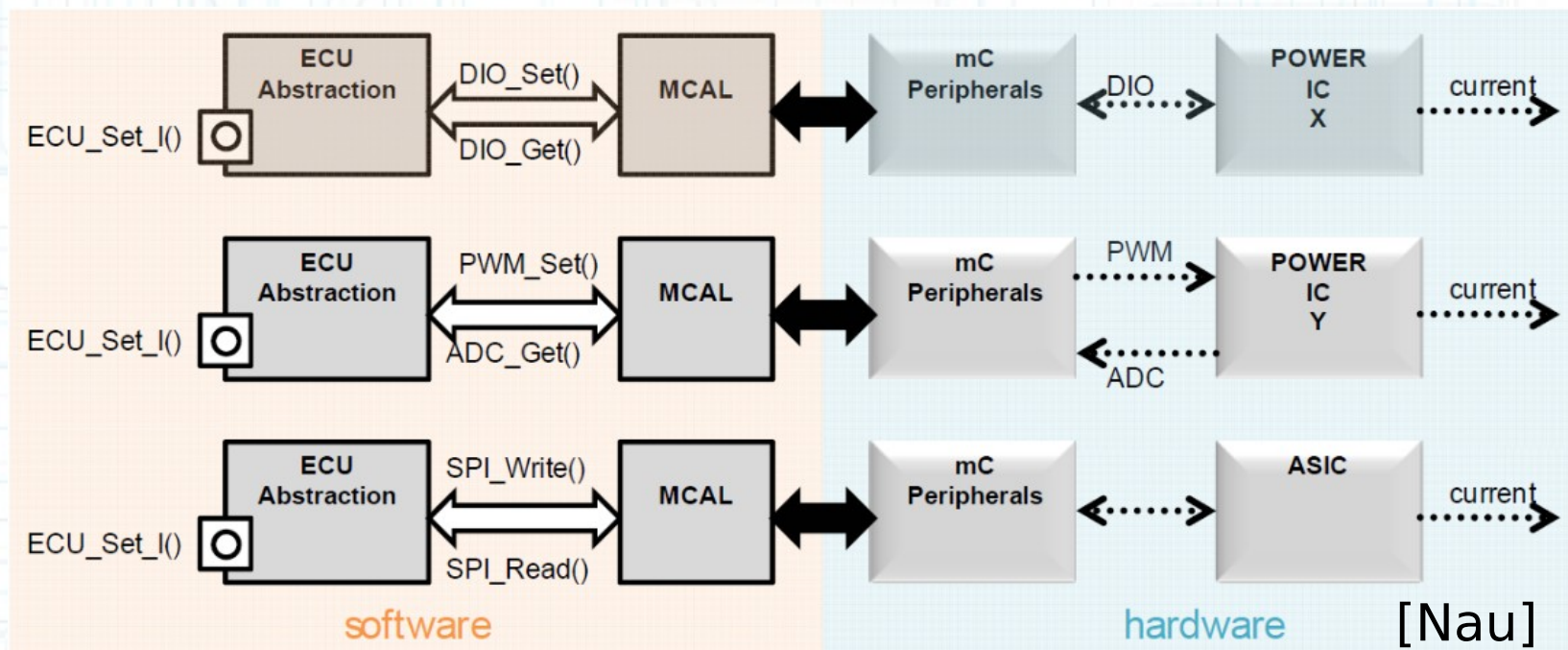
Scheduling

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ECU Abstraction

- 1 command 3 different implementations



Scheduling

- Timing Protection Service
- Priority Ceiling Protocol
- Schedule table



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Scheduling: Policy

- OSEK concepts:
 - Highest Priority, FIFO
 - Resource groups
- **Timing Protection Service**
 - Execution time $>$ WCET (predefined)
 - Holding shared resource too long
 - Violate arrival rate (time spent in time frame)
 - Periodic server concept

Scheduling: Schedule table

- Like alarms linked to a counter
- Single-shot or cyclic
- Offline scheduling techniques
- Activation points of all tasks
- No preemption

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V4.0 Multicore Architecture

- LE: Locatable Entities
- Multi-core startup/ shutdown
- IOC: **Inter-OS-App**clication **Com**municator
- SpinlockTypes

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Questions



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