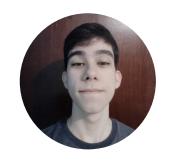
# **GUILHERME TEIXEIRA MARTINS**

#### **Electrical Engineer**

@ guilherme.martins@ee.ufcg.edu.br

**E**guilhermemartinst



# PROFESSIONAL AND ACADEMIC EXPERIENCES

#### Digital Hardware Developer

#### **Embedded Lab, UFCG**

2020-2023

- Training in digital microelectronics (digital design, verification, and backend fundamentals).
- Basics of the UVM (Universal Verification Methodology) methodology throughout the verification process.
- Systemverilog language for test development.
- Participation in the development of a verification IP for the AXI 4-Lite communication protocol
- Documentation production with strategies and limitations of developed tests (verification plan).
- Utilization of Cadence Design Systems' XCELIUM and SimVision Test and Simulation Platform.

Teaching Initiation Program and Improvement of Undergraduate Teaching - Logic Circuits

#### **UFCG**

2020

• Student tutor on the Logic Circuits course, helping and teaching other undergraduate students.

## **CERTIFICATIONS**

SystemVerilog for Design and Verification

**CADENCE DESIGN SYSTEMS** 

2023

Digital Certficate

C++ Language Fundamentals

**CADENCE DESIGN SYSTEMS** 

2023

• Digital Certficate

SystemC Language Fundamentals CADENCE DESIGN SYSTEMS

2023

• Digital Certficate

## **ABOUT ME**

I am an Electrical Engineer with a strong interest in programming and microelectronics, particularly focusing on hardware verification within microelectronics. I gained exposure to the field of digital microelectronics through university projects and have successfully completed several projects in this area. I am someone who highly values teamwork, believing that effective collaboration and communication are crucial in achieving common goals. I enjoy devising creative and innovative solutions to problems and am constantly seeking ways to enhance processes. I firmly believe that a positive and collaborative work environment is essential for both efficient and healthy performance. My main professional goal is to work in the field of microelectronics as a verification engineer.

## **HARD SKILLS**

System Verilog	C/C++ Python
HTML5 CSS	Linux Verilog
UVM Verification XCELIUM	
Atmel Studio SimVision Matlab	
Simulink LTspice SimulIDE	
Arduino Auto	oCAD LaTex Git
ASIC RTL Microsoft Office	

# **LANGUAGES**

Portuguese - Native

English - Advanced

### **EDUCATION**

Universidade Federal de Campina Grande - Bachelor's degree in Electrical Engineering

May 2017-August 2023

# **SOFT SKILLS**

Communication Planning Teamwork Proactivity

Creativity Flexibility Leadership Empathy