Present	Flip-flop inputs				
state	w = 0		w = 1		Output
$y_2 y_1$	J_2K_2	J_1K_1	J_2K_2	J_1K_1	Z
0 0	0 1	0 1	0 0	1 1	0
0.1	0.1	0 1	10	1 1	0
10	0.1	0 1	0 0	10	0
11	0.1	0 1	10	10	1

The excitation table for the circuit in Figure 6.77. Figure 6.78