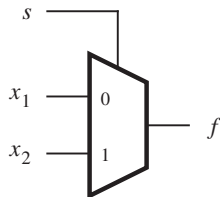


(b) Circuit



(c) Graphical symbol

$s$	$f(s, x_1, x_2)$
0	$x_1$
1	$x_2$

(d) More compact truth-table representation

**Figure 2.33** Implementation of a multiplexer.