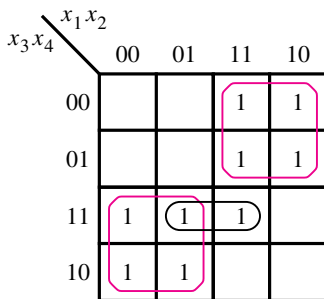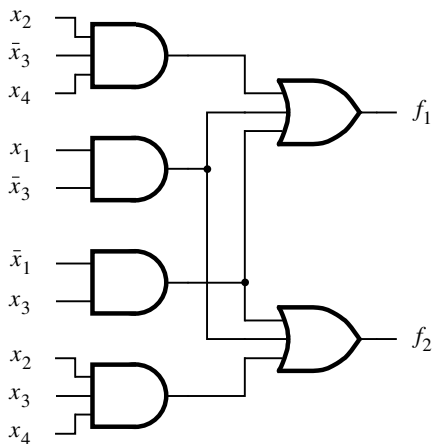(a) Function $f_1$

(b) Function $f_2$

(c) Combined circuit for $f_1$ and $f_2$

**Figure 2.64**    An example of multiple-output synthesis.