Present state $y_2 y_1$	Next state $w = 0$ $Y_2 Y_1$	$w = 1$ $Y_2 Y_1$	Output z
0 0	0 0	0 1	0
0 1	0 0	1 0	0
1 0	0 0	1 1	0
1 1	0 0	1 1	1

(a) State-assigned table

Present state	Next state $w = 0$	$w = 1$	Output z
A	A	B	0
B	A	C	0
C	A	D	0
D	A	D	1

(b) State table

Figure 6.76   Tables for the circuit in Figure 6.75.