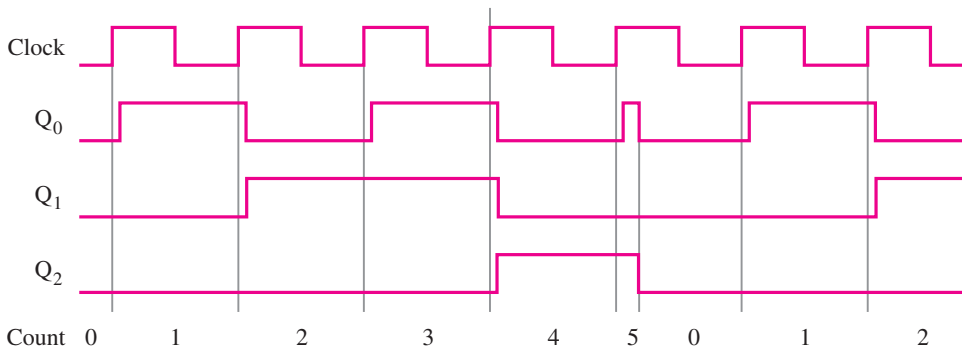


(a) Circuit



(b) Timing diagram

Figure 5.26 A modulo-6 counter with asynchronous reset.