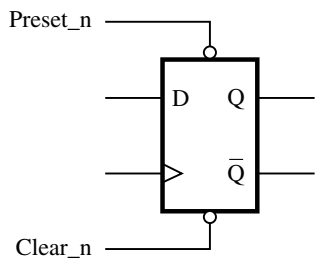
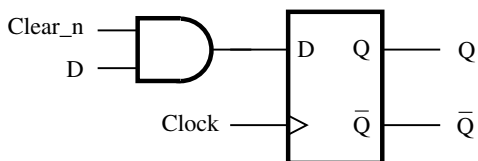


(a) Circuit



(b) Graphical symbol



(c) Adding a synchronous clear

Figure 5.13 Positive-edge-triggered D flip-flop with *Clear* and *Preset*.