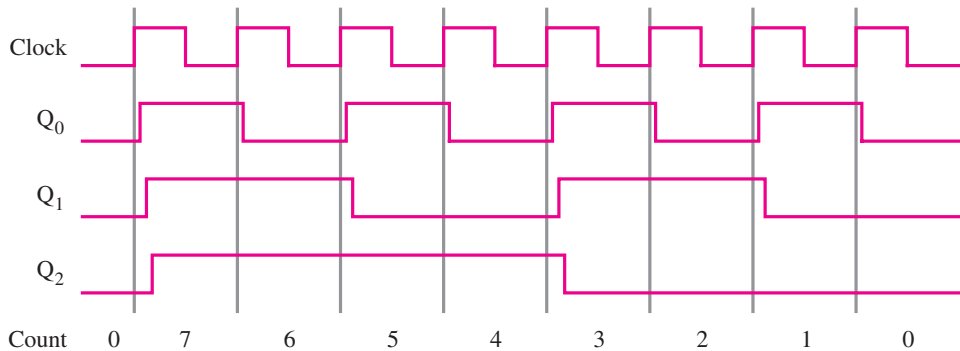


(a) Circuit



(b) Timing diagram

Figure 7.21 A three-bit down-counter.