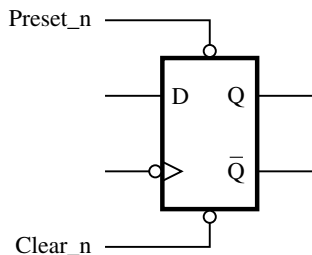


(a) Circuit



(b) Graphical symbol

Figure 5.12 Master-slave D flip-flop with *Clear* and *Preset*.