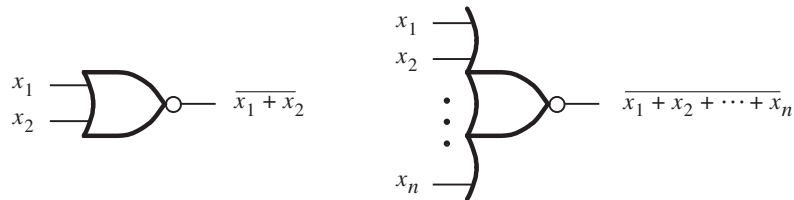


(a) NAND gates



(b) NOR gates

Figure 2.25 NAND and NOR gates.