Verilog operators. Operator type Operator symbols

Table 4.2

Bitwise	~	1's complement	1
	&	Bitwise AND	2
		Bitwise OR	2
	\wedge	Bitwise XOR	2
	$\sim \wedge$ or $\wedge \sim$	Bitwise XNOR	2
Logical	!	NOT	1
	&&	AND	2
		OR	2
Reduction	&	Reduction AND	1
	~&	Reduction NAND	1
		Reduction OR	1
	~	Reduction NOR	1
	^	Reduction XOR	1
	\sim^{\wedge} or $^{\wedge}\sim$	Reduction XNOR	1

Operation performed

Number of operands