(a) Circuit
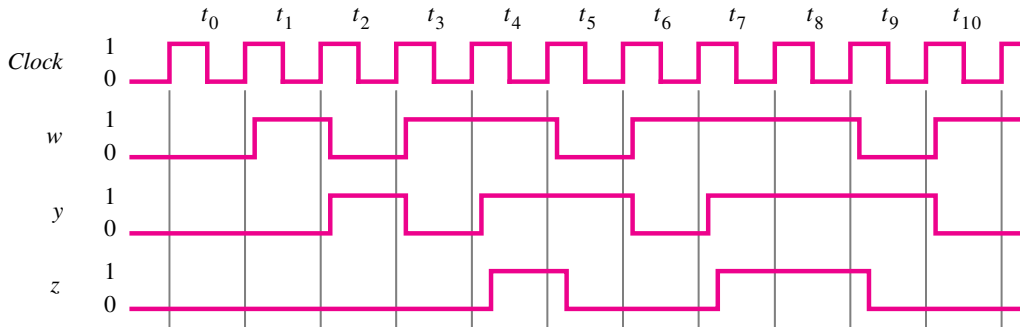

(b) Timing diagram

Figure 6.26   Implementation of the FSM in Figure 6.25.