

The diagram consists of three vertical panels, each representing a different computing architecture. Each panel is divided into two main sections: a top teal section labeled 'APLICAÇÃO' and a bottom light purple section. The bottom section of each panel has a jagged, mountain-like boundary line separating it from the top section. The first panel (ASIC) has a large purple area and a small teal area. The second panel (Reconfigurable Computation) has a medium-sized purple area and a medium-sized teal area, with a thin white gap between them. The third panel (Processor) has a very small purple area and a very large teal area, with a large white gap between them.

APLICAÇÃO

ASIC

APLICAÇÃO

**COMPUTAÇÃO
RECONFIGURÁVEL**

APLICAÇÃO

PROCESSADOR