

Top-level module

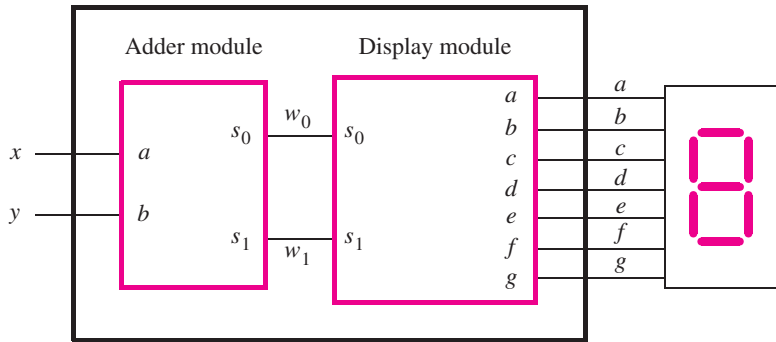


Figure 2.44 A logic circuit with two modules.