(a) Circuit
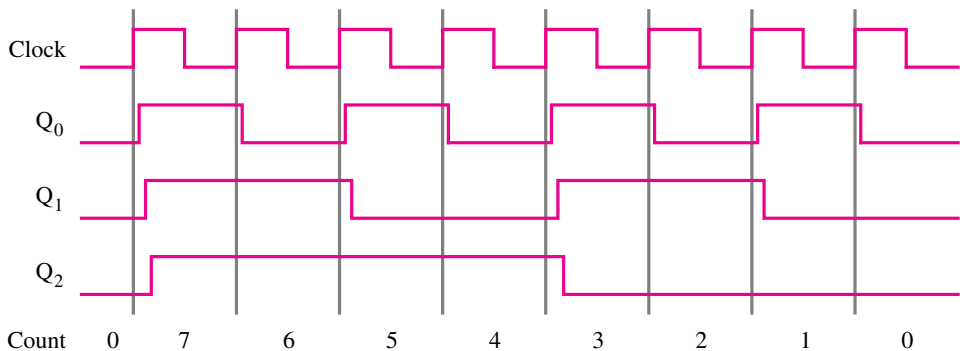

(b) Timing diagram

Figure 5.20 A three-bit down-counter.