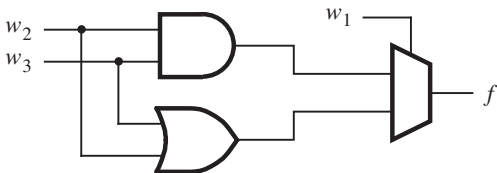


$w_1$	$w_2$	$w_3$	$f$		$w_1$	$f$	
0	0	0	0	}	0	$w_2 w_3$	
0	0	1	0			$w_2 + w_3$	
0	1	0	0				
0	1	1	1				
1	0	0	0	}	1		
1	0	1	1				
1	1	0	1				
1	1	1	1				

(a) Truth table



(b) Circuit

**Figure 4.10** The three-input majority function implemented using a 2-to-1 multiplexer.