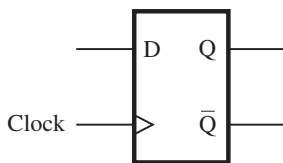(a) Circuit

(b) Graphical symbol

Figure 7.11 A positive-edge-triggered D flip-flop.