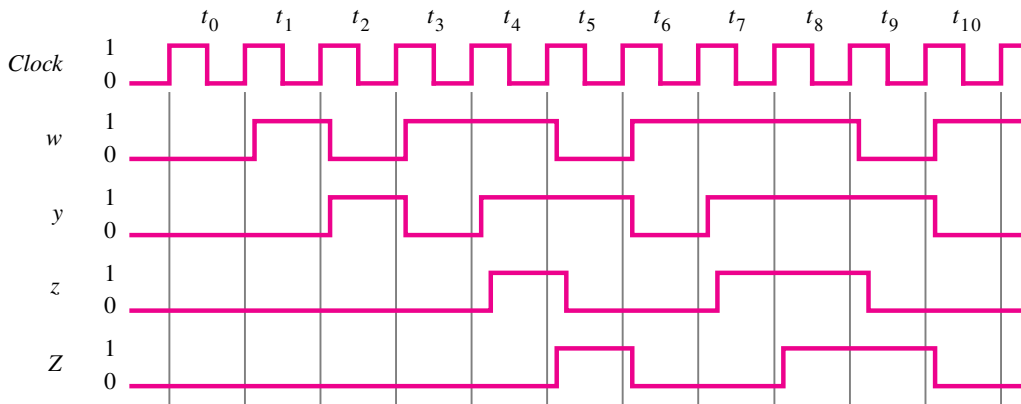


(a) Circuit



(b) Timing diagram

Figure 6.27 Circuit that implements the specification in Figure 6.2.