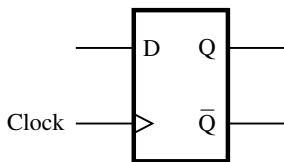


(a) Circuit



(b) Graphical symbol

Figure 5.11 A positive-edge-triggered D flip-flop.