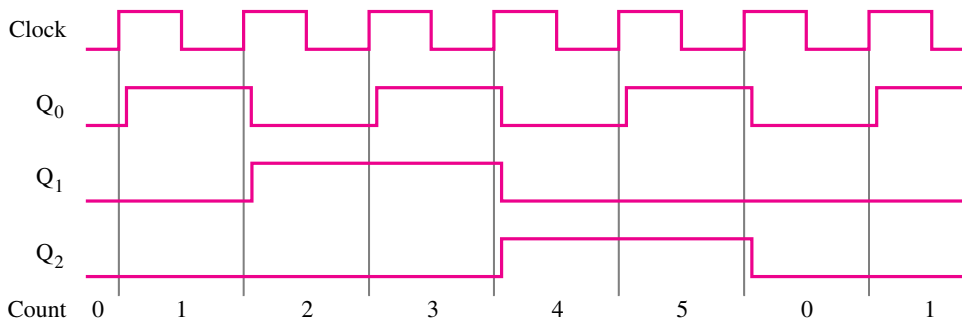


(a) Circuit



(b) Timing diagram

Figure 5.25 A modulo-6 counter with synchronous reset.