

(a) AND gates

$$\begin{array}{c}
x_1 \\
x_2 \\
\vdots \\
x_n
\end{array}$$

$$\begin{array}{c}
x_2 \\
\vdots \\
x_1 + x_2 + \dots + x_n
\end{array}$$

(b) OR gates



Figure 2.8 The basic gates.