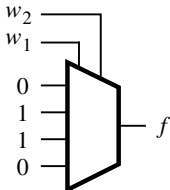


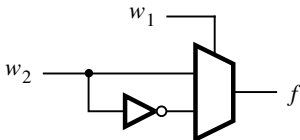
w_1	w_2	f
0	0	0
0	1	1
1	0	1
1	1	0



(a) Implementation using a 4-to-1 multiplexer

w_1	w_2	f		w_1	f
0	0	0	}	0	w_2
0	1	1		1	\bar{w}_2
1	0	1	}		
1	1	0			

(b) Modified truth table



(c) Circuit

Figure 4.6 Synthesis of a logic function using mutiplexers.