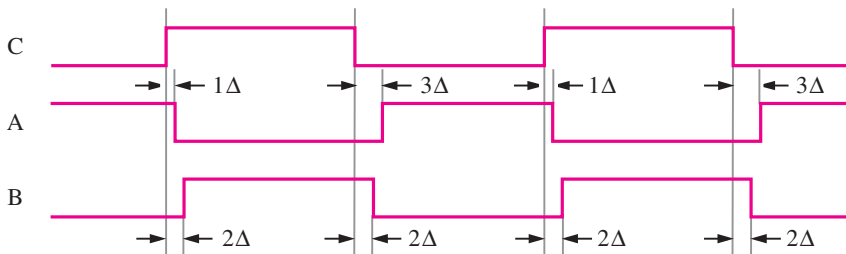


(a) Circuit



(b) Timing diagram

**Figure 5.70** Circuit for Example 5.18.