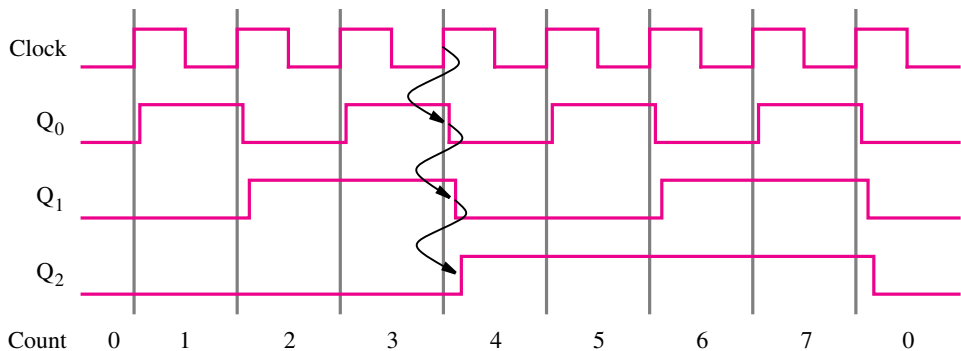


(a) Circuit



(b) Timing diagram

Figure 5.19 A three-bit up-counter.