GUILLAUME MASSÉ

I am an Apprentice Engineer in Electronics at INSA Rennes. My apprenticeship company is Thales SIX GTS France, where I focus on Soc FPGA development. I am looking for a 3 month internship between mid-June and December 2024.

PERSONAL DATA

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Guillaume MASSÉ

SKILLS

Soft:

- Collaborative
- Independent
- Meticulous
- Flexible

Hard:

Electronics

- Electronic circuit design (Logic gates / combinational and sequential)
- Hardware description language (VHDL)
- Processor architecture (ALU)
- Operating system (Linux)
- Analog Electronics (HF/Filtering/Oscillat**or)**

Automation

Computer Languages

- C/C++/Python/Bash)

Automatism

Computer skills

- MatLab
- Quartus/Vivado
- Kicad
- LTspice
- LabView
- Git

INTERESTS

- Basketball for 5 years + 2 year as a coach
- Electronics and programming with personal projects
- Science in general

LANGUAGE

- French (native)
- English: B2 (CEFR)

REFERENCES

Available upon request

WORK EXPERIENCE

Apprenticeship for Master University degree / Thales SIX GTS France Cholet, 110 Av. du Maréchal Leclerc, 49300 Cholet

2022 to 2025

Developing complex systems in VHDL language (hardware description language) to implement it on FPGA. I have improved my skills in:

> architecture design/description in VHDL/RTL simulation (VHDL/Cocotb)

Projects:

> research on reconfiguration design flow to create partitions that can be reconfigurable without editing the static part of the design (DPR).
More information: https://docs.xilinx.com/r/en-US/ug909-vivado-partial-reconfiguration/Introduction

> Function development and simulation to deal with samples

> Testbench development in co-simulation to open hardware on Ethernet module

2-year University degree (Orléans University) : Final intership / Novo Nordisk Chartres, 45 Avenue d'Orléans, 28000 Chartres

April to June 2022

Developing acquiring system to do preventive maintenance on camshaft-driven machines.
> Using LabVIEW language with acquire system of Texas Instrument
> Designing electronic boards to convert numeric Grey code into analog signal
> Preliminary analysis of data thanks to digital processing

TRAINING

Master's degree in engineering : 4th year in Electronics System and Telecommunication in apprenticeship / INSA of Rennes (France)

2022 to 2025

> Embedded systems using electronic concepts / autonomous electronic systems

> Designing networks for wired and wireless media

> Digital and analog telecommunications systems

> Acting as a responsible, humanistic professional, mindful of environmental and social issues

Projects:

> Development systems to measure the characteristics of bipolar transistors.

> Development of a predictive algorithm in C++

2–year University Degree in Technology of Electrical and Programming Engineering / Orléans University (France)

2020 to 2022 | Head of class

> Electronic board design, production and programming

> Automation of industrial processes

> Electronics embedded in mobile objects (avionics, robotics, ...)

> Implementation, management and maintenance of industrial networks

Projects:

> Stabilize power supply/Switching power supply/Radar system/Radio receiver AM of 144MHz